

Figure 1. ASIC architecture

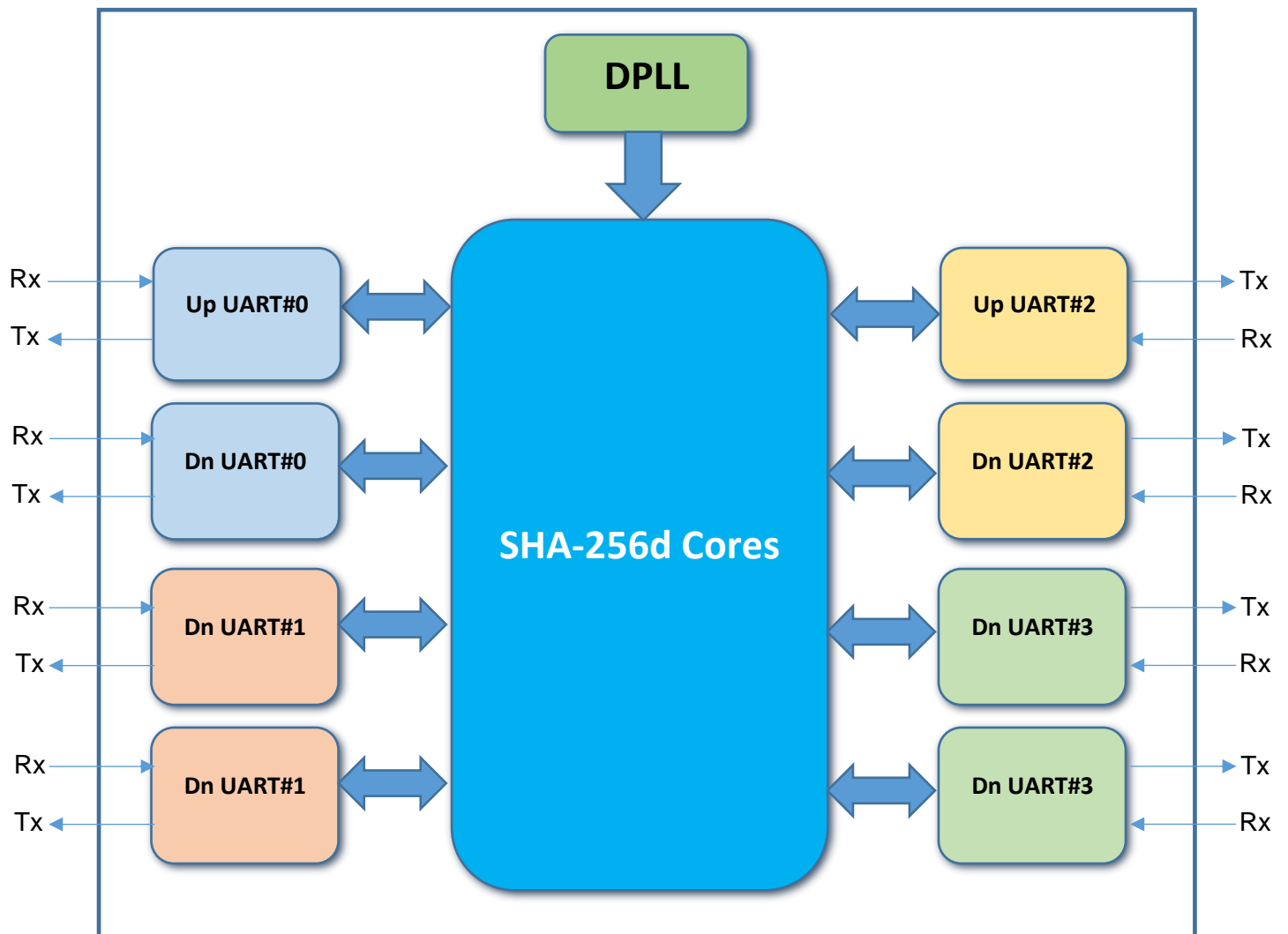


Table 1. ASIC operation conditions

ASC55A9918				
PARAMETER	UNIT	MIN	TYP	MAX
Core voltage (VDD_CORE)	V	0.50	0.55	0.61
Core current (VDD_CORE)	A	26.52	33.15	39.75
I/O voltage (IO_VDD)	V	1.62	1.8	1.98
PLL Digital power (PLL_VDD)	V	0.80	0.9	0.98
Operating temperature (T <sub>0</sub> )	°C	-	25	110
ASC55A9921				
Core voltage (VDD_CORE)	V	0.50	0.56	0.62
Core current (VDD_CORE)	A	32.27	40.39	48.45
I/O voltage (IO_VDD)	V	1.62	1.8	1.98
PLL Digital power (PLL_VDD)	V	0.80	0.9	0.98
Operating temperature (T <sub>0</sub> )	°C	-	25	110