

Figure 1. ASIC architecture

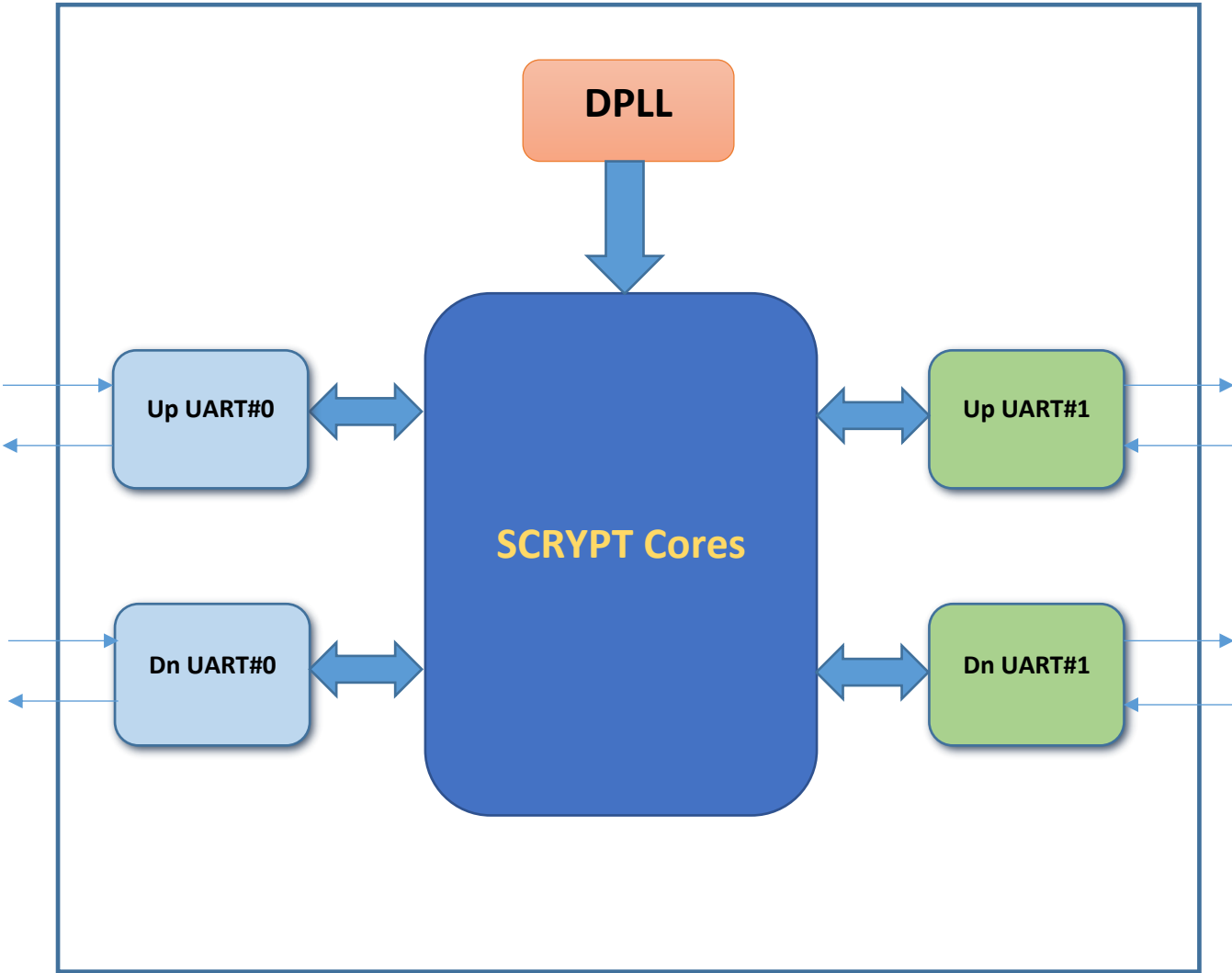


Table 1. ASIC operation conditions

ASC55A9011				
PARAMETER	UNIT	MIN	TYP	MAX
Core voltage (VDD_CORE)	V	0.50	0.55	0.60
Core current (VDD_CORE)	A	11.26	13.29	14.15
I/O voltage (IO_VDD)	V	1.62	1.80	1.98
PLL Digital power (PLL_VDD)	V	0.80	0.90	0.98
Operating temperature (T _O)	°C	-	25	110