Figure 1. **ASIC architecture**
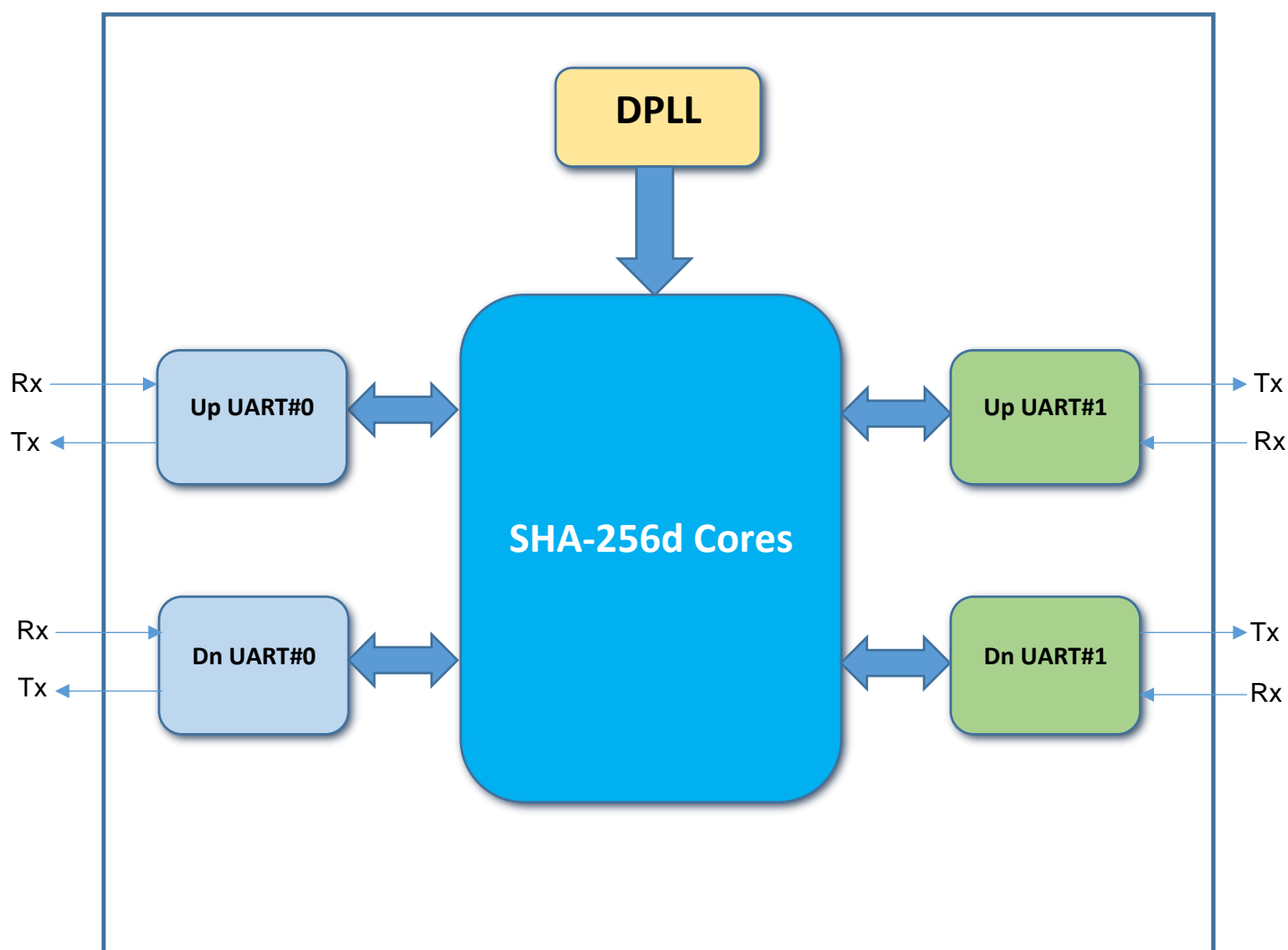

Table 1. **ASIC operation conditions**

ASC53E4390				
PARAMETER	**UNIT**	**MIN**	**TYP**	**MAX**
Core voltage (VDD_CORE)	V	0.65	0.72	0.79
Core current (VDD_CORE)	A	35.19	42.82	51.10
I/O voltage (IO_VDD)	V	1.62	1.8	1.98
PLL Digital power (PLL_VDD)	V	0.80	0.9	0.98
Operating temperature (T_O)	°C	-	25	110
ASC54F5201				
Core voltage (VDD_CORE)	V	0.59	0.65	0.72
Core current (VDD_CORE)	A	30.11	37.11	44.53
I/O voltage (IO_VDD)	V	1.62	1.8	1.98
PLL Digital power (PLL_VDD)	V	0.80	0.9	0.98
Operating temperature (T_O)	°C	-	25	110