

Figure 1. ASIC architecture

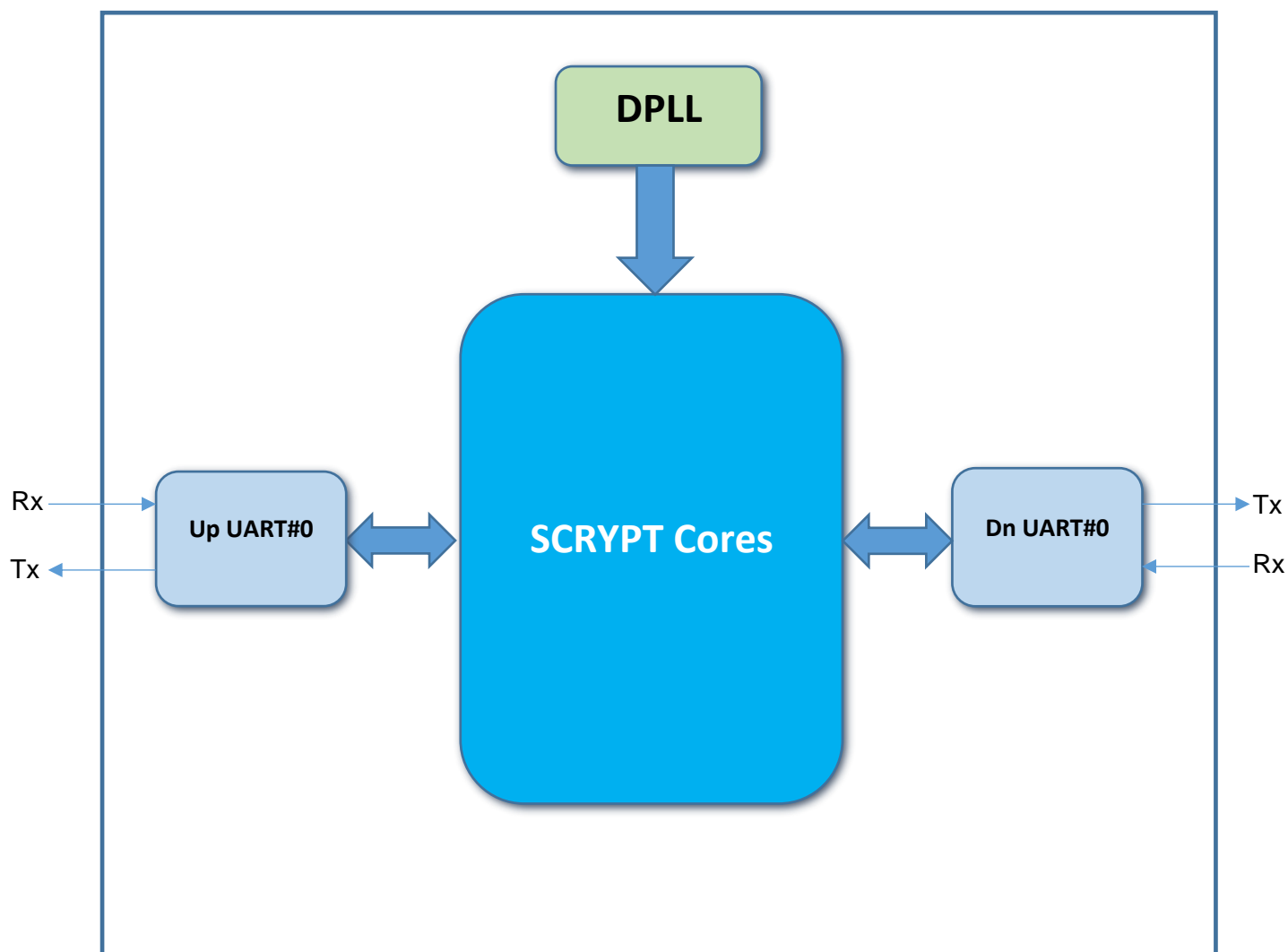


Table 1. ASIC operation conditions

ASC53E4592				
PARAMETER	UNIT	MIN	TYP	MAX
Core voltage (VDD_CORE)	V	0.68	0.75	0.83
Core current (VDD_CORE)	A	9.85	12.32	14.78
I/O voltage (IO_VDD)	V	1.62	1.8	1.98
PLL Digital power (PLL_VDD)	V	0.80	0.9	0.98
Operating temperature (T _O)	°C	-	25	110