



Chip: ASC55A9009

Package: TFBGA 361

Algorithm: SCRYPT

Process: 16 nm (TSMC)

Hash Rate: 4.5 MH/s

W/MH/s: 1.18

Frequency: 1000 MHz

Voltage: 0.56 V

T_J: -40...+90° C (T_M: +110° C)

- Optimized 128 (new design) hashing cores in single chip
- Custom package with power bars for low voltage, high current feeding
- 2xUART communication interface
- Chain mode, max. 96 chips per chain
- Fully adjustable clock frequency
- Hardware addressing and software addressing
- High effective thermal specifications
- Very low thermal emission