Chip: ASC55A9011

Package: NFBGA 361

Algorithm: SCRYPT

Process: 16 nm (TSMC)

Hash Rate: 6.83 MH/s

W/MH/s: 1.07

Frequency: 1200 MHz

Voltage: 0.55 V

T_I: -40…+90º C (T_M: +110º C)

- Optimized 192 (new design) hashing cores in single chip
- Custom package with power bars for low voltage, high current feeding
- 2xUART communication interface
- Chain mode, max. 128 chips per chain
- Fully adjustable clock frequency
- Hardware addressing and software addressing
- High effective thermal specifications
- Very low thermal emission