



Chip: ASC53E4390

Package: LBGA 81

Algorithm: SHA-256d

Process: 28 nm (TSMC)

Hash Rate: 68 GH/s

W/GH/s: 0.45

Frequency: 650 MHz

Voltage: 0.72 V

T_j: -20...+85° C (T_M: +110° C)

- Optimized 64 hashing cores in single chip
- Custom package with power bars for low voltage, high current feeding
- 2xUART communication interface
- Chain mode, max. 112 chips per chain
- Fully adjustable clock frequency
- Hardware addressing and software addressing
- High effective thermal specifications