



Chip: ASC53E4592

Package: LFBGA 112

Algorithm: Scrypt

Process: 28 nm (TSMC)

Hash Rate: 2 MH/s

W/MH/s: 4.55

Frequency: 1100 MHz

Voltage: 0.75 V

T_J: -20...+85° C (T_M: +110° C)

- Optimized 48 hashing cores in single chip
- Custom package with power bars for low voltage, high current feeding
- UART communication interface
- Chain mode, max. 72 chips per chain
- Fully adjustable clock frequency
- Hardware addressing and software addressing
- High effective thermal specifications