Chip: ASC54F5201

Package: LFBGA 144

Algorithm: SHA-256d

Process: 28 nm (TSMC)

Hash Rate: 96 GH/s

W/GH/s: 0.25

Frequency: 900 MHz

Voltage: 0.65 V

T_I: -20…+85° C (T_M: +110° C)

- Optimized 80 hashing cores in single chip
- Custom package with power bars for low voltage, high current feeding
- 2xUART communication interface
- Chain mode, max. 112 chips per chain
- Fully adjustable clock frequency
- Hardware addressing and software addressing
- High effective thermal specifications