



**Chip: ASC55A9918**

**Package: TFBGA 324**

**Algorithm: SHA-256d**

**Process: 16 nm (TSMC)**

**Hash Rate: 198 GH/s**

**W/GH/s: 0.092**

**Frequency: 1250 MHz**

**Voltage: 0.55 V**

**T<sub>J</sub>: -40...+90° C (T<sub>M</sub>: +110° C)**

- Optimized 150 (new design) hashing cores in single chip
- Custom package with power bars for low voltage, high current feeding
- 4xUART communication interface
- Chain mode, max. 256 chips per chain
- Fully adjustable clock frequency
- Hardware addressing and software addressing
- High effective thermal specifications
- Very low thermal emission