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	Revisions					
Rev	Description	Date	Approved			
В	Prototype Release	25-Mar-14	J.H			





NOTES

1. Unless Otherwise Specified:

All resistors are in ohms

All capacitors are in uF

All voltages are DC

All polarized capacitors are aluminum electrolytic

- 2. Interrupted lines coded with the same letter or letter combinations are electrically connected.
- 3. Device type number is for reference only. The number varies with the manufacturer.
- 4. Special signal usage:
 - _B Denotes Active-Low Signal
 - <> or [] Denotes Vectored Signals
- 5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

Power & Ground Nets

	1 00001 4 4	Tourid TVoto					
MOTHER BOARD SUPPLIED POWER							
1.25V_MB_SR	1.25V	From the MB switching regulator					
3.3V_MB_SR	3.3V	From the MB switching regulator					
5V_MB_SR	5V	From the MB switching regulator					
5V_MB_LR	5V	From the MB linear regulator					
EXTERNALLY SUPPLIED POWER							
1.25V_EXT	1.25V	External power into pin 1 of the terminal block					
3.3V_EXT	3.3V	External power into pin 2 of the terminal block					
5V_EXT	5V	External power into pin 3 of the terminal block					
POWER TO THE MCU							
VDD_LV_CORE	1.25V	Power to the core logic on the MCU					
VDD_LV_REG	1.25V	Power derived from MB or EXT 5V or 3.3V and regulated by the MCU through an external transistor					
VDD_LV_STBY	3.3V or 5V	Standby power to the MCU					
VDD_HV_IO_JTAG	3.3V or 5V	Power to the JTAG and clock circuits on the MCU					
VDD_HV_FLA	3.3V	Power to the MCU flash memory (regulated internally by MCU)					
VDD_HV_IO_MSC	3.3V or 5V	Power to the MSC circuit on the MCU					
VDD_HV_IO_FEC	3.3V or 5V	Power to the FEC circuit on the MCU					
VDD_HV_PMC	5V	Power to the PMC circuit on the MCU					
VDD_HV_IO_MAIN	5V	Power to the I/O circuits on the MCU					

OTHER POWER NETS

VDD_HV_ADV_SAR 5V

VDD_HV_ADV_SD 5V

VDD_HV_IO_MB	3.3V or 5V	Power for I/O circuits on the Mother Board
OSC_PWR	3.3V or 5V	Power for the clock oscillator

Power and reference voltage to the SAR ADC on the MCU

Power and reference voltage to the SD ADC on the MCU

GROUND NETS

GND	0V	Main digital ground
VSSA_JTAG	0V	Filtered ground for the JTAG and clock circuits
VSSA_ADC	0V	Filtered ground for the on chip ADC circuits

















