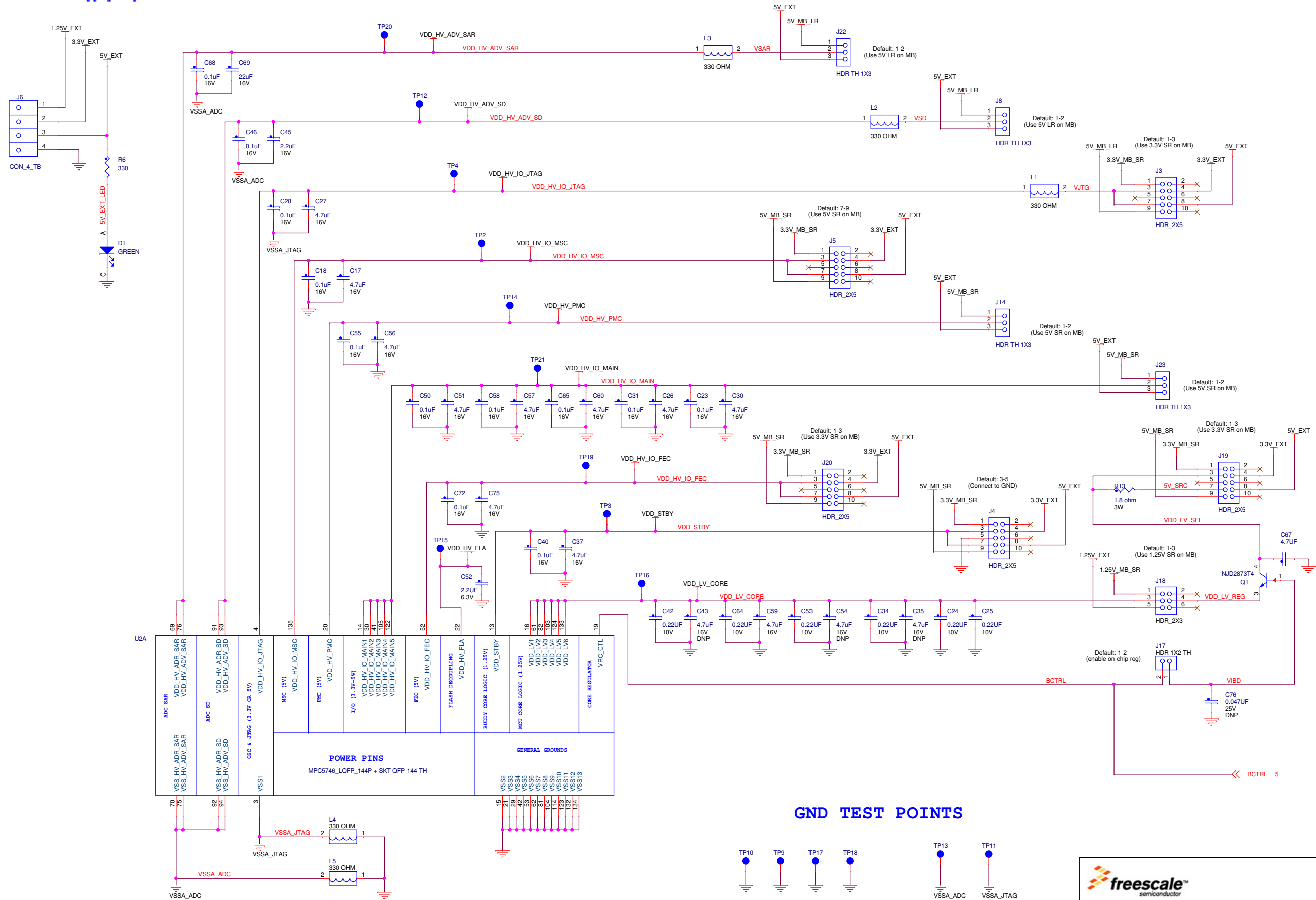
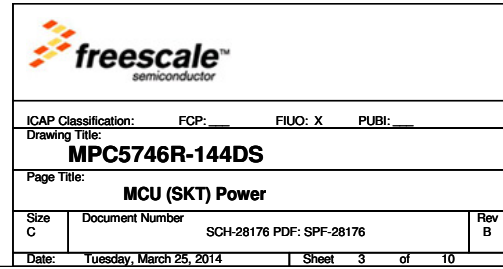
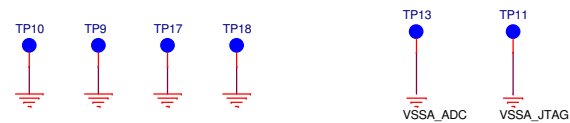
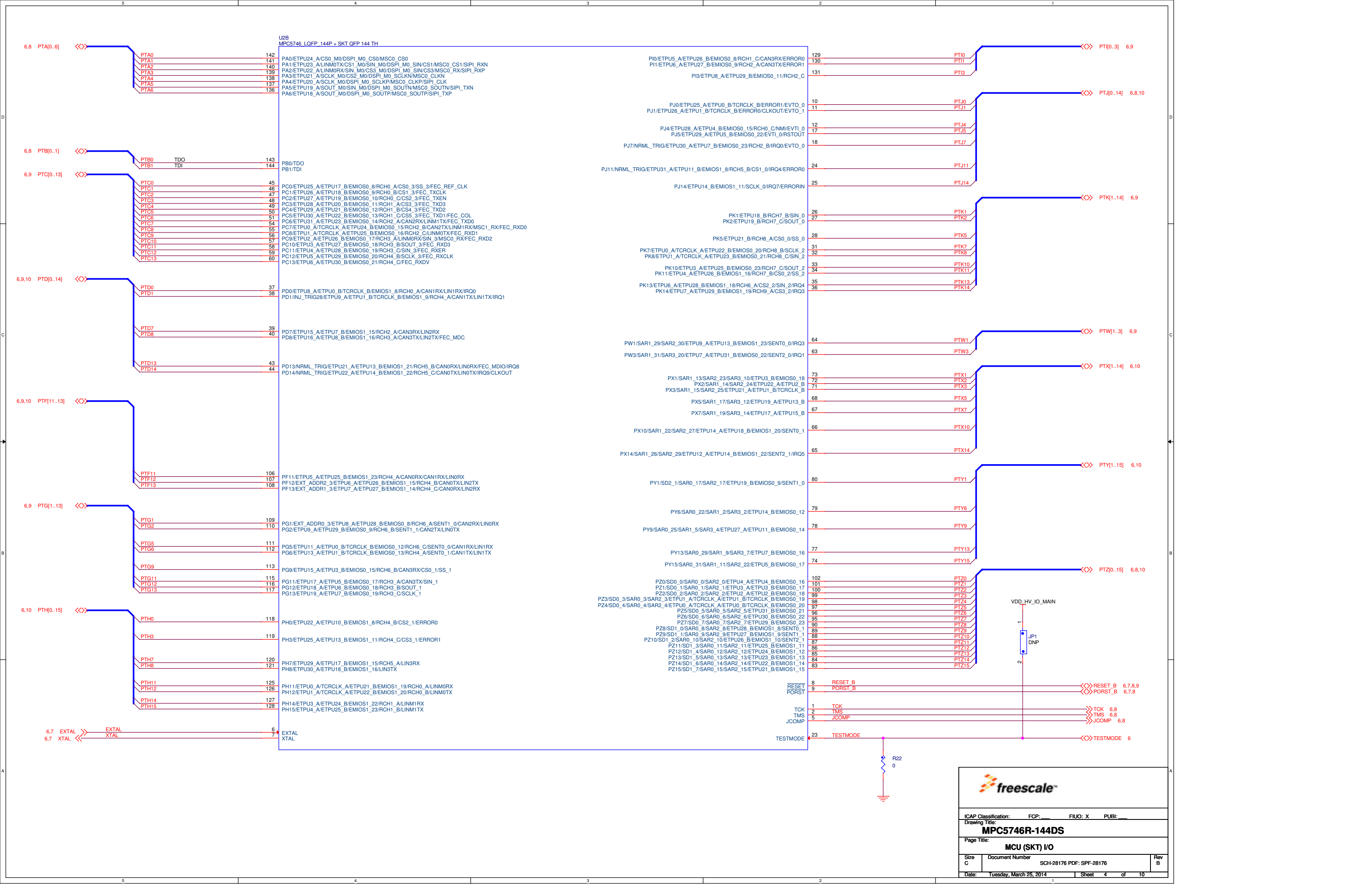


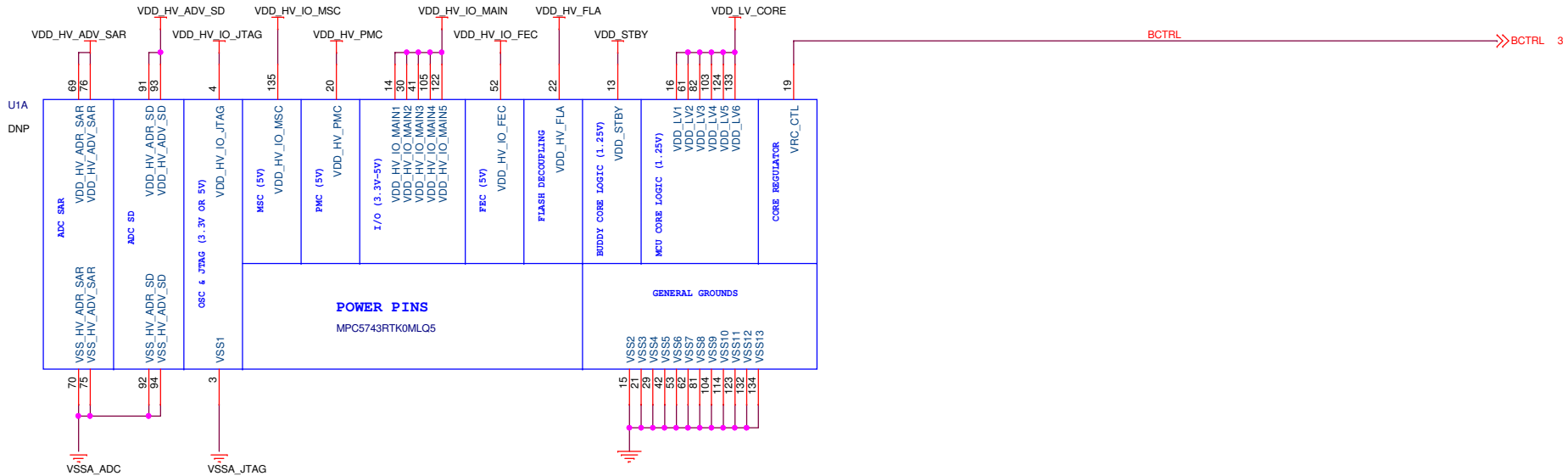
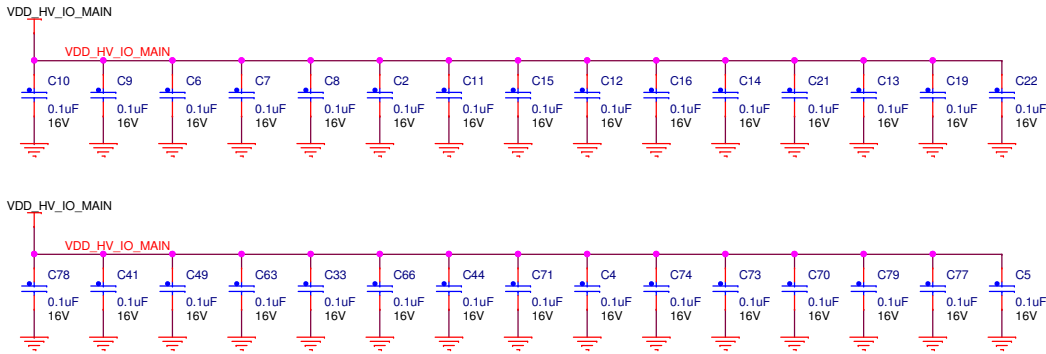
External Power Supply Input

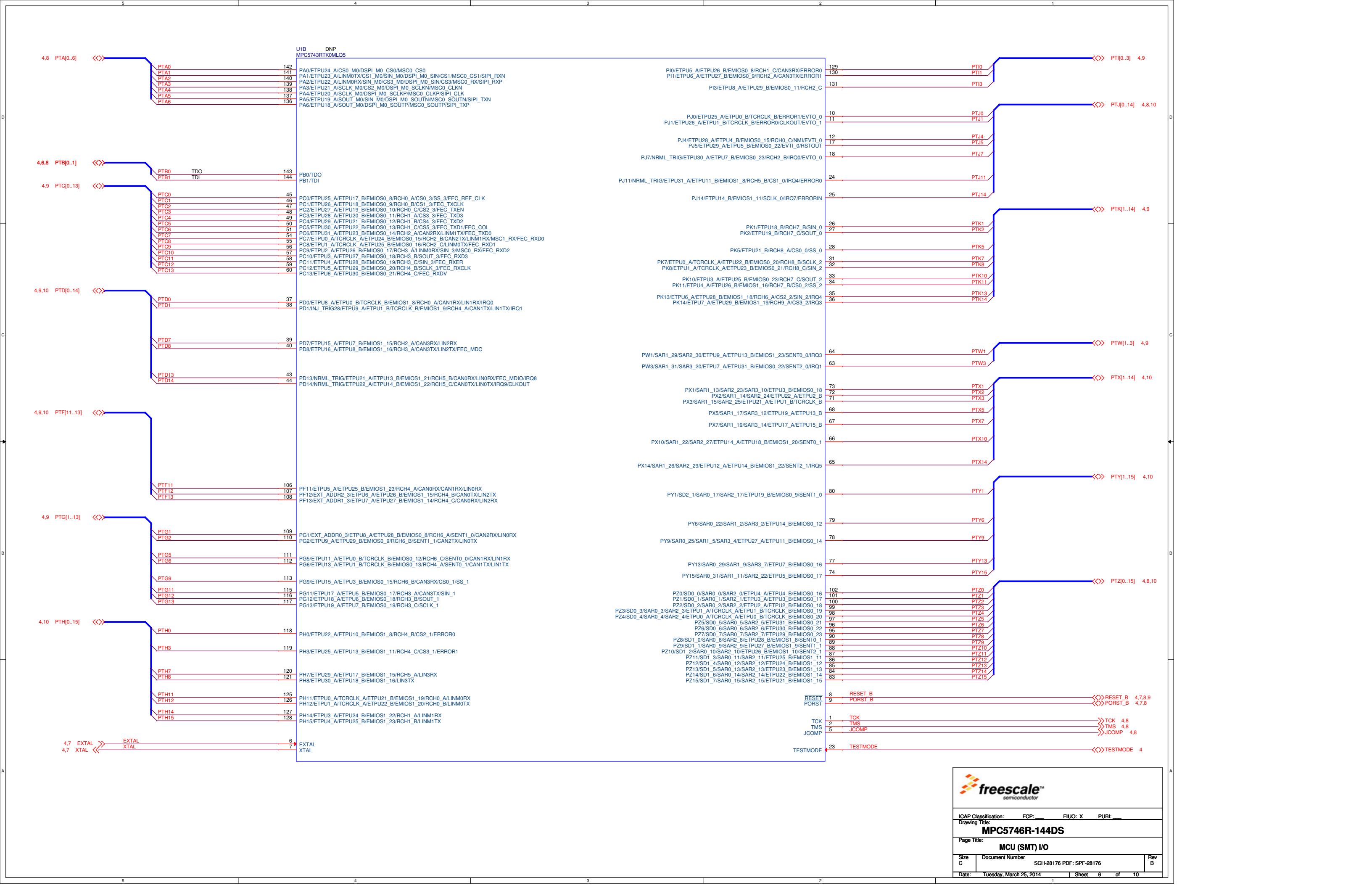


GND TEST POINTS









Configuration Jumpers and Headers	Clock Source		
	Crystal	Oscillator	SMA
Shunt to terminate EXTAL with 49.9 ohm to GND	Remove	Remove	Install
Shunt to connect XTAL to GND	Remove	Install	Install
Shunt to connect EXTAL to the crystal	Install	Remove	Remove
Shunt to connect EXTAL to the OSC	Remove	Install	Remove
Shunt to connect EXTAL to the SMA connector	Remove	Remove	Install
Header to disable the Oscillator	Install	Remove	Install



ICAP Classification: FCP: _____ FIUO: X PUBI: _____

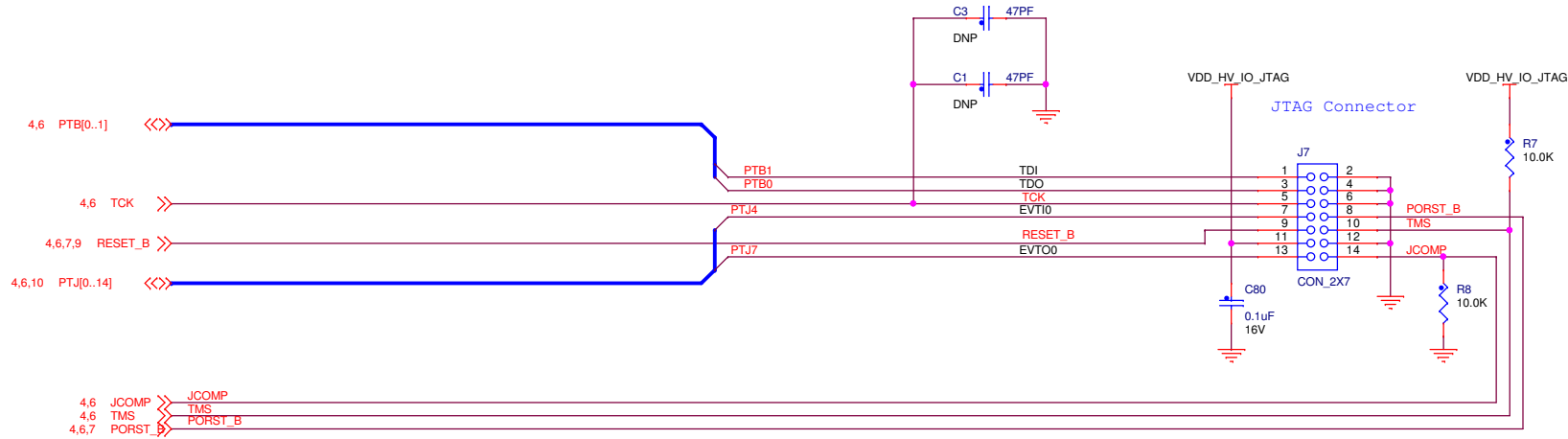
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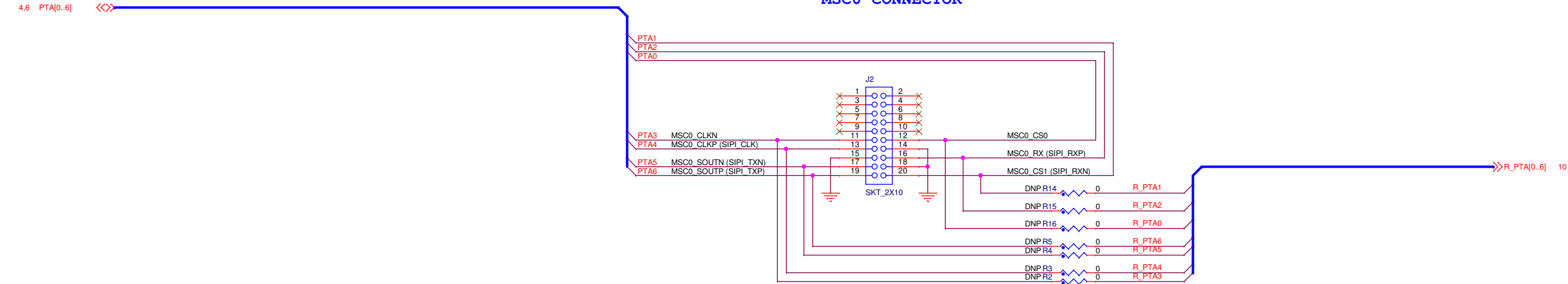
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JTAG INTERFACE



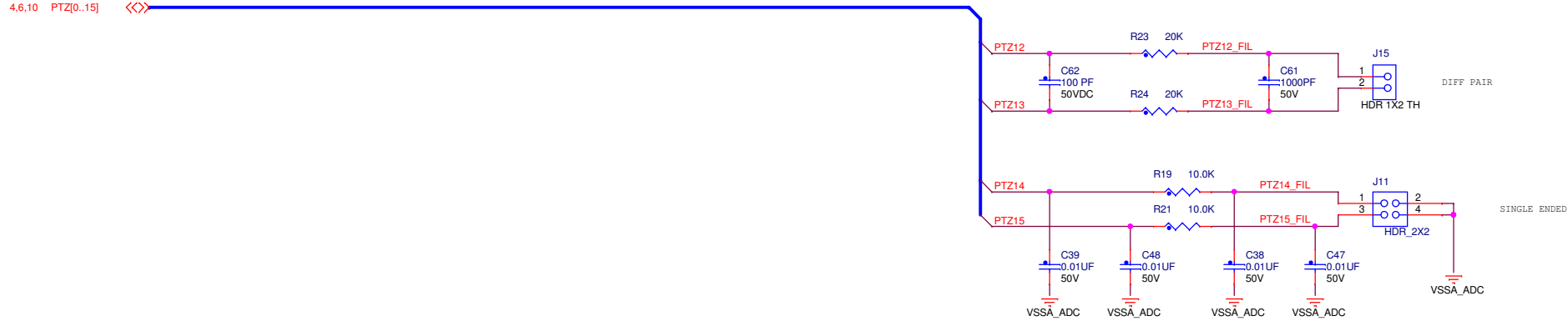
MSC0 CONNECTOR



Note to those desiring to use the SIPI interface:

The MSC0 and LFAST or SIPI signals are on the same pins of the MCU. Connector J2 may be used to bring out the SIPI signals. The SIPI functions are shown above in parenthesis.

ANALOG FILTERS



ICAP Classification: FCP: FILJO: X PUBI:

Drawing Title:

MPC5746R-144DS

Page Title:

JTAG, MSC1, & SIPI / ANALOG FILT

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