Node	Customi	Directio	Locatio	I/O	VREF	Fitter	I/O	Reserve	Current	Slew	Differen	Strict
Name	ze	n	n	Bank	Group	1	Standar		Strengt	Rate	tial Pair	
	Column s					n	d		h			ation
altera_r		Input				PIN_16	2.5 V (d		8mA (de			
eserved							efault)		fault)			
_tck												
altera_r		Input				PIN_15			8mA (de			
eserved							efault)		fault)			
_tdi							0 = > / / 1			0 () (
altera_r		Output				PIN_20			8mA (de	1		
eserved tdo							efault)		fault)	lt)		
altera_r		lnnut				PIN_18	2 5 1/14		8mA (de			
eserved		Input				_	efault)		fault)			
tms							Crautty		lautty			
clk_clk		Input				PIN 23	2.5 V (d		8mA (de			
-		1				_	efault)		fault)			
reset_re		Input				PIN_34	2.5 V (d		8mA (de			
set						_	efault)		fault)			
sdram_a		Output				PIN_42	2.5 V (d		8mA (de	2 (defau		
ddr[12]							efault)		fault)	lt)		
sdram_a		Output				PIN_14	2.5 V (d		8mA (de	2 (defau		
ddr[11]						1	efault)			lt)		
sdram_a		Output				PIN_28			8mA (de			
ddr[10]							efault)		,	lt)		
sdram_a		Output				PIN_3	2.5 V (d		8mA (de	1		
ddr[9]		_					efault)		· ·	lt)		
sdram_a		Output				PIN_10			8mA (de			
ddr[8]		0 1				DINI 2	efault)			lt)		
sdram_a ddr[7]		Output				1	2.5 V (d efault)		8mA (de fault)	2 (аетаи lt)		
sdram_a		Output				PIN_31			8mA (de			
ddr[6]		Output				_	efault)		fault)			
sdram_a		Output				PIN_13	1		8mA (de			
ddr[5]		Catput				_	efault)		fault)	lt)		
sdram_a		Output					2.5 V (d		8mA (de			
ddr[4]							efault)			lt)		
sdram_a		Output				PIN_11	i		8mA (de	2 (defau		
ddr[3]						_	efault)			lt)		
sdram_a		Output				PIN_30	2.5 V (d		8mA (de	2 (defau		
ddr[2]							efault)		fault)	lt)		
sdram_a		Output				PIN_7	2.5 V (d		8mA (de	2 (defau		
ddr[1]							efault)		fault)	lt)		
sdram_a		Output				PIN_13			8mA (de			
ddr[0]							efault)		fault)	lt)		
sdram_		Output				PIN_13			8mA (de			
ba[1]						8	efault)		fault)	lt)		

s PIN_13 2.5 V (d 8mA (de 2 (defau	Node	Customi	Directio	Locatio	I/O	VREF	Fitter	I/O	Reserve	Current	Slew	Differen	Strict
S S PIN_13 2.5 V (d SmA (de 2 (defau fault) Sman (de 2 (d	Name	ze	n	n		Group	Locatio	Standar	d	Strengt			
Safram_ Output PIN_13 2.5 V d Fault Equity Equaty Equaty		Column					n	d		h			ation
Safram_ c		S											
Safram_ c	sdram		Output				PIN 13	2.5 V (d		8mA (de	2 (defau		
Safram_c Output PIN_32 2.5 V (d	ba[0]						_	1					
Sdram_ c Sdram_ cdd[15] Sdram_ dd[15] Sdram_ dd[16] Sdram_ dd[17] Sdram_ dd[18] Sd			Output							· ·	2 (defau		
Safram_c Coutput PIN_43 2.5 V (d Fault) Equation Equ	_						_						
			Output										
Safam_c Safa	_							1					
Scham_ Bidir PiN_11 2.5 V BmA (de 2 (defau de fault) f			Output				PIN 14						
Sdram_ Bidir PIN_11 2.5 V SmA (de 2 (defau defaut) faut) faut) faut) fauti faut) fauti faut) fauti faut) fauti	_						_						
Sdram_ Bidir PIN_12 2.5 V BmA (de 2 (defau default) Common Co			Bidir								-		
Sidam_ Bidir PIN_12 2.5 V (d 8mA (de 2 (defau fault) fault) fault	_						_	1					
			Bidir										
Bidir PIN_12 2.5 V Control Bidir PIN_13 2.5 V Control Bidir PIN_14 Control Bidir PIN_15 Control Bidir PIN_15 Control Bidir PIN_16 Control Bidir PIN_17 Control Bidir PIN_18 Control Bidir PIN_19 Control Bidir Bidir PIN_19 Control Bidir Bidir PIN_19 Control Bidir Bidir PIN_19 Control Bidir Bidir Bidir PIN_19 Control Bidir Bidir Bidir PIN_19 Control Bidir Bi	_		Dia				_	1					
Softan_ Bidir PIN_12 2.5 V (d BmA (de 2 (defau fault) Early Earl			Bidir							· ·	·		
Bidir PIN_12 2.5 V Color BmA (de 2 (defau default) Color	_		Dia				_	1					
dq[12]			Bidir										
Bidir PIN_12 2.5 V (d BmA (de 2 (defau fault) lt)	_		Dian.				_	1					
			Bidir										
Bidir PIN_11 2.5 V BmA Ge 2 Gefau Galt G	_		Dian.				_						
Description			Ridir								-		
Bidir PIN_11 2.5 V (d BmA (de 2 (defau fault) lt)	_		Dian				_	1					
1			Ridir										
Bidir Bidi	_		Didii				_	1					
dq[8] 6 efault) fault) lt) sdram dq[7] Bidir PIN_11 2.5 V (d 2 efault) 8mA (de 2 (defau fault) lt) sdram dq[6] Bidir PIN_11 2.5 V (d 8mA (de 2 (defau fault) lt) lt) sdram dq[6] Bidir PIN_12 2.5 V (d 8mA (de 2 (defau fault) lt) lt) sdram dq[5] Bidir PIN_11 2.5 V (d 8mA (de 2 (defau fault) lt) lt) sdram_ dq[4] Bidir PIN_12 2.5 V (d 8mA (de 2 (defau fault) lt) lt) sdram_ dq[3] Bidir PIN_12 2.5 V (d 8mA (de 2 (defau fault) lt) lt) sdram_ dq[2] Bidir PIN_12 2.5 V (d 8mA (de 2 (defau fault) lt) lt) sdram_ dq[2] Bidir PIN_11 2.5 V (d 8mA (de 2 (defau fault) lt) lt) sdram_ dq[1] PIN_13 2.5 V (d 8mA (de 2 (defau fault) lt) lt) sdram_ dq[0] PIN_14 2.5 V (d 8mA (de 2 (defau fault) lt) sdram_ dq[0] PIN_14 2.5 V (d 8mA (de 2 (defau fault) lt) sdram_ dq[0] PIN_14 2.5 V (d 8mA (de 2 (defau fault) lt) <t< td=""><td></td><td></td><td>Ridir</td><td></td><td></td><td></td><td></td><td></td><td></td><td>· ·</td><td></td><td></td><td></td></t<>			Ridir							· ·			
Bidir Bidi	_		biuii				_	1					
dq[7]			Didir										
Bidir Bidi	_		biuii				_	1					
dq[6] 9 efault) lt) sdram_ Bidir PIN_12 2.5 V (d 8mA (de 2 (defau fault)) dq[5] 5 efault) lt) sdram_ Bidir PIN_11 2.5 V (d 8mA (de 2 (defau fault)) dq[4] 3 efault) fault) lt) sdram_ Bidir PIN_12 2.5 V (d 8mA (de 2 (defau fault)) dq[3] 9 efault) fault) lt) sdram_ Bidir PIN_12 2.5 V (d 8mA (de 2 (defau fault)) dq[2] 8 efault) fault) lt) sdram_ Bidir PIN_11 2.5 V (d 8mA (de 2 (defau fault)) dq[1] 4 efault) fault) lt) sdram_ Bidir PIN_13 2.5 V (d 8mA (de 2 (defau fault)) dq[0] 3 efault) fault) lt) sdram_ Output PIN_14 2.5 V (d 8mA (de 2 (defau fault)) dq[1] 2 6 <td></td> <td></td> <td>Ridir</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td>-</td> <td></td> <td></td> <td></td>			Ridir							-			
Bidir Bidi	_		Didii				_	1		I	l		
dq[5] 5 efault) lt) sdram_ dq[4] Bidir PIN_11 2.5 V (d and perault) 8mA (de and perault) 2 (defau and perault) sdram_ dq[4] Bidir PIN_12 2.5 V (d and perault) 8mA (de and perault) 2 (defau and perault) dq[3] PIN_12 2.5 V (d and perault) 8mA (de and perault) 2 (defau and perault) sdram_ dq[2] Bidir PIN_11 2.5 V (d and perault) 8mA (de and perault) 2 (defau and perault) sdram_ dq[1] Bidir PIN_13 2.5 V (d and perault) 8mA (de and perault) 2 (defau and perault) sdram_ dq[0] PIN_14 2.5 V (d and perault) 8mA (de and perault) 2 (defau and perault) sdram_ dqm[1] Output PIN_13 2.5 V (d and perault) 8mA (de and perault) 2 (defau and perault) sdram_ dam_ dam_ dam_ dam_ dam_ dam_ dam_ d			Ridir				-	1		-	-		
Bidir Bidi			Didii				_						
dq[4] 3 efault) lt) sdram_ dq[3] Bidir PIN_12 2.5 V (d 90 8mA (de 2 (defau 90 9ma)) 8mA (de 2 (defau 90 9ma)) sdram_ dq[2] Bidir PIN_12 2.5 V (d 8mA (de 2 (defau 90 9ma)) 8mA (de 2 (defau 90 9ma)) sdram_ dq[2] Bidir PIN_12 2.5 V (d 8mA (de 2 (defau 90 9ma)) 8mA (de 2 (defau 90 9ma)) sdram_ dq[1] 4 efault) lt) sdram_ dq[0] 3 efault) lt) sdram_ dqm[1] Output PIN_14 2.5 V (d 8mA (de 2 (defau 90 9ma)) sdram_ dqm[1] PIN_13 2.5 V (d 8mA (de 2 (defau 90 9ma)) sdram_ dqm[1] PIN_13 2.5 V (d 8mA (de 2 (defau 90 9ma)) sdram_ dqm[1] PIN_13 2.5 V (d 8mA (de 2 (defau 90 9ma)) sdram_ dqm[1] PIN_13 2.5 V (d 8mA (de 2 (defau 90 9ma))			Ridir					i		-			
Bidir PIN_12 2.5 V (d 8mA (de 2 (defau 9 efault) lt)	_		Dian				_	1					
dq[3] 9 efault) lt) sdram_ dq[2] Bidir PIN_12			Ridir				-			· ·	·		
Bidir PIN_12 2.5 V (d 8mA (de 2 (defau fault) lt)	_		Dian				_	1					
dq[2] 8 efault) lt) sdram_ Bidir PIN_11 2.5 V (d 8mA (de 2 (defau default) lt) dq[1] 4 efault) fault) lt) sdram_ Bidir PIN_13 2.5 V (d 8mA (de 2 (defau default) lt) dq[0] 3 efault) fault) lt) sdram_ Output PIN_14 2.5 V (d 8mA (de 2 (defau default) lt) dqm[1] 2 efault) lt) sdram_ Output PIN_13 2.5 V (d 8mA (de 2 (defau default) lt)			Ridir							-			
sdram_ dq[1] Bidir PIN_11	_		Didii				_	1		l .	l		
dq[1] 4 efault) fault) lt) sdram_ Bidir PIN_13 2.5 V (d 8mA (de 2 (defau fault) dq[0] 3 efault) lt) sdram_ Output PIN_14 2.5 V (d 8mA (de 2 (defau fault) dqm[1] 2 efault) lt) sdram_ Output PIN_13 2.5 V (d 8mA (de 2 (defau			Ridir										
sdram_ dq[0] Bidir PIN_13	_		Didii				_	,					
dq[0] 3 efault) fault) lt) sdram_ Output PIN_14 2.5 V (d 8mA (de 2 (defau dault) dqm[1] 2 efault) lt) sdram_ Output PIN_13 2.5 V (d 8mA (de 2 (defau dault)			Ridir								-		
sdram_ Output PIN_14 2.5 V (d grault) 8mA (de 2 (defau fault) dqm[1] 2 efault) fault) lt) sdram_ Output PIN_13 2.5 V (d smA (de 2 (defau smA (de	_		Didii				_						
dqm[1] 2 efault) lt) sdram_ Output PIN_13 2.5 V (d 8mA (de 2 (defau			Output					i					
sdram_ Output PIN_13 2.5 V (d 8mA (de 2 (defau	_		σαιραι				_	1					
			Outout] 				· ·	-		
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	ачицој							Ciautty		iautij			

	Customi	Directio	Locatio	1/0	VREF	Fitter			Current		Differen	
Name	ze	n	n	Bank	Group		Standar	d	Strengt	Rate	tial Pair	
	Column					n	d		h			ation
	S											
sdram_r		Output				PIN_46	2.5 V (d		8mA (de	2 (defau		
as_n							efault)		fault)	lt)		
sdram_		Output				PIN_14	2.5 V (d		8mA (de	2 (defau		
we_n						3	efault)		fault)	lt)		
test_res		Output				PIN_50	2.5 V (d		8mA (de	2 (defau		
ult_expo							efault)		fault)	lt)		
rt[1]												
test_res		Output				PIN_49	2.5 V (d		8mA (de	2 (defau		
ult_expo							efault)		fault)	lt)		
rt[0]												
< <new n<="" td=""><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></new>												
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