

hsa-runtime64

Generated by Doxygen 1.9.5

1 Deprecated List	1
2 Module Index	7
2.1 Modules	7
3 Class Index	9
3.1 Class List	9
4 File Index	13
4.1 File List	13
5 Module Documentation	15
5.1 Runtime Notifications	15
5.1.1 Detailed Description	16
5.1.2 Typedef Documentation	16
5.1.2.1 hsa_file_t	16
5.1.3 Enumeration Type Documentation	16
5.1.3.1 hsa_access_permission_t	16
5.1.3.2 hsa_status_t	17
5.1.4 Function Documentation	18
5.1.4.1 hsa_init()	18
5.1.4.2 hsa_shut_down()	18
5.1.4.3 hsa_status_string()	19
5.2 System and Agent Information	19
5.2.1 Detailed Description	21
5.2.2 Enumeration Type Documentation	21
5.2.2.1 hsa_agent_feature_t	22
5.2.2.2 hsa_agent_info_t	22
5.2.2.3 hsa_cache_info_t	27
5.2.2.4 hsa_default_float_rounding_mode_t	27
5.2.2.5 hsa_device_type_t	28
5.2.2.6 hsa_endianness_t	28
5.2.2.7 hsa_exception_policy_t	28
5.2.2.8 hsa_extension_t	29
5.2.2.9 hsa_machine_model_t	29
5.2.2.10 hsa_profile_t	29
5.2.2.11 hsa_system_info_t	30
5.2.3 Function Documentation	31
5.2.3.1 hsa_agent_extension_supported()	31
5.2.3.2 hsa_agent_get_exception_policies()	31
5.2.3.3 hsa_agent_get_info()	32
5.2.3.4 hsa_agent_iterate_caches()	32
5.2.3.5 hsa_agent_major_extension_supported()	33
5.2.3.6 hsa_cache_get_info()	34

5.2.3.7 hsa_extension_get_name()	34
5.2.3.8 hsa_iterate_agents()	35
5.2.3.9 hsa_system_extension_supported()	35
5.2.3.10 hsa_system_get_extension_table()	36
5.2.3.11 hsa_system_get_info()	36
5.2.3.12 hsa_system_get_major_extension_table()	37
5.2.3.13 hsa_system_major_extension_supported()	38
5.3 Signals	38
5.3.1 Detailed Description	41
5.3.2 Typedef Documentation	41
5.3.2.1 hsa_signal_value_t	42
5.3.3 Enumeration Type Documentation	42
5.3.3.1 hsa_signal_condition_t	42
5.3.3.2 hsa_wait_state_t	42
5.3.4 Function Documentation	42
5.3.4.1 hsa_signal_add_acq_rel()	43
5.3.4.2 hsa_signal_add_acquire()	43
5.3.4.3 hsa_signal_add_relaxed()	43
5.3.4.4 hsa_signal_add_release()	44
5.3.4.5 hsa_signal_add_scacq_screl()	44
5.3.4.6 hsa_signal_add_scacquire()	44
5.3.4.7 hsa_signal_add_screlease()	45
5.3.4.8 hsa_signal_and_acq_rel()	45
5.3.4.9 hsa_signal_and_acquire()	46
5.3.4.10 hsa_signal_and_relaxed()	46
5.3.4.11 hsa_signal_and_release()	46
5.3.4.12 hsa_signal_and_scacq_screl()	47
5.3.4.13 hsa_signal_and_scacquire()	47
5.3.4.14 hsa_signal_and_screlease()	48
5.3.4.15 hsa_signal_cas_acq_rel()	48
5.3.4.16 hsa_signal_cas_acquire()	48
5.3.4.17 hsa_signal_cas_relaxed()	49
5.3.4.18 hsa_signal_cas_release()	49
5.3.4.19 hsa_signal_cas_scacq_screl()	50
5.3.4.20 hsa_signal_cas_scacquire()	50
5.3.4.21 hsa_signal_cas_screlease()	51
5.3.4.22 hsa_signal_create()	51
5.3.4.23 hsa_signal_destroy()	52
5.3.4.24 hsa_signal_exchange_acq_rel()	53
5.3.4.25 hsa_signal_exchange_acquire()	53
5.3.4.26 hsa_signal_exchange_relaxed()	54
5.3.4.27 hsa_signal_exchange_release()	54

5.3.4.28	hsa_signal_exchange_scacq_screl()	54
5.3.4.29	hsa_signal_exchange_scacquire()	55
5.3.4.30	hsa_signal_exchange_screlease()	55
5.3.4.31	hsa_signal_group_create()	56
5.3.4.32	hsa_signal_group_destroy()	56
5.3.4.33	hsa_signal_group_wait_any_relaxed()	57
5.3.4.34	hsa_signal_group_wait_any_scacquire()	58
5.3.4.35	hsa_signal_load_acquire()	59
5.3.4.36	hsa_signal_load_relaxed()	59
5.3.4.37	hsa_signal_load_scacquire()	60
5.3.4.38	hsa_signal_or_acq_rel()	60
5.3.4.39	hsa_signal_or_acquire()	60
5.3.4.40	hsa_signal_or_relaxed()	61
5.3.4.41	hsa_signal_or_release()	61
5.3.4.42	hsa_signal_or_scacq_screl()	61
5.3.4.43	hsa_signal_or_scacquire()	62
5.3.4.44	hsa_signal_or_screlease()	62
5.3.4.45	hsa_signal_silent_store_relaxed()	63
5.3.4.46	hsa_signal_silent_store_screlease()	63
5.3.4.47	hsa_signal_store_relaxed()	63
5.3.4.48	hsa_signal_store_release()	64
5.3.4.49	hsa_signal_store_screlease()	64
5.3.4.50	hsa_signal_subtract_acq_rel()	64
5.3.4.51	hsa_signal_subtract_acquire()	65
5.3.4.52	hsa_signal_subtract_relaxed()	65
5.3.4.53	hsa_signal_subtract_release()	66
5.3.4.54	hsa_signal_subtract_scacq_screl()	66
5.3.4.55	hsa_signal_subtract_scacquire()	66
5.3.4.56	hsa_signal_subtract_screlease()	67
5.3.4.57	hsa_signal_wait_acquire()	67
5.3.4.58	hsa_signal_wait_relaxed()	68
5.3.4.59	hsa_signal_wait_scacquire()	69
5.3.4.60	hsa_signal_xor_acq_rel()	69
5.3.4.61	hsa_signal_xor_acquire()	70
5.3.4.62	hsa_signal_xor_relaxed()	70
5.3.4.63	hsa_signal_xor_release()	71
5.3.4.64	hsa_signal_xor_scacq_screl()	71
5.3.4.65	hsa_signal_xor_scacquire()	71
5.3.4.66	hsa_signal_xor_screlease()	72
5.4	Memory	72
5.4.1	Detailed Description	73
5.4.2	Enumeration Type Documentation	73

5.4.2.1 hsa_region_global_flag_t	73
5.4.2.2 hsa_region_info_t	74
5.4.2.3 hsa_region_segment_t	75
5.4.3 Function Documentation	76
5.4.3.1 hsa_agent_iterate_regions()	76
5.4.3.2 hsa_memory_allocate()	76
5.4.3.3 hsa_memory_assign_agent()	77
5.4.3.4 hsa_memory_copy()	78
5.4.3.5 hsa_memory_deregister()	78
5.4.3.6 hsa_memory_free()	79
5.4.3.7 hsa_memory_register()	79
5.4.3.8 hsa_region_get_info()	80
5.5 Queues	80
5.5.1 Detailed Description	82
5.5.2 Typedef Documentation	82
5.5.2.1 hsa_queue_t	82
5.5.2.2 hsa_queue_type32_t	82
5.5.3 Enumeration Type Documentation	83
5.5.3.1 hsa_queue_feature_t	83
5.5.3.2 hsa_queue_type_t	83
5.5.4 Function Documentation	83
5.5.4.1 hsa_queue_add_write_index_acq_rel()	84
5.5.4.2 hsa_queue_add_write_index_acquire()	84
5.5.4.3 hsa_queue_add_write_index_relaxed()	84
5.5.4.4 hsa_queue_add_write_index_release()	85
5.5.4.5 hsa_queue_add_write_index_scacq_screl()	85
5.5.4.6 hsa_queue_add_write_index_scacquire()	86
5.5.4.7 hsa_queue_add_write_index_screlease()	86
5.5.4.8 hsa_queue_cas_write_index_acq_rel()	86
5.5.4.9 hsa_queue_cas_write_index_acquire()	87
5.5.4.10 hsa_queue_cas_write_index_relaxed()	87
5.5.4.11 hsa_queue_cas_write_index_release()	88
5.5.4.12 hsa_queue_cas_write_index_scacq_screl()	88
5.5.4.13 hsa_queue_cas_write_index_scacquire()	89
5.5.4.14 hsa_queue_cas_write_index_screlease()	89
5.5.4.15 hsa_queue_create()	90
5.5.4.16 hsa_queue_destroy()	91
5.5.4.17 hsa_queue_inactivate()	92
5.5.4.18 hsa_queue_load_read_index_acquire()	92
5.5.4.19 hsa_queue_load_read_index_relaxed()	93
5.5.4.20 hsa_queue_load_read_index_scacquire()	93
5.5.4.21 hsa_queue_load_write_index_acquire()	93

5.5.4.22 hsa_queue_load_write_index_relaxed()	94
5.5.4.23 hsa_queue_load_write_index_scacquire()	94
5.5.4.24 hsa_queue_store_read_index_relaxed()	94
5.5.4.25 hsa_queue_store_read_index_release()	95
5.5.4.26 hsa_queue_store_read_index_screlease()	95
5.5.4.27 hsa_queue_store_write_index_relaxed()	96
5.5.4.28 hsa_queue_store_write_index_release()	96
5.5.4.29 hsa_queue_store_write_index_screlease()	96
5.5.4.30 hsa_soft_queue_create()	97
5.6 Architected Queuing Language	98
5.6.1 Detailed Description	99
5.6.2 Typedef Documentation	99
5.6.2.1 hsa_amd_packet_type8_t	99
5.6.2.2 hsa_signal_condition32_t	100
5.6.3 Enumeration Type Documentation	100
5.6.3.1 hsa_amd_packet_type_t	100
5.6.3.2 hsa_fence_scope_t	100
5.6.3.3 hsa_kernel_dispatch_packet_setup_t	101
5.6.3.4 hsa_kernel_dispatch_packet_setup_width_t	101
5.6.3.5 hsa_packet_header_t	101
5.6.3.6 hsa_packet_header_width_t	102
5.6.3.7 hsa_packet_type_t	102
5.7 Instruction Set Architecture	103
5.7.1 Detailed Description	104
5.7.2 Enumeration Type Documentation	104
5.7.2.1 hsa_flush_mode_t	104
5.7.2.2 hsa_fp_type_t	105
5.7.2.3 hsa_isa_info_t	105
5.7.2.4 hsa_round_method_t	107
5.7.2.5 hsa_wavefront_info_t	107
5.7.3 Function Documentation	107
5.7.3.1 hsa_agent_iterate_isas()	108
5.7.3.2 hsa_isa_compatible()	108
5.7.3.3 hsa_isa_from_name()	109
5.7.3.4 hsa_isa_get_exception_policies()	109
5.7.3.5 hsa_isa_get_info()	110
5.7.3.6 hsa_isa_get_info_alt()	110
5.7.3.7 hsa_isa_get_round_method()	111
5.7.3.8 hsa_isa_iterate_wavefronts()	112
5.7.3.9 hsa_wavefront_get_info()	112
5.8 Executable	113
5.8.1 Detailed Description	115

5.8.2 Typedef Documentation	115
5.8.2.1 hsa_executable_symbol_t	116
5.8.3 Enumeration Type Documentation	116
5.8.3.1 hsa_executable_info_t	116
5.8.3.2 hsa_executable_state_t	116
5.8.3.3 hsa_executable_symbol_info_t	117
5.8.3.4 hsa_symbol_kind_t	120
5.8.3.5 hsa_symbol_linkage_t	120
5.8.3.6 hsa_variable_allocation_t	120
5.8.3.7 hsa_variable_segment_t	120
5.8.4 Function Documentation	121
5.8.4.1 hsa_code_object_reader_create_from_file()	121
5.8.4.2 hsa_code_object_reader_create_from_memory()	121
5.8.4.3 hsa_code_object_reader_destroy()	122
5.8.4.4 hsa_executable_agent_global_variable_define()	122
5.8.4.5 hsa_executable_create()	123
5.8.4.6 hsa_executable_create_alt()	124
5.8.4.7 hsa_executable_destroy()	125
5.8.4.8 hsa_executable_freeze()	125
5.8.4.9 hsa_executable_get_info()	126
5.8.4.10 hsa_executable_get_symbol()	126
5.8.4.11 hsa_executable_get_symbol_by_name()	127
5.8.4.12 hsa_executable_global_variable_define()	128
5.8.4.13 hsa_executable_iterate_agent_symbols()	128
5.8.4.14 hsa_executable_iterate_program_symbols()	129
5.8.4.15 hsa_executable_iterate_symbols()	130
5.8.4.16 hsa_executable_load_agent_code_object()	130
5.8.4.17 hsa_executable_load_program_code_object()	132
5.8.4.18 hsa_executable_readonly_variable_define()	133
5.8.4.19 hsa_executable_symbol_get_info()	134
5.8.4.20 hsa_executable_validate()	134
5.8.4.21 hsa_executable_validate_alt()	135
5.9 Code Objects (deprecated).	136
5.9.1 Detailed Description	137
5.9.2 Typedef Documentation	137
5.9.2.1 hsa_callback_data_t	137
5.9.2.2 hsa_code_object_t	138
5.9.2.3 hsa_code_symbol_t	138
5.9.3 Enumeration Type Documentation	138
5.9.3.1 hsa_code_object_info_t	138
5.9.3.2 hsa_code_object_type_t	139
5.9.3.3 hsa_code_symbol_info_t	139

5.9.4 Function Documentation	141
5.9.4.1 hsa_code_object_deserialize()	141
5.9.4.2 hsa_code_object_destroy()	142
5.9.4.3 hsa_code_object_get_info()	142
5.9.4.4 hsa_code_object_get_symbol()	143
5.9.4.5 hsa_code_object_get_symbol_from_name()	144
5.9.4.6 hsa_code_object_iterate_symbols()	144
5.9.4.7 hsa_code_object_serialize()	145
5.9.4.8 hsa_code_symbol_get_info()	146
5.9.4.9 hsa_executable_load_code_object()	146
5.10 Finalization Extensions	147
5.10.1 Detailed Description	148
5.10.2 Enumeration Type Documentation	148
5.10.2.1 anonymous enum	148
5.11 Finalization Program	148
5.11.1 Detailed Description	149
5.11.2 Typedef Documentation	149
5.11.2.1 hsa_ext_module_t	150
5.11.3 Enumeration Type Documentation	150
5.11.3.1 hsa_ext_finalizer_call_convention_t	150
5.11.3.2 hsa_ext_program_info_t	150
5.11.4 Function Documentation	150
5.11.4.1 hsa_ext_program_add_module()	151
5.11.4.2 hsa_ext_program_create()	151
5.11.4.3 hsa_ext_program_destroy()	152
5.11.4.4 hsa_ext_program_finalize()	152
5.11.4.5 hsa_ext_program_get_info()	153
5.11.4.6 hsa_ext_program_iterate_modules()	154
5.12 Images and Samplers	155
5.12.1 Detailed Description	158
5.12.2 Macro Definition Documentation	158
5.12.2.1 hsa_ext_images_1	158
5.12.2.2 hsa_ext_images_1_00	159
5.12.3 Typedef Documentation	159
5.12.3.1 hsa_ext_image_channel_order32_t	159
5.12.3.2 hsa_ext_image_channel_type32_t	159
5.12.3.3 hsa_ext_image_t	159
5.12.3.4 hsa_ext_sampler_addressing_mode32_t	159
5.12.3.5 hsa_ext_sampler_coordinate_mode32_t	160
5.12.3.6 hsa_ext_sampler_filter_mode32_t	160
5.12.4 Enumeration Type Documentation	160
5.12.4.1 anonymous enum	160

5.12.4.2 anonymous enum	160
5.12.4.3 hsa_ext_image_capability_t	162
5.12.4.4 hsa_ext_image_channel_order_t	162
5.12.4.5 hsa_ext_image_channel_type_t	162
5.12.4.6 hsa_ext_image_data_layout_t	163
5.12.4.7 hsa_ext_image_geometry_t	163
5.12.4.8 hsa_ext_sampler_addressing_mode_t	164
5.12.4.9 hsa_ext_sampler_coordinate_mode_t	164
5.12.4.10 hsa_ext_sampler_filter_mode_t	165
5.12.5 Function Documentation	165
5.12.5.1 hsa_ext_image_clear()	165
5.12.5.2 hsa_ext_image_copy()	166
5.12.5.3 hsa_ext_image_create()	167
5.12.5.4 hsa_ext_image_create_with_layout()	168
5.12.5.5 hsa_ext_image_data_get_info()	170
5.12.5.6 hsa_ext_image_data_get_info_with_layout()	171
5.12.5.7 hsa_ext_image_destroy()	172
5.12.5.8 hsa_ext_image_export()	173
5.12.5.9 hsa_ext_image_get_capability()	174
5.12.5.10 hsa_ext_image_get_capability_with_layout()	175
5.12.5.11 hsa_ext_image_import()	175
5.12.5.12 hsa_ext_sampler_create()	176
5.12.5.13 hsa_ext_sampler_destroy()	177
6 Class Documentation	179
6.1 amd_control_directives_s Struct Reference	179
6.1.1 Detailed Description	179
6.1.2 Member Data Documentation	179
6.1.2.1 enable_break_exceptions	179
6.1.2.2 enable_detect_exceptions	180
6.1.2.3 enabled_control_directives	180
6.1.2.4 max_dynamic_group_size	180
6.1.2.5 max_flat_grid_size	180
6.1.2.6 max_flat_workgroup_size	180
6.1.2.7 required_dim	180
6.1.2.8 required_grid_size	181
6.1.2.9 required_workgroup_size	181
6.1.2.10 reserved1	181
6.1.2.11 reserved2	181
6.2 amd_kernel_code_s Struct Reference	181
6.2.1 Detailed Description	182
6.2.2 Member Data Documentation	182

6.2.2.1 amd_kernel_code_version_major	182
6.2.2.2 amd_kernel_code_version_minor	183
6.2.2.3 amd_machine_kind	183
6.2.2.4 amd_machine_version_major	183
6.2.2.5 amd_machine_version_minor	183
6.2.2.6 amd_machine_version_stepping	183
6.2.2.7 call_convention	183
6.2.2.8 compute_pgm_rsrc1	184
6.2.2.9 compute_pgm_rsrc2	184
6.2.2.10 control_directives	184
6.2.2.11 debug_private_segment_buffer_sgpr	184
6.2.2.12 debug_wavefront_private_segment_offset_sgpr	184
6.2.2.13 gds_segment_byte_size	184
6.2.2.14 group_segment_alignment	185
6.2.2.15 kernarg_segment_alignment	185
6.2.2.16 kernarg_segment_byte_size	185
6.2.2.17 kernel_code_entry_byte_offset	185
6.2.2.18 kernel_code_prefetch_byte_offset	185
6.2.2.19 kernel_code_prefetch_byte_size	185
6.2.2.20 kernel_code_properties	186
6.2.2.21 max_scratch_backing_memory_byte_size	186
6.2.2.22 private_segment_alignment	186
6.2.2.23 reserved1	186
6.2.2.24 reserved_sgpr_count	186
6.2.2.25 reserved_sgpr_first	186
6.2.2.26 reserved_vgpr_count	187
6.2.2.27 reserved_vgpr_first	187
6.2.2.28 runtime_loader_kernel_symbol	187
6.2.2.29 wavefront_sgpr_count	187
6.2.2.30 wavefront_size	187
6.2.2.31 workgroup_fbarrier_count	187
6.2.2.32 workgroup_group_segment_byte_size	188
6.2.2.33 workitem_private_segment_byte_size	188
6.2.2.34 workitem_vgpr_count	188
6.3 amd_queue_s Struct Reference	188
6.3.1 Detailed Description	189
6.3.2 Member Data Documentation	189
6.3.2.1 compute_tmpring_size	189
6.3.2.2 group_segment_aperture_base_hi	189
6.3.2.3 hsa_queue	189
6.3.2.4 legacy_doorbell_lock	189
6.3.2.5 max_cu_id	189

6.3.2.6 max_legacy_doorbell_dispatch_id_plus_1	190
6.3.2.7 max_wave_id	190
6.3.2.8 private_segment_aperture_base_hi	190
6.3.2.9 queue_inactive_signal	190
6.3.2.10 queue_properties	190
6.3.2.11 read_dispatch_id	190
6.3.2.12 read_dispatch_id_field_base_byte_offset	191
6.3.2.13 reserved1	191
6.3.2.14 reserved2	191
6.3.2.15 reserved3	191
6.3.2.16 reserved4	191
6.3.2.17 scratch_backing_memory_byte_size	191
6.3.2.18 scratch_backing_memory_location	192
6.3.2.19 scratch_resource_descriptor	192
6.3.2.20 scratch_wave64_lane_byte_size	192
6.3.2.21 write_dispatch_id	192
6.4 amd_runtime_loader_debug_info_s Struct Reference	192
6.4.1 Detailed Description	192
6.4.2 Member Data Documentation	193
6.4.2.1 elf_raw	193
6.4.2.2 elf_size	193
6.4.2.3 kernel_name	193
6.4.2.4 owning_segment	193
6.5 amd_signal_s Struct Reference	194
6.5.1 Detailed Description	194
6.5.2 Member Data Documentation	194
6.5.2.1 end_ts	194
6.5.2.2 event_id	194
6.5.2.3 event_mailbox_ptr	195
6.5.2.4 hardware_doorbell_ptr	195
6.5.2.5 kind	195
6.5.2.6 legacy_hardware_doorbell_ptr	195
6.5.2.7 queue_ptr	195
6.5.2.8 reserved1	195
6.5.2.9 reserved2	196
6.5.2.10 reserved3	196
6.5.2.11 start_ts	196
6.5.2.12 value	196
6.6 AmdExtTable Struct Reference	196
6.7 amdgpu_hsa_image_descriptor_s Struct Reference	196
6.7.1 Detailed Description	197
6.7.2 Member Data Documentation	197

6.7.2.1 array	197
6.7.2.2 channel_order	197
6.7.2.3 channel_type	197
6.7.2.4 depth	197
6.7.2.5 geometry	197
6.7.2.6 height	198
6.7.2.7 kind	198
6.7.2.8 reserved1	198
6.7.2.9 size	198
6.7.2.10 width	198
6.8 amdgpu_hsa_note_code_object_version_s Struct Reference	198
6.8.1 Detailed Description	199
6.8.2 Member Data Documentation	199
6.8.2.1 major_version	199
6.8.2.2 minor_version	199
6.9 amdgpu_hsa_note_hsail_s Struct Reference	199
6.9.1 Detailed Description	199
6.9.2 Member Data Documentation	199
6.9.2.1 default_float_round	200
6.9.2.2 hsail_major_version	200
6.9.2.3 hsail_minor_version	200
6.9.2.4 machine_model	200
6.9.2.5 profile	200
6.10 amdgpu_hsa_note_isa_s Struct Reference	200
6.10.1 Detailed Description	201
6.10.2 Member Data Documentation	201
6.10.2.1 architecture_name_size	201
6.10.2.2 major	201
6.10.2.3 minor	201
6.10.2.4 stepping	201
6.10.2.5 vendor_and_architecture_name	201
6.10.2.6 vendor_name_size	202
6.11 amdgpu_hsa_note_producer_options_s Struct Reference	202
6.11.1 Detailed Description	202
6.11.2 Member Data Documentation	202
6.11.2.1 producer_options	202
6.11.2.2 producer_options_size	202
6.12 amdgpu_hsa_note_producer_s Struct Reference	203
6.12.1 Detailed Description	203
6.12.2 Member Data Documentation	203
6.12.2.1 producer_major_version	203
6.12.2.2 producer_minor_version	203

6.12.2.3 producer_name	203
6.12.2.4 producer_name_size	203
6.12.2.5 reserved	204
6.13 amdgpu_hsa_sampler_descriptor_s Struct Reference	204
6.13.1 Detailed Description	204
6.13.2 Member Data Documentation	204
6.13.2.1 addressing	204
6.13.2.2 coord	204
6.13.2.3 filter	205
6.13.2.4 kind	205
6.13.2.5 reserved1	205
6.13.2.6 size	205
6.14 ApiTableVersion Struct Reference	205
6.14.1 Detailed Description	205
6.14.2 Member Data Documentation	206
6.14.2.1 major_id	206
6.14.2.2 minor_id	206
6.14.2.3 reserved	206
6.14.2.4 step_id	206
6.15 BrigBase Struct Reference	206
6.15.1 Detailed Description	207
6.15.2 Member Data Documentation	207
6.15.2.1 byteCount	207
6.15.2.2 kind	207
6.16 BrigData Struct Reference	207
6.16.1 Detailed Description	207
6.16.2 Member Data Documentation	207
6.16.2.1 byteCount	208
6.16.2.2 bytes	208
6.17 BrigDirectiveArgBlock Struct Reference	208
6.17.1 Detailed Description	208
6.17.2 Member Data Documentation	208
6.17.2.1 base	208
6.18 BrigDirectiveComment Struct Reference	209
6.18.1 Detailed Description	209
6.18.2 Member Data Documentation	209
6.18.2.1 base	209
6.18.2.2 name	209
6.19 BrigDirectiveControl Struct Reference	209
6.19.1 Detailed Description	210
6.19.2 Member Data Documentation	210
6.19.2.1 base	210

6.19.2.2 control	210
6.19.2.3 operands	210
6.19.2.4 reserved	210
6.20 BrigDirectiveExecutable Struct Reference	210
6.20.1 Detailed Description	211
6.20.2 Member Data Documentation	211
6.20.2.1 base	211
6.20.2.2 firstCodeBlockEntry	211
6.20.2.3 firstInArg	211
6.20.2.4 inArgCount	212
6.20.2.5 linkage	212
6.20.2.6 modifier	212
6.20.2.7 name	212
6.20.2.8 nextModuleEntry	212
6.20.2.9 outArgCount	212
6.20.2.10 reserved	213
6.21 BrigDirectiveExtension Struct Reference	213
6.21.1 Detailed Description	213
6.21.2 Member Data Documentation	213
6.21.2.1 base	213
6.21.2.2 name	213
6.22 BrigDirectiveFbarrier Struct Reference	214
6.22.1 Detailed Description	214
6.22.2 Member Data Documentation	214
6.22.2.1 base	214
6.22.2.2 linkage	214
6.22.2.3 modifier	214
6.22.2.4 name	215
6.22.2.5 reserved	215
6.23 BrigDirectiveLabel Struct Reference	215
6.23.1 Detailed Description	215
6.23.2 Member Data Documentation	215
6.23.2.1 base	215
6.23.2.2 name	216
6.24 BrigDirectiveLoc Struct Reference	216
6.24.1 Detailed Description	216
6.24.2 Member Data Documentation	216
6.24.2.1 base	216
6.24.2.2 column	216
6.24.2.3 filename	217
6.24.2.4 line	217
6.25 BrigDirectiveModule Struct Reference	217

6.25.1 Detailed Description	217
6.25.2 Member Data Documentation	217
6.25.2.1 base	217
6.25.2.2 defaultFloatRound	218
6.25.2.3 hsailMajor	218
6.25.2.4 hsailMinor	218
6.25.2.5 machineModel	218
6.25.2.6 name	218
6.25.2.7 profile	218
6.25.2.8 reserved	219
6.26 BrigDirectiveNone Struct Reference	219
6.26.1 Detailed Description	219
6.26.2 Member Data Documentation	219
6.26.2.1 base	219
6.27 BrigDirectivePragma Struct Reference	219
6.27.1 Detailed Description	220
6.27.2 Member Data Documentation	220
6.27.2.1 base	220
6.27.2.2 operands	220
6.28 BrigDirectiveVariable Struct Reference	220
6.28.1 Detailed Description	221
6.28.2 Member Data Documentation	221
6.28.2.1 align	221
6.28.2.2 allocation	221
6.28.2.3 base	221
6.28.2.4 dim	221
6.28.2.5 init	221
6.28.2.6 linkage	222
6.28.2.7 modifier	222
6.28.2.8 name	222
6.28.2.9 reserved	222
6.28.2.10 segment	222
6.28.2.11 type	222
6.29 BrigInstAddr Struct Reference	223
6.29.1 Detailed Description	223
6.29.2 Member Data Documentation	223
6.29.2.1 base	223
6.29.2.2 reserved	223
6.29.2.3 segment	223
6.30 BrigInstAtomic Struct Reference	224
6.30.1 Detailed Description	224
6.30.2 Member Data Documentation	224

6.30.2.1 atomicOperation	224
6.30.2.2 base	224
6.30.2.3 equivClass	224
6.30.2.4 memoryOrder	225
6.30.2.5 memoryScope	225
6.30.2.6 reserved	225
6.30.2.7 segment	225
6.31 BrigInstBase Struct Reference	225
6.31.1 Detailed Description	226
6.31.2 Member Data Documentation	226
6.31.2.1 base	226
6.31.2.2 opcode	226
6.31.2.3 operands	226
6.31.2.4 type	226
6.32 BrigInstBasic Struct Reference	226
6.32.1 Detailed Description	227
6.32.2 Member Data Documentation	227
6.32.2.1 base	227
6.33 BrigInstBr Struct Reference	227
6.33.1 Detailed Description	227
6.33.2 Member Data Documentation	227
6.33.2.1 base	228
6.33.2.2 reserved	228
6.33.2.3 width	228
6.34 BrigInstCmp Struct Reference	228
6.34.1 Detailed Description	228
6.34.2 Member Data Documentation	228
6.34.2.1 base	229
6.34.2.2 compare	229
6.34.2.3 modifier	229
6.34.2.4 pack	229
6.34.2.5 reserved	229
6.34.2.6 sourceType	229
6.35 BrigInstCvt Struct Reference	230
6.35.1 Detailed Description	230
6.35.2 Member Data Documentation	230
6.35.2.1 base	230
6.35.2.2 modifier	230
6.35.2.3 round	230
6.35.2.4 sourceType	231
6.36 BrigInstImage Struct Reference	231
6.36.1 Detailed Description	231

6.36.2 Member Data Documentation	231
6.36.2.1 base	231
6.36.2.2 coordType	231
6.36.2.3 equivClass	232
6.36.2.4 geometry	232
6.36.2.5 imageType	232
6.36.2.6 reserved	232
6.37 BrigInstLane Struct Reference	232
6.37.1 Detailed Description	233
6.37.2 Member Data Documentation	233
6.37.2.1 base	233
6.37.2.2 reserved	233
6.37.2.3 sourceType	233
6.37.2.4 width	233
6.38 BrigInstMem Struct Reference	233
6.38.1 Detailed Description	234
6.38.2 Member Data Documentation	234
6.38.2.1 align	234
6.38.2.2 base	234
6.38.2.3 equivClass	234
6.38.2.4 modifier	234
6.38.2.5 reserved	235
6.38.2.6 segment	235
6.38.2.7 width	235
6.39 BrigInstMemFence Struct Reference	235
6.39.1 Detailed Description	235
6.39.2 Member Data Documentation	235
6.39.2.1 base	236
6.39.2.2 globalSegmentMemoryScope	236
6.39.2.3 groupSegmentMemoryScope	236
6.39.2.4 imageSegmentMemoryScope	236
6.39.2.5 memoryOrder	236
6.40 BrigInstMod Struct Reference	236
6.40.1 Detailed Description	237
6.40.2 Member Data Documentation	237
6.40.2.1 base	237
6.40.2.2 modifier	237
6.40.2.3 pack	237
6.40.2.4 reserved	237
6.40.2.5 round	238
6.41 BrigInstQueryImage Struct Reference	238
6.41.1 Detailed Description	238

6.41.2 Member Data Documentation	238
6.41.2.1 base	238
6.41.2.2 geometry	238
6.41.2.3 imageType	239
6.41.2.4 query	239
6.42 BrigInstQuerySampler Struct Reference	239
6.42.1 Detailed Description	239
6.42.2 Member Data Documentation	239
6.42.2.1 base	239
6.42.2.2 query	240
6.42.2.3 reserved	240
6.43 BrigInstQueue Struct Reference	240
6.43.1 Detailed Description	240
6.43.2 Member Data Documentation	240
6.43.2.1 base	240
6.43.2.2 memoryOrder	241
6.43.2.3 reserved	241
6.43.2.4 segment	241
6.44 BrigInstSeg Struct Reference	241
6.44.1 Detailed Description	241
6.44.2 Member Data Documentation	241
6.44.2.1 base	242
6.44.2.2 reserved	242
6.44.2.3 segment	242
6.45 BrigInstSegCvt Struct Reference	242
6.45.1 Detailed Description	242
6.45.2 Member Data Documentation	242
6.45.2.1 base	243
6.45.2.2 modifier	243
6.45.2.3 segment	243
6.45.2.4 sourceType	243
6.46 BrigInstSignal Struct Reference	243
6.46.1 Detailed Description	244
6.46.2 Member Data Documentation	244
6.46.2.1 base	244
6.46.2.2 memoryOrder	244
6.46.2.3 signalOperation	244
6.46.2.4 signalType	244
6.47 BrigInstSourceType Struct Reference	244
6.47.1 Detailed Description	245
6.47.2 Member Data Documentation	245
6.47.2.1 base	245

6.47.2.2 reserved	245
6.47.2.3 sourceType	245
6.48 BrigModuleHeader Struct Reference	246
6.48.1 Detailed Description	246
6.48.2 Member Data Documentation	246
6.48.2.1 brigMajor	246
6.48.2.2 brigMinor	246
6.48.2.3 byteCount	246
6.48.2.4 hash	247
6.48.2.5 identification	247
6.48.2.6 reserved	247
6.48.2.7 sectionCount	247
6.48.2.8 sectionIndex	247
6.49 BrigOperandAddress Struct Reference	247
6.49.1 Detailed Description	248
6.49.2 Member Data Documentation	248
6.49.2.1 base	248
6.49.2.2 offset	248
6.49.2.3 reg	248
6.49.2.4 symbol	248
6.50 BrigOperandAlign Struct Reference	249
6.50.1 Detailed Description	249
6.50.2 Member Data Documentation	249
6.50.2.1 align	249
6.50.2.2 base	249
6.50.2.3 reserved	249
6.51 BrigOperandCodeList Struct Reference	250
6.51.1 Detailed Description	250
6.51.2 Member Data Documentation	250
6.51.2.1 base	250
6.51.2.2 elements	250
6.52 BrigOperandCodeRef Struct Reference	250
6.52.1 Detailed Description	251
6.52.2 Member Data Documentation	251
6.52.2.1 base	251
6.52.2.2 ref	251
6.53 BrigOperandConstantBytes Struct Reference	251
6.53.1 Detailed Description	251
6.53.2 Member Data Documentation	251
6.53.2.1 base	252
6.53.2.2 bytes	252
6.53.2.3 reserved	252

6.53.2.4 type	252
6.54 BrigOperandConstantImage Struct Reference	252
6.54.1 Detailed Description	253
6.54.2 Member Data Documentation	253
6.54.2.1 array	253
6.54.2.2 base	253
6.54.2.3 channelOrder	253
6.54.2.4 channelType	253
6.54.2.5 depth	253
6.54.2.6 geometry	254
6.54.2.7 height	254
6.54.2.8 reserved	254
6.54.2.9 type	254
6.54.2.10 width	254
6.55 BrigOperandConstantOperandList Struct Reference	254
6.55.1 Detailed Description	255
6.55.2 Member Data Documentation	255
6.55.2.1 base	255
6.55.2.2 elements	255
6.55.2.3 reserved	255
6.55.2.4 type	255
6.56 BrigOperandConstantSampler Struct Reference	256
6.56.1 Detailed Description	256
6.56.2 Member Data Documentation	256
6.56.2.1 addressing	256
6.56.2.2 base	256
6.56.2.3 coord	256
6.56.2.4 filter	257
6.56.2.5 reserved	257
6.56.2.6 type	257
6.57 BrigOperandOperandList Struct Reference	257
6.57.1 Detailed Description	257
6.57.2 Member Data Documentation	257
6.57.2.1 base	258
6.57.2.2 elements	258
6.58 BrigOperandRegister Struct Reference	258
6.58.1 Detailed Description	258
6.58.2 Member Data Documentation	258
6.58.2.1 base	258
6.58.2.2 regKind	259
6.58.2.3 regNum	259
6.59 BrigOperandString Struct Reference	259

6.59.1 Detailed Description	259
6.59.2 Member Data Documentation	259
6.59.2.1 base	259
6.59.2.2 string	260
6.60 BrigOperandWavesize Struct Reference	260
6.60.1 Detailed Description	260
6.60.2 Member Data Documentation	260
6.60.2.1 base	260
6.61 BrigSectionHeader Struct Reference	260
6.61.1 Detailed Description	261
6.61.2 Member Data Documentation	261
6.61.2.1 byteCount	261
6.61.2.2 headerByteCount	261
6.61.2.3 name	261
6.61.2.4 nameLength	261
6.62 BrigUInt64 Struct Reference	261
6.62.1 Detailed Description	262
6.62.2 Member Data Documentation	262
6.62.2.1 hi	262
6.62.2.2 lo	262
6.63 CoreApiTable Struct Reference	262
6.63.1 Detailed Description	265
6.63.2 Member Data Documentation	265
6.63.2.1 hsa_agent_extension_supported_fn	265
6.63.2.2 hsa_agent_get_exception_policies_fn	265
6.63.2.3 hsa_agent_get_info_fn	265
6.63.2.4 hsa_agent_iterate_caches_fn	265
6.63.2.5 hsa_agent_iterate_isas_fn	265
6.63.2.6 hsa_agent_iterate_regions_fn	266
6.63.2.7 hsa_agent_major_extension_supported_fn	266
6.63.2.8 hsa_cache_get_info_fn	266
6.63.2.9 hsa_code_object_deserialize_fn	266
6.63.2.10 hsa_code_object_destroy_fn	266
6.63.2.11 hsa_code_object_get_info_fn	266
6.63.2.12 hsa_code_object_get_symbol_fn	267
6.63.2.13 hsa_code_object_get_symbol_from_name_fn	267
6.63.2.14 hsa_code_object_iterate_symbols_fn	267
6.63.2.15 hsa_code_object_reader_create_from_file_fn	267
6.63.2.16 hsa_code_object_reader_create_from_memory_fn	267
6.63.2.17 hsa_code_object_reader_destroy_fn	267
6.63.2.18 hsa_code_object_serialize_fn	268
6.63.2.19 hsa_code_symbol_get_info_fn	268

6.63.2.20 hsa_executable_agent_global_variable_define_fn	268
6.63.2.21 hsa_executable_create_alt_fn	268
6.63.2.22 hsa_executable_create_fn	268
6.63.2.23 hsa_executable_destroy_fn	268
6.63.2.24 hsa_executable_freeze_fn	269
6.63.2.25 hsa_executable_get_info_fn	269
6.63.2.26 hsa_executable_get_symbol_by_name_fn	269
6.63.2.27 hsa_executable_get_symbol_fn	269
6.63.2.28 hsa_executable_global_variable_define_fn	269
6.63.2.29 hsa_executable_iterate_agent_symbols_fn	269
6.63.2.30 hsa_executable_iterate_program_symbols_fn	270
6.63.2.31 hsa_executable_iterate_symbols_fn	270
6.63.2.32 hsa_executable_load_agent_code_object_fn	270
6.63.2.33 hsa_executable_load_code_object_fn	270
6.63.2.34 hsa_executable_load_program_code_object_fn	270
6.63.2.35 hsa_executable_readonly_variable_define_fn	270
6.63.2.36 hsa_executable_symbol_get_info_fn	271
6.63.2.37 hsa_executable_validate_alt_fn	271
6.63.2.38 hsa_executable_validate_fn	271
6.63.2.39 hsa_extension_get_name_fn	271
6.63.2.40 hsa_init_fn	271
6.63.2.41 hsa_isa_compatible_fn	271
6.63.2.42 hsa_isa_from_name_fn	272
6.63.2.43 hsa_isa_get_exception_policies_fn	272
6.63.2.44 hsa_isa_get_info_alt_fn	272
6.63.2.45 hsa_isa_get_info_fn	272
6.63.2.46 hsa_isa_get_round_method_fn	272
6.63.2.47 hsa_isa_iterate_wavefronts_fn	272
6.63.2.48 hsa_iterate_agents_fn	273
6.63.2.49 hsa_memory_allocate_fn	273
6.63.2.50 hsa_memory_assign_agent_fn	273
6.63.2.51 hsa_memory_copy_fn	273
6.63.2.52 hsa_memory_deregister_fn	273
6.63.2.53 hsa_memory_free_fn	273
6.63.2.54 hsa_memory_register_fn	274
6.63.2.55 hsa_queue_add_write_index_relaxed_fn	274
6.63.2.56 hsa_queue_add_write_index_scacq_screl_fn	274
6.63.2.57 hsa_queue_add_write_index_scacquire_fn	274
6.63.2.58 hsa_queue_add_write_index_screlease_fn	274
6.63.2.59 hsa_queue_cas_write_index_relaxed_fn	274
6.63.2.60 hsa_queue_cas_write_index_scacq_screl_fn	275
6.63.2.61 hsa_queue_cas_write_index_scacquire_fn	275

6.63.2.62 hsa_queue_cas_write_index_screlease_fn	275
6.63.2.63 hsa_queue_create_fn	275
6.63.2.64 hsa_queue_destroy_fn	275
6.63.2.65 hsa_queue_inactivate_fn	275
6.63.2.66 hsa_queue_load_read_index_relaxed_fn	276
6.63.2.67 hsa_queue_load_read_index_scacquire_fn	276
6.63.2.68 hsa_queue_load_write_index_relaxed_fn	276
6.63.2.69 hsa_queue_load_write_index_scacquire_fn	276
6.63.2.70 hsa_queue_store_read_index_relaxed_fn	276
6.63.2.71 hsa_queue_store_read_index_screlease_fn	276
6.63.2.72 hsa_queue_store_write_index_relaxed_fn	277
6.63.2.73 hsa_queue_store_write_index_screlease_fn	277
6.63.2.74 hsa_region_get_info_fn	277
6.63.2.75 hsa_shut_down_fn	277
6.63.2.76 hsa_signal_add_relaxed_fn	277
6.63.2.77 hsa_signal_add_scacq_screl_fn	277
6.63.2.78 hsa_signal_add_scacquire_fn	278
6.63.2.79 hsa_signal_add_screlease_fn	278
6.63.2.80 hsa_signal_and_relaxed_fn	278
6.63.2.81 hsa_signal_and_scacq_screl_fn	278
6.63.2.82 hsa_signal_and_scacquire_fn	278
6.63.2.83 hsa_signal_and_screlease_fn	278
6.63.2.84 hsa_signal_cas_relaxed_fn	279
6.63.2.85 hsa_signal_cas_scacq_screl_fn	279
6.63.2.86 hsa_signal_cas_scacquire_fn	279
6.63.2.87 hsa_signal_cas_screlease_fn	279
6.63.2.88 hsa_signal_create_fn	279
6.63.2.89 hsa_signal_destroy_fn	279
6.63.2.90 hsa_signal_exchange_relaxed_fn	280
6.63.2.91 hsa_signal_exchange_scacq_screl_fn	280
6.63.2.92 hsa_signal_exchange_scacquire_fn	280
6.63.2.93 hsa_signal_exchange_screlease_fn	280
6.63.2.94 hsa_signal_group_create_fn	280
6.63.2.95 hsa_signal_group_destroy_fn	280
6.63.2.96 hsa_signal_group_wait_any_relaxed_fn	281
6.63.2.97 hsa_signal_group_wait_any_scacquire_fn	281
6.63.2.98 hsa_signal_load_relaxed_fn	281
6.63.2.99 hsa_signal_load_scacquire_fn	281
6.63.2.100 hsa_signal_or_relaxed_fn	281
6.63.2.101 hsa_signal_or_scacq_screl_fn	281
6.63.2.102 hsa_signal_or_scacquire_fn	282
6.63.2.103 hsa_signal_or_screlease_fn	282

6.63.2.104 hsa_signal_silent_store_relaxed_fn	282
6.63.2.105 hsa_signal_silent_store_screlease_fn	282
6.63.2.106 hsa_signal_store_relaxed_fn	282
6.63.2.107 hsa_signal_store_screlease_fn	282
6.63.2.108 hsa_signal_subtract_relaxed_fn	283
6.63.2.109 hsa_signal_subtract_scacq_screl_fn	283
6.63.2.110 hsa_signal_subtract_scacquire_fn	283
6.63.2.111 hsa_signal_subtract_screlease_fn	283
6.63.2.112 hsa_signal_wait_relaxed_fn	283
6.63.2.113 hsa_signal_wait_scacquire_fn	283
6.63.2.114 hsa_signal_xor_relaxed_fn	284
6.63.2.115 hsa_signal_xor_scacq_screl_fn	284
6.63.2.116 hsa_signal_xor_scacquire_fn	284
6.63.2.117 hsa_signal_xor_screlease_fn	284
6.63.2.118 hsa_soft_queue_create_fn	284
6.63.2.119 hsa_status_string_fn	284
6.63.2.120 hsa_system_extension_supported_fn	285
6.63.2.121 hsa_system_get_extension_table_fn	285
6.63.2.122 hsa_system_get_info_fn	285
6.63.2.123 hsa_system_get_major_extension_table_fn	285
6.63.2.124 hsa_system_major_extension_supported_fn	285
6.63.2.125 hsa_wavefront_get_info_fn	285
6.63.2.126 version	286
6.64 FinalizerExtTable Struct Reference	286
6.64.1 Detailed Description	286
6.64.2 Member Data Documentation	286
6.64.2.1 hsa_ext_program_add_module_fn	286
6.64.2.2 hsa_ext_program_create_fn	286
6.64.2.3 hsa_ext_program_destroy_fn	287
6.64.2.4 hsa_ext_program_finalize_fn	287
6.64.2.5 hsa_ext_program_get_info_fn	287
6.64.2.6 hsa_ext_program_iterate_modules_fn	287
6.64.2.7 version	287
6.65 hsa_agent_dispatch_packet_s Struct Reference	287
6.65.1 Detailed Description	288
6.65.2 Member Data Documentation	288
6.65.2.1 arg	288
6.65.2.2 completion_signal	288
6.65.2.3 header	288
6.65.2.4 reserved0	289
6.65.2.5 reserved1	289
6.65.2.6 reserved2	289

6.65.2.7 return_address	289
6.65.2.8 type	289
6.66 hsa_agent_s Struct Reference	290
6.66.1 Detailed Description	290
6.66.2 Member Data Documentation	290
6.66.2.1 handle	290
6.67 hsa_amd_barrier_value_packet_s Struct Reference	290
6.67.1 Detailed Description	291
6.67.2 Member Data Documentation	291
6.67.2.1 completion_signal	291
6.67.2.2 cond	291
6.67.2.3 header	292
6.67.2.4 mask	292
6.67.2.5 reserved0	292
6.67.2.6 reserved1	292
6.67.2.7 reserved2	292
6.67.2.8 reserved3	293
6.67.2.9 signal	293
6.67.2.10 value	293
6.68 hsa_amd_event_s Struct Reference	293
6.68.1 Detailed Description	294
6.68.2 Member Data Documentation	294
6.68.2.1 event_type	294
6.68.2.2 memory_fault	294
6.69 hsa_amd_gpu_memory_fault_info_s Struct Reference	294
6.69.1 Detailed Description	294
6.69.2 Member Data Documentation	295
6.69.2.1 agent	295
6.69.2.2 fault_reason_mask	295
6.69.2.3 virtual_address	295
6.70 hsa_amd_hdp_flush_s Struct Reference	295
6.70.1 Detailed Description	295
6.70.2 Member Data Documentation	295
6.70.2.1 HDP_MEM_FLUSH_CNTL	296
6.70.2.2 HDP_REG_FLUSH_CNTL	296
6.71 hsa_amd_image_descriptor_s Struct Reference	296
6.71.1 Detailed Description	296
6.71.2 Member Data Documentation	296
6.71.2.1 data	296
6.71.2.2 deviceId	297
6.71.2.3 version	297
6.72 hsa_amd_ipc_memory_s Struct Reference	297

6.72.1 Detailed Description	297
6.72.2 Member Data Documentation	297
6.72.2.1 handle	297
6.73 hsa_amd_memory_pool_link_info_s Struct Reference	298
6.73.1 Detailed Description	298
6.73.2 Member Data Documentation	298
6.73.2.1 atomic_support_32bit	298
6.73.2.2 atomic_support_64bit	298
6.73.2.3 coherent_support	299
6.73.2.4 link_type	299
6.73.2.5 max_bandwidth	299
6.73.2.6 max_latency	299
6.73.2.7 min_bandwidth	299
6.73.2.8 min_latency	300
6.73.2.9 numa_distance	300
6.74 hsa_amd_memory_pool_s Struct Reference	300
6.74.1 Detailed Description	300
6.74.2 Member Data Documentation	301
6.74.2.1 handle	301
6.75 hsa_amd_packet_header_s Struct Reference	301
6.75.1 Detailed Description	301
6.75.2 Member Data Documentation	301
6.75.2.1 AmdFormat	301
6.75.2.2 header	302
6.75.2.3 reserved	302
6.76 hsa_amd_pointer_info_s Struct Reference	302
6.76.1 Detailed Description	302
6.76.2 Member Data Documentation	303
6.76.2.1 agentBaseAddress	303
6.76.2.2 agentOwner	303
6.76.2.3 global_flags	303
6.76.2.4 hostBaseAddress	303
6.76.2.5 size	303
6.76.2.6 sizeInBytes	304
6.76.2.7 type	304
6.76.2.8 userData	304
6.77 hsa_amd_profiling_async_copy_time_s Struct Reference	304
6.77.1 Detailed Description	304
6.77.2 Member Data Documentation	305
6.77.2.1 end	305
6.77.2.2 start	305
6.78 hsa_amd_profiling_dispatch_time_s Struct Reference	305

6.78.1 Detailed Description	305
6.78.2 Member Data Documentation	306
6.78.2.1 end	306
6.78.2.2 start	306
6.79 hsa_amd_svm_attribute_pair_s Struct Reference	306
6.79.1 Detailed Description	306
6.79.2 Member Data Documentation	306
6.79.2.1 attribute	306
6.79.2.2 value	307
6.80 hsa_barrier_and_packet_s Struct Reference	307
6.80.1 Detailed Description	307
6.80.2 Member Data Documentation	307
6.80.2.1 completion_signal	307
6.80.2.2 dep_signal	308
6.80.2.3 header	308
6.80.2.4 reserved0	308
6.80.2.5 reserved1	308
6.80.2.6 reserved2	308
6.81 hsa_barrier_or_packet_s Struct Reference	309
6.81.1 Detailed Description	309
6.81.2 Member Data Documentation	309
6.81.2.1 completion_signal	309
6.81.2.2 dep_signal	309
6.81.2.3 header	310
6.81.2.4 reserved0	310
6.81.2.5 reserved1	310
6.81.2.6 reserved2	310
6.82 hsa_cache_s Struct Reference	310
6.82.1 Detailed Description	311
6.82.2 Member Data Documentation	311
6.82.2.1 handle	311
6.83 hsa_callback_data_s Struct Reference	311
6.83.1 Detailed Description	311
6.83.2 Member Data Documentation	312
6.83.2.1 handle	312
6.84 hsa_code_object_reader_s Struct Reference	312
6.84.1 Detailed Description	312
6.84.2 Member Data Documentation	312
6.84.2.1 handle	312
6.85 hsa_code_object_s Struct Reference	313
6.85.1 Detailed Description	313
6.85.2 Member Data Documentation	313

6.85.2.1 handle	313
6.86 hsa_code_symbol_s Struct Reference	313
6.86.1 Detailed Description	314
6.86.2 Member Data Documentation	314
6.86.2.1 handle	314
6.87 hsa_dim3_s Struct Reference	314
6.87.1 Detailed Description	314
6.87.2 Member Data Documentation	315
6.87.2.1 x	315
6.87.2.2 y	315
6.87.2.3 z	315
6.88 hsa_executable_s Struct Reference	315
6.88.1 Detailed Description	316
6.88.2 Member Data Documentation	316
6.88.2.1 handle	316
6.89 hsa_executable_symbol_s Struct Reference	316
6.89.1 Detailed Description	316
6.89.2 Member Data Documentation	316
6.89.2.1 handle	317
6.90 hsa_ext_amd_aql_pm4_packet_t Struct Reference	317
6.90.1 Detailed Description	317
6.90.2 Member Data Documentation	317
6.90.2.1 completion_signal	317
6.90.2.2 header	317
6.90.2.3 pm4_command	318
6.91 hsa_ext_control_directives_s Struct Reference	318
6.91.1 Detailed Description	318
6.91.2 Member Data Documentation	318
6.91.2.1 break_exceptions_mask	319
6.91.2.2 control_directives_mask	319
6.91.2.3 detect_exceptions_mask	319
6.91.2.4 max_dynamic_group_size	319
6.91.2.5 max_flat_grid_size	320
6.91.2.6 max_flat_workgroup_size	320
6.91.2.7 required_dim	320
6.91.2.8 required_grid_size	321
6.91.2.9 required_workgroup_size	321
6.91.2.10 reserved1	321
6.91.2.11 reserved2	321
6.92 hsa_ext_finalizer_1_00_pfn_s Struct Reference	322
6.92.1 Detailed Description	322
6.92.2 Member Data Documentation	322

6.92.2.1 hsa_ext_program_add_module	322
6.92.2.2 hsa_ext_program_create	322
6.92.2.3 hsa_ext_program_destroy	322
6.92.2.4 hsa_ext_program_finalize	323
6.92.2.5 hsa_ext_program_get_info	323
6.92.2.6 hsa_ext_program_iterate_modules	323
6.93 hsa_ext_image_data_info_s Struct Reference	323
6.93.1 Detailed Description	323
6.93.2 Member Data Documentation	324
6.93.2.1 alignment	324
6.93.2.2 size	324
6.94 hsa_ext_image_descriptor_s Struct Reference	324
6.94.1 Detailed Description	324
6.94.2 Member Data Documentation	325
6.94.2.1 array_size	325
6.94.2.2 depth	325
6.94.2.3 format	325
6.94.2.4 geometry	325
6.94.2.5 height	326
6.94.2.6 width	326
6.95 hsa_ext_image_format_s Struct Reference	326
6.95.1 Detailed Description	326
6.95.2 Member Data Documentation	326
6.95.2.1 channel_order	327
6.95.2.2 channel_type	327
6.96 hsa_ext_image_region_s Struct Reference	327
6.96.1 Detailed Description	327
6.96.2 Member Data Documentation	327
6.96.2.1 offset	328
6.96.2.2 range	328
6.97 hsa_ext_image_s Struct Reference	328
6.97.1 Detailed Description	328
6.97.2 Member Data Documentation	328
6.97.2.1 handle	329
6.98 hsa_ext_images_1_00_pfn_s Struct Reference	329
6.98.1 Detailed Description	329
6.98.2 Member Data Documentation	330
6.98.2.1 hsa_ext_image_clear	330
6.98.2.2 hsa_ext_image_copy	330
6.98.2.3 hsa_ext_image_create	330
6.98.2.4 hsa_ext_image_data_get_info	330
6.98.2.5 hsa_ext_image_destroy	330

6.98.2.6 hsa_ext_image_export	331
6.98.2.7 hsa_ext_image_get_capability	331
6.98.2.8 hsa_ext_image_import	331
6.98.2.9 hsa_ext_sampler_create	331
6.98.2.10 hsa_ext_sampler_destroy	331
6.99 hsa_ext_images_1_pfn_s Struct Reference	332
6.99.1 Detailed Description	332
6.99.2 Member Data Documentation	332
6.99.2.1 hsa_ext_image_clear	333
6.99.2.2 hsa_ext_image_copy	333
6.99.2.3 hsa_ext_image_create	333
6.99.2.4 hsa_ext_image_create_with_layout	333
6.99.2.5 hsa_ext_image_data_get_info	333
6.99.2.6 hsa_ext_image_data_get_info_with_layout	334
6.99.2.7 hsa_ext_image_destroy	334
6.99.2.8 hsa_ext_image_export	334
6.99.2.9 hsa_ext_image_get_capability	334
6.99.2.10 hsa_ext_image_get_capability_with_layout	334
6.99.2.11 hsa_ext_image_import	335
6.99.2.12 hsa_ext_sampler_create	335
6.99.2.13 hsa_ext_sampler_destroy	335
6.100 hsa_ext_program_s Struct Reference	335
6.100.1 Detailed Description	335
6.100.2 Member Data Documentation	336
6.100.2.1 handle	336
6.101 hsa_ext_sampler_descriptor_s Struct Reference	336
6.101.1 Detailed Description	336
6.101.2 Member Data Documentation	336
6.101.2.1 address_mode	336
6.101.2.2 coordinate_mode	337
6.101.2.3 filter_mode	337
6.102 hsa_ext_sampler_s Struct Reference	337
6.102.1 Detailed Description	337
6.102.2 Member Data Documentation	337
6.102.2.1 handle	338
6.103 hsa_isa_s Struct Reference	338
6.103.1 Detailed Description	338
6.103.2 Member Data Documentation	338
6.103.2.1 handle	338
6.104 hsa_kernel_dispatch_packet_s Struct Reference	339
6.104.1 Detailed Description	339
6.104.2 Member Data Documentation	339

6.104.2.1 completion_signal	339
6.104.2.2 grid_size_x	340
6.104.2.3 grid_size_y	340
6.104.2.4 grid_size_z	340
6.104.2.5 group_segment_size	340
6.104.2.6 header	340
6.104.2.7 kernarg_address	341
6.104.2.8 kernel_object	341
6.104.2.9 private_segment_size	341
6.104.2.10 reserved0	341
6.104.2.11 reserved1	341
6.104.2.12 reserved2	342
6.104.2.13 setup	342
6.104.2.14 workgroup_size_x	342
6.104.2.15 workgroup_size_y	342
6.104.2.16 workgroup_size_z	342
6.105 hsa_loaded_code_object_s Struct Reference	343
6.105.1 Detailed Description	343
6.105.2 Member Data Documentation	343
6.105.2.1 handle	343
6.106 hsa_pitched_ptr_s Struct Reference	343
6.106.1 Detailed Description	343
6.106.2 Member Data Documentation	344
6.106.2.1 base	344
6.106.2.2 pitch	344
6.106.2.3 slice	344
6.107 hsa_queue_s Struct Reference	344
6.107.1 Detailed Description	345
6.107.2 Member Data Documentation	345
6.107.2.1 base_address	345
6.107.2.2 doorbell_signal	345
6.107.2.3 features	345
6.107.2.4 id	346
6.107.2.5 reserved0	346
6.107.2.6 reserved1	346
6.107.2.7 size	346
6.107.2.8 type	346
6.108 hsa_region_s Struct Reference	347
6.108.1 Detailed Description	347
6.108.2 Member Data Documentation	347
6.108.2.1 handle	347
6.109 hsa_signal_group_s Struct Reference	347

6.109.1 Detailed Description	348
6.109.2 Member Data Documentation	348
6.109.2.1 handle	348
6.110 hsa_signal_s Struct Reference	348
6.110.1 Detailed Description	348
6.110.2 Member Data Documentation	348
6.110.2.1 handle	349
6.111 hsa_ven_amd_aqlprofile_1_00_pfn_s Struct Reference	349
6.111.1 Detailed Description	349
6.111.2 Member Data Documentation	349
6.111.2.1 hsa_ven_amd_aqlprofile_error_string	350
6.111.2.2 hsa_ven_amd_aqlprofile_get_info	350
6.111.2.3 hsa_ven_amd_aqlprofile_iterate_data	350
6.111.2.4 hsa_ven_amd_aqlprofile_legacy_get_pm4	350
6.111.2.5 hsa_ven_amd_aqlprofile_read	350
6.111.2.6 hsa_ven_amd_aqlprofile_start	351
6.111.2.7 hsa_ven_amd_aqlprofile_stop	351
6.111.2.8 hsa_ven_amd_aqlprofile_validate_event	351
6.111.2.9 hsa_ven_amd_aqlprofile_version_major	351
6.111.2.10 hsa_ven_amd_aqlprofile_version_minor	351
6.112 hsa_ven_amd_aqlprofile_descriptor_t Struct Reference	352
6.112.1 Detailed Description	352
6.112.2 Member Data Documentation	352
6.112.2.1 ptr	352
6.112.2.2 size	352
6.113 hsa_ven_amd_aqlprofile_event_t Struct Reference	352
6.113.1 Detailed Description	352
6.113.2 Member Data Documentation	353
6.113.2.1 block_index	353
6.113.2.2 block_name	353
6.113.2.3 counter_id	353
6.114 hsa_ven_amd_aqlprofile_id_query_t Struct Reference	353
6.114.1 Detailed Description	353
6.114.2 Member Data Documentation	353
6.114.2.1 id	354
6.114.2.2 instance_count	354
6.114.2.3 name	354
6.115 hsa_ven_amd_aqlprofile_info_data_t Struct Reference	354
6.115.1 Detailed Description	354
6.115.2 Member Data Documentation	355
6.115.2.1 event	355
6.115.2.2 result	355

6.115.2.3 sample_id	355
6.115.2.4 trace_data	355
6.116 hsa_ven_amd_aqlprofile_parameter_t Struct Reference	355
6.116.1 Detailed Description	356
6.116.2 Member Data Documentation	356
6.116.2.1 parameter_name	356
6.116.2.2 value	356
6.117 hsa_ven_amd_aqlprofile_profile_t Struct Reference	356
6.117.1 Detailed Description	356
6.117.2 Member Data Documentation	357
6.117.2.1 agent	357
6.117.2.2 command_buffer	357
6.117.2.3 event_count	357
6.117.2.4 events	357
6.117.2.5 output_buffer	357
6.117.2.6 parameter_count	358
6.117.2.7 parameters	358
6.117.2.8 type	358
6.118 hsa_ven_amd_loader_1_00_pfn_s Struct Reference	358
6.118.1 Detailed Description	358
6.118.2 Member Data Documentation	359
6.118.2.1 hsa_ven_amd_loader_query_executable	359
6.118.2.2 hsa_ven_amd_loader_query_host_address	359
6.118.2.3 hsa_ven_amd_loader_query_segment_descriptors	359
6.119 hsa_ven_amd_loader_1_01_pfn_s Struct Reference	359
6.119.1 Detailed Description	360
6.119.2 Member Data Documentation	360
6.119.2.1 hsa_ven_amd_loader_executable_iterate_loaded_code_objects	360
6.119.2.2 hsa_ven_amd_loader_loaded_code_object_get_info	360
6.119.2.3 hsa_ven_amd_loader_query_executable	360
6.119.2.4 hsa_ven_amd_loader_query_host_address	360
6.119.2.5 hsa_ven_amd_loader_query_segment_descriptors	361
6.120 hsa_ven_amd_loader_1_02_pfn_s Struct Reference	361
6.120.1 Detailed Description	361
6.120.2 Member Data Documentation	361
6.120.2.1 hsa_ven_amd_loader_code_object_reader_create_from_file_with_offset_size	362
6.120.2.2 hsa_ven_amd_loader_executable_iterate_loaded_code_objects	362
6.120.2.3 hsa_ven_amd_loader_loaded_code_object_get_info	362
6.120.2.4 hsa_ven_amd_loader_query_executable	362
6.120.2.5 hsa_ven_amd_loader_query_host_address	362
6.120.2.6 hsa_ven_amd_loader_query_segment_descriptors	363
6.121 hsa_ven_amd_loader_1_03_pfn_s Struct Reference	363

6.121.1 Detailed Description	363
6.121.2 Member Data Documentation	363
6.121.2.1 hsa_ven_amd_loader_code_object_reader_create_from_file_with_offset_size	364
6.121.2.2 hsa_ven_amd_loader_executable_iterate_loaded_code_objects	364
6.121.2.3 hsa_ven_amd_loader_iterate_executables	364
6.121.2.4 hsa_ven_amd_loader_loaded_code_object_get_info	364
6.121.2.5 hsa_ven_amd_loader_query_executable	364
6.121.2.6 hsa_ven_amd_loader_query_host_address	365
6.121.2.7 hsa_ven_amd_loader_query_segment_descriptors	365
6.122 hsa_ven_amd_loader_segment_descriptor_s Struct Reference	365
6.122.1 Detailed Description	365
6.122.2 Member Data Documentation	366
6.122.2.1 agent	366
6.122.2.2 code_object_storage_base	366
6.122.2.3 code_object_storage_offset	366
6.122.2.4 code_object_storage_size	367
6.122.2.5 code_object_storage_type	367
6.122.2.6 executable	367
6.122.2.7 segment_base	367
6.122.2.8 segment_size	368
6.123 hsa_wavefront_s Struct Reference	368
6.123.1 Detailed Description	368
6.123.2 Member Data Documentation	368
6.123.2.1 handle	368
6.124 HsaApiTable Struct Reference	369
6.124.1 Detailed Description	369
6.124.2 Member Data Documentation	369
6.124.2.1 amd_ext_	369
6.124.2.2 core_	369
6.124.2.3 finalizer_ext_	369
6.124.2.4 image_ext_	370
6.124.2.5 version	370
6.125 HsaApiTableContainer Struct Reference	370
6.125.1 Detailed Description	370
6.125.2 Constructor & Destructor Documentation	370
6.125.2.1 HsaApiTableContainer()	370
6.125.3 Member Data Documentation	371
6.125.3.1 amd_ext	371
6.125.3.2 core	371
6.125.3.3 finalizer_ext	371
6.125.3.4 image_ext	371
6.125.3.5 root	371

6.126 ImageExtTable Struct Reference	372
6.126.1 Detailed Description	372
6.126.2 Member Data Documentation	372
6.126.2.1 hsa_ext_image_clear_fn	372
6.126.2.2 hsa_ext_image_copy_fn	372
6.126.2.3 hsa_ext_image_create_fn	373
6.126.2.4 hsa_ext_image_create_with_layout_fn	373
6.126.2.5 hsa_ext_image_data_get_info_fn	373
6.126.2.6 hsa_ext_image_data_get_info_with_layout_fn	373
6.126.2.7 hsa_ext_image_destroy_fn	373
6.126.2.8 hsa_ext_image_export_fn	373
6.126.2.9 hsa_ext_image_get_capability_fn	374
6.126.2.10 hsa_ext_image_get_capability_with_layout_fn	374
6.126.2.11 hsa_ext_image_import_fn	374
6.126.2.12 hsa_ext_sampler_create_fn	374
6.126.2.13 hsa_ext_sampler_destroy_fn	374
6.126.2.14 version	374
7 File Documentation	375
7.1 amd_hsa_common.h	375
7.2 amd_hsa_elf.h	376
7.3 amd_hsa_kernel_code.h	381
7.4 amd_hsa_queue.h	384
7.5 amd_hsa_signal.h	385
7.6 Brig.h	386
7.7 hsa.h	399
7.8 hsa_api_trace.h	416
7.9 hsa_ext_amd.h	422
7.10 hsa_ext_finalize.h	430
7.11 hsa_ext_image.h	432
7.12 hsa_ven_amd_aqlprofile.h	438
7.13 hsa_ven_amd_loader.h	442
Index	447

Chapter 1

Deprecated List

Member [hsa_agent_extension_supported](#) (uint16_t extension, hsa_agent_t agent, uint16_t version_major, uint16_t version_minor, bool *result)

Member [hsa_agent_get_exception_policies](#) (hsa_agent_t agent, hsa_profile_t profile, uint16_t *mask)

Use [hsa_isa_get_exception_policies](#) for a given instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, this function uses the first value returned by [hsa_agent_iterate_isas](#).

Member [HSA_AGENT_INFO_BASE_PROFILE_DEFAULT_FLOAT_ROUNDING_MODES](#)

Query [HSA_ISA_INFO_BASE_PROFILE_DEFAULT_FLOAT_ROUNDING_MODES](#) for a given instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by [hsa_agent_iterate_isas](#).

Member [HSA_AGENT_INFO_CACHE_SIZE](#)

Query [hsa_agent_iterate_caches](#) to retrieve information about the caches present in a given agent.

Member [HSA_AGENT_INFO_DEFAULT_FLOAT_ROUNDING_MODE](#)

Query [HSA_ISA_INFO_DEFAULT_FLOAT_ROUNDING_MODES](#) for a given instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by [hsa_agent_iterate_isas](#).

Member [HSA_AGENT_INFO_FAST_F16_OPERATION](#)

Query [HSA_ISA_INFO_FAST_F16_OPERATION](#) for a given instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by [hsa_agent_iterate_isas](#).

Member [HSA_AGENT_INFO_FBARRIER_MAX_SIZE](#)

Query [HSA_ISA_INFO_FBARRIER_MAX_SIZE](#) for a given instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by [hsa_agent_iterate_isas](#).

Member [HSA_AGENT_INFO_GRID_MAX_DIM](#)

Query [HSA_ISA_INFO_GRID_MAX_DIM](#) for a given instruction set architecture supported by the agent instead.

Member [HSA_AGENT_INFO_GRID_MAX_SIZE](#)

Query [HSA_ISA_INFO_GRID_MAX_SIZE](#) for a given instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by [hsa_agent_iterate_isas](#).

Member [HSA_AGENT_INFO_ISA](#)

An agent may support multiple instruction set architectures. See [hsa_agent_iterate_isas](#). If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by [hsa_agent_iterate_isas](#).

Member [HSA_AGENT_INFO_MACHINE_MODEL](#)

Query [HSA_ISA_INFO_MACHINE_MODELS](#) for a given instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by [hsa_agent_iterate_isas](#).

Member [HSA_AGENT_INFO_NODE](#)

NUMA information is not exposed anywhere else in the API.

Member [HSA_AGENT_INFO_PROFILE](#)

Query [HSA_ISA_INFO_PROFILES](#) for a given instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by [hsa_agent_iterate_isas](#).

Member [HSA_AGENT_INFO_QUEUES_MAX](#)

The maximum number of queues is not statically determined.

Member [HSA_AGENT_INFO_WAVEFRONT_SIZE](#)

Query [HSA_WAVEFRONT_INFO_SIZE](#) for a given wavefront and instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by [hsa_agent_iterate_isas](#) and the first wavefront enumerated by [hsa_isa_iterate_wavefronts](#) for that ISA.

Member [HSA_AGENT_INFO_WORKGROUP_MAX_DIM](#)

Query [HSA_ISA_INFO_WORKGROUP_MAX_DIM](#) for a given instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by [hsa_agent_iterate_isas](#).

Member [HSA_AGENT_INFO_WORKGROUP_MAX_SIZE](#)

Query [HSA_ISA_INFO_WORKGROUP_MAX_SIZE](#) for a given instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by [hsa_agent_iterate_isas](#).

Member [hsa_callback_data_t](#)

Member [hsa_code_object_deserialize](#) (void *serialized_code_object, size_t serialized_code_object_size, const char *options, hsa_code_object_t *code_object)

Member [hsa_code_object_destroy](#) (hsa_code_object_t code_object)

Member [hsa_code_object_get_info](#) (hsa_code_object_t code_object, hsa_code_object_info_t attribute, void *value)

Member [hsa_code_object_get_symbol](#) (hsa_code_object_t code_object, const char *symbol_name, hsa_code_symbol_t *symbol)

Member [hsa_code_object_get_symbol_from_name](#) (hsa_code_object_t code_object, const char *module_name, const char *symbol_name, hsa_code_symbol_t *symbol)

Member [hsa_code_object_info_t](#)

Member [hsa_code_object_iterate_symbols](#) (hsa_code_object_t code_object, hsa_status_t(*callback)(hsa_code_object_t code_object, hsa_code_symbol_t symbol, void *data), void *data)

Member [hsa_code_object_serialize](#) (hsa_code_object_t code_object, hsa_status_t(*alloc_callback)(size_t size, hsa_callback_data_t data, void **address), hsa_callback_data_t callback_data, const char *options, void **serialized_code_object, size_t *serialized_code_object_size)

Member [hsa_code_object_t](#)

Member [hsa_code_object_type_t](#)

Member [hsa_code_symbol_get_info](#) ([hsa_code_symbol_t](#) code_symbol, [hsa_code_symbol_info_t](#) attribute, void *value)

Member [hsa_code_symbol_info_t](#)

Member [hsa_code_symbol_t](#)

Member [hsa_executable_create](#) ([hsa_profile_t](#) profile, [hsa_executable_state_t](#) executable_state, const char *options, [hsa_executable_t](#) *executable)

Use [hsa_executable_create_alt](#) instead, which allows the application to specify the default floating-point rounding mode of the executable and assumes an unfrozen initial state.

Member [hsa_executable_get_symbol](#) ([hsa_executable_t](#) executable, const char *module_name, const char *symbol_name, [hsa_agent_t](#) agent, [int32_t](#) call_convention, [hsa_executable_symbol_t](#) *symbol)

Use [hsa_executable_get_symbol_by_name](#) instead.

Member [hsa_executable_iterate_symbols](#) ([hsa_executable_t](#) executable, [hsa_status_t](#)(*callback)([hsa_executable_t](#) exec, [hsa_executable_symbol_t](#) symbol, void *data), void *data)

Member [hsa_executable_load_code_object](#) ([hsa_executable_t](#) executable, [hsa_agent_t](#) agent, [hsa_code_object_t](#) code_object, const char *options)

Member [HSA_EXECUTABLE_SYMBOL_INFO_AGENT](#)

Member [HSA_EXECUTABLE_SYMBOL_INFO_INDIRECT_FUNCTION_CALL_CONVENTION](#)

Member [HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_CALL_CONVENTION](#)

Member [HSA_EXECUTABLE_SYMBOL_INFO_MODULE_NAME](#)

Member [HSA_EXECUTABLE_SYMBOL_INFO_MODULE_NAME_LENGTH](#)

Member [HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_ALIGNMENT](#)

Member [HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_ALLOCATION](#)

Member [HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_IS_CONST](#)

Member [HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_SEGMENT](#)

Member [HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_SIZE](#)

Member [HSA_EXT_IMAGE_CAPABILITY_READ_MODIFY_WRITE](#)

Images of this geometry, format, and layout can be accessed from read-modify-write atomic operations in the agent.

Member [hsa_isa_compatible](#) ([hsa_isa_t](#) code_object_isa, [hsa_isa_t](#) agent_isa, bool *result)

Use [hsa_agent_iterate_isas](#) to query which instructions set architectures are supported by a given agent.

Member [hsa_isa_get_info](#) ([hsa_isa_t](#) isa, [hsa_isa_info_t](#) attribute, [uint32_t](#) index, void *value)

The concept of call convention has been deprecated. If the application wants to query the value of an attribute for a given instruction set architecture, use [hsa_isa_get_info_alt](#) instead. If the application wants to query an attribute that is specific to a given combination of ISA and wavefront, use [hsa_wavefront_get_info](#).

Member [HSA_ISA_INFO_CALL_CONVENTION_COUNT](#)

Member [HSA_ISA_INFO_CALL_CONVENTION_INFO_WAVEFRONT_SIZE](#)

Member [HSA_ISA_INFO_CALL_CONVENTION_INFO_WAVEFRONTS_PER_COMPUTE_UNIT](#)

Member [HSA_PACKET_HEADER_ACQUIRE_FENCE_SCOPE](#)

Renamed as [HSA_PACKET_HEADER_SCACQUIRE_FENCE_SCOPE](#).

Member [HSA_PACKET_HEADER_RELEASE_FENCE_SCOPE](#)

Renamed as [HSA_PACKET_HEADER_SCRELEASE_FENCE_SCOPE](#).

Member [HSA_PACKET_HEADER_WIDTH_ACQUIRE_FENCE_SCOPE](#)

Use [HSA_PACKET_HEADER_WIDTH_SCACQUIRE_FENCE_SCOPE](#).

Member [HSA_PACKET_HEADER_WIDTH_RELEASE_FENCE_SCOPE](#)

Use [HSA_PACKET_HEADER_WIDTH_SCRELEASE_FENCE_SCOPE](#).

Member [hsa_queue_add_write_index_acq_rel](#) (const [hsa_queue_t](#) *queue, [uint64_t](#) value)

Renamed as [hsa_queue_add_write_index_scacq_screl](#).

Member [hsa_queue_add_write_index_acquire](#) (const [hsa_queue_t](#) *queue, [uint64_t](#) value)

Renamed as [hsa_queue_add_write_index_scacquire](#).

Member [hsa_queue_add_write_index_release](#) (const [hsa_queue_t](#) *queue, [uint64_t](#) value)

Renamed as [hsa_queue_add_write_index_screlease](#).

Member [hsa_queue_cas_write_index_acq_rel](#) (const [hsa_queue_t](#) *queue, [uint64_t](#) expected, [uint64_t](#) value)

Renamed as [hsa_queue_cas_write_index_scacq_screl](#).

Member [hsa_queue_cas_write_index_acquire](#) (const [hsa_queue_t](#) *queue, [uint64_t](#) expected, [uint64_t](#) value)

Renamed as [hsa_queue_cas_write_index_scacquire](#).

Member [hsa_queue_cas_write_index_release](#) (const [hsa_queue_t](#) *queue, [uint64_t](#) expected, [uint64_t](#) value)

Renamed as [hsa_queue_cas_write_index_screlease](#).

Member [hsa_queue_load_read_index_acquire](#) (const [hsa_queue_t](#) *queue)

Renamed as [hsa_queue_load_read_index_scacquire](#).

Member [hsa_queue_load_write_index_acquire](#) (const [hsa_queue_t](#) *queue)

Renamed as [hsa_queue_load_write_index_scacquire](#).

Member [hsa_queue_store_read_index_release](#) (const [hsa_queue_t](#) *queue, [uint64_t](#) value)

Renamed as [hsa_queue_store_read_index_screlease](#).

Member [hsa_queue_store_write_index_release](#) (const [hsa_queue_t](#) *queue, [uint64_t](#) value)

Renamed as [hsa_queue_store_write_index_screlease](#).

Member [hsa_signal_add_acq_rel](#) ([hsa_signal_t](#) signal, [hsa_signal_value_t](#) value)

Renamed as [hsa_signal_add_scacq_screl](#).

Member [hsa_signal_add_acquire](#) ([hsa_signal_t](#) signal, [hsa_signal_value_t](#) value)

Renamed as [hsa_signal_add_scacquire](#).

-
- Member `hsa_signal_add_release`** (`hsa_signal_t` signal, `hsa_signal_value_t` value)
Renamed as `hsa_signal_add_screlease`.
- Member `hsa_signal_and_acq_rel`** (`hsa_signal_t` signal, `hsa_signal_value_t` value)
Renamed as `hsa_signal_and_scacq_screl`.
- Member `hsa_signal_and_acquire`** (`hsa_signal_t` signal, `hsa_signal_value_t` value)
Renamed as `hsa_signal_and_scacquire`.
- Member `hsa_signal_and_release`** (`hsa_signal_t` signal, `hsa_signal_value_t` value)
Renamed as `hsa_signal_and_screlease`.
- Member `hsa_signal_cas_acq_rel`** (`hsa_signal_t` signal, `hsa_signal_value_t` expected, `hsa_signal_value_t` value)
Renamed as `hsa_signal_cas_scacq_screl`.
- Member `hsa_signal_cas_acquire`** (`hsa_signal_t` signal, `hsa_signal_value_t` expected, `hsa_signal_value_t` value)
Renamed as `hsa_signal_cas_scacquire`.
- Member `hsa_signal_cas_release`** (`hsa_signal_t` signal, `hsa_signal_value_t` expected, `hsa_signal_value_t` value)
Renamed as `hsa_signal_cas_screlease`.
- Member `hsa_signal_exchange_acq_rel`** (`hsa_signal_t` signal, `hsa_signal_value_t` value)
Renamed as `hsa_signal_exchange_scacq_screl`.
- Member `hsa_signal_exchange_acquire`** (`hsa_signal_t` signal, `hsa_signal_value_t` value)
Renamed as `hsa_signal_exchange_scacquire`.
- Member `hsa_signal_exchange_release`** (`hsa_signal_t` signal, `hsa_signal_value_t` value)
Renamed as `hsa_signal_exchange_screlease`.
- Member `hsa_signal_load_acquire`** (`hsa_signal_t` signal)
Renamed as `hsa_signal_load_scacquire`.
- Member `hsa_signal_or_acq_rel`** (`hsa_signal_t` signal, `hsa_signal_value_t` value)
Renamed as `hsa_signal_or_scacq_screl`.
- Member `hsa_signal_or_acquire`** (`hsa_signal_t` signal, `hsa_signal_value_t` value)
Renamed as `hsa_signal_or_scacquire`.
- Member `hsa_signal_or_release`** (`hsa_signal_t` signal, `hsa_signal_value_t` value)
Renamed as `hsa_signal_or_screlease`.
- Member `hsa_signal_store_release`** (`hsa_signal_t` signal, `hsa_signal_value_t` value)
Renamed as `hsa_signal_store_screlease`.
- Member `hsa_signal_subtract_acq_rel`** (`hsa_signal_t` signal, `hsa_signal_value_t` value)
Renamed as `hsa_signal_subtract_scacq_screl`.
- Member `hsa_signal_subtract_acquire`** (`hsa_signal_t` signal, `hsa_signal_value_t` value)
Renamed as `hsa_signal_subtract_scacquire`.
- Member `hsa_signal_subtract_release`** (`hsa_signal_t` signal, `hsa_signal_value_t` value)
Renamed as `hsa_signal_subtract_screlease`.
- Member `hsa_signal_wait_acquire`** (`hsa_signal_t` signal, `hsa_signal_condition_t` condition, `hsa_signal_value_t` compare_value, `uint64_t` timeout_hint, `hsa_wait_state_t` wait_state_hint)
Renamed as `hsa_signal_wait_scacquire`.
- Member `hsa_signal_xor_acq_rel`** (`hsa_signal_t` signal, `hsa_signal_value_t` value)
Renamed as `hsa_signal_xor_scacq_screl`.
- Member `hsa_signal_xor_acquire`** (`hsa_signal_t` signal, `hsa_signal_value_t` value)
Renamed as `hsa_signal_xor_scacquire`.

Member [hsa_signal_xor_release](#) (hsa_signal_t signal, hsa_signal_value_t value)

Renamed as [hsa_signal_xor_screlease](#).

Member [hsa_system_extension_supported](#) (uint16_t extension, uint16_t version_major, uint16_t version_↵
_minor, bool *result)

Member [hsa_system_get_extension_table](#) (uint16_t extension, uint16_t version_major, uint16_t version_↵
minor, void *table)

Chapter 2

Module Index

2.1 Modules

Here is a list of all modules:

Runtime Notifications	15
System and Agent Information	19
Signals	38
Memory	72
Queues	80
Architected Queuing Language	98
Instruction Set Architecture.	103
Executable	113
Code Objects (deprecated).	136
Finalization Extensions	147
Finalization Program	148
Images and Samplers	155

Chapter 3

Class Index

3.1 Class List

Here are the classes, structs, unions and interfaces with brief descriptions:

amd_control_directives_s	179
amd_kernel_code_s	181
amd_queue_s	188
amd_runtime_loader_debug_info_s	192
amd_signal_s	194
AmdExtTable	196
amdgpu_hsa_image_descriptor_s	196
amdgpu_hsa_note_code_object_version_s	198
amdgpu_hsa_note_hsail_s	199
amdgpu_hsa_note_isa_s	200
amdgpu_hsa_note_producer_options_s	202
amdgpu_hsa_note_producer_s	203
amdgpu_hsa_sampler_descriptor_s	204
ApiTableVersion	205
BrigBase	206
BrigData	207
BrigDirectiveArgBlock	208
BrigDirectiveComment	209
BrigDirectiveControl	209
BrigDirectiveExecutable	210
BrigDirectiveExtension	213
BrigDirectiveFbarrier	214
BrigDirectiveLabel	215
BrigDirectiveLoc	216
BrigDirectiveModule	217
BrigDirectiveNone	219
BrigDirectivePragma	219
BrigDirectiveVariable	220
BrigInstAddr	223
BrigInstAtomic	224
BrigInstBase	225
BrigInstBasic	226
BrigInstBr	227
BrigInstCmp	228
BrigInstCvt	230

BrigInstImage	231
BrigInstLane	232
BrigInstMem	233
BrigInstMemFence	235
BrigInstMod	236
BrigInstQueryImage	238
BrigInstQuerySampler	239
BrigInstQueue	240
BrigInstSeg	241
BrigInstSegCvt	242
BrigInstSignal	243
BrigInstSourceType	244
BrigModuleHeader	246
BrigOperandAddress	247
BrigOperandAlign	249
BrigOperandCodeList	250
BrigOperandCodeRef	250
BrigOperandConstantBytes	251
BrigOperandConstantImage	252
BrigOperandConstantOperandList	254
BrigOperandConstantSampler	256
BrigOperandOperandList	257
BrigOperandRegister	258
BrigOperandString	259
BrigOperandWavesize	260
BrigSectionHeader	260
BrigUInt64	261
CoreApiTable	262
FinalizerExtTable	286
hsa_agent_dispatch_packet_s	
Agent dispatch packet	287
hsa_agent_s	
Struct containing an opaque handle to an agent, a device that participates in the HSA memory model. An agent can submit AQL packets for execution, and may also accept AQL packets for execution (agent dispatch packets or kernel dispatch packets launching HSAIL-derived binaries)	290
hsa_amd_barrier_value_packet_s	
AMD barrier value packet. Halts packet processing and waits for (signal_value & mask) cond value to be satisfied, where signal_value is the value of the signal signal	290
hsa_amd_event_s	
AMD GPU event data passed to event handler	293
hsa_amd_gpu_memory_fault_info_s	
AMD GPU memory fault event data	294
hsa_amd_hdp_flush_s	295
hsa_amd_image_descriptor_s	
Encodes an opaque vendor specific image format. The length of data depends on the underlying format. This structure must not be copied as its true length can not be determined	296
hsa_amd_ipc_memory_s	
256-bit process independent identifier for a ROCr shared memory allocation	297
hsa_amd_memory_pool_link_info_s	
Link properties when accessing the memory pool from the specified agent	298
hsa_amd_memory_pool_s	
A memory pool encapsulates physical storage on an agent along with a memory access model	300
hsa_amd_packet_header_s	
AMD vendor specific AQL packet header	301
hsa_amd_pointer_info_s	
Describes a memory allocation known to ROCr. Within a ROCr major version this structure can only grow	302

hsa_amd_profiling_async_copy_time_s	
Structure containing profiling async copy time information	304
hsa_amd_profiling_dispatch_time_s	
Structure containing profiling dispatch time information	305
hsa_amd_svm_attribute_pair_s	306
hsa_barrier_and_packet_s	
Barrier-AND packet	307
hsa_barrier_or_packet_s	
Barrier-OR packet	309
hsa_cache_s	
Cache handle	310
hsa_callback_data_s	
Application data handle that is passed to the serialization and deserialization functions	311
hsa_code_object_reader_s	
Code object reader handle. A code object reader is used to load a code object from file (when created using hsa_code_object_reader_create_from_file), or from memory (if created using hsa_code_object_reader_create_from_memory)	312
hsa_code_object_s	
Struct containing an opaque handle to a code object, which contains ISA for finalized kernels and indirect functions together with information about the global or readonly segment variables they reference	313
hsa_code_symbol_s	
Code object symbol handle	313
hsa_dim3_s	
Three-dimensional coordinate	314
hsa_executable_s	
Struct containing an opaque handle to an executable, which contains ISA for finalized kernels and indirect functions together with the allocated global or readonly segment variables they reference	315
hsa_executable_symbol_s	
Executable symbol handle	316
hsa_ext_amd_aql_pm4_packet_t	317
hsa_ext_control_directives_s	
Control directives specify low-level information about the finalization process	318
hsa_ext_finalizer_1_00_pfn_s	322
hsa_ext_image_data_info_s	
Agent specific image size and alignment requirements, populated by hsa_ext_image_data_get_info and hsa_ext_image_data_get_info_with_layout	323
hsa_ext_image_descriptor_s	
Implementation independent image descriptor	324
hsa_ext_image_format_s	
Image format	326
hsa_ext_image_region_s	
Image region	327
hsa_ext_image_s	
Image handle, populated by hsa_ext_image_create or hsa_ext_image_create_with_layout . Image handles are only unique within an agent, not across agents	328
hsa_ext_images_1_00_pfn_s	
The function pointer table for the images v1.00 extension. Can be returned by hsa_system_get_extension_table or hsa_system_get_major_extension_table	329
hsa_ext_images_1_pfn_s	
The function pointer table for the images v1 extension. Can be returned by hsa_system_get_extension_table or hsa_system_get_major_extension_table	332
hsa_ext_program_s	
An opaque handle to a HSAIL program, which groups a set of HSAIL modules that collectively define functions and variables used by kernels and indirect functions	335
hsa_ext_sampler_descriptor_s	
Implementation independent sampler descriptor	336

hsa_ext_sampler_s	
Sampler handle. Samplers are populated by hsa_ext_sampler_create . Sampler handles are only unique within an agent, not across agents	337
hsa_isa_s	
Instruction set architecture	338
hsa_kernel_dispatch_packet_s	
AQL kernel dispatch packet	339
hsa_loaded_code_object_s	
Loaded code object handle	343
hsa_pitched_ptr_s	343
hsa_queue_s	
User mode queue	344
hsa_region_s	
A memory region represents a block of virtual memory with certain properties. For example, the HSA runtime represents fine-grained memory in the global segment using a region. A region might be associated with more than one agent	347
hsa_signal_group_s	
Group of signals	347
hsa_signal_s	
Signal handle	348
hsa_ven_amd_aqlprofile_1_00_pfn_s	
Extension function table	349
hsa_ven_amd_aqlprofile_descriptor_t	352
hsa_ven_amd_aqlprofile_event_t	352
hsa_ven_amd_aqlprofile_id_query_t	353
hsa_ven_amd_aqlprofile_info_data_t	354
hsa_ven_amd_aqlprofile_parameter_t	355
hsa_ven_amd_aqlprofile_profile_t	356
hsa_ven_amd_loader_1_00_pfn_s	
Extension function table version 1.00	358
hsa_ven_amd_loader_1_01_pfn_s	
Extension function table version 1.01	359
hsa_ven_amd_loader_1_02_pfn_s	
Extension function table version 1.02	361
hsa_ven_amd_loader_1_03_pfn_s	
Extension function table version 1.03	363
hsa_ven_amd_loader_segment_descriptor_s	
Loaded memory segment descriptor	365
hsa_wavefront_s	
Wavefront handle	368
HsaApiTable	369
HsaApiTableContainer	370
ImageExtTable	372

Chapter 4

File Index

4.1 File List

Here is a list of all documented files with brief descriptions:

/home/alexv/Programming/ROCR-Runtime/include/amd_hsa_common.h	375
/home/alexv/Programming/ROCR-Runtime/include/amd_hsa_elf.h	376
/home/alexv/Programming/ROCR-Runtime/include/amd_hsa_kernel_code.h	381
/home/alexv/Programming/ROCR-Runtime/include/amd_hsa_queue.h	384
/home/alexv/Programming/ROCR-Runtime/include/amd_hsa_signal.h	385
/home/alexv/Programming/ROCR-Runtime/include/Brig.h	386
/home/alexv/Programming/ROCR-Runtime/include/hsa.h	399
/home/alexv/Programming/ROCR-Runtime/include/hsa_api_trace.h	416
/home/alexv/Programming/ROCR-Runtime/include/hsa_ext_amd.h	422
/home/alexv/Programming/ROCR-Runtime/include/hsa_ext_finalize.h	430
/home/alexv/Programming/ROCR-Runtime/include/hsa_ext_image.h	432
/home/alexv/Programming/ROCR-Runtime/include/hsa_ven_amd_aqlprofile.h	438
/home/alexv/Programming/ROCR-Runtime/include/hsa_ven_amd_loader.h	442

Chapter 5

Module Documentation

5.1 Runtime Notifications

Classes

- struct [hsa_dim3_s](#)
Three-dimensional coordinate.

Typedefs

- typedef struct [hsa_dim3_s](#) [hsa_dim3_t](#)
Three-dimensional coordinate.
- typedef int [hsa_file_t](#)
POSIX file descriptor.

Enumerations

- enum [hsa_status_t](#) {
 [HSA_STATUS_SUCCESS](#) = 0x0 , [HSA_STATUS_INFO_BREAK](#) = 0x1 , [HSA_STATUS_ERROR](#) = 0x1000 ,
 [HSA_STATUS_ERROR_INVALID_ARGUMENT](#) = 0x1001 ,
 [HSA_STATUS_ERROR_INVALID_QUEUE_CREATION](#) = 0x1002 , [HSA_STATUS_ERROR_INVALID_ALLOCATION](#)
 = 0x1003 , [HSA_STATUS_ERROR_INVALID_AGENT](#) = 0x1004 , [HSA_STATUS_ERROR_INVALID_REGION](#)
 = 0x1005 ,
 [HSA_STATUS_ERROR_INVALID_SIGNAL](#) = 0x1006 , [HSA_STATUS_ERROR_INVALID_QUEUE](#) = 0x1007 ,
 [HSA_STATUS_ERROR_OUT_OF_RESOURCES](#) = 0x1008 , [HSA_STATUS_ERROR_INVALID_PACKET_FORMAT](#)
 = 0x1009 ,
 [HSA_STATUS_ERROR_RESOURCE_FREE](#) = 0x100A , [HSA_STATUS_ERROR_NOT_INITIALIZED](#) =
 0x100B , [HSA_STATUS_ERROR_REFCOUNT_OVERFLOW](#) = 0x100C , [HSA_STATUS_ERROR_INCOMPATIBLE_ARGUMENT](#)
 = 0x100D ,
 [HSA_STATUS_ERROR_INVALID_INDEX](#) = 0x100E , [HSA_STATUS_ERROR_INVALID_ISA](#) = 0x100F ,
 [HSA_STATUS_ERROR_INVALID_ISA_NAME](#) = 0x1017 , [HSA_STATUS_ERROR_INVALID_CODE_OBJECT](#)
 = 0x1010 ,
 [HSA_STATUS_ERROR_INVALID_EXECUTABLE](#) = 0x1011 , [HSA_STATUS_ERROR_FROZEN_EXECUTABLE](#)
 = 0x1012 , [HSA_STATUS_ERROR_INVALID_SYMBOL_NAME](#) = 0x1013 , [HSA_STATUS_ERROR_VARIABLE_ALREADY_DEFINED](#)
 = 0x1014 ,
 [HSA_STATUS_ERROR_VARIABLE_UNDEFINED](#) = 0x1015 , [HSA_STATUS_ERROR_EXCEPTION](#) =
 0x1016 , [HSA_STATUS_ERROR_INVALID_CODE_SYMBOL](#) = 0x1018 , [HSA_STATUS_ERROR_INVALID_EXECUTABLE_SYMBOL](#)
}

```

= 0x1019 ,
HSA_STATUS_ERROR_INVALID_FILE = 0x1020 , HSA_STATUS_ERROR_INVALID_CODE_OBJECT_READER
= 0x1021 , HSA_STATUS_ERROR_INVALID_CACHE = 0x1022 , HSA_STATUS_ERROR_INVALID_WAVEFRONT
= 0x1023 ,
HSA_STATUS_ERROR_INVALID_SIGNAL_GROUP = 0x1024 , HSA_STATUS_ERROR_INVALID_RUNTIME_STATE
= 0x1025 , HSA_STATUS_ERROR_FATAL = 0x1026 }

```

Status codes.

- enum `hsa_access_permission_t` { `HSA_ACCESS_PERMISSION_RO` = 1 , `HSA_ACCESS_PERMISSION_WO` = 2 , `HSA_ACCESS_PERMISSION_RW` = 3 }

Access permissions.

Functions

- `hsa_status_t` HSA_API `hsa_status_string` (`hsa_status_t` status, const char **status_string)

Query additional information about a status code.

- `hsa_status_t` HSA_API `hsa_init` ()

Initialize the HSA runtime.

- `hsa_status_t` HSA_API `hsa_shut_down` ()

Shut down the HSA runtime.

5.1.1 Detailed Description

5.1.2 Typedef Documentation

5.1.2.1 `hsa_file_t`

```
typedef int hsa_file_t
```

POSIX file descriptor.

Definition at line 336 of file `hsa.h`.

5.1.3 Enumeration Type Documentation

5.1.3.1 `hsa_access_permission_t`

```
enum hsa_access_permission_t
```

Access permissions.

Enumerator

<code>HSA_ACCESS_PERMISSION_RO</code>	Read-only access.
<code>HSA_ACCESS_PERMISSION_WO</code>	Write-only access.
<code>HSA_ACCESS_PERMISSION_RW</code>	Read and write access.

Definition at line 318 of file [hsa.h](#).

5.1.3.2 hsa_status_t

enum [hsa_status_t](#)

Status codes.

Enumerator

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_INFO_BREAK	A traversal over a list of elements has been interrupted by the application before completing.
HSA_STATUS_ERROR	A generic error has occurred.
HSA_STATUS_ERROR_INVALID_ARGUMENT	One of the actual arguments does not meet a precondition stated in the documentation of the corresponding formal argument.
HSA_STATUS_ERROR_INVALID_QUEUE_CREATION	The requested queue creation is not valid.
HSA_STATUS_ERROR_INVALID_ALLOCATION	The requested allocation is not valid.
HSA_STATUS_ERROR_INVALID_AGENT	The agent is invalid.
HSA_STATUS_ERROR_INVALID_REGION	The memory region is invalid.
HSA_STATUS_ERROR_INVALID_SIGNAL	The signal is invalid.
HSA_STATUS_ERROR_INVALID_QUEUE	The queue is invalid.
HSA_STATUS_ERROR_OUT_OF_RESOURCES	The HSA runtime failed to allocate the necessary resources. This error may also occur when the HSA runtime needs to spawn threads or create internal OS-specific events.
HSA_STATUS_ERROR_INVALID_PACKET_FORMAT	The AQL packet is malformed.
HSA_STATUS_ERROR_RESOURCE_FREE	An error has been detected while releasing a resource.
HSA_STATUS_ERROR_NOT_INITIALIZED	An API other than hsa_init has been invoked while the reference count of the HSA runtime is 0.
HSA_STATUS_ERROR_REFCOUNT_OVERFLOW	The maximum reference count for the object has been reached.
HSA_STATUS_ERROR_INCOMPATIBLE_ARGUMENTS	The arguments passed to a functions are not compatible.
HSA_STATUS_ERROR_INVALID_INDEX	The index is invalid.
HSA_STATUS_ERROR_INVALID_ISA	The instruction set architecture is invalid.
HSA_STATUS_ERROR_INVALID_ISA_NAME	The instruction set architecture name is invalid.
HSA_STATUS_ERROR_INVALID_CODE_OBJECT	The code object is invalid.
HSA_STATUS_ERROR_INVALID_EXECUTABLE	The executable is invalid.
HSA_STATUS_ERROR_FROZEN_EXECUTABLE	The executable is frozen.
HSA_STATUS_ERROR_INVALID_SYMBOL_NAME	There is no symbol with the given name.
HSA_STATUS_ERROR_VARIABLE_ALREADY_DEFINED	The variable is already defined.
HSA_STATUS_ERROR_VARIABLE_UNDEFINED	The variable is undefined.
HSA_STATUS_ERROR_EXCEPTION	An HSAIL operation resulted in a hardware exception.
HSA_STATUS_ERROR_INVALID_CODE_SYMBOL	The code object symbol is invalid.

Enumerator

HSA_STATUS_ERROR_INVALID_EXECUTABLE_↔ SYMBOL	The executable symbol is invalid.
HSA_STATUS_ERROR_INVALID_FILE	The file descriptor is invalid.
HSA_STATUS_ERROR_INVALID_CODE_↔ OBJECT_READER	The code object reader is invalid.
HSA_STATUS_ERROR_INVALID_CACHE	The cache is invalid.
HSA_STATUS_ERROR_INVALID_WAVEFRONT	The wavefront is invalid.
HSA_STATUS_ERROR_INVALID_SIGNAL_GROUP	The signal group is invalid.
HSA_STATUS_ERROR_INVALID_RUNTIME_STATE	The HSA runtime is not in the configuration state.
HSA_STATUS_ERROR_FATAL	The queue received an error that may require process termination.

Definition at line 118 of file [hsa.h](#).

5.1.4 Function Documentation

5.1.4.1 hsa_init()

```
hsa_status_t HSA_API hsa_init ( )
```

Initialize the HSA runtime.

Initializes the HSA runtime if it is not already initialized, and increases the reference counter associated with the HSA runtime for the current process. Invocation of any HSA function other than [hsa_init](#) results in undefined behavior if the current HSA runtime reference counter is less than one.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_OUT_OF_RESOURCES	The HSA runtime failed to allocate the required resources.
HSA_STATUS_ERROR_REFCOUNT_OVERFLOW	The HSA runtime reference count reaches INT32_MAX.

5.1.4.2 hsa_shut_down()

```
hsa_status_t HSA_API hsa_shut_down ( )
```

Shut down the HSA runtime.

Decreases the reference count of the HSA runtime instance. When the reference count reaches 0, the HSA runtime is no longer considered valid but the application might call [hsa_init](#) to initialize the HSA runtime again.

Once the reference count of the HSA runtime reaches 0, all the resources associated with it (queues, signals, agent information, etc.) are considered invalid and any attempt to reference them in subsequent API calls results in undefined behavior. When the reference count reaches 0, the HSA runtime may release resources associated with it.

Return values

<code>HSA_STATUS_SUCCESS</code>	The function has been executed successfully.
<code>HSA_STATUS_ERROR_NOT_INITIALIZED</code>	The HSA runtime has not been initialized.

5.1.4.3 `hsa_status_string()`

```

hsa_status_t HSA_API hsa_status_string (
    hsa_status_t status,
    const char ** status_string )

```

Query additional information about a status code.

Parameters

in	<code>status</code>	Status code.
out	<code>status_string</code>	A NUL-terminated string that describes the error status.

Return values

<code>HSA_STATUS_SUCCESS</code>	The function has been executed successfully.
<code>HSA_STATUS_ERROR_NOT_INITIALIZED</code>	The HSA runtime has not been initialized.
<code>HSA_STATUS_ERROR_INVALID_ARGUMENT</code>	<code>status</code> is an invalid status code, or <code>status_string</code> is NULL.

5.2 System and Agent Information

Classes

- struct [`hsa_agent_s`](#)
Struct containing an opaque handle to an agent, a device that participates in the HSA memory model. An agent can submit AQL packets for execution, and may also accept AQL packets for execution (agent dispatch packets or kernel dispatch packets launching HSAIL-derived binaries).
- struct [`hsa_cache_s`](#)
Cache handle.

Typedefs

- typedef struct [`hsa_agent_s`](#) [`hsa_agent_t`](#)
Struct containing an opaque handle to an agent, a device that participates in the HSA memory model. An agent can submit AQL packets for execution, and may also accept AQL packets for execution (agent dispatch packets or kernel dispatch packets launching HSAIL-derived binaries).
- typedef struct [`hsa_cache_s`](#) [`hsa_cache_t`](#)
Cache handle.

Enumerations

- enum `hsa_endianness_t` { `HSA_ENDIANNESNESS_LITTLE` = 0 , `HSA_ENDIANNESNESS_BIG` = 1 }

Endianness. A convention used to interpret the bytes making up a data word.

- enum `hsa_machine_model_t` { `HSA_MACHINE_MODEL_SMALL` = 0 , `HSA_MACHINE_MODEL_LARGE` = 1 }

Machine model. A machine model determines the size of certain data types in HSA runtime and an agent.

- enum `hsa_profile_t` { `HSA_PROFILE_BASE` = 0 , `HSA_PROFILE_FULL` = 1 }

Profile. A profile indicates a particular level of feature support. For example, in the base profile the application must use the HSA runtime allocator to reserve shared virtual memory, while in the full profile any host pointer can be shared across all the agents.

- enum `hsa_system_info_t` {
`HSA_SYSTEM_INFO_VERSION_MAJOR` = 0 , `HSA_SYSTEM_INFO_VERSION_MINOR` = 1 , `HSA_SYSTEM_INFO_TIMESTAMP`
= 2 , `HSA_SYSTEM_INFO_TIMESTAMP_FREQUENCY` = 3 ,
`HSA_SYSTEM_INFO_SIGNAL_MAX_WAIT` = 4 , `HSA_SYSTEM_INFO_ENDIANNESNESS` = 5 , `HSA_SYSTEM_INFO_MACHINE_MODEL`
= 6 , `HSA_SYSTEM_INFO_EXTENSIONS` = 7 ,
`HSA_AMD_SYSTEM_INFO_BUILD_VERSION` = 0x200 , `HSA_AMD_SYSTEM_INFO_SVM_SUPPORTED`
= 0x201 , `HSA_AMD_SYSTEM_INFO_SVM_ACCESSIBLE_BY_DEFAULT` = 0x202 }

System attributes.

- enum `hsa_extension_t` {
`HSA_EXTENSION_FINALIZER` = 0 , `HSA_EXTENSION_IMAGES` = 1 , `HSA_EXTENSION_PERFORMANCE_COUNTERS`
= 2 , `HSA_EXTENSION_PROFILING_EVENTS` = 3 ,
`HSA_EXTENSION_STD_LAST` = 3 , `HSA_AMD_FIRST_EXTENSION` = 0x200 , `HSA_EXTENSION_AMD_PROFILER`
= 0x200 , `HSA_EXTENSION_AMD_LOADER` = 0x201 ,
`HSA_EXTENSION_AMD_AQLPROFILE` = 0x202 , `HSA_AMD_LAST_EXTENSION` = 0x202 }

HSA extensions.

- enum `hsa_agent_feature_t` { `HSA_AGENT_FEATURE_KERNEL_DISPATCH` = 1 , `HSA_AGENT_FEATURE_AGENT_DISPATCH`
= 2 }

Agent features.

- enum `hsa_device_type_t` { `HSA_DEVICE_TYPE_CPU` = 0 , `HSA_DEVICE_TYPE_GPU` = 1 , `HSA_DEVICE_TYPE_DSP`
= 2 }

Hardware device type.

- enum `hsa_default_float_rounding_mode_t` { `HSA_DEFAULT_FLOAT_ROUNDING_MODE_DEFAULT` = 0 ,
`HSA_DEFAULT_FLOAT_ROUNDING_MODE_ZERO` = 1 , `HSA_DEFAULT_FLOAT_ROUNDING_MODE_NEAR`
= 2 }

Default floating-point rounding mode.

- enum `hsa_agent_info_t` {
`HSA_AGENT_INFO_NAME` = 0 , `HSA_AGENT_INFO_VENDOR_NAME` = 1 , `HSA_AGENT_INFO_FEATURE`
= 2 , `HSA_AGENT_INFO_MACHINE_MODEL` = 3 ,
`HSA_AGENT_INFO_PROFILE` = 4 , `HSA_AGENT_INFO_DEFAULT_FLOAT_ROUNDING_MODE` = 5 ,
`HSA_AGENT_INFO_BASE_PROFILE_DEFAULT_FLOAT_ROUNDING_MODES` = 23 , `HSA_AGENT_INFO_FAST_F16_OPA`
= 24 ,
`HSA_AGENT_INFO_WAVEFRONT_SIZE` = 6 , `HSA_AGENT_INFO_WORKGROUP_MAX_DIM` = 7 ,
`HSA_AGENT_INFO_WORKGROUP_MAX_SIZE` = 8 , `HSA_AGENT_INFO_GRID_MAX_DIM` = 9 ,
`HSA_AGENT_INFO_GRID_MAX_SIZE` = 10 , `HSA_AGENT_INFO_FBARRIER_MAX_SIZE` = 11 ,
`HSA_AGENT_INFO_QUEUES_MAX` = 12 , `HSA_AGENT_INFO_QUEUE_MIN_SIZE` = 13 ,
`HSA_AGENT_INFO_QUEUE_MAX_SIZE` = 14 , `HSA_AGENT_INFO_QUEUE_TYPE` = 15 , `HSA_AGENT_INFO_NODE`
= 16 , `HSA_AGENT_INFO_DEVICE` = 17 ,
`HSA_AGENT_INFO_CACHE_SIZE` = 18 , `HSA_AGENT_INFO_ISA` = 19 , `HSA_AGENT_INFO_EXTENSIONS`
= 20 , `HSA_AGENT_INFO_VERSION_MAJOR` = 21 ,
`HSA_AGENT_INFO_VERSION_MINOR` = 22 , `HSA_AGENT_INFO_LAST` = INT32_MAX }

Agent attributes.

- enum `hsa_exception_policy_t` { `HSA_EXCEPTION_POLICY_BREAK` = 1 , `HSA_EXCEPTION_POLICY_DETECT`
= 2 }

Exception policies applied in the presence of hardware exceptions.

- enum `hsa_cache_info_t` { `HSA_CACHE_INFO_NAME_LENGTH` = 0 , `HSA_CACHE_INFO_NAME` = 1 , `HSA_CACHE_INFO_LEVEL` = 2 , `HSA_CACHE_INFO_SIZE` = 3 }

Cache attributes.

Functions

- `hsa_status_t` HSA_API `hsa_system_get_info` (`hsa_system_info_t` attribute, void *value)
Get the current value of a system attribute.
- `hsa_status_t` HSA_API `hsa_extension_get_name` (uint16_t extension, const char **name)
Query the name of a given extension.
- `hsa_status_t` HSA_API HSA_DEPRECATED `hsa_system_extension_supported` (uint16_t extension, uint16_t version_major, uint16_t version_minor, bool *result)
Query if a given version of an extension is supported by the HSA implementation.
- `hsa_status_t` HSA_API `hsa_system_major_extension_supported` (uint16_t extension, uint16_t version_major, uint16_t *version_minor, bool *result)
Query if a given version of an extension is supported by the HSA implementation. All minor versions from 0 up to the returned `version_minor` must be supported by the implementation.
- `hsa_status_t` HSA_API HSA_DEPRECATED `hsa_system_get_extension_table` (uint16_t extension, uint16_t version_major, uint16_t version_minor, void *table)
Retrieve the function pointers corresponding to a given version of an extension. Portable applications are expected to invoke the extension API using the returned function pointers.
- `hsa_status_t` HSA_API `hsa_system_get_major_extension_table` (uint16_t extension, uint16_t version_major, size_t table_length, void *table)
Retrieve the function pointers corresponding to a given major version of an extension. Portable applications are expected to invoke the extension API using the returned function pointers.
- `hsa_status_t` HSA_API `hsa_agent_get_info` (`hsa_agent_t` agent, `hsa_agent_info_t` attribute, void *value)
Get the current value of an attribute for a given agent.
- `hsa_status_t` HSA_API `hsa_iterate_agents` (`hsa_status_t`(*callback)(`hsa_agent_t` agent, void *data), void *data)
Iterate over the available agents, and invoke an application-defined callback on every iteration.
- `hsa_status_t` HSA_API HSA_DEPRECATED `hsa_agent_get_exception_policies` (`hsa_agent_t` agent, `hsa_profile_t` profile, uint16_t *mask)
Retrieve the exception policy support for a given combination of agent and profile.
- `hsa_status_t` HSA_API `hsa_cache_get_info` (`hsa_cache_t` cache, `hsa_cache_info_t` attribute, void *value)
Get the current value of an attribute for a given cache object.
- `hsa_status_t` HSA_API `hsa_agent_iterate_caches` (`hsa_agent_t` agent, `hsa_status_t`(*callback)(`hsa_cache_t` cache, void *data), void *data)
Iterate over the memory caches of a given agent, and invoke an application-defined callback on every iteration.
- `hsa_status_t` HSA_API HSA_DEPRECATED `hsa_agent_extension_supported` (uint16_t extension, `hsa_agent_t` agent, uint16_t version_major, uint16_t version_minor, bool *result)
Query if a given version of an extension is supported by an agent.
- `hsa_status_t` HSA_API `hsa_agent_major_extension_supported` (uint16_t extension, `hsa_agent_t` agent, uint16_t version_major, uint16_t *version_minor, bool *result)
Query if a given version of an extension is supported by an agent. All minor versions from 0 up to the returned `version_minor` must be supported.

5.2.1 Detailed Description

5.2.2 Enumeration Type Documentation

5.2.2.1 hsa_agent_feature_t

enum [hsa_agent_feature_t](#)

Agent features.

Enumerator

HSA_AGENT_FEATURE_KERNEL_DISPATCH	The agent supports AQL packets of kernel dispatch type. If this feature is enabled, the agent is also a kernel agent.
HSA_AGENT_FEATURE_AGENT_DISPATCH	The agent supports AQL packets of agent dispatch type.

Definition at line 752 of file [hsa.h](#).

5.2.2.2 hsa_agent_info_t

enum [hsa_agent_info_t](#)

Agent attributes.

Enumerator

HSA_AGENT_INFO_NAME	Agent name. The type of this attribute is a NUL-terminated char[64]. The name must be at most 63 characters long (not including the NUL terminator) and all array elements not used for the name must be NUL.
HSA_AGENT_INFO_VENDOR_NAME	Name of vendor. The type of this attribute is a NUL-terminated char[64]. The name must be at most 63 characters long (not including the NUL terminator) and all array elements not used for the name must be NUL.
HSA_AGENT_INFO_FEATURE	Agent capability. The type of this attribute is hsa_agent_feature_t .
HSA_AGENT_INFO_MACHINE_MODEL	<p>Deprecated Query HSA_ISA_INFO_MACHINE_MODELS for a given instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by hsa_agent_iterate_isas.</p> <p>Machine model supported by the agent. The type of this attribute is hsa_machine_model_t.</p>

Enumerator

HSA_AGENT_INFO_PROFILE	<p>Deprecated Query HSA_ISA_INFO_PROFILES for a given instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by hsa_agent_iterate_isas.</p> <p>Profile supported by the agent. The type of this attribute is hsa_profile_t.</p>
HSA_AGENT_INFO_DEFAULT_FLOAT_ROUNDING_MODE↔	<p>Deprecated Query HSA_ISA_INFO_DEFAULT_FLOAT_ROUNDING_MODES for a given instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by hsa_agent_iterate_isas.</p> <p>Default floating-point rounding mode. The type of this attribute is hsa_default_float_rounding_mode_t, but the value HSA_DEFAULT_FLOAT_ROUNDING_MODE_DEFAULT is not allowed.</p>
HSA_AGENT_INFO_BASE_PROFILE_DEFAULT_FLOAT_ROUNDING_MODES↔	<p>Deprecated Query HSA_ISA_INFO_BASE_PROFILE_DEFAULT_FLOAT_ROUNDING_MODES for a given instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by hsa_agent_iterate_isas.</p> <p>A bit-mask of hsa_default_float_rounding_mode_t values, representing the default floating-point rounding modes supported by the agent in the Base profile. The type of this attribute is uint32_t. The default floating-point rounding mode (HSA_AGENT_INFO_DEFAULT_FLOAT_ROUNDING_MODE) bit must not be set.</p>

Enumerator

HSA_AGENT_INFO_FAST_F16_OPERATION	<p>Deprecated Query HSA_ISA_INFO_FAST_F16_OPERATION for a given intruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by hsa_agent_iterate_isas.</p> <p>Flag indicating that the f16 HSAIL operation is at least as fast as the f32 operation in the current agent. The value of this attribute is undefined if the agent is not a kernel agent. The type of this attribute is bool.</p>
HSA_AGENT_INFO_WAVEFRONT_SIZE	<p>Deprecated Query HSA_WAVEFRONT_INFO_SIZE for a given wavefront and intruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by hsa_agent_iterate_isas and the first wavefront enumerated by hsa_isa_iterate_wavefronts for that ISA.</p> <p>Number of work-items in a wavefront. Must be a power of 2 in the range [1,256]. The value of this attribute is undefined if the agent is not a kernel agent. The type of this attribute is uint32_t.</p>
HSA_AGENT_INFO_WORKGROUP_MAX_DIM	<p>Deprecated Query HSA_ISA_INFO_WORKGROUP_MAX_DIM for a given intruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by hsa_agent_iterate_isas.</p> <p>Maximum number of work-items of each dimension of a work-group. Each maximum must be greater than 0. No maximum can exceed the value of HSA_AGENT_INFO_WORKGROUP_MAX_SIZE. The value of this attribute is undefined if the agent is not a kernel agent. The type of this attribute is uint16_t[3].</p>

Enumerator

HSA_AGENT_INFO_WORKGROUP_MAX_SIZE	<p>Deprecated Query HSA_ISA_INFO_WORKGROUP_MAX_SIZE for a given instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by hsa_agent_iterate_isas.</p> <p>Maximum total number of work-items in a work-group. The value of this attribute is undefined if the agent is not a kernel agent. The type of this attribute is <code>uint32_t</code>.</p>
HSA_AGENT_INFO_GRID_MAX_DIM	<p>Deprecated Query HSA_ISA_INFO_GRID_MAX_DIM for a given instruction set architecture supported by the agent instead.</p> <p>Maximum number of work-items of each dimension of a grid. Each maximum must be greater than 0, and must not be smaller than the corresponding value in HSA_AGENT_INFO_WORKGROUP_MAX_DIM. No maximum can exceed the value of HSA_AGENT_INFO_GRID_MAX_SIZE. The value of this attribute is undefined if the agent is not a kernel agent. The type of this attribute is hsa_dim3_t.</p>
HSA_AGENT_INFO_GRID_MAX_SIZE	<p>Deprecated Query HSA_ISA_INFO_GRID_MAX_SIZE for a given instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by hsa_agent_iterate_isas.</p> <p>Maximum total number of work-items in a grid. The value of this attribute is undefined if the agent is not a kernel agent. The type of this attribute is <code>uint32_t</code>.</p>
HSA_AGENT_INFO_FBARRIER_MAX_SIZE	<p>Deprecated Query HSA_ISA_INFO_FBARRIER_MAX_SIZE for a given instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by hsa_agent_iterate_isas.</p> <p>Maximum number of fbarriers per work-group. Must be at least 32. The value of this attribute is undefined if the agent is not a kernel agent. The type of this attribute is <code>uint32_t</code>.</p>

Enumerator

HSA_AGENT_INFO_QUEUES_MAX	<p>Deprecated The maximum number of queues is not statically determined.</p> <p>Maximum number of queues that can be active (created but not destroyed) at one time in the agent. The type of this attribute is <code>uint32_t</code>.</p>
HSA_AGENT_INFO_QUEUE_MIN_SIZE	<p>Minimum number of packets that a queue created in the agent can hold. Must be a power of 2 greater than 0. Must not exceed the value of <code>HSA_AGENT_INFO_QUEUE_MAX_SIZE</code>. The type of this attribute is <code>uint32_t</code>.</p>
HSA_AGENT_INFO_QUEUE_MAX_SIZE	<p>Maximum number of packets that a queue created in the agent can hold. Must be a power of 2 greater than 0. The type of this attribute is <code>uint32_t</code>.</p>
HSA_AGENT_INFO_QUEUE_TYPE	<p>Type of a queue created in the agent. The type of this attribute is <code>hsa_queue_type32_t</code>.</p>
HSA_AGENT_INFO_NODE	<p>Deprecated NUMA information is not exposed anywhere else in the API.</p> <p>Identifier of the NUMA node associated with the agent. The type of this attribute is <code>uint32_t</code>.</p>
HSA_AGENT_INFO_DEVICE	<p>Type of hardware device associated with the agent. The type of this attribute is <code>hsa_device_type_t</code>.</p>
HSA_AGENT_INFO_CACHE_SIZE	<p>Deprecated Query <code>hsa_agent_iterate_caches</code> to retrieve information about the caches present in a given agent.</p> <p>Array of data cache sizes (L1..L4). Each size is expressed in bytes. A size of 0 for a particular level indicates that there is no cache information for that level. The type of this attribute is <code>uint32_t[4]</code>.</p>
HSA_AGENT_INFO_ISA	<p>Deprecated An agent may support multiple instruction set architectures. See <code>hsa_agent_iterate_isas</code>. If more than one ISA is supported by the agent, the returned value corresponds to the first ISA enumerated by <code>hsa_agent_iterate_isas</code>.</p> <p>Instruction set architecture of the agent. The type of this attribute is <code>hsa_isa_t</code>.</p>
HSA_AGENT_INFO_EXTENSIONS	<p>Bit-mask indicating which extensions are supported by the agent. An extension with an ID of <code>i</code> is supported if the bit at position <code>i</code> is set. The type of this attribute is <code>uint8_t[128]</code>.</p>
HSA_AGENT_INFO_VERSION_MAJOR	<p>Major version of the HSA runtime specification supported by the agent. The type of this attribute is <code>uint16_t</code>.</p>

Enumerator

HSA_AGENT_INFO_VERSION_MINOR	Minor version of the HSA runtime specification supported by the agent. The type of this attribute is <code>uint16_t</code> .
HSA_AGENT_INFO_LAST	This enum does not have a fixed underlying type, thus in C++ post D2338: If the enumeration type does not have a fixed underlying type, the value is unchanged if the original value is within the range of the enumeration values (9.7.1 [dcl.enum]), and otherwise, the behavior is undefined. Thus increase the range of this enum to encompass vendor extensions.

Definition at line 806 of file [hsa.h](#).

5.2.2.3 `hsa_cache_info_t`

```
enum hsa_cache_info_t
```

Cache attributes.

Enumerator

HSA_CACHE_INFO_NAME_LENGTH	The length of the cache name in bytes, not including the NUL terminator. The type of this attribute is <code>uint32_t</code> .
HSA_CACHE_INFO_NAME	Human-readable description. The type of this attribute is a NUL-terminated character array with the length equal to the value of HSA_CACHE_INFO_NAME_LENGTH attribute.
HSA_CACHE_INFO_LEVEL	Cache level. A L1 cache must return a value of 1, a L2 must return a value of 2, and so on. The type of this attribute is <code>uint8_t</code> .
HSA_CACHE_INFO_SIZE	Cache size, in bytes. A value of 0 indicates that there is no size information available. The type of this attribute is <code>uint32_t</code> .

Definition at line 1160 of file [hsa.h](#).

5.2.2.4 `hsa_default_float_rounding_mode_t`

```
enum hsa_default_float_rounding_mode_t
```

Default floating-point rounding mode.

Enumerator

HSA_DEFAULT_FLOAT_ROUNDING_MODE_DEFAULT	Use a default floating-point rounding mode specified elsewhere.
HSA_DEFAULT_FLOAT_ROUNDING_MODE_ZERO	Operations that specify the default floating-point mode are rounded to zero by default.
HSA_DEFAULT_FLOAT_ROUNDING_MODE_NEAR	Operations that specify the default floating-point mode are rounded to the nearest representable number and that ties should be broken by selecting the value with an even least significant bit.
Generated by Doxygen	

Definition at line 785 of file [hsa.h](#).

5.2.2.5 hsa_device_type_t

enum [hsa_device_type_t](#)

Hardware device type.

Enumerator

HSA_DEVICE_TYPE_CPU	CPU device.
HSA_DEVICE_TYPE_GPU	GPU device.
HSA_DEVICE_TYPE_DSP	DSP device.

Definition at line 767 of file [hsa.h](#).

5.2.2.6 hsa_endianness_t

enum [hsa_endianness_t](#)

Endianness. A convention used to interpret the bytes making up a data word.

Enumerator

HSA_ENDIANNESS_LITTLE	The least significant byte is stored in the smallest address.
HSA_ENDIANNESS_BIG	The most significant byte is stored in the smallest address.

Definition at line 398 of file [hsa.h](#).

5.2.2.7 hsa_exception_policy_t

enum [hsa_exception_policy_t](#)

Exception policies applied in the presence of hardware exceptions.

Enumerator

HSA_EXCEPTION_POLICY_BREAK	If a hardware exception is detected, a work-item signals an exception.
HSA_EXCEPTION_POLICY_DETECT	If a hardware exception is detected, a hardware status bit is set.

Definition at line 1103 of file [hsa.h](#).

5.2.2.8 hsa_extension_t

enum [hsa_extension_t](#)

HSA extensions.

Enumerator

HSA_EXTENSION_FINALIZER	Finalizer extension.
HSA_EXTENSION_IMAGES	Images extension.
HSA_EXTENSION_PERFORMANCE_COUNTERS	Performance counter extension.
HSA_EXTENSION_PROFILING_EVENTS	Profiling events extension.
HSA_EXTENSION_STD_LAST	Extension count.
HSA_AMD_FIRST_EXTENSION	First AMD extension number.
HSA_EXTENSION_AMD_PROFILER	Profiler extension.
HSA_EXTENSION_AMD_LOADER	Loader extension.
HSA_EXTENSION_AMD_AQLPROFILE	AqlProfile extension.
HSA_AMD_LAST_EXTENSION	Last AMD extension.

Definition at line [529](#) of file [hsa.h](#).

5.2.2.9 hsa_machine_model_t

enum [hsa_machine_model_t](#)

Machine model. A machine model determines the size of certain data types in HSA runtime and an agent.

Enumerator

HSA_MACHINE_MODEL_SMALL	Small machine model. Addresses use 32 bits.
HSA_MACHINE_MODEL_LARGE	Large machine model. Addresses use 64 bits.

Definition at line [413](#) of file [hsa.h](#).

5.2.2.10 hsa_profile_t

enum [hsa_profile_t](#)

Profile. A profile indicates a particular level of feature support. For example, in the base profile the application must use the HSA runtime allocator to reserve shared virtual memory, while in the full profile any host pointer can be shared across all the agents.

Enumerator

HSA_PROFILE_BASE	Base profile.
HSA_PROFILE_FULL	Full profile.

Definition at line 430 of file [hsa.h](#).

5.2.2.11 hsa_system_info_t

enum [hsa_system_info_t](#)

System attributes.

Enumerator

HSA_SYSTEM_INFO_VERSION_MAJOR	Major version of the HSA runtime specification supported by the implementation. The type of this attribute is uint16_t .
HSA_SYSTEM_INFO_VERSION_MINOR	Minor version of the HSA runtime specification supported by the implementation. The type of this attribute is uint16_t .
HSA_SYSTEM_INFO_TIMESTAMP	Current timestamp. The value of this attribute monotonically increases at a constant rate. The type of this attribute is uint64_t .
HSA_SYSTEM_INFO_TIMESTAMP_FREQUENCY	Timestamp value increase rate, in Hz. The timestamp (clock) frequency is in the range 1-400MHz. The type of this attribute is uint64_t .
HSA_SYSTEM_INFO_SIGNAL_MAX_WAIT	Maximum duration of a signal wait operation. Expressed as a count based on the timestamp frequency. The type of this attribute is uint64_t .
HSA_SYSTEM_INFO_ENDIANNESS	Endianness of the system. The type of this attribute is hsa_endianness_t .
HSA_SYSTEM_INFO_MACHINE_MODEL	Machine model supported by the HSA runtime. The type of this attribute is hsa_machine_model_t .
HSA_SYSTEM_INFO_EXTENSIONS	Bit-mask indicating which extensions are supported by the implementation. An extension with an ID of <i>i</i> is supported if the bit at position <i>i</i> is set. The type of this attribute is uint8_t[128] .
HSA_AMD_SYSTEM_INFO_BUILD_VERSION	String containing the ROCr build identifier.
HSA_AMD_SYSTEM_INFO_SVM_SUPPORTED	Returns true if <code>hsa_amd_svm_*</code> APIs are supported by the driver. The type of this attribute is bool .
HSA_AMD_SYSTEM_INFO_SVM_ACCESSIBLE_↔ BY_DEFAULT	Returns true if all Agents have access to system allocated memory (such as that allocated by <code>mmap</code> , <code>malloc</code> , or <code>new</code>) by default. If false then system allocated memory may only be made SVM accessible to an Agent by declaration of accessibility with <code>hsa_amd_svm_set_attributes</code> . The type of this attribute is bool .

Definition at line 444 of file [hsa.h](#).

5.2.3 Function Documentation

5.2.3.1 hsa_agent_extension_supported()

```
hsa_status_t HSA_API HSA_DEPRECATED hsa_agent_extension_supported (
    uint16_t extension,
    hsa_agent_t agent,
    uint16_t version_major,
    uint16_t version_minor,
    bool * result )
```

Query if a given version of an extension is supported by an agent.

Deprecated

Parameters

in	<i>extension</i>	Extension identifier.
in	<i>agent</i>	Agent.
in	<i>version_major</i>	Major version number.
in	<i>version_minor</i>	Minor version number.
out	<i>result</i>	Pointer to a memory location where the HSA runtime stores the result of the check. The result is true if the specified version of the extension is supported, and false otherwise. The result must be false if hsa_system_extension_supported returns false for the same extension version.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_AGENT	The agent is invalid.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>extension</i> is not a valid extension, or <i>result</i> is NULL.

5.2.3.2 hsa_agent_get_exception_policies()

```
hsa_status_t HSA_API HSA_DEPRECATED hsa_agent_get_exception_policies (
    hsa_agent_t agent,
    hsa_profile_t profile,
    uint16_t * mask )
```

Retrieve the exception policy support for a given combination of agent and profile.

Deprecated Use [hsa_isa_get_exception_policies](#) for a given instruction set architecture supported by the agent instead. If more than one ISA is supported by the agent, this function uses the first value returned by [hsa_agent_iterate_isas](#).

Parameters

in	<i>agent</i>	Agent.
in	<i>profile</i>	Profile.
out	<i>mask</i>	Pointer to a memory location where the HSA runtime stores a mask of hsa_exception_policy_t values. Must not be NULL.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_AGENT	The agent is invalid.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>profile</i> is not a valid profile, or <i>mask</i> is NULL.

5.2.3.3 `hsa_agent_get_info()`

```

hsa_status_t HSA_API hsa_agent_get_info (
    hsa_agent_t agent,
    hsa_agent_info_t attribute,
    void * value )

```

Get the current value of an attribute for a given agent.

Parameters

in	<i>agent</i>	A valid agent.
in	<i>attribute</i>	Attribute to query.
out	<i>value</i>	Pointer to an application-allocated buffer where to store the value of the attribute. If the buffer passed by the application is not large enough to hold the value of <i>attribute</i> , the behavior is undefined.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_AGENT	The agent is invalid.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>attribute</i> is an invalid agent attribute, or <i>value</i> is NULL.

5.2.3.4 `hsa_agent_iterate_caches()`

```

hsa_status_t HSA_API hsa_agent_iterate_caches (
    hsa_agent_t agent,
    hsa_status_t (*)(hsa_cache_t cache, void *data) callback,
    void * data )

```

Iterate over the memory caches of a given agent, and invoke an application-defined callback on every iteration.

Caches are visited in ascending order according to the value of the [HSA_CACHE_INFO_LEVEL](#) attribute.

Parameters

in	<i>agent</i>	A valid agent.
in	<i>callback</i>	Callback to be invoked once per cache that is present in the agent. The HSA runtime passes two arguments to the callback: the cache and the application data. If <i>callback</i> returns a status other than HSA_STATUS_SUCCESS for a particular iteration, the traversal stops and that value is returned.
in	<i>data</i>	Application data that is passed to <i>callback</i> on every iteration. May be NULL.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_AGENT	The agent is invalid.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>callback</i> is NULL.

5.2.3.5 hsa_agent_major_extension_supported()

```

hsa_status_t HSA_API hsa_agent_major_extension_supported (
    uint16_t extension,
    hsa_agent_t agent,
    uint16_t version_major,
    uint16_t * version_minor,
    bool * result )

```

Query if a given version of an extension is supported by an agent. All minor versions from 0 up to the returned *version_minor* must be supported.

Parameters

in	<i>extension</i>	Extension identifier.
in	<i>agent</i>	Agent.
in	<i>version_major</i>	Major version number.
out	<i>version_minor</i>	Minor version number.
out	<i>result</i>	Pointer to a memory location where the HSA runtime stores the result of the check. The result is true if the specified version of the extension is supported, and false otherwise. The result must be false if hsa_system_extension_supported returns false for the same extension version.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_AGENT	The agent is invalid.

Return values

<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	extension is not a valid extension, or version_minor is NULL, or result is NULL.
--	--

5.2.3.6 hsa_cache_get_info()

```

hsa_status_t HSA_API hsa_cache_get_info (
    hsa_cache_t cache,
    hsa_cache_info_t attribute,
    void * value )

```

Get the current value of an attribute for a given cache object.

Parameters

in	<i>cache</i>	Cache.
in	<i>attribute</i>	Attribute to query.
out	<i>value</i>	Pointer to an application-allocated buffer where to store the value of the attribute. If the buffer passed by the application is not large enough to hold the value of <i>attribute</i> , the behavior is undefined.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_CACHE</i>	The cache is invalid.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>attribute</i> is an invalid instruction set architecture attribute, or <i>value</i> is NULL.

5.2.3.7 hsa_extension_get_name()

```

hsa_status_t HSA_API hsa_extension_get_name (
    uint16_t extension,
    const char ** name )

```

Query the name of a given extension.

Parameters

in	<i>extension</i>	Extension identifier. If the extension is not supported by the implementation (see <i>HSA_SYSTEM_INFO_EXTENSIONS</i>), the behavior is undefined.
out	<i>name</i>	Pointer to a memory location where the HSA runtime stores the extension name. The extension name is a NUL-terminated string.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<code>extension</code> is not a valid extension, or <code>name</code> is NULL.

5.2.3.8 `hsa_iterate_agents()`

```
hsa_status_t HSA_API hsa_iterate_agents (
    hsa_status_t(*) (hsa_agent_t agent, void *data) callback,
    void * data )
```

Iterate over the available agents, and invoke an application-defined callback on every iteration.

Parameters

in	<i>callback</i>	Callback to be invoked once per agent. The HSA runtime passes two arguments to the callback: the agent and the application data. If <code>callback</code> returns a status other than <i>HSA_STATUS_SUCCESS</i> for a particular iteration, the traversal stops and <code>hsa_iterate_agents</code> returns that status value.
in	<i>data</i>	Application data that is passed to <code>callback</code> on every iteration. May be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<code>callback</code> is NULL.

5.2.3.9 `hsa_system_extension_supported()`

```
hsa_status_t HSA_API HSA_DEPRECATED hsa_system_extension_supported (
    uint16_t extension,
    uint16_t version_major,
    uint16_t version_minor,
    bool * result )
```

Query if a given version of an extension is supported by the HSA implementation.

Deprecated

Parameters

in	<i>extension</i>	Extension identifier.
in	<i>version_major</i>	Major version number.
in	<i>version_minor</i>	Minor version number.
Generated by <code>hsa_iterate_agents</code>	<i>result</i>	Pointer to a memory location where the HSA runtime stores the result of the check. The result is true if the specified version of the extension is supported, and false otherwise.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<code>extension</code> is not a valid extension, or <code>result</code> is NULL.

5.2.3.10 `hsa_system_get_extension_table()`

```

hsa_status_t HSA_API HSA_DEPRECATED hsa_system_get_extension_table (
    uint16_t extension,
    uint16_t version_major,
    uint16_t version_minor,
    void * table )

```

Retrieve the function pointers corresponding to a given version of an extension. Portable applications are expected to invoke the extension API using the returned function pointers.

Deprecated

The application is responsible for verifying that the given version of the extension is supported by the HSA implementation (see [hsa_system_extension_supported](#)). If the given combination of extension, major version, and minor version is not supported by the implementation, the behavior is undefined.

Parameters

in	<i>extension</i>	Extension identifier.
in	<i>version_major</i>	Major version number for which to retrieve the function pointer table.
in	<i>version_minor</i>	Minor version number for which to retrieve the function pointer table.
out	<i>table</i>	Pointer to an application-allocated function pointer table that is populated by the HSA runtime. Must not be NULL. The memory associated with <code>table</code> can be reused or freed after the function returns.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<code>extension</code> is not a valid extension, or <code>table</code> is NULL.

5.2.3.11 `hsa_system_get_info()`

```

hsa_status_t HSA_API hsa_system_get_info (
    hsa_system_info_t attribute,
    void * value )

```

Get the current value of a system attribute.

Parameters

in	<i>attribute</i>	Attribute to query.
out	<i>value</i>	Pointer to an application-allocated buffer where to store the value of the attribute. If the buffer passed by the application is not large enough to hold the value of <i>attribute</i> , the behavior is undefined.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>attribute</i> is an invalid system attribute, or <i>value</i> is NULL.

5.2.3.12 `hsa_system_get_major_extension_table()`

```

hsa_status_t HSA_API hsa_system_get_major_extension_table (
    uint16_t extension,
    uint16_t version_major,
    size_t table_length,
    void * table )

```

Retrieve the function pointers corresponding to a given major version of an extension. Portable applications are expected to invoke the extension API using the returned function pointers.

The application is responsible for verifying that the given major version of the extension is supported by the HSA implementation (see [hsa_system_major_extension_supported](#)). If the given combination of extension and major version is not supported by the implementation, the behavior is undefined. Additionally if the length doesn't allow space for a full minor version, it is implementation defined if only some of the function pointers for that minor version get written.

Parameters

in	<i>extension</i>	Extension identifier.
in	<i>version_major</i>	Major version number for which to retrieve the function pointer table.
in	<i>table_length</i>	Size in bytes of the function pointer table to be populated. The implementation will not write more than this many bytes to the table.
out	<i>table</i>	Pointer to an application-allocated function pointer table that is populated by the HSA runtime. Must not be NULL. The memory associated with <i>table</i> can be reused or freed after the function returns.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>extension</i> is not a valid extension, or <i>table</i> is NULL.

5.2.3.13 hsa_system_major_extension_supported()

```

hsa_status_t HSA_API hsa_system_major_extension_supported (
    uint16_t extension,
    uint16_t version_major,
    uint16_t * version_minor,
    bool * result )

```

Query if a given version of an extension is supported by the HSA implementation. All minor versions from 0 up to the returned `version_minor` must be supported by the implementation.

Parameters

in	<i>extension</i>	Extension identifier.
in	<i>version_major</i>	Major version number.
out	<i>version_minor</i>	Minor version number.
out	<i>result</i>	Pointer to a memory location where the HSA runtime stores the result of the check. The result is true if the specified version of the extension is supported, and false otherwise.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>extension</i> is not a valid extension, or <i>version_minor</i> is NULL, or <i>result</i> is NULL.

5.3 Signals

Classes

- struct [*hsa_signal_s*](#)
Signal handle.
- struct [*hsa_signal_group_s*](#)
Group of signals.

Typedefs

- typedef struct [*hsa_signal_s*](#) **hsa_signal_t**
Signal handle.
- typedef int32_t [*hsa_signal_value_t*](#)
Signal value. The value occupies 32 bits in small machine mode, and 64 bits in large machine mode.
- typedef struct [*hsa_signal_group_s*](#) **hsa_signal_group_t**
Group of signals.

Enumerations

- enum `hsa_signal_condition_t` { `HSA_SIGNAL_CONDITION_EQ` = 0 , `HSA_SIGNAL_CONDITION_NE` = 1 , `HSA_SIGNAL_CONDITION_LT` = 2 , `HSA_SIGNAL_CONDITION_GTE` = 3 }
- Wait condition operator.*
- enum `hsa_wait_state_t` { `HSA_WAIT_STATE_BLOCKED` = 0 , `HSA_WAIT_STATE_ACTIVE` = 1 }
- State of the application thread during a signal wait.*

Functions

- `hsa_status_t` HSA_API `hsa_signal_create` (`hsa_signal_value_t` initial_value, `uint32_t` num_consumers, `const hsa_agent_t` *consumers, `hsa_signal_t` *signal)
- Create a signal.*
- `hsa_status_t` HSA_API `hsa_signal_destroy` (`hsa_signal_t` signal)
- Destroy a signal previous created by `hsa_signal_create`.*
- `hsa_signal_value_t` HSA_API `hsa_signal_load_scacquire` (`hsa_signal_t` signal)
- Atomically read the current value of a signal.*
- `hsa_signal_value_t` HSA_API `hsa_signal_load_relaxed` (`hsa_signal_t` signal)
- Atomically read the current value of a signal.*
- `hsa_signal_value_t` HSA_API HSA_DEPRECATED `hsa_signal_load_acquire` (`hsa_signal_t` signal)
- `void` HSA_API `hsa_signal_store_relaxed` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- Atomically set the value of a signal.*
- `void` HSA_API `hsa_signal_store_screlease` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- Atomically set the value of a signal.*
- `void` HSA_API HSA_DEPRECATED `hsa_signal_store_release` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- `void` HSA_API `hsa_signal_silent_store_relaxed` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- Atomically set the value of a signal without necessarily notifying the the agents waiting on it.*
- `void` HSA_API `hsa_signal_silent_store_screlease` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- Atomically set the value of a signal without necessarily notifying the the agents waiting on it.*
- `hsa_signal_value_t` HSA_API `hsa_signal_exchange_scacq_screl` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- Atomically set the value of a signal and return its previous value.*
- `hsa_signal_value_t` HSA_API HSA_DEPRECATED `hsa_signal_exchange_acq_rel` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- `hsa_signal_value_t` HSA_API `hsa_signal_exchange_scacquire` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- Atomically set the value of a signal and return its previous value.*
- `hsa_signal_value_t` HSA_API HSA_DEPRECATED `hsa_signal_exchange_acquire` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- `hsa_signal_value_t` HSA_API `hsa_signal_exchange_relaxed` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- Atomically set the value of a signal and return its previous value.*
- `hsa_signal_value_t` HSA_API `hsa_signal_exchange_screlease` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- Atomically set the value of a signal and return its previous value.*
- `hsa_signal_value_t` HSA_API HSA_DEPRECATED `hsa_signal_exchange_release` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- `hsa_signal_value_t` HSA_API `hsa_signal_cas_scacq_screl` (`hsa_signal_t` signal, `hsa_signal_value_t` expected, `hsa_signal_value_t` value)
- Atomically set the value of a signal if the observed value is equal to the expected value. The observed value is returned regardless of whether the replacement was done.*

- `hsa_signal_value_t` HSA_API HSA_DEPRECATED `hsa_signal_cas_acq_rel` (`hsa_signal_t` signal, `hsa_signal_value_t` expected, `hsa_signal_value_t` value)
- `hsa_signal_value_t` HSA_API `hsa_signal_cas_scacquire` (`hsa_signal_t` signal, `hsa_signal_value_t` expected, `hsa_signal_value_t` value)

Atomically set the value of a signal if the observed value is equal to the expected value. The observed value is returned regardless of whether the replacement was done.

- `hsa_signal_value_t` HSA_API HSA_DEPRECATED `hsa_signal_cas_acquire` (`hsa_signal_t` signal, `hsa_signal_value_t` expected, `hsa_signal_value_t` value)
- `hsa_signal_value_t` HSA_API `hsa_signal_cas_relaxed` (`hsa_signal_t` signal, `hsa_signal_value_t` expected, `hsa_signal_value_t` value)

Atomically set the value of a signal if the observed value is equal to the expected value. The observed value is returned regardless of whether the replacement was done.

- `hsa_signal_value_t` HSA_API `hsa_signal_cas_screlease` (`hsa_signal_t` signal, `hsa_signal_value_t` expected, `hsa_signal_value_t` value)

Atomically set the value of a signal if the observed value is equal to the expected value. The observed value is returned regardless of whether the replacement was done.

- `hsa_signal_value_t` HSA_API HSA_DEPRECATED `hsa_signal_cas_release` (`hsa_signal_t` signal, `hsa_signal_value_t` expected, `hsa_signal_value_t` value)
- void HSA_API `hsa_signal_add_scacq_screl` (`hsa_signal_t` signal, `hsa_signal_value_t` value)

Atomically increment the value of a signal by a given amount.

- void HSA_API HSA_DEPRECATED `hsa_signal_add_acq_rel` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- void HSA_API `hsa_signal_add_scacquire` (`hsa_signal_t` signal, `hsa_signal_value_t` value)

Atomically increment the value of a signal by a given amount.

- void HSA_API HSA_DEPRECATED `hsa_signal_add_acquire` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- void HSA_API `hsa_signal_add_relaxed` (`hsa_signal_t` signal, `hsa_signal_value_t` value)

Atomically increment the value of a signal by a given amount.

- void HSA_API `hsa_signal_add_screlease` (`hsa_signal_t` signal, `hsa_signal_value_t` value)

Atomically increment the value of a signal by a given amount.

- void HSA_API HSA_DEPRECATED `hsa_signal_add_release` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- void HSA_API `hsa_signal_subtract_scacq_screl` (`hsa_signal_t` signal, `hsa_signal_value_t` value)

Atomically decrement the value of a signal by a given amount.

- void HSA_API HSA_DEPRECATED `hsa_signal_subtract_acq_rel` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- void HSA_API `hsa_signal_subtract_scacquire` (`hsa_signal_t` signal, `hsa_signal_value_t` value)

Atomically decrement the value of a signal by a given amount.

- void HSA_API HSA_DEPRECATED `hsa_signal_subtract_acquire` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- void HSA_API `hsa_signal_subtract_relaxed` (`hsa_signal_t` signal, `hsa_signal_value_t` value)

Atomically decrement the value of a signal by a given amount.

- void HSA_API `hsa_signal_subtract_screlease` (`hsa_signal_t` signal, `hsa_signal_value_t` value)

Atomically decrement the value of a signal by a given amount.

- void HSA_API HSA_DEPRECATED `hsa_signal_subtract_release` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- void HSA_API `hsa_signal_and_scacq_screl` (`hsa_signal_t` signal, `hsa_signal_value_t` value)

Atomically perform a bitwise AND operation between the value of a signal and a given value.

- void HSA_API HSA_DEPRECATED `hsa_signal_and_acq_rel` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- void HSA_API `hsa_signal_and_scacquire` (`hsa_signal_t` signal, `hsa_signal_value_t` value)

Atomically perform a bitwise AND operation between the value of a signal and a given value.

- void HSA_API HSA_DEPRECATED `hsa_signal_and_acquire` (`hsa_signal_t` signal, `hsa_signal_value_t` value)
- void HSA_API `hsa_signal_and_relaxed` (`hsa_signal_t` signal, `hsa_signal_value_t` value)

Atomically perform a bitwise AND operation between the value of a signal and a given value.

- void HSA_API `hsa_signal_and_screlease` (`hsa_signal_t` signal, `hsa_signal_value_t` value)

Atomically perform a bitwise AND operation between the value of a signal and a given value.

- void HSA_API HSA_DEPRECATED [hsa_signal_and_release](#) (hsa_signal_t signal, hsa_signal_value_t value)
- void HSA_API [hsa_signal_or_scacq_screl](#) (hsa_signal_t signal, hsa_signal_value_t value)

Atomically perform a bitwise OR operation between the value of a signal and a given value.

- void HSA_API HSA_DEPRECATED [hsa_signal_or_acq_rel](#) (hsa_signal_t signal, hsa_signal_value_t value)
- void HSA_API [hsa_signal_or_scacquire](#) (hsa_signal_t signal, hsa_signal_value_t value)

Atomically perform a bitwise OR operation between the value of a signal and a given value.

- void HSA_API HSA_DEPRECATED [hsa_signal_or_acquire](#) (hsa_signal_t signal, hsa_signal_value_t value)
- void HSA_API [hsa_signal_or_relaxed](#) (hsa_signal_t signal, hsa_signal_value_t value)

Atomically perform a bitwise OR operation between the value of a signal and a given value.

- void HSA_API [hsa_signal_or_screlease](#) (hsa_signal_t signal, hsa_signal_value_t value)

Atomically perform a bitwise OR operation between the value of a signal and a given value.

- void HSA_API HSA_DEPRECATED [hsa_signal_or_release](#) (hsa_signal_t signal, hsa_signal_value_t value)
- void HSA_API [hsa_signal_xor_scacq_screl](#) (hsa_signal_t signal, hsa_signal_value_t value)

Atomically perform a bitwise XOR operation between the value of a signal and a given value.

- void HSA_API HSA_DEPRECATED [hsa_signal_xor_acq_rel](#) (hsa_signal_t signal, hsa_signal_value_t value)
- void HSA_API [hsa_signal_xor_scacquire](#) (hsa_signal_t signal, hsa_signal_value_t value)

Atomically perform a bitwise XOR operation between the value of a signal and a given value.

- void HSA_API HSA_DEPRECATED [hsa_signal_xor_acquire](#) (hsa_signal_t signal, hsa_signal_value_t value)
- void HSA_API [hsa_signal_xor_relaxed](#) (hsa_signal_t signal, hsa_signal_value_t value)

Atomically perform a bitwise XOR operation between the value of a signal and a given value.

- void HSA_API [hsa_signal_xor_screlease](#) (hsa_signal_t signal, hsa_signal_value_t value)

Atomically perform a bitwise XOR operation between the value of a signal and a given value.

- void HSA_API HSA_DEPRECATED [hsa_signal_xor_release](#) (hsa_signal_t signal, hsa_signal_value_t value)
- [hsa_signal_value_t](#) HSA_API [hsa_signal_wait_scacquire](#) (hsa_signal_t signal, [hsa_signal_condition_t](#) condition, [hsa_signal_value_t](#) compare_value, [uint64_t](#) timeout_hint, [hsa_wait_state_t](#) wait_state_hint)

Wait until a signal value satisfies a specified condition, or a certain amount of time has elapsed.

- [hsa_signal_value_t](#) HSA_API [hsa_signal_wait_relaxed](#) (hsa_signal_t signal, [hsa_signal_condition_t](#) condition, [hsa_signal_value_t](#) compare_value, [uint64_t](#) timeout_hint, [hsa_wait_state_t](#) wait_state_hint)

Wait until a signal value satisfies a specified condition, or a certain amount of time has elapsed.

- [hsa_signal_value_t](#) HSA_API HSA_DEPRECATED [hsa_signal_wait_acquire](#) (hsa_signal_t signal, [hsa_signal_condition_t](#) condition, [hsa_signal_value_t](#) compare_value, [uint64_t](#) timeout_hint, [hsa_wait_state_t](#) wait_state_hint)
- [hsa_status_t](#) HSA_API [hsa_signal_group_create](#) ([uint32_t](#) num_signals, const [hsa_signal_t](#) *signals, [uint32_t](#) num_consumers, const [hsa_agent_t](#) *consumers, [hsa_signal_group_t](#) *signal_group)

Create a signal group.

- [hsa_status_t](#) HSA_API [hsa_signal_group_destroy](#) ([hsa_signal_group_t](#) signal_group)

Destroy a signal group previous created by [hsa_signal_group_create](#).

- [hsa_status_t](#) HSA_API [hsa_signal_group_wait_any_scacquire](#) ([hsa_signal_group_t](#) signal_group, const [hsa_signal_condition_t](#) *conditions, const [hsa_signal_value_t](#) *compare_values, [hsa_wait_state_t](#) wait_state_hint, [hsa_signal_t](#) *signal, [hsa_signal_value_t](#) *value)

Wait until the value of at least one of the signals in a signal group satisfies its associated condition.

- [hsa_status_t](#) HSA_API [hsa_signal_group_wait_any_relaxed](#) ([hsa_signal_group_t](#) signal_group, const [hsa_signal_condition_t](#) *conditions, const [hsa_signal_value_t](#) *compare_values, [hsa_wait_state_t](#) wait_state_hint, [hsa_signal_t](#) *signal, [hsa_signal_value_t](#) *value)

Wait until the value of at least one of the signals in a signal group satisfies its associated condition.

5.3.1 Detailed Description

5.3.2 Typedef Documentation

5.3.2.1 hsa_signal_value_t

```
typedef int32_t hsa_signal_value_t
```

Signal value. The value occupies 32 bits in small machine mode, and 64 bits in large machine mode.

Definition at line 1340 of file [hsa.h](#).

5.3.3 Enumeration Type Documentation

5.3.3.1 hsa_signal_condition_t

```
enum hsa_signal_condition_t
```

Wait condition operator.

Enumerator

HSA_SIGNAL_CONDITION_EQ	The two operands are equal.
HSA_SIGNAL_CONDITION_NE	The two operands are not equal.
HSA_SIGNAL_CONDITION_LT	The first operand is less than the second operand.
HSA_SIGNAL_CONDITION_GTE	The first operand is greater than or equal to the second operand.

Definition at line 1948 of file [hsa.h](#).

5.3.3.2 hsa_wait_state_t

```
enum hsa_wait_state_t
```

State of the application thread during a signal wait.

Enumerator

HSA_WAIT_STATE_BLOCKED	The application thread may be rescheduled while waiting on the signal.
HSA_WAIT_STATE_ACTIVE	The application thread stays active while waiting on a signal.

Definition at line 1970 of file [hsa.h](#).

5.3.4 Function Documentation

5.3.4.1 hsa_signal_add_acq_rel()

```
void HSA_API HSA_DEPRECATED hsa_signal_add_acq_rel (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Deprecated Renamed as [hsa_signal_add_scacq_screl](#).

Atomically increment the value of a signal by a given amount.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to add to the value of the signal.

5.3.4.2 hsa_signal_add_acquire()

```
void HSA_API HSA_DEPRECATED hsa_signal_add_acquire (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Deprecated Renamed as [hsa_signal_add_scacquire](#).

Atomically increment the value of a signal by a given amount.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to add to the value of the signal.

5.3.4.3 hsa_signal_add_relaxed()

```
void HSA_API hsa_signal_add_relaxed (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically increment the value of a signal by a given amount.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to add to the value of the signal.

5.3.4.4 hsa_signal_add_release()

```
void HSA_API HSA_DEPRECATED hsa_signal_add_release (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Deprecated Renamed as [hsa_signal_add_screlease](#).

Atomically increment the value of a signal by a given amount.

If the value of the signal is changed, all the agents waiting on *signal* for which *value* satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to add to the value of the signal.

5.3.4.5 hsa_signal_add_scacq_screl()

```
void HSA_API hsa_signal_add_scacq_screl (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically increment the value of a signal by a given amount.

If the value of the signal is changed, all the agents waiting on *signal* for which *value* satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to add to the value of the signal.

5.3.4.6 hsa_signal_add_scacquire()

```
void HSA_API hsa_signal_add_scacquire (
```



```

    hsa_signal_t signal,
    hsa_signal_value_t value )

```

Atomically increment the value of a signal by a given amount.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to add to the value of the signal.

5.3.4.7 hsa_signal_add_screlease()

```

void HSA_API hsa_signal_add_screlease (
    hsa_signal_t signal,
    hsa_signal_value_t value )

```

Atomically increment the value of a signal by a given amount.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to add to the value of the signal.

5.3.4.8 hsa_signal_and_acq_rel()

```

void HSA_API HSA_DEPRECATED hsa_signal_and_acq_rel (
    hsa_signal_t signal,
    hsa_signal_value_t value )

```

Deprecated Renamed as `hsa_signal_and_scacq_screl`.

Atomically perform a bitwise AND operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to AND with the value of the signal.

5.3.4.9 hsa_signal_and_acquire()

```
void HSA_API HSA_DEPRECATED hsa_signal_and_acquire (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Deprecated Renamed as `hsa_signal_and_scacquire`.

Atomically perform a bitwise AND operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to AND with the value of the signal.

5.3.4.10 hsa_signal_and_relaxed()

```
void HSA_API hsa_signal_and_relaxed (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically perform a bitwise AND operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to AND with the value of the signal.

5.3.4.11 hsa_signal_and_release()

```
void HSA_API HSA_DEPRECATED hsa_signal_and_release (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Deprecated Renamed as `hsa_signal_and_screlease`.

Atomically perform a bitwise AND operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to AND with the value of the signal.

5.3.4.12 `hsa_signal_and_scacq_screl()`

```
void HSA_API hsa_signal_and_scacq_screl (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically perform a bitwise AND operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to AND with the value of the signal.

5.3.4.13 `hsa_signal_and_scacquire()`

```
void HSA_API hsa_signal_and_scacquire (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically perform a bitwise AND operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to AND with the value of the signal.

5.3.4.14 hsa_signal_and_screlease()

```
void HSA_API hsa_signal_and_screlease (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically perform a bitwise AND operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to AND with the value of the signal.

5.3.4.15 hsa_signal_cas_acq_rel()

```
hsa_signal_value_t HSA_API HSA_DEPRECATED hsa_signal_cas_acq_rel (
    hsa_signal_t signal,
    hsa_signal_value_t expected,
    hsa_signal_value_t value )
```

Deprecated Renamed as [hsa_signal_cas_scacq_screl](#).

Atomically set the value of a signal if the observed value is equal to the expected value. The observed value is returned regardless of whether the replacement was done.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>expected</i>	Value to compare with.
in	<i>value</i>	New value.

Returns

Observed value of the signal.

5.3.4.16 hsa_signal_cas_acquire()

```
hsa_signal_value_t HSA_API HSA_DEPRECATED hsa_signal_cas_acquire (
    hsa_signal_t signal,
    hsa_signal_value_t expected,
    hsa_signal_value_t value )
```

Deprecated Renamed as [hsa_signal_cas_scacquire](#).

Atomically set the value of a signal if the observed value is equal to the expected value. The observed value is returned regardless of whether the replacement was done.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>expected</i>	Value to compare with.
in	<i>value</i>	New value.

Returns

Observed value of the signal.

5.3.4.17 hsa_signal_cas_relaxed()

```
hsa_signal_value_t HSA_API hsa_signal_cas_relaxed (
    hsa_signal_t signal,
    hsa_signal_value_t expected,
    hsa_signal_value_t value )
```

Atomically set the value of a signal if the observed value is equal to the expected value. The observed value is returned regardless of whether the replacement was done.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>expected</i>	Value to compare with.
in	<i>value</i>	New value.

Returns

Observed value of the signal.

5.3.4.18 hsa_signal_cas_release()

```
hsa_signal_value_t HSA_API HSA_DEPRECATED hsa_signal_cas_release (
    hsa_signal_t signal,
    hsa_signal_value_t expected,
    hsa_signal_value_t value )
```

Deprecated Renamed as [hsa_signal_cas_screlease](#).

Atomically set the value of a signal if the observed value is equal to the expected value. The observed value is returned regardless of whether the replacement was done.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>expected</i>	Value to compare with.
in	<i>value</i>	New value.

Returns

Observed value of the signal.

5.3.4.19 `hsa_signal_cas_scacq_screl()`

```
hsa_signal_value_t HSA_API hsa_signal_cas_scacq_screl (
    hsa_signal_t signal,
    hsa_signal_value_t expected,
    hsa_signal_value_t value )
```

Atomically set the value of a signal if the observed value is equal to the expected value. The observed value is returned regardless of whether the replacement was done.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>expected</i>	Value to compare with.
in	<i>value</i>	New value.

Returns

Observed value of the signal.

5.3.4.20 `hsa_signal_cas_scacquire()`

```
hsa_signal_value_t HSA_API hsa_signal_cas_scacquire (
    hsa_signal_t signal,
```

```

    hsa_signal_value_t expected,
    hsa_signal_value_t value )

```

Atomically set the value of a signal if the observed value is equal to the expected value. The observed value is returned regardless of whether the replacement was done.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>expected</i>	Value to compare with.
in	<i>value</i>	New value.

Returns

Observed value of the signal.

5.3.4.21 hsa_signal_cas_screlease()

```

hsa_signal_value_t HSA_API hsa_signal_cas_screlease (
    hsa_signal_t signal,
    hsa_signal_value_t expected,
    hsa_signal_value_t value )

```

Atomically set the value of a signal if the observed value is equal to the expected value. The observed value is returned regardless of whether the replacement was done.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>expected</i>	Value to compare with.
in	<i>value</i>	New value.

Returns

Observed value of the signal.

5.3.4.22 hsa_signal_create()

```

hsa_status_t HSA_API hsa_signal_create (
    hsa_signal_value_t initial_value,

```

```
uint32_t num_consumers,
const hsa_agent_t * consumers,
hsa_signal_t * signal )
```

Create a signal.

Parameters

in	<i>initial_value</i>	Initial value of the signal.
in	<i>num_consumers</i>	Size of <i>consumers</i> . A value of 0 indicates that any agent might wait on the signal.
in	<i>consumers</i>	List of agents that might consume (wait on) the signal. If <i>num_consumers</i> is 0, this argument is ignored; otherwise, the HSA runtime might use the list to optimize the handling of the signal object. If an agent not listed in <i>consumers</i> waits on the returned signal, the behavior is undefined. The memory associated with <i>consumers</i> can be reused or freed after the function returns.
out	<i>signal</i>	Pointer to a memory location where the HSA runtime will store the newly created signal handle. Must not be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_OUT_OF_RESOURCES</i>	The HSA runtime failed to allocate the required resources.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>signal</i> is NULL, <i>num_consumers</i> is greater than 0 but <i>consumers</i> is NULL, or <i>consumers</i> contains duplicates.

5.3.4.23 hsa_signal_destroy()

```
hsa_status_t HSA_API hsa_signal_destroy (
    hsa_signal_t signal )
```

Destroy a signal previous created by [*hsa_signal_create*](#).

Parameters

in	<i>signal</i>	Signal.
----	---------------	---------

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_SIGNAL</i>	<i>signal</i> is invalid.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	The handle in <i>signal</i> is 0.

5.3.4.24 hsa_signal_exchange_acq_rel()

```
hsa_signal_value_t HSA_API HSA_DEPRECATED hsa_signal_exchange_acq_rel (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Deprecated Renamed as [hsa_signal_exchange_scacq_screl](#).

Atomically set the value of a signal and return its previous value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	New value.

Returns

Value of the signal prior to the exchange.

5.3.4.25 hsa_signal_exchange_acquire()

```
hsa_signal_value_t HSA_API HSA_DEPRECATED hsa_signal_exchange_acquire (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Deprecated Renamed as [hsa_signal_exchange_scacquire](#).

Atomically set the value of a signal and return its previous value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	New value.

Returns

Value of the signal prior to the exchange.

5.3.4.26 hsa_signal_exchange_relaxed()

```
hsa_signal_value_t HSA_API hsa_signal_exchange_relaxed (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically set the value of a signal and return its previous value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	New value.

Returns

Value of the signal prior to the exchange.

5.3.4.27 hsa_signal_exchange_release()

```
hsa_signal_value_t HSA_API HSA_DEPRECATED hsa_signal_exchange_release (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Deprecated Renamed as [hsa_signal_exchange_screlease](#).

Atomically set the value of a signal and return its previous value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	New value.

Returns

Value of the signal prior to the exchange.

5.3.4.28 hsa_signal_exchange_scacq_screl()

```
hsa_signal_value_t HSA_API hsa_signal_exchange_scacq_screl (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically set the value of a signal and return its previous value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	New value.

Returns

Value of the signal prior to the exchange.

5.3.4.29 `hsa_signal_exchange_scacquire()`

```
hsa_signal_value_t HSA_API hsa_signal_exchange_scacquire (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically set the value of a signal and return its previous value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	New value.

Returns

Value of the signal prior to the exchange.

5.3.4.30 `hsa_signal_exchange_screlease()`

```
hsa_signal_value_t HSA_API hsa_signal_exchange_screlease (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically set the value of a signal and return its previous value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	New value.

Returns

Value of the signal prior to the exchange.

5.3.4.31 `hsa_signal_group_create()`

```
hsa_status_t HSA_API hsa_signal_group_create (
    uint32_t num_signals,
    const hsa_signal_t * signals,
    uint32_t num_consumers,
    const hsa_agent_t * consumers,
    hsa_signal_group_t * signal_group )
```

Create a signal group.

Parameters

in	<i>num_signals</i>	Number of elements in <i>signals</i> . Must not be 0.
in	<i>signals</i>	List of signals in the group. The list must not contain any repeated elements. Must not be NULL.
in	<i>num_consumers</i>	Number of elements in <i>consumers</i> . Must not be 0.
in	<i>consumers</i>	List of agents that might consume (wait on) the signal group. The list must not contain repeated elements, and must be a subset of the set of agents that are allowed to wait on all the signals in the group. If an agent not listed in <i>consumers</i> waits on the returned group, the behavior is undefined. The memory associated with <i>consumers</i> can be reused or freed after the function returns. Must not be NULL.
out	<i>signal_group</i>	Pointer to newly created signal group. Must not be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_OUT_OF_RESOURCES</i>	The HSA runtime failed to allocate the required resources.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>num_signals</i> is 0, <i>signals</i> is NULL, <i>num_consumers</i> is 0, <i>consumers</i> is NULL, or <i>signal_group</i> is NULL.

5.3.4.32 `hsa_signal_group_destroy()`

```
hsa_status_t HSA_API hsa_signal_group_destroy (
    hsa_signal_group_t signal_group )
```

Destroy a signal group previous created by [hsa_signal_group_create](#).

Parameters

in	<i>signal_group</i>	Signal group.
----	---------------------	---------------

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_SIGNAL_GROUP	<i>signal_group</i> is invalid.

5.3.4.33 hsa_signal_group_wait_any_relaxed()

```
hsa_status_t HSA_API hsa_signal_group_wait_any_relaxed (
    hsa_signal_group_t signal_group,
    const hsa_signal_condition_t * conditions,
    const hsa_signal_value_t * compare_values,
    hsa_wait_state_t wait_state_hint,
    hsa_signal_t * signal,
    hsa_signal_value_t * value )
```

Wait until the value of at least one of the signals in a signal group satisfies its associated condition.

The function is guaranteed to return if the value of at least one of the signals in the group satisfies its associated condition at some point in time during the wait, but the signal value returned to the application may no longer satisfy the condition. The application must ensure that signals in the group are used in such way that wait wakeup conditions are not invalidated before dependent threads have woken up.

When this operation internally loads the value of the passed signal, it uses the memory order indicated in the function name.

Parameters

in	<i>signal_group</i>	Signal group.
in	<i>conditions</i>	List of conditions. Each condition, and the value at the same index in <i>compare_values</i> , is used to compare the value of the signal at that index in <i>signal_group</i> (the signal passed by the application to hsa_signal_group_create at that particular index). The size of <i>conditions</i> must not be smaller than the number of signals in <i>signal_group</i> ; any extra elements are ignored. Must not be NULL.
in	<i>compare_values</i>	List of comparison values. The size of <i>compare_values</i> must not be smaller than the number of signals in <i>signal_group</i> ; any extra elements are ignored. Must not be NULL.
in	<i>wait_state_hint</i>	Hint used by the application to indicate the preferred waiting state. The actual waiting state is decided by the HSA runtime and may not match the provided hint. A value of HSA_WAIT_STATE_ACTIVE may improve the latency of response to a signal update by avoiding rescheduling overhead.
out	<i>signal</i>	Signal in the group that satisfied the associated condition. If several signals satisfied their condition, the function can return any of those signals. Must not be NULL.
out	<i>value</i>	Observed value for <i>signal</i> , which might no longer satisfy the specified condition. Must not be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_INVALID_SIGNAL_GROUP</i>	<code>signal_group</code> is invalid.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<code>conditions</code> is NULL, <code>compare_values</code> is NULL, <code>signal</code> is NULL, or <code>value</code> is NULL.

5.3.4.34 `hsa_signal_group_wait_any_scacquire()`

```

hsa_status_t HSA_API hsa_signal_group_wait_any_scacquire (
    hsa_signal_group_t signal_group,
    const hsa_signal_condition_t * conditions,
    const hsa_signal_value_t * compare_values,
    hsa_wait_state_t wait_state_hint,
    hsa_signal_t * signal,
    hsa_signal_value_t * value )

```

Wait until the value of at least one of the signals in a signal group satisfies its associated condition.

The function is guaranteed to return if the value of at least one of the signals in the group satisfies its associated condition at some point in time during the wait, but the signal value returned to the application may no longer satisfy the condition. The application must ensure that signals in the group are used in such way that wait wakeup conditions are not invalidated before dependent threads have woken up.

When this operation internally loads the value of the passed signal, it uses the memory order indicated in the function name.

Parameters

in	<i>signal_group</i>	Signal group.
in	<i>conditions</i>	List of conditions. Each condition, and the value at the same index in <code>compare_values</code> , is used to compare the value of the signal at that index in <code>signal_group</code> (the signal passed by the application to <code>hsa_signal_group_create</code> at that particular index). The size of <code>conditions</code> must not be smaller than the number of signals in <code>signal_group</code> ; any extra elements are ignored. Must not be NULL.
in	<i>compare_values</i>	List of comparison values. The size of <code>compare_values</code> must not be smaller than the number of signals in <code>signal_group</code> ; any extra elements are ignored. Must not be NULL.
in	<i>wait_state_hint</i>	Hint used by the application to indicate the preferred waiting state. The actual waiting state is decided by the HSA runtime and may not match the provided hint. A value of <code>HSA_WAIT_STATE_ACTIVE</code> may improve the latency of response to a signal update by avoiding rescheduling overhead.
out	<i>signal</i>	Signal in the group that satisfied the associated condition. If several signals satisfied their condition, the function can return any of those signals. Must not be NULL.
out	<i>value</i>	Observed value for <code>signal</code> , which might no longer satisfy the specified condition. Must not be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_INVALID_SIGNAL_GROUP</i>	<code>signal_group</code> is invalid.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<code>conditions</code> is NULL, <code>compare_values</code> is NULL, <code>signal</code> is NULL, or <code>value</code> is NULL.

5.3.4.35 `hsa_signal_load_acquire()`

```
hsa_signal_value_t HSA_API HSA_DEPRECATED hsa_signal_load_acquire (
    hsa_signal_t signal )
```

Deprecated Renamed as `hsa_signal_load_scacquire`.

Atomically read the current value of a signal.

Parameters

in	<i>signal</i>	Signal.
----	---------------	---------

Returns

Value of the signal.

5.3.4.36 `hsa_signal_load_relaxed()`

```
hsa_signal_value_t HSA_API hsa_signal_load_relaxed (
    hsa_signal_t signal )
```

Atomically read the current value of a signal.

Parameters

in	<i>signal</i>	Signal.
----	---------------	---------

Returns

Value of the signal.

5.3.4.37 hsa_signal_load_scacquire()

```
hsa_signal_value_t HSA_API hsa_signal_load_scacquire (
    hsa_signal_t signal )
```

Atomically read the current value of a signal.

Parameters

in	<i>signal</i>	Signal.
----	---------------	---------

Returns

Value of the signal.

5.3.4.38 hsa_signal_or_acq_rel()

```
void HSA_API HSA_DEPRECATED hsa_signal_or_acq_rel (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Deprecated Renamed as [hsa_signal_or_scacq_screl](#).

Atomically perform a bitwise OR operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to OR with the value of the signal.

5.3.4.39 hsa_signal_or_acquire()

```
void HSA_API HSA_DEPRECATED hsa_signal_or_acquire (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Deprecated Renamed as [hsa_signal_or_scacquire](#).

Atomically perform a bitwise OR operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to OR with the value of the signal.

5.3.4.40 hsa_signal_or_relaxed()

```
void HSA_API hsa_signal_or_relaxed (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically perform a bitwise OR operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on *signal* for which *value* satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to OR with the value of the signal.

5.3.4.41 hsa_signal_or_release()

```
void HSA_API HSA_DEPRECATED hsa_signal_or_release (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Deprecated Renamed as [hsa_signal_or_screlease](#).

Atomically perform a bitwise OR operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on *signal* for which *value* satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to OR with the value of the signal.

5.3.4.42 hsa_signal_or_scacq_screl()

```
void HSA_API hsa_signal_or_scacq_screl (
```

```

    hsa_signal_t signal,
    hsa_signal_value_t value )

```

Atomically perform a bitwise OR operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to OR with the value of the signal.

5.3.4.43 hsa_signal_or_scacquire()

```

void HSA_API hsa_signal_or_scacquire (
    hsa_signal_t signal,
    hsa_signal_value_t value )

```

Atomically perform a bitwise OR operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to OR with the value of the signal.

5.3.4.44 hsa_signal_or_screlease()

```

void HSA_API hsa_signal_or_screlease (
    hsa_signal_t signal,
    hsa_signal_value_t value )

```

Atomically perform a bitwise OR operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to OR with the value of the signal.

5.3.4.45 hsa_signal_silent_store_relaxed()

```
void HSA_API hsa_signal_silent_store_relaxed (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically set the value of a signal without necessarily notifying the the agents waiting on it.

The agents waiting on `signal` may not wake up even when the new value satisfies their wait condition. If the application wants to update the signal and there is no need to notify any agent, invoking this function can be more efficient than calling the non-silent counterpart.

Parameters

in	<i>signal</i>	Signal.
in	<i>value</i>	New signal value.

5.3.4.46 hsa_signal_silent_store_screlease()

```
void HSA_API hsa_signal_silent_store_screlease (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically set the value of a signal without necessarily notifying the the agents waiting on it.

The agents waiting on `signal` may not wake up even when the new value satisfies their wait condition. If the application wants to update the signal and there is no need to notify any agent, invoking this function can be more efficient than calling the non-silent counterpart.

Parameters

in	<i>signal</i>	Signal.
in	<i>value</i>	New signal value.

5.3.4.47 hsa_signal_store_relaxed()

```
void HSA_API hsa_signal_store_relaxed (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically set the value of a signal.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal.
in	<i>value</i>	New signal value.

5.3.4.48 hsa_signal_store_release()

```
void HSA_API HSA_DEPRECATED hsa_signal_store_release (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Deprecated Renamed as [hsa_signal_store_screlease](#).

Atomically set the value of a signal.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal.
in	<i>value</i>	New signal value.

5.3.4.49 hsa_signal_store_screlease()

```
void HSA_API hsa_signal_store_screlease (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically set the value of a signal.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal.
in	<i>value</i>	New signal value.

5.3.4.50 hsa_signal_subtract_acq_rel()

```
void HSA_API HSA_DEPRECATED hsa_signal_subtract_acq_rel (
```

```

    hsa_signal_t signal,
    hsa_signal_value_t value )

```

Deprecated Renamed as [hsa_signal_subtract_scacq_screl](#).

Atomically decrement the value of a signal by a given amount.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to subtract from the value of the signal.

5.3.4.51 `hsa_signal_subtract_acquire()`

```

void HSA_API HSA_DEPRECATED hsa_signal_subtract_acquire (
    hsa_signal_t signal,
    hsa_signal_value_t value )

```

Deprecated Renamed as [hsa_signal_subtract_scacquire](#).

Atomically decrement the value of a signal by a given amount.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to subtract from the value of the signal.

5.3.4.52 `hsa_signal_subtract_relaxed()`

```

void HSA_API hsa_signal_subtract_relaxed (
    hsa_signal_t signal,
    hsa_signal_value_t value )

```

Atomically decrement the value of a signal by a given amount.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to subtract from the value of the signal.

5.3.4.53 hsa_signal_subtract_release()

```
void HSA_API HSA_DEPRECATED hsa_signal_subtract_release (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Deprecated Renamed as [hsa_signal_subtract_screlease](#).

Atomically decrement the value of a signal by a given amount.

If the value of the signal is changed, all the agents waiting on *signal* for which *value* satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to subtract from the value of the signal.

5.3.4.54 hsa_signal_subtract_scacq_screl()

```
void HSA_API hsa_signal_subtract_scacq_screl (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically decrement the value of a signal by a given amount.

If the value of the signal is changed, all the agents waiting on *signal* for which *value* satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to subtract from the value of the signal.

5.3.4.55 hsa_signal_subtract_scacquire()

```
void HSA_API hsa_signal_subtract_scacquire (
```

```

    hsa_signal_t signal,
    hsa_signal_value_t value )

```

Atomically decrement the value of a signal by a given amount.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to subtract from the value of the signal.

5.3.4.56 hsa_signal_subtract_screlease()

```

void HSA_API hsa_signal_subtract_screlease (
    hsa_signal_t signal,
    hsa_signal_value_t value )

```

Atomically decrement the value of a signal by a given amount.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to subtract from the value of the signal.

5.3.4.57 hsa_signal_wait_acquire()

```

hsa_signal_value_t HSA_API HSA_DEPRECATED hsa_signal_wait_acquire (
    hsa_signal_t signal,
    hsa_signal_condition_t condition,
    hsa_signal_value_t compare_value,
    uint64_t timeout_hint,
    hsa_wait_state_t wait_state_hint )

```

Deprecated Renamed as `hsa_signal_wait_scacquire`.

Wait until a signal value satisfies a specified condition, or a certain amount of time has elapsed.

A wait operation can spuriously resume at any time sooner than the timeout (for example, due to system or other external factors) even when the condition has not been met.

The function is guaranteed to return if the signal value satisfies the condition at some point in time during the wait, but the value returned to the application might not satisfy the condition. The application must ensure that signals are used in such way that wait wakeup conditions are not invalidated before dependent threads have woken up.

When the wait operation internally loads the value of the passed signal, it uses the memory order indicated in the function name.

Parameters

in	<i>signal</i>	Signal.
in	<i>condition</i>	Condition used to compare the signal value with <code>compare_value</code> .
in	<i>compare_value</i>	Value to compare with.
in	<i>timeout_hint</i>	Maximum duration of the wait. Specified in the same unit as the system timestamp. The operation might block for a shorter or longer time even if the condition is not met. A value of <code>UINT64_MAX</code> indicates no maximum.
in	<i>wait_state_hint</i>	Hint used by the application to indicate the preferred waiting state. The actual waiting state is ultimately decided by HSA runtime and may not match the provided hint. A value of <code>HSA_WAIT_STATE_ACTIVE</code> may improve the latency of response to a signal update by avoiding rescheduling overhead.

Returns

Observed value of the signal, which might not satisfy the specified condition.

5.3.4.58 `hsa_signal_wait_relaxed()`

```

hsa_signal_value_t HSA_API hsa_signal_wait_relaxed (
    hsa_signal_t signal,
    hsa_signal_condition_t condition,
    hsa_signal_value_t compare_value,
    uint64_t timeout_hint,
    hsa_wait_state_t wait_state_hint )

```

Wait until a signal value satisfies a specified condition, or a certain amount of time has elapsed.

A wait operation can spuriously resume at any time sooner than the timeout (for example, due to system or other external factors) even when the condition has not been met.

The function is guaranteed to return if the signal value satisfies the condition at some point in time during the wait, but the value returned to the application might not satisfy the condition. The application must ensure that signals are used in such way that wait wakeup conditions are not invalidated before dependent threads have woken up.

When the wait operation internally loads the value of the passed signal, it uses the memory order indicated in the function name.

Parameters

in	<i>signal</i>	Signal.
in	<i>condition</i>	Condition used to compare the signal value with <code>compare_value</code> .
in	<i>compare_value</i>	Value to compare with.
in	<i>timeout_hint</i>	Maximum duration of the wait. Specified in the same unit as the system timestamp. The operation might block for a shorter or longer time even if the condition is not met. A value of <code>UINT64_MAX</code> indicates no maximum.
in	<i>wait_state_hint</i>	Hint used by the application to indicate the preferred waiting state. The actual waiting state is ultimately decided by HSA runtime and may not match the provided hint. A value of <code>HSA_WAIT_STATE_ACTIVE</code> may improve the latency of response to a signal update by avoiding rescheduling overhead.

Returns

Observed value of the signal, which might not satisfy the specified condition.

5.3.4.59 hsa_signal_wait_scacquire()

```
hsa_signal_value_t HSA_API hsa_signal_wait_scacquire (
    hsa_signal_t signal,
    hsa_signal_condition_t condition,
    hsa_signal_value_t compare_value,
    uint64_t timeout_hint,
    hsa_wait_state_t wait_state_hint )
```

Wait until a signal value satisfies a specified condition, or a certain amount of time has elapsed.

A wait operation can spuriously resume at any time sooner than the timeout (for example, due to system or other external factors) even when the condition has not been met.

The function is guaranteed to return if the signal value satisfies the condition at some point in time during the wait, but the value returned to the application might not satisfy the condition. The application must ensure that signals are used in such way that wait wakeup conditions are not invalidated before dependent threads have woken up.

When the wait operation internally loads the value of the passed signal, it uses the memory order indicated in the function name.

Parameters

in	<i>signal</i>	Signal.
in	<i>condition</i>	Condition used to compare the signal value with <code>compare_value</code> .
in	<i>compare_value</i>	Value to compare with.
in	<i>timeout_hint</i>	Maximum duration of the wait. Specified in the same unit as the system timestamp. The operation might block for a shorter or longer time even if the condition is not met. A value of <code>UINT64_MAX</code> indicates no maximum.
in	<i>wait_state_hint</i>	Hint used by the application to indicate the preferred waiting state. The actual waiting state is ultimately decided by HSA runtime and may not match the provided hint. A value of HSA_WAIT_STATE_ACTIVE may improve the latency of response to a signal update by avoiding rescheduling overhead.

Returns

Observed value of the signal, which might not satisfy the specified condition.

5.3.4.60 hsa_signal_xor_acq_rel()

```
void HSA_API HSA_DEPRECATED hsa_signal_xor_acq_rel (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Deprecated Renamed as [hsa_signal_xor_scacq_screl](#).

Atomically perform a bitwise XOR operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to XOR with the value of the signal.

5.3.4.61 `hsa_signal_xor_acquire()`

```
void HSA_API HSA_DEPRECATED hsa_signal_xor_acquire (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Deprecated Renamed as [hsa_signal_xor_scacquire](#).

Atomically perform a bitwise XOR operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to XOR with the value of the signal.

5.3.4.62 `hsa_signal_xor_relaxed()`

```
void HSA_API hsa_signal_xor_relaxed (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically perform a bitwise XOR operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to XOR with the value of the signal.

5.3.4.63 hsa_signal_xor_release()

```
void HSA_API HSA_DEPRECATED hsa_signal_xor_release (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Deprecated Renamed as [hsa_signal_xor_screlease](#).

Atomically perform a bitwise XOR operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to XOR with the value of the signal.

5.3.4.64 hsa_signal_xor_scacq_screl()

```
void HSA_API hsa_signal_xor_scacq_screl (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically perform a bitwise XOR operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <code>signal</code> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to XOR with the value of the signal.

5.3.4.65 hsa_signal_xor_scacquire()

```
void HSA_API hsa_signal_xor_scacquire (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically perform a bitwise XOR operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on `signal` for which `value` satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to XOR with the value of the signal.

5.3.4.66 hsa_signal_xor_screlease()

```
void HSA_API hsa_signal_xor_screlease (
    hsa_signal_t signal,
    hsa_signal_value_t value )
```

Atomically perform a bitwise XOR operation between the value of a signal and a given value.

If the value of the signal is changed, all the agents waiting on *signal* for which *value* satisfies their wait condition are awakened.

Parameters

in	<i>signal</i>	Signal. If <i>signal</i> is a queue doorbell signal, the behavior is undefined.
in	<i>value</i>	Value to XOR with the value of the signal.

5.4 Memory

Classes

- struct [hsa_region_s](#)

A memory region represents a block of virtual memory with certain properties. For example, the HSA runtime represents fine-grained memory in the global segment using a region. A region might be associated with more than one agent.

Typedefs

- typedef struct [hsa_region_s](#) [hsa_region_t](#)

A memory region represents a block of virtual memory with certain properties. For example, the HSA runtime represents fine-grained memory in the global segment using a region. A region might be associated with more than one agent.

Enumerations

- enum [hsa_region_segment_t](#) {
[HSA_REGION_SEGMENT_GLOBAL](#) = 0 , [HSA_REGION_SEGMENT_READONLY](#) = 1 , [HSA_REGION_SEGMENT_PRIVATE](#)
= 2 , [HSA_REGION_SEGMENT_GROUP](#) = 3 ,
[HSA_REGION_SEGMENT_KERNARG](#) = 4 }

Memory segments associated with a region.

- enum `hsa_region_global_flag_t` { `HSA_REGION_GLOBAL_FLAG_KERNARG` = 1 , `HSA_REGION_GLOBAL_FLAG_FINE_GRAINED` = 2 , `HSA_REGION_GLOBAL_FLAG_COARSE_GRAINED` = 4 }

Global region flags.

- enum `hsa_region_info_t` {
`HSA_REGION_INFO_SEGMENT` = 0 , `HSA_REGION_INFO_GLOBAL_FLAGS` = 1 , `HSA_REGION_INFO_SIZE` = 2 , `HSA_REGION_INFO_ALLOC_MAX_SIZE` = 4 ,
`HSA_REGION_INFO_ALLOC_MAX_PRIVATE_WORKGROUP_SIZE` = 8 , `HSA_REGION_INFO_RUNTIME_ALLOC_ALLOWED` = 5 , `HSA_REGION_INFO_RUNTIME_ALLOC_GRANULE` = 6 , `HSA_REGION_INFO_RUNTIME_ALLOC_ALIGNMENT` = 7 }

Attributes of a memory region.

Functions

- `hsa_status_t` HSA_API `hsa_region_get_info` (`hsa_region_t` region, `hsa_region_info_t` attribute, void *value)
Get the current value of an attribute of a region.
- `hsa_status_t` HSA_API `hsa_agent_iterate_regions` (`hsa_agent_t` agent, `hsa_status_t`(*callback)(`hsa_region_t` region, void *data), void *data)
Iterate over the memory regions associated with a given agent, and invoke an application-defined callback on every iteration.
- `hsa_status_t` HSA_API `hsa_memory_allocate` (`hsa_region_t` region, `size_t` size, void **ptr)
Allocate a block of memory in a given region.
- `hsa_status_t` HSA_API `hsa_memory_free` (void *ptr)
Deallocate a block of memory previously allocated using `hsa_memory_allocate`.
- `hsa_status_t` HSA_API `hsa_memory_copy` (void *dst, const void *src, `size_t` size)
Copy a block of memory from the location pointed to by `src` to the memory block pointed to by `dst`.
- `hsa_status_t` HSA_API `hsa_memory_assign_agent` (void *ptr, `hsa_agent_t` agent, `hsa_access_permission_t` access)
Change the ownership of a global, coarse-grained buffer.
- `hsa_status_t` HSA_API `hsa_memory_register` (void *ptr, `size_t` size)
Register a global, fine-grained buffer.
- `hsa_status_t` HSA_API `hsa_memory_deregister` (void *ptr, `size_t` size)
Deregister memory previously registered using `hsa_memory_register`.

5.4.1 Detailed Description

5.4.2 Enumeration Type Documentation

5.4.2.1 `hsa_region_global_flag_t`

```
enum hsa_region_global_flag_t
```

Global region flags.

Enumerator

HSA_REGION_GLOBAL_FLAG_KERNARG	The application can use memory in the region to store kernel arguments, and provide the values for the kernarg segment of a kernel dispatch. If this flag is set, then HSA_REGION_GLOBAL_FLAG_FINE_GRAINED must be set.
HSA_REGION_GLOBAL_FLAG_FINE_GRAINED	Updates to memory in this region are immediately visible to all the agents under the terms of the HSA memory model. If this flag is set, then HSA_REGION_GLOBAL_FLAG_COARSE_GRAINED must not be set.
HSA_REGION_GLOBAL_FLAG_COARSE_GRAINED	Updates to memory in this region can be performed by a single agent at a time. If a different agent in the system is allowed to access the region, the application must explicitly invoke hsa_memory_assign_agent in order to transfer ownership to that agent for a particular buffer.

Definition at line 3197 of file [hsa.h](#).

5.4.2.2 hsa_region_info_t

enum [hsa_region_info_t](#)

Attributes of a memory region.

Enumerator

HSA_REGION_INFO_SEGMENT	Segment where memory in the region can be used. The type of this attribute is hsa_region_segment_t .
HSA_REGION_INFO_GLOBAL_FLAGS	Flag mask. The value of this attribute is undefined if the value of HSA_REGION_INFO_SEGMENT is not HSA_REGION_SEGMENT_GLOBAL . The type of this attribute is uint32_t , a bit-field of hsa_region_global_flag_t values.
HSA_REGION_INFO_SIZE	Size of this region, in bytes. The type of this attribute is size_t .
HSA_REGION_INFO_ALLOC_MAX_SIZE	Maximum allocation size in this region, in bytes. Must not exceed the value of HSA_REGION_INFO_SIZE . The type of this attribute is size_t . If the region is in the global or readonly segments, this is the maximum size that the application can pass to hsa_memory_allocate . If the region is in the group segment, this is the maximum size (per work-group) that can be requested for a given kernel dispatch. If the region is in the private segment, this is the maximum size (per work-item) that can be requested for a specific kernel dispatch, and must be at least 256 bytes.

Enumerator

HSA_REGION_INFO_ALLOC_MAX_PRIVATE_WORKGROUP_SIZE ↔	Maximum size (per work-group) of private memory that can be requested for a specific kernel dispatch. Must be at least 65536 bytes. The type of this attribute is <code>uint32_t</code> . The value of this attribute is undefined if the region is not in the private segment.
HSA_REGION_INFO_RUNTIME_ALLOC_ALLOWED	Indicates whether memory in this region can be allocated using <code>hsa_memory_allocate</code> . The type of this attribute is <code>bool</code> . The value of this flag is always false for regions in the group and private segments.
HSA_REGION_INFO_RUNTIME_ALLOC_GRANULE	Allocation granularity of buffers allocated by <code>hsa_memory_allocate</code> in this region. The size of a buffer allocated in this region is a multiple of the value of this attribute. The value of this attribute is only defined if <code>HSA_REGION_INFO_RUNTIME_ALLOC_ALLOWED</code> is true for this region. The type of this attribute is <code>size_t</code> .
HSA_REGION_INFO_RUNTIME_ALLOC_ALIGNMENT ↔	Alignment of buffers allocated by <code>hsa_memory_allocate</code> in this region. The value of this attribute is only defined if <code>HSA_REGION_INFO_RUNTIME_ALLOC_ALLOWED</code> is true for this region, and must be a power of 2. The type of this attribute is <code>size_t</code> .

Definition at line 3222 of file [hsa.h](#).

5.4.2.3 `hsa_region_segment_t`

```
enum hsa_region_segment_t
```

Memory segments associated with a region.

Enumerator

HSA_REGION_SEGMENT_GLOBAL	Global segment. Used to hold data that is shared by all agents.
HSA_REGION_SEGMENT_READONLY	Read-only segment. Used to hold data that remains constant during the execution of a kernel.
HSA_REGION_SEGMENT_PRIVATE	Private segment. Used to hold data that is local to a single work-item.
HSA_REGION_SEGMENT_GROUP	Group segment. Used to hold data that is shared by the work-items of a work-group.
HSA_REGION_SEGMENT_KERNARG	Kernarg segment. Used to store kernel arguments.

Definition at line 3169 of file [hsa.h](#).

5.4.3 Function Documentation

5.4.3.1 hsa_agent_iterate_regions()

```
hsa_status_t HSA_API hsa_agent_iterate_regions (
    hsa_agent_t agent,
    hsa_status_t (*)(hsa_region_t region, void *data) callback,
    void * data )
```

Iterate over the memory regions associated with a given agent, and invoke an application-defined callback on every iteration.

Parameters

in	<i>agent</i>	A valid agent.
in	<i>callback</i>	Callback to be invoked once per region that is accessible from the agent. The HSA runtime passes two arguments to the callback, the region and the application data. If <i>callback</i> returns a status other than HSA_STATUS_SUCCESS for a particular iteration, the traversal stops and hsa_agent_iterate_regions returns that status value.
in	<i>data</i>	Application data that is passed to <i>callback</i> on every iteration. May be NULL.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_AGENT	The agent is invalid.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>callback</i> is NULL.

5.4.3.2 hsa_memory_allocate()

```
hsa_status_t HSA_API hsa_memory_allocate (
    hsa_region_t region,
    size_t size,
    void ** ptr )
```

Allocate a block of memory in a given region.

Parameters

in	<i>region</i>	Region where to allocate memory from. The region must have the HSA_REGION_INFO_RUNTIME_ALLOC_ALLOWED flag set.
in	<i>size</i>	Allocation size, in bytes. Must not be zero. This value is rounded up to the nearest multiple of HSA_REGION_INFO_RUNTIME_ALLOC_GRANULE in <i>region</i> .
out	<i>ptr</i>	Pointer to the location where to store the base address of the allocated block. The returned base address is aligned to the value of HSA_REGION_INFO_RUNTIME_ALLOC_ALIGNMENT in <i>region</i> . If the allocation fails, the returned value is undefined.

Return values

<code>HSA_STATUS_SUCCESS</code>	The function has been executed successfully.
<code>HSA_STATUS_ERROR_NOT_INITIALIZED</code>	The HSA runtime has not been initialized.
<code>HSA_STATUS_ERROR_OUT_OF_RESOURCES</code>	The HSA runtime failed to allocate the required resources.
<code>HSA_STATUS_ERROR_INVALID_REGION</code>	The region is invalid.
<code>HSA_STATUS_ERROR_INVALID_ALLOCATION</code>	The host is not allowed to allocate memory in <code>region</code> , or <code>size</code> is greater than the value of <code>HSA_REGION_INFO_ALLOC_MAX_SIZE</code> in <code>region</code> .
<code>HSA_STATUS_ERROR_INVALID_ARGUMENT</code>	<code>ptr</code> is NULL, or <code>size</code> is 0.

5.4.3.3 `hsa_memory_assign_agent()`

```

hsa_status_t HSA_API hsa_memory_assign_agent (
    void * ptr,
    hsa_agent_t agent,
    hsa_access_permission_t access )

```

Change the ownership of a global, coarse-grained buffer.

The contents of a coarse-grained buffer are visible to an agent only after ownership has been explicitly transferred to that agent. Once the operation completes, the previous owner cannot longer access the data in the buffer.

An implementation of the HSA runtime is allowed, but not required, to change the physical location of the buffer when ownership is transferred to a different agent. In general the application must not assume this behavior. The virtual location (address) of the passed buffer is never modified.

Parameters

in	<i>ptr</i>	Base address of a global buffer. The pointer must match an address previously returned by <code>hsa_memory_allocate</code> . The size of the buffer affected by the ownership change is identical to the size of that previous allocation. If <code>ptr</code> points to a fine-grained global buffer, no operation is performed and the function returns success. If <code>ptr</code> does not point to global memory, the behavior is undefined.
in	<i>agent</i>	Agent that becomes the owner of the buffer. The application is responsible for ensuring that <code>agent</code> has access to the region that contains the buffer. It is allowed to change ownership to an agent that is already the owner of the buffer, with the same or different access permissions.
in	<i>access</i>	Access permissions requested for the new owner.

Return values

<code>HSA_STATUS_SUCCESS</code>	The function has been executed successfully.
<code>HSA_STATUS_ERROR_NOT_INITIALIZED</code>	The HSA runtime has not been initialized.
<code>HSA_STATUS_ERROR_INVALID_AGENT</code>	The agent is invalid.
<code>HSA_STATUS_ERROR_OUT_OF_RESOURCES</code>	The HSA runtime failed to allocate the required resources.
<code>HSA_STATUS_ERROR_INVALID_ARGUMENT</code>	<code>ptr</code> is NULL, or <code>access</code> is not a valid access value.

5.4.3.4 hsa_memory_copy()

```
hsa_status_t HSA_API hsa_memory_copy (
    void * dst,
    const void * src,
    size_t size )
```

Copy a block of memory from the location pointed to by `src` to the memory block pointed to by `dst`.

Parameters

out	<i>dst</i>	Buffer where the content is to be copied. If <i>dst</i> is in coarse-grained memory, the copied data is only visible to the agent currently assigned (hsa_memory_assign_agent) to <i>dst</i> .
in	<i>src</i>	A valid pointer to the source of data to be copied. The source buffer must not overlap with the destination buffer. If the source buffer is in coarse-grained memory then it must be assigned to an agent, from which the data will be retrieved.
in	<i>size</i>	Number of bytes to copy. If <i>size</i> is 0, no copy is performed and the function returns success. Copying a number of bytes larger than the size of the buffers pointed by <i>dst</i> or <i>src</i> results in undefined behavior.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_ARGUMENT	The source or destination pointers are NULL.

5.4.3.5 hsa_memory_deregister()

```
hsa_status_t HSA_API hsa_memory_deregister (
    void * ptr,
    size_t size )
```

Deregister memory previously registered using [hsa_memory_register](#).

If the memory interval being deregistered does not match a previous registration (start and end addresses), the behavior is undefined.

Parameters

in	<i>ptr</i>	A pointer to the base of the buffer to be deregistered. If a NULL pointer is passed, no operation is performed.
in	<i>size</i>	Size of the buffer to be deregistered.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.

5.4.3.6 hsa_memory_free()

```
hsa_status_t HSA_API hsa_memory_free (
    void * ptr )
```

Deallocate a block of memory previously allocated using [hsa_memory_allocate](#).

Parameters

in	<i>ptr</i>	Pointer to a memory block. If <i>ptr</i> does not match a value previously returned by hsa_memory_allocate , the behavior is undefined.
----	------------	---

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.

5.4.3.7 hsa_memory_register()

```
hsa_status_t HSA_API hsa_memory_register (
    void * ptr,
    size_t size )
```

Register a global, fine-grained buffer.

Registering a buffer serves as an indication to the HSA runtime that the memory might be accessed from a kernel agent other than the host. Registration is a performance hint that allows the HSA runtime implementation to know which buffers will be accessed by some of the kernel agents ahead of time.

Registration is only recommended for buffers in the global segment that have not been allocated using the HSA allocator ([hsa_memory_allocate](#)), but an OS allocator instead. Registering an OS-allocated buffer in the base profile is equivalent to a no-op.

Registrations should not overlap.

Parameters

in	<i>ptr</i>	A buffer in global, fine-grained memory. If a NULL pointer is passed, no operation is performed. If the buffer has been allocated using hsa_memory_allocate , or has already been registered, no operation is performed.
in	<i>size</i>	Requested registration size in bytes. A size of 0 is only allowed if <i>ptr</i> is NULL.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.

Return values

<code>HSA_STATUS_ERROR_OUT_OF_RESOURCES</code>	The HSA runtime failed to allocate the required resources.
<code>HSA_STATUS_ERROR_INVALID_ARGUMENT</code>	<code>size</code> is 0 but <code>ptr</code> is not NULL.

5.4.3.8 `hsa_region_get_info()`

```

hsa_status_t HSA_API hsa_region_get_info (
    hsa_region_t region,
    hsa_region_info_t attribute,
    void * value )

```

Get the current value of an attribute of a region.

Parameters

in	<i>region</i>	A valid region.
in	<i>attribute</i>	Attribute to query.
out	<i>value</i>	Pointer to a application-allocated buffer where to store the value of the attribute. If the buffer passed by the application is not large enough to hold the value of <i>attribute</i> , the behavior is undefined.

Return values

<code>HSA_STATUS_SUCCESS</code>	The function has been executed successfully.
<code>HSA_STATUS_ERROR_NOT_INITIALIZED</code>	The HSA runtime has not been initialized.
<code>HSA_STATUS_ERROR_INVALID_REGION</code>	The region is invalid.
<code>HSA_STATUS_ERROR_INVALID_ARGUMENT</code>	<i>attribute</i> is an invalid region attribute, or <i>value</i> is NULL.

5.5 Queues

Classes

- struct [`hsa_queue_s`](#)
User mode queue.

Typedefs

- typedef uint32_t [`hsa_queue_type32_t`](#)
A fixed-size type used to represent [`hsa_queue_type_t`](#) constants.
- typedef struct [`hsa_queue_s`](#) [`hsa_queue_t`](#)
User mode queue.

Enumerations

- enum `hsa_queue_type_t` { `HSA_QUEUE_TYPE_MULTI` = 0 , `HSA_QUEUE_TYPE_SINGLE` = 1 , `HSA_QUEUE_TYPE_COOPERATIVE` = 2 }

Queue type. Intended to be used for dynamic queue protocol determination.

- enum `hsa_queue_feature_t` { `HSA_QUEUE_FEATURE_KERNEL_DISPATCH` = 1 , `HSA_QUEUE_FEATURE_AGENT_DISPATCH` = 2 }

Queue features.

Functions

- `hsa_status_t` HSA_API `hsa_queue_create` (`hsa_agent_t` agent, `uint32_t` size, `hsa_queue_type32_t` type, `void(*callback)(hsa_status_t status, hsa_queue_t *source, void *data)`, `void *data`, `uint32_t` private_segment_size, `uint32_t` group_segment_size, `hsa_queue_t **queue`)

Create a user mode queue.

- `hsa_status_t` HSA_API `hsa_soft_queue_create` (`hsa_region_t` region, `uint32_t` size, `hsa_queue_type32_t` type, `uint32_t` features, `hsa_signal_t` doorbell_signal, `hsa_queue_t **queue`)

Create a queue for which the application or a kernel is responsible for processing the AQL packets.

- `hsa_status_t` HSA_API `hsa_queue_destroy` (`hsa_queue_t *queue`)

Destroy a user mode queue.

- `hsa_status_t` HSA_API `hsa_queue_inactivate` (`hsa_queue_t *queue`)

Inactivate a queue.

- `uint64_t` HSA_API HSA_DEPRECATED `hsa_queue_load_read_index_acquire` (const `hsa_queue_t *queue`)

- `uint64_t` HSA_API `hsa_queue_load_read_index_scacquire` (const `hsa_queue_t *queue`)

Atomically load the read index of a queue.

- `uint64_t` HSA_API `hsa_queue_load_read_index_relaxed` (const `hsa_queue_t *queue`)

Atomically load the read index of a queue.

- `uint64_t` HSA_API HSA_DEPRECATED `hsa_queue_load_write_index_acquire` (const `hsa_queue_t *queue`)

- `uint64_t` HSA_API `hsa_queue_load_write_index_scacquire` (const `hsa_queue_t *queue`)

Atomically load the write index of a queue.

- `uint64_t` HSA_API `hsa_queue_load_write_index_relaxed` (const `hsa_queue_t *queue`)

Atomically load the write index of a queue.

- `void` HSA_API `hsa_queue_store_write_index_relaxed` (const `hsa_queue_t *queue`, `uint64_t` value)

Atomically set the write index of a queue.

- `void` HSA_API HSA_DEPRECATED `hsa_queue_store_write_index_release` (const `hsa_queue_t *queue`, `uint64_t` value)

- `void` HSA_API `hsa_queue_store_write_index_screlease` (const `hsa_queue_t *queue`, `uint64_t` value)

Atomically set the write index of a queue.

- `uint64_t` HSA_API HSA_DEPRECATED `hsa_queue_cas_write_index_acq_rel` (const `hsa_queue_t *queue`, `uint64_t` expected, `uint64_t` value)

- `uint64_t` HSA_API `hsa_queue_cas_write_index_scacq_screl` (const `hsa_queue_t *queue`, `uint64_t` expected, `uint64_t` value)

Atomically set the write index of a queue if the observed value is equal to the expected value. The application can inspect the returned value to determine if the replacement was done.

- `uint64_t` HSA_API HSA_DEPRECATED `hsa_queue_cas_write_index_acquire` (const `hsa_queue_t *queue`, `uint64_t` expected, `uint64_t` value)

- `uint64_t` HSA_API `hsa_queue_cas_write_index_scacquire` (const `hsa_queue_t *queue`, `uint64_t` expected, `uint64_t` value)

Atomically set the write index of a queue if the observed value is equal to the expected value. The application can inspect the returned value to determine if the replacement was done.

- `uint64_t` HSA_API `hsa_queue_cas_write_index_relaxed` (const `hsa_queue_t *queue`, `uint64_t` expected, `uint64_t` value)

Atomically set the write index of a queue if the observed value is equal to the expected value. The application can inspect the returned value to determine if the replacement was done.

- uint64_t HSA_API HSA_DEPRECATED [hsa_queue_cas_write_index_release](#) (const [hsa_queue_t](#) *queue, uint64_t expected, uint64_t value)
- uint64_t HSA_API [hsa_queue_cas_write_index_screlease](#) (const [hsa_queue_t](#) *queue, uint64_t expected, uint64_t value)

Atomically set the write index of a queue if the observed value is equal to the expected value. The application can inspect the returned value to determine if the replacement was done.

- uint64_t HSA_API HSA_DEPRECATED [hsa_queue_add_write_index_acq_rel](#) (const [hsa_queue_t](#) *queue, uint64_t value)
- uint64_t HSA_API [hsa_queue_add_write_index_scacq_screl](#) (const [hsa_queue_t](#) *queue, uint64_t value)

Atomically increment the write index of a queue by an offset.

- uint64_t HSA_API HSA_DEPRECATED [hsa_queue_add_write_index_acquire](#) (const [hsa_queue_t](#) *queue, uint64_t value)
- uint64_t HSA_API [hsa_queue_add_write_index_scacquire](#) (const [hsa_queue_t](#) *queue, uint64_t value)

Atomically increment the write index of a queue by an offset.

- uint64_t HSA_API [hsa_queue_add_write_index_relaxed](#) (const [hsa_queue_t](#) *queue, uint64_t value)

Atomically increment the write index of a queue by an offset.

- uint64_t HSA_API HSA_DEPRECATED [hsa_queue_add_write_index_release](#) (const [hsa_queue_t](#) *queue, uint64_t value)
- uint64_t HSA_API [hsa_queue_add_write_index_screlease](#) (const [hsa_queue_t](#) *queue, uint64_t value)

Atomically increment the write index of a queue by an offset.

- void HSA_API [hsa_queue_store_read_index_relaxed](#) (const [hsa_queue_t](#) *queue, uint64_t value)

Atomically set the read index of a queue.

- void HSA_API HSA_DEPRECATED [hsa_queue_store_read_index_release](#) (const [hsa_queue_t](#) *queue, uint64_t value)
- void HSA_API [hsa_queue_store_read_index_screlease](#) (const [hsa_queue_t](#) *queue, uint64_t value)

Atomically set the read index of a queue.

5.5.1 Detailed Description

5.5.2 Typedef Documentation

5.5.2.1 [hsa_queue_t](#)

```
typedef struct hsa\_queue\_s hsa\_queue\_t
```

User mode queue.

The queue structure is read-only and allocated by the HSA runtime, but agents can directly modify the contents of the buffer pointed by *base_address*, or use HSA runtime APIs to access the doorbell signal.

5.5.2.2 [hsa_queue_type32_t](#)

```
typedef uint32_t hsa\_queue\_type32\_t
```

A fixed-size type used to represent [hsa_queue_type_t](#) constants.

Definition at line 2242 of file [hsa.h](#).

5.5.3 Enumeration Type Documentation

5.5.3.1 hsa_queue_feature_t

enum [hsa_queue_feature_t](#)

Queue features.

Enumerator

HSA_QUEUE_FEATURE_KERNEL_DISPATCH	Queue supports kernel dispatch packets.
HSA_QUEUE_FEATURE_AGENT_DISPATCH	Queue supports agent dispatch packets.

Definition at line [2247](#) of file [hsa.h](#).

5.5.3.2 hsa_queue_type_t

enum [hsa_queue_type_t](#)

Queue type. Intended to be used for dynamic queue protocol determination.

Enumerator

HSA_QUEUE_TYPE_MULTI	Queue supports multiple producers. Use of multiproducer queue mechanics is required.
HSA_QUEUE_TYPE_SINGLE	Queue only supports a single producer. In some scenarios, the application may want to limit the submission of AQL packets to a single agent. Queues that support a single producer may be more efficient than queues supporting multiple producers. Use of multiproducer queue mechanics is not supported.
HSA_QUEUE_TYPE_COOPERATIVE	Queue supports multiple producers and cooperative dispatches. Cooperative dispatches are able to use GWS synchronization. Queues of this type may be limited in number. The runtime may return the same queue to serve multiple hsa_queue_create calls when this type is given. Callers must inspect the returned queue to discover queue size. Queues of this type are reference counted and require a matching number of hsa_queue_destroy calls to release. Use of multiproducer queue mechanics is required. See HSA_AMD_AGENT_INFO_COOPERATIVE_QUEUES to query agent support for this type.

Definition at line [2212](#) of file [hsa.h](#).

5.5.4 Function Documentation

5.5.4.1 hsa_queue_add_write_index_acq_rel()

```
uint64_t HSA_API HSA_DEPRECATED hsa_queue_add_write_index_acq_rel (
    const hsa_queue_t * queue,
    uint64_t value )
```

Deprecated Renamed as [hsa_queue_add_write_index_scacq_screl](#).

Atomically increment the write index of a queue by an offset.

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>value</i>	Value to add to the write index.

Returns

Previous value of the write index.

5.5.4.2 hsa_queue_add_write_index_acquire()

```
uint64_t HSA_API HSA_DEPRECATED hsa_queue_add_write_index_acquire (
    const hsa_queue_t * queue,
    uint64_t value )
```

Deprecated Renamed as [hsa_queue_add_write_index_scacquire](#).

Atomically increment the write index of a queue by an offset.

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>value</i>	Value to add to the write index.

Returns

Previous value of the write index.

5.5.4.3 hsa_queue_add_write_index_relaxed()

```
uint64_t HSA_API hsa_queue_add_write_index_relaxed (
    const hsa_queue_t * queue,
    uint64_t value )
```

Atomically increment the write index of a queue by an offset.

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>value</i>	Value to add to the write index.

Returns

Previous value of the write index.

5.5.4.4 hsa_queue_add_write_index_release()

```
uint64_t HSA_API HSA_DEPRECATED hsa_queue_add_write_index_release (
    const hsa_queue_t * queue,
    uint64_t value )
```

Deprecated Renamed as [hsa_queue_add_write_index_screlease](#).

Atomically increment the write index of a queue by an offset.

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>value</i>	Value to add to the write index.

Returns

Previous value of the write index.

5.5.4.5 hsa_queue_add_write_index_scacq_screl()

```
uint64_t HSA_API hsa_queue_add_write_index_scacq_screl (
    const hsa_queue_t * queue,
    uint64_t value )
```

Atomically increment the write index of a queue by an offset.

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>value</i>	Value to add to the write index.

Returns

Previous value of the write index.

5.5.4.6 hsa_queue_add_write_index_scacquire()

```
uint64_t HSA_API hsa_queue_add_write_index_scacquire (
    const hsa_queue_t * queue,
    uint64_t value )
```

Atomically increment the write index of a queue by an offset.

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>value</i>	Value to add to the write index.

Returns

Previous value of the write index.

5.5.4.7 hsa_queue_add_write_index_screlease()

```
uint64_t HSA_API hsa_queue_add_write_index_screlease (
    const hsa_queue_t * queue,
    uint64_t value )
```

Atomically increment the write index of a queue by an offset.

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>value</i>	Value to add to the write index.

Returns

Previous value of the write index.

5.5.4.8 hsa_queue_cas_write_index_acq_rel()

```
uint64_t HSA_API HSA_DEPRECATED hsa_queue_cas_write_index_acq_rel (
    const hsa_queue_t * queue,
    uint64_t expected,
    uint64_t value )
```

Deprecated Renamed as [hsa_queue_cas_write_index_scacq_screl](#).

Atomically set the write index of a queue if the observed value is equal to the expected value. The application can inspect the returned value to determine if the replacement was done.

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>expected</i>	Expected value.
in	<i>value</i>	Value to assign to the write index if <i>expected</i> matches the observed write index. Must be greater than <i>expected</i> .

Returns

Previous value of the write index.

5.5.4.9 hsa_queue_cas_write_index_acquire()

```
uint64_t HSA_API HSA_DEPRECATED hsa_queue_cas_write_index_acquire (
    const hsa_queue_t * queue,
    uint64_t expected,
    uint64_t value )
```

Deprecated Renamed as [hsa_queue_cas_write_index_scacquire](#).

Atomically set the write index of a queue if the observed value is equal to the expected value. The application can inspect the returned value to determine if the replacement was done.

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>expected</i>	Expected value.
in	<i>value</i>	Value to assign to the write index if <i>expected</i> matches the observed write index. Must be greater than <i>expected</i> .

Returns

Previous value of the write index.

5.5.4.10 hsa_queue_cas_write_index_relaxed()

```
uint64_t HSA_API hsa_queue_cas_write_index_relaxed (
    const hsa_queue_t * queue,
```

```
uint64_t expected,
uint64_t value )
```

Atomically set the write index of a queue if the observed value is equal to the expected value. The application can inspect the returned value to determine if the replacement was done.

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>expected</i>	Expected value.
in	<i>value</i>	Value to assign to the write index if <i>expected</i> matches the observed write index. Must be greater than <i>expected</i> .

Returns

Previous value of the write index.

5.5.4.11 hsa_queue_cas_write_index_release()

```
uint64_t HSA_API HSA_DEPRECATED hsa_queue_cas_write_index_release (
    const hsa_queue_t * queue,
    uint64_t expected,
    uint64_t value )
```

Deprecated Renamed as [hsa_queue_cas_write_index_screlease](#).

Atomically set the write index of a queue if the observed value is equal to the expected value. The application can inspect the returned value to determine if the replacement was done.

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>expected</i>	Expected value.
in	<i>value</i>	Value to assign to the write index if <i>expected</i> matches the observed write index. Must be greater than <i>expected</i> .

Returns

Previous value of the write index.

5.5.4.12 hsa_queue_cas_write_index_scacq_screl()

```
uint64_t HSA_API hsa_queue_cas_write_index_scacq_screl (
    const hsa_queue_t * queue,
```

```
uint64_t expected,
uint64_t value )
```

Atomically set the write index of a queue if the observed value is equal to the expected value. The application can inspect the returned value to determine if the replacement was done.

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>expected</i>	Expected value.
in	<i>value</i>	Value to assign to the write index if <i>expected</i> matches the observed write index. Must be greater than <i>expected</i> .

Returns

Previous value of the write index.

5.5.4.13 hsa_queue_cas_write_index_scacquire()

```
uint64_t HSA_API hsa_queue_cas_write_index_scacquire (
    const hsa_queue_t * queue,
    uint64_t expected,
    uint64_t value )
```

Atomically set the write index of a queue if the observed value is equal to the expected value. The application can inspect the returned value to determine if the replacement was done.

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>expected</i>	Expected value.
in	<i>value</i>	Value to assign to the write index if <i>expected</i> matches the observed write index. Must be greater than <i>expected</i> .

Returns

Previous value of the write index.

5.5.4.14 hsa_queue_cas_write_index_screlease()

```
uint64_t HSA_API hsa_queue_cas_write_index_screlease (
    const hsa_queue_t * queue,
    uint64_t expected,
    uint64_t value )
```

Atomically set the write index of a queue if the observed value is equal to the expected value. The application can inspect the returned value to determine if the replacement was done.

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>expected</i>	Expected value.
in	<i>value</i>	Value to assign to the write index if <i>expected</i> matches the observed write index. Must be greater than <i>expected</i> .

Returns

Previous value of the write index.

5.5.4.15 `hsa_queue_create()`

```

hsa_status_t HSA_API hsa_queue_create (
    hsa_agent_t agent,
    uint32_t size,
    hsa_queue_type32_t type,
    void(*) (hsa_status_t status, hsa_queue_t *source, void *data) callback,
    void * data,
    uint32_t private_segment_size,
    uint32_t group_segment_size,
    hsa_queue_t ** queue )

```

Create a user mode queue.

The HSA runtime creates the queue structure, the underlying packet buffer, the completion signal, and the write and read indexes. The initial value of the write and read indexes is 0. The type of every packet in the buffer is initialized to [HSA_PACKET_TYPE_INVALID](#).

The application should only rely on the error code returned to determine if the queue is valid.

Parameters

in	<i>agent</i>	Agent where to create the queue.
in	<i>size</i>	Number of packets the queue is expected to hold. Must be a power of 2 between 1 and the value of HSA_AGENT_INFO_QUEUE_MAX_SIZE in <i>agent</i> . The size of the newly created queue is the maximum of <i>size</i> and the value of HSA_AGENT_INFO_QUEUE_MIN_SIZE in <i>agent</i> .
in	<i>type</i>	Type of the queue, a bitwise OR of <i>hsa_queue_type_t</i> values. If the value of HSA_AGENT_INFO_QUEUE_TYPE in <i>agent</i> is HSA_QUEUE_TYPE_SINGLE , then <i>type</i> must also be HSA_QUEUE_TYPE_SINGLE .
in	<i>callback</i>	Callback invoked by the HSA runtime for every asynchronous event related to the newly created queue. May be NULL. The HSA runtime passes three arguments to the callback: a code identifying the event that triggered the invocation, a pointer to the queue where the event originated, and the application data.
in	<i>data</i>	Application data that is passed to <i>callback</i> on every iteration. May be NULL.

Parameters

in	<i>private_segment_size</i>	Hint indicating the maximum expected private segment usage per work-item, in bytes. There may be performance degradation if the application places a kernel dispatch packet in the queue and the corresponding private segment usage exceeds <i>private_segment_size</i> . If the application does not want to specify any particular value for this argument, <i>private_segment_size</i> must be <code>UINT32_MAX</code> . If the queue does not support kernel dispatch packets, this argument is ignored.
in	<i>group_segment_size</i>	Hint indicating the maximum expected group segment usage per work-group, in bytes. There may be performance degradation if the application places a kernel dispatch packet in the queue and the corresponding group segment usage exceeds <i>group_segment_size</i> . If the application does not want to specify any particular value for this argument, <i>group_segment_size</i> must be <code>UINT32_MAX</code> . If the queue does not support kernel dispatch packets, this argument is ignored.
out	<i>queue</i>	Memory location where the HSA runtime stores a pointer to the newly created queue.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_OUT_OF_RESOURCES</i>	The HSA runtime failed to allocate the required resources.
<i>HSA_STATUS_ERROR_INVALID_AGENT</i>	The agent is invalid.
<i>HSA_STATUS_ERROR_INVALID_QUEUE_CREATION</i>	<i>agent</i> does not support queues of the given type.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>size</i> is not a power of two, <i>size</i> is 0, <i>type</i> is an invalid queue type, or <i>queue</i> is NULL.

5.5.4.16 `hsa_queue_destroy()`

```
hsa_status_t HSA_API hsa_queue_destroy (
    hsa_queue_t * queue )
```

Destroy a user mode queue.

When a queue is destroyed, the state of the AQL packets that have not been yet fully processed (their completion phase has not finished) becomes undefined. It is the responsibility of the application to ensure that all pending queue operations are finished if their results are required.

The resources allocated by the HSA runtime during queue creation (queue structure, ring buffer, doorbell signal) are released. The queue should not be accessed after being destroyed.

Parameters

in	<i>queue</i>	Pointer to a queue created using <code>hsa_queue_create</code> .
----	--------------	--

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_QUEUE</i>	The queue is invalid.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	queue is NULL.

5.5.4.17 `hsa_queue_inactivate()`

```
hsa_status_t HSA_API hsa_queue_inactivate (
    hsa_queue_t * queue )
```

Inactivate a queue.

Inactivating the queue aborts any pending executions and prevent any new packets from being processed. Any more packets written to the queue once it is inactivated will be ignored by the packet processor.

Parameters

in	<i>queue</i>	Pointer to a queue.
----	--------------	---------------------

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_QUEUE</i>	The queue is invalid.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	queue is NULL.

5.5.4.18 `hsa_queue_load_read_index_acquire()`

```
uint64_t HSA_API HSA_DEPRECATED hsa_queue_load_read_index_acquire (
    const hsa_queue_t * queue )
```

Deprecated Renamed as [`hsa_queue_load_read_index_scacquire`](#).

Atomically load the read index of a queue.

Parameters

in	<i>queue</i>	Pointer to a queue.
----	--------------	---------------------

Returns

Read index of the queue pointed by `queue`.

5.5.4.19 hsa_queue_load_read_index_relaxed()

```
uint64_t HSA_API hsa_queue_load_read_index_relaxed (
    const hsa_queue_t * queue )
```

Atomically load the read index of a queue.

Parameters

in	<i>queue</i>	Pointer to a queue.
----	--------------	---------------------

Returns

Read index of the queue pointed by `queue`.

5.5.4.20 hsa_queue_load_read_index_scacquire()

```
uint64_t HSA_API hsa_queue_load_read_index_scacquire (
    const hsa_queue_t * queue )
```

Atomically load the read index of a queue.

Parameters

in	<i>queue</i>	Pointer to a queue.
----	--------------	---------------------

Returns

Read index of the queue pointed by `queue`.

5.5.4.21 hsa_queue_load_write_index_acquire()

```
uint64_t HSA_API HSA_DEPRECATED hsa_queue_load_write_index_acquire (
    const hsa_queue_t * queue )
```

Deprecated Renamed as [hsa_queue_load_write_index_scacquire](#).

Atomically load the write index of a queue.

Parameters

in	<i>queue</i>	Pointer to a queue.
----	--------------	---------------------

Returns

Write index of the queue pointed by *queue*.

5.5.4.22 hsa_queue_load_write_index_relaxed()

```
uint64_t HSA_API hsa_queue_load_write_index_relaxed (
    const hsa_queue_t * queue )
```

Atomically load the write index of a queue.

Parameters

in	<i>queue</i>	Pointer to a queue.
----	--------------	---------------------

Returns

Write index of the queue pointed by *queue*.

5.5.4.23 hsa_queue_load_write_index_scacquire()

```
uint64_t HSA_API hsa_queue_load_write_index_scacquire (
    const hsa_queue_t * queue )
```

Atomically load the write index of a queue.

Parameters

in	<i>queue</i>	Pointer to a queue.
----	--------------	---------------------

Returns

Write index of the queue pointed by *queue*.

5.5.4.24 hsa_queue_store_read_index_relaxed()

```
void HSA_API hsa_queue_store_read_index_relaxed (
    const hsa_queue_t * queue,
    uint64_t value )
```

Atomically set the read index of a queue.

Modifications of the read index are not allowed and result in undefined behavior if the queue is associated with an agent for which only the corresponding packet processor is permitted to update the read index.

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>value</i>	Value to assign to the read index.

5.5.4.25 hsa_queue_store_read_index_release()

```
void HSA_API HSA_DEPRECATED hsa_queue_store_read_index_release (
    const hsa_queue_t * queue,
    uint64_t value )
```

Deprecated Renamed as [hsa_queue_store_read_index_screlease](#).

Atomically set the read index of a queue.

Modifications of the read index are not allowed and result in undefined behavior if the queue is associated with an agent for which only the corresponding packet processor is permitted to update the read index.

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>value</i>	Value to assign to the read index.

5.5.4.26 hsa_queue_store_read_index_screlease()

```
void HSA_API hsa_queue_store_read_index_screlease (
    const hsa_queue_t * queue,
    uint64_t value )
```

Atomically set the read index of a queue.

Modifications of the read index are not allowed and result in undefined behavior if the queue is associated with an agent for which only the corresponding packet processor is permitted to update the read index.

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>value</i>	Value to assign to the read index.

5.5.4.27 `hsa_queue_store_write_index_relaxed()`

```
void HSA_API hsa_queue_store_write_index_relaxed (
    const hsa\_queue\_t * queue,
    uint64_t value )
```

Atomically set the write index of a queue.

It is recommended that the application uses this function to update the write index when there is a single agent submitting work to the queue (the queue type is [HSA_QUEUE_TYPE_SINGLE](#)).

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>value</i>	Value to assign to the write index.

5.5.4.28 `hsa_queue_store_write_index_release()`

```
void HSA_API HSA_DEPRECATED hsa_queue_store_write_index_release (
    const hsa\_queue\_t * queue,
    uint64_t value )
```

Deprecated Renamed as [hsa_queue_store_write_index_screlease](#).

Atomically set the write index of a queue.

It is recommended that the application uses this function to update the write index when there is a single agent submitting work to the queue (the queue type is [HSA_QUEUE_TYPE_SINGLE](#)).

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>value</i>	Value to assign to the write index.

5.5.4.29 `hsa_queue_store_write_index_screlease()`

```
void HSA_API hsa_queue_store_write_index_screlease (
    const hsa\_queue\_t * queue,
    uint64_t value )
```

Atomically set the write index of a queue.

It is recommended that the application uses this function to update the write index when there is a single agent submitting work to the queue (the queue type is [HSA_QUEUE_TYPE_SINGLE](#)).

Parameters

in	<i>queue</i>	Pointer to a queue.
in	<i>value</i>	Value to assign to the write index.

5.5.4.30 hsa_soft_queue_create()

```

hsa_status_t HSA_API hsa_soft_queue_create (
    hsa_region_t region,
    uint32_t size,
    hsa_queue_type32_t type,
    uint32_t features,
    hsa_signal_t doorbell_signal,
    hsa_queue_t ** queue )

```

Create a queue for which the application or a kernel is responsible for processing the AQL packets.

The application can use this function to create queues where AQL packets are not parsed by the packet processor associated with an agent, but rather by a unit of execution running on that agent (for example, a thread in the host application).

The application is responsible for ensuring that all the producers and consumers of the resulting queue can access the provided doorbell signal and memory region. The application is also responsible for ensuring that the unit of execution processing the queue packets supports the indicated features (AQL packet types).

When the queue is created, the HSA runtime allocates the packet buffer using *region*, and the write and read indexes. The initial value of the write and read indexes is 0, and the type of every packet in the buffer is initialized to [HSA_PACKET_TYPE_INVALID](#). The value of the *size*, *type*, *features*, and *doorbell_signal* fields in the returned queue match the values passed by the application.

Parameters

in	<i>region</i>	Memory region that the HSA runtime should use to allocate the AQL packet buffer and any other queue metadata.
in	<i>size</i>	Number of packets the queue is expected to hold. Must be a power of 2 greater than 0.
in	<i>type</i>	Queue type.
in	<i>features</i>	Supported queue features. This is a bit-field of hsa_queue_feature_t values.
in	<i>doorbell_signal</i>	Doorbell signal that the HSA runtime must associate with the returned queue. The signal handle must not be 0.
out	<i>queue</i>	Memory location where the HSA runtime stores a pointer to the newly created queue. The application should not rely on the value returned for this argument but only in the status code to determine if the queue is valid. Must not be NULL.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_OUT_OF_RESOURCES	The HSA runtime failed to allocate the required resources.

Return values

HSA_STATUS_ERROR_INVALID_ARGUMENT	size is not a power of two, size is 0, type is an invalid queue type, the doorbell signal handle is 0, or queue is NULL.
---	--

5.6 Architected Queuing Language

Classes

- struct [hsa_kernel_dispatch_packet_s](#)
AQL kernel dispatch packet.
- struct [hsa_agent_dispatch_packet_s](#)
Agent dispatch packet.
- struct [hsa_barrier_and_packet_s](#)
Barrier-AND packet.
- struct [hsa_barrier_or_packet_s](#)
Barrier-OR packet.
- struct [hsa_amd_packet_header_s](#)
AMD vendor specific AQL packet header.
- struct [hsa_amd_barrier_value_packet_s](#)
AMD barrier value packet. Halts packet processing and waits for (signal_value & mask) cond value to be satisfied, where signal_value is the value of the signal signal.

Typedefs

- typedef struct [hsa_kernel_dispatch_packet_s](#) [hsa_kernel_dispatch_packet_t](#)
AQL kernel dispatch packet.
- typedef struct [hsa_agent_dispatch_packet_s](#) [hsa_agent_dispatch_packet_t](#)
Agent dispatch packet.
- typedef struct [hsa_barrier_and_packet_s](#) [hsa_barrier_and_packet_t](#)
Barrier-AND packet.
- typedef struct [hsa_barrier_or_packet_s](#) [hsa_barrier_or_packet_t](#)
Barrier-OR packet.
- typedef uint32_t [hsa_signal_condition32_t](#)
A fixed-size type used to represent [hsa_signal_condition_t](#) constants.
- typedef uint8_t [hsa_amd_packet_type8_t](#)
A fixed-size type used to represent [hsa_amd_packet_type_t](#) constants.
- typedef struct [hsa_amd_packet_header_s](#) [hsa_amd_vendor_packet_header_t](#)
AMD vendor specific AQL packet header.
- typedef struct [hsa_amd_barrier_value_packet_s](#) [hsa_amd_barrier_value_packet_t](#)
AMD barrier value packet. Halts packet processing and waits for (signal_value & ::mask) ::cond ::value to be satisfied, where signal_value is the value of the signal ::signal.

Enumerations

- enum `hsa_packet_type_t` {
`HSA_PACKET_TYPE_VENDOR_SPECIFIC` = 0 , `HSA_PACKET_TYPE_INVALID` = 1 , `HSA_PACKET_TYPE_KERNEL_DISPATCH` = 2 , `HSA_PACKET_TYPE_BARRIER_AND` = 3 ,
`HSA_PACKET_TYPE_AGENT_DISPATCH` = 4 , `HSA_PACKET_TYPE_BARRIER_OR` = 5 }
Packet type.
- enum `hsa_fence_scope_t` { `HSA_FENCE_SCOPE_NONE` = 0 , `HSA_FENCE_SCOPE_AGENT` = 1 ,
`HSA_FENCE_SCOPE_SYSTEM` = 2 }
Scope of the memory fence operation associated with a packet.
- enum `hsa_packet_header_t` {
`HSA_PACKET_HEADER_TYPE` = 0 , `HSA_PACKET_HEADER_BARRIER` = 8 , `HSA_PACKET_HEADER_SCACQUIRE_FENCE_SCOPE` = 9 , `HSA_PACKET_HEADER_ACQUIRE_FENCE_SCOPE` = 9 ,
`HSA_PACKET_HEADER_SCRELEASE_FENCE_SCOPE` = 11 , `HSA_PACKET_HEADER_RELEASE_FENCE_SCOPE` = 11 }
Sub-fields of the header field that is present in any AQL packet. The offset (with respect to the address of header) of a sub-field is identical to its enumeration constant. The width of each sub-field is determined by the corresponding value in `hsa_packet_header_width_t`. The offset and the width are expressed in bits.
- enum `hsa_packet_header_width_t` {
`HSA_PACKET_HEADER_WIDTH_TYPE` = 8 , `HSA_PACKET_HEADER_WIDTH_BARRIER` = 1 , `HSA_PACKET_HEADER_WIDTH_SCACQUIRE_FENCE_SCOPE` = 2 , `HSA_PACKET_HEADER_WIDTH_ACQUIRE_FENCE_SCOPE` = 2 ,
`HSA_PACKET_HEADER_WIDTH_SCRELEASE_FENCE_SCOPE` = 2 , `HSA_PACKET_HEADER_WIDTH_RELEASE_FENCE_SCOPE` = 2 }
Width (in bits) of the sub-fields in `hsa_packet_header_t`.
- enum `hsa_kernel_dispatch_packet_setup_t` { `HSA_KERNEL_DISPATCH_PACKET_SETUP_DIMENSIONS` = 0 }
Sub-fields of the kernel dispatch packet setup field. The offset (with respect to the address of setup) of a sub-field is identical to its enumeration constant. The width of each sub-field is determined by the corresponding value in `hsa_kernel_dispatch_packet_setup_width_t`. The offset and the width are expressed in bits.
- enum `hsa_kernel_dispatch_packet_setup_width_t` { `HSA_KERNEL_DISPATCH_PACKET_SETUP_DIMENSIONS` = 2 }
Width (in bits) of the sub-fields in `hsa_kernel_dispatch_packet_setup_t`.
- enum `hsa_amd_packet_type_t` { `HSA_AMD_PACKET_TYPE_BARRIER_VALUE` = 2 }
AMD vendor specific packet type.

5.6.1 Detailed Description

5.6.2 Typedef Documentation

5.6.2.1 `hsa_amd_packet_type8_t`

```
typedef uint8_t hsa_amd_packet_type8_t
```

A fixed-size type used to represent `hsa_amd_packet_type_t` constants.

Definition at line 82 of file `hsa_ext_amd.h`.

5.6.2.2 hsa_signal_condition32_t

```
typedef uint32_t hsa_signal_condition32_t
```

A fixed-size type used to represent [hsa_signal_condition_t](#) constants.

Definition at line 65 of file [hsa_ext_amd.h](#).

5.6.3 Enumeration Type Documentation

5.6.3.1 hsa_amd_packet_type_t

```
enum hsa_amd_packet_type_t
```

AMD vendor specific packet type.

Enumerator

HSA_AMD_PACKET_TYPE_BARRIER_VALUE	Packet used by agents to delay processing of subsequent packets until a configurable condition is satisfied by an HSA signal. Only kernel dispatch queues created from AMD GPU Agents support this packet.
-----------------------------------	--

Definition at line 70 of file [hsa_ext_amd.h](#).

5.6.3.2 hsa_fence_scope_t

```
enum hsa_fence_scope_t
```

Scope of the memory fence operation associated with a packet.

Enumerator

HSA_FENCE_SCOPE_NONE	No scope (no fence is applied). The packet relies on external fences to ensure visibility of memory updates.
HSA_FENCE_SCOPE_AGENT	The fence is applied with agent scope for the global segment.
HSA_FENCE_SCOPE_SYSTEM	The fence is applied across both agent and system scope for the global segment.

Definition at line 2807 of file [hsa.h](#).

5.6.3.3 hsa_kernel_dispatch_packet_setup_t

enum [hsa_kernel_dispatch_packet_setup_t](#)

Sub-fields of the kernel dispatch packet *setup* field. The offset (with respect to the address of *setup*) of a sub-field is identical to its enumeration constant. The width of each sub-field is determined by the corresponding value in [hsa_kernel_dispatch_packet_setup_width_t](#). The offset and the width are expressed in bits.

Enumerator

HSA_KERNEL_DISPATCH_PACKET_SETUP_↔ DIMENSIONS	Number of dimensions of the grid. Valid values are 1, 2, or 3.
--	--

Definition at line 2899 of file [hsa.h](#).

5.6.3.4 hsa_kernel_dispatch_packet_setup_width_t

enum [hsa_kernel_dispatch_packet_setup_width_t](#)

Width (in bits) of the sub-fields in [hsa_kernel_dispatch_packet_setup_t](#).

Definition at line 2911 of file [hsa.h](#).

5.6.3.5 hsa_packet_header_t

enum [hsa_packet_header_t](#)

Sub-fields of the *header* field that is present in any AQL packet. The offset (with respect to the address of *header*) of a sub-field is identical to its enumeration constant. The width of each sub-field is determined by the corresponding value in [hsa_packet_header_width_t](#). The offset and the width are expressed in bits.

Enumerator

HSA_PACKET_HEADER_TYPE	Packet type. The value of this sub-field must be one of hsa_packet_type_t . If the type is HSA_PACKET_TYPE_VENDOR_SPECIFIC , the packet layout is vendor-specific.
HSA_PACKET_HEADER_BARRIER	Barrier bit. If the barrier bit is set, the processing of the current packet only launches when all preceding packets (within the same queue) are complete.
HSA_PACKET_HEADER_SCACQUIRE_FENCE_↔ SCOPE	Acquire fence scope. The value of this sub-field determines the scope and type of the memory fence operation applied before the packet enters the active phase. An acquire fence ensures that any subsequent global segment or image loads by any unit of execution that belongs to a dispatch that has not yet entered the active phase on any queue of the same kernel agent, sees any data previously released at the scopes specified by the acquire fence. The value of this sub-field must be one of hsa_fence_scope_t .

Enumerator

HSA_PACKET_HEADER_ACQUIRE_FENCE_↔ SCOPE	Deprecated Renamed as HSA_PACKET_HEADER_SCACQUIRE_FENCE_SCOPE .
HSA_PACKET_HEADER_SCRELEASE_FENCE_↔ SCOPE	Release fence scope, The value of this sub-field determines the scope and type of the memory fence operation applied after kernel completion but before the packet is completed. A release fence makes any global segment or image data that was stored by any unit of execution that belonged to a dispatch that has completed the active phase on any queue of the same kernel agent visible in all the scopes specified by the release fence. The value of this sub-field must be one of hsa_fence_scope_t .
HSA_PACKET_HEADER_RELEASE_FENCE_↔ SCOPE	Deprecated Renamed as HSA_PACKET_HEADER_SCRELEASE_FENCE_SCOPE .

Definition at line [2831](#) of file [hsa.h](#).

5.6.3.6 hsa_packet_header_width_t

enum [hsa_packet_header_width_t](#)

Width (in bits) of the sub-fields in [hsa_packet_header_t](#).

Enumerator

HSA_PACKET_HEADER_WIDTH_ACQUIRE_↔ FENCE_SCOPE	Deprecated Use HSA_PACKET_HEADER_WIDTH_↔ SCACQUIRE_FENCE_SCOPE .
HSA_PACKET_HEADER_WIDTH_RELEASE_↔ FENCE_SCOPE	Deprecated Use HSA_PACKET_HEADER_WIDTH_↔ SCRELEASE_FENCE_SCOPE .

Definition at line [2877](#) of file [hsa.h](#).

5.6.3.7 hsa_packet_type_t

enum [hsa_packet_type_t](#)

Packet type.

Enumerator

HSA_PACKET_TYPE_VENDOR_SPECIFIC	Vendor-specific packet.
HSA_PACKET_TYPE_INVALID	The packet has been processed in the past, but has not been reassigned to the packet processor. A packet processor must not process a packet of this type. All queues support this packet type.
HSA_PACKET_TYPE_KERNEL_DISPATCH	Packet used by agents for dispatching jobs to kernel agents. Not all queues support packets of this type (see hsa_queue_feature_t).
HSA_PACKET_TYPE_BARRIER_AND	Packet used by agents to delay processing of subsequent packets, and to express complex dependencies between multiple packets. All queues support this packet type.
HSA_PACKET_TYPE_AGENT_DISPATCH	Packet used by agents for dispatching jobs to agents. Not all queues support packets of this type (see hsa_queue_feature_t).
HSA_PACKET_TYPE_BARRIER_OR	Packet used by agents to delay processing of subsequent packets, and to express complex dependencies between multiple packets. All queues support this packet type.

Definition at line 2769 of file [hsa.h](#).

5.7 Instruction Set Architecture.

Classes

- struct [hsa_isa_s](#)
Instruction set architecture.
- struct [hsa_wavefront_s](#)
Wavefront handle.

Typedefs

- typedef struct [hsa_isa_s](#) [hsa_isa_t](#)
Instruction set architecture.
- typedef struct [hsa_wavefront_s](#) [hsa_wavefront_t](#)
Wavefront handle.

Enumerations

- enum [hsa_isa_info_t](#) {
[HSA_ISA_INFO_NAME_LENGTH](#) = 0 , [HSA_ISA_INFO_NAME](#) = 1 , [HSA_ISA_INFO_CALL_CONVENTION_COUNT](#) = 2 , [HSA_ISA_INFO_CALL_CONVENTION_INFO_WAVEFRONT_SIZE](#) = 3 ,
[HSA_ISA_INFO_CALL_CONVENTION_INFO_WAVEFRONTS_PER_COMPUTE_UNIT](#) = 4 , [HSA_ISA_INFO_MACHINE_MODEL](#) = 5 , [HSA_ISA_INFO_PROFILES](#) = 6 , [HSA_ISA_INFO_DEFAULT_FLOAT_ROUNDING_MODES](#) = 7 ,
[HSA_ISA_INFO_BASE_PROFILE_DEFAULT_FLOAT_ROUNDING_MODES](#) = 8 , [HSA_ISA_INFO_FAST_F16_OPERATION](#) = 9 , [HSA_ISA_INFO_WORKGROUP_MAX_DIM](#) = 12 , [HSA_ISA_INFO_WORKGROUP_MAX_SIZE](#) = 13 ,
[HSA_ISA_INFO_GRID_MAX_DIM](#) = 14 , [HSA_ISA_INFO_GRID_MAX_SIZE](#) = 16 , [HSA_ISA_INFO_FBARRIER_MAX_SIZE](#) = 17 }

Instruction set architecture attributes.

- enum `hsa_fp_type_t` { `HSA_FP_TYPE_16` = 1 , `HSA_FP_TYPE_32` = 2 , `HSA_FP_TYPE_64` = 4 }

Floating-point types.

- enum `hsa_flush_mode_t` { `HSA_FLUSH_MODE_FTZ` = 1 , `HSA_FLUSH_MODE_NON_FTZ` = 2 }

Flush to zero modes.

- enum `hsa_round_method_t` { `HSA_ROUND_METHOD_SINGLE` = 1 , `HSA_ROUND_METHOD_DOUBLE` = 2 }

Round methods.

- enum `hsa_wavefront_info_t` { `HSA_WAVEFRONT_INFO_SIZE` = 0 }

Wavefront attributes.

Functions

- `hsa_status_t` HSA_API `hsa_isa_from_name` (const char *name, `hsa_isa_t` *isa)

Retrieve a reference to an instruction set architecture handle out of a symbolic name.

- `hsa_status_t` HSA_API `hsa_agent_iterate_isas` (`hsa_agent_t` agent, `hsa_status_t`(*callback)(`hsa_isa_t` isa, void *data), void *data)

Iterate over the instruction sets supported by the given agent, and invoke an application-defined callback on every iteration. The iterator is deterministic: if an agent supports several instruction set architectures, they are traversed in the same order in every invocation of this function.

- `hsa_status_t` HSA_API HSA_DEPRECATED `hsa_isa_get_info` (`hsa_isa_t` isa, `hsa_isa_info_t` attribute, uint32_t index, void *value)

Get the current value of an attribute for a given instruction set architecture (ISA).

- `hsa_status_t` HSA_API `hsa_isa_get_info_alt` (`hsa_isa_t` isa, `hsa_isa_info_t` attribute, void *value)

Get the current value of an attribute for a given instruction set architecture (ISA).

- `hsa_status_t` HSA_API `hsa_isa_get_exception_policies` (`hsa_isa_t` isa, `hsa_profile_t` profile, uint16_t *mask)

Retrieve the exception policy support for a given combination of instruction set architecture and profile.

- `hsa_status_t` HSA_API `hsa_isa_get_round_method` (`hsa_isa_t` isa, `hsa_fp_type_t` fp_type, `hsa_flush_mode_t` flush_mode, `hsa_round_method_t` *round_method)

Retrieve the round method (single or double) used to implement the floating-point multiply add instruction (mad) for a given combination of instruction set architecture, floating-point type, and flush to zero modifier.

- `hsa_status_t` HSA_API `hsa_wavefront_get_info` (`hsa_wavefront_t` wavefront, `hsa_wavefront_info_t` attribute, void *value)

Get the current value of a wavefront attribute.

- `hsa_status_t` HSA_API `hsa_isa_iterate_wavefronts` (`hsa_isa_t` isa, `hsa_status_t`(*callback)(`hsa_wavefront_t` wavefront, void *data), void *data)

Iterate over the different wavefronts supported by an instruction set architecture, and invoke an application-defined callback on every iteration.

- `hsa_status_t` HSA_API HSA_DEPRECATED `hsa_isa_compatible` (`hsa_isa_t` code_object_isa, `hsa_isa_t` agent_isa, bool *result)

Check if the instruction set architecture of a code object can be executed on an agent associated with another architecture.

5.7.1 Detailed Description

5.7.2 Enumeration Type Documentation

5.7.2.1 `hsa_flush_mode_t`

enum `hsa_flush_mode_t`

Flush to zero modes.

Enumerator

HSA_FLUSH_MODE_FTZ	Flush to zero.
HSA_FLUSH_MODE_NON_FTZ	Do not flush to zero.

Definition at line 3839 of file [hsa.h](#).

5.7.2.2 hsa_fp_type_t

enum [hsa_fp_type_t](#)

Floating-point types.

Enumerator

HSA_FP_TYPE_16	16-bit floating-point type.
HSA_FP_TYPE_32	32-bit floating-point type.
HSA_FP_TYPE_64	64-bit floating-point type.

Definition at line 3821 of file [hsa.h](#).

5.7.2.3 hsa_isa_info_t

enum [hsa_isa_info_t](#)

Instruction set architecture attributes.

Enumerator

HSA_ISA_INFO_NAME_LENGTH	The length of the ISA name in bytes, not including the NUL terminator. The type of this attribute is uint32_t.
HSA_ISA_INFO_NAME	Human-readable description. The type of this attribute is character array with the length equal to the value of HSA_ISA_INFO_NAME_LENGTH attribute.
HSA_ISA_INFO_CALL_CONVENTION_COUNT	Deprecated Number of call conventions supported by the instruction set architecture. Must be greater than zero. The type of this attribute is uint32_t.
HSA_ISA_INFO_CALL_CONVENTION_INFO↔ WAVEFRONT_SIZE	Deprecated Number of work-items in a wavefront for a given call convention. Must be a power of 2 in the range [1,256]. The type of this attribute is uint32_t.

Enumerator

HSA_ISA_INFO_CALL_CONVENTION_INFO_↔ WAVEFRONTS_PER_COMPUTE_UNIT	Deprecated Number of wavefronts per compute unit for a given call convention. In practice, other factors (for example, the amount of group memory used by a work-group) may further limit the number of wavefronts per compute unit. The type of this attribute is uint32_t.
HSA_ISA_INFO_MACHINE_MODELS	Machine models supported by the instruction set architecture. The type of this attribute is a bool[2]. If the ISA supports the small machine model, the element at index HSA_MACHINE_MODEL_SMALL is true. If the ISA supports the large model, the element at index HSA_MACHINE_MODEL_LARGE is true.
HSA_ISA_INFO_PROFILES	Profiles supported by the instruction set architecture. The type of this attribute is a bool[2]. If the ISA supports the base profile, the element at index HSA_PROFILE_BASE is true. If the ISA supports the full profile, the element at index HSA_PROFILE_FULL is true.
HSA_ISA_INFO_DEFAULT_FLOAT_ROUNDING_↔ MODES	Default floating-point rounding modes supported by the instruction set architecture. The type of this attribute is a bool[3]. The value at a given index is true if the corresponding rounding mode in hsa_default_float_rounding_mode_t is supported. At least one default mode has to be supported. If the default mode is supported, then HSA_ISA_INFO_BASE_PROFILE_DEFAULT_FLOAT_ROUNDING_MODE must report that both the zero and the near roundings modes are supported.
HSA_ISA_INFO_BASE_PROFILE_DEFAULT_↔ FLOAT_ROUNDING_MODES	Default floating-point rounding modes supported by the instruction set architecture in the Base profile. The type of this attribute is a bool[3]. The value at a given index is true if the corresponding rounding mode in hsa_default_float_rounding_mode_t is supported. The value at index HSA_DEFAULT_FLOAT_ROUNDING_MODE_DEFAULT must be false. At least one of the values at indexes HSA_DEFAULT_FLOAT_ROUNDING_MODE_ZERO or HSA_DEFAULT_FLOAT_ROUNDING_MODE_NEAR must be true.
HSA_ISA_INFO_FAST_F16_OPERATION	Flag indicating that the f16 HSAIL operation is at least as fast as the f32 operation in the instruction set architecture. The type of this attribute is bool.
HSA_ISA_INFO_WORKGROUP_MAX_DIM	Maximum number of work-items of each dimension of a work-group. Each maximum must be greater than 0. No maximum can exceed the value of HSA_ISA_INFO_WORKGROUP_MAX_SIZE . The type of this attribute is uint16_t[3].
HSA_ISA_INFO_WORKGROUP_MAX_SIZE	Maximum total number of work-items in a work-group. The type of this attribute is uint32_t.

Enumerator

HSA_ISA_INFO_GRID_MAX_DIM	Maximum number of work-items of each dimension of a grid. Each maximum must be greater than 0, and must not be smaller than the corresponding value in HSA_ISA_INFO_WORKGROUP_MAX_DIM . No maximum can exceed the value of HSA_ISA_INFO_GRID_MAX_SIZE . The type of this attribute is hsa_dim3_t .
HSA_ISA_INFO_GRID_MAX_SIZE	Maximum total number of work-items in a grid. The type of this attribute is uint64_t .
HSA_ISA_INFO_FBARrier_MAX_SIZE	Maximum number of fbarriers per work-group. Must be at least 32. The type of this attribute is uint32_t .

Definition at line [3611](#) of file [hsa.h](#).

5.7.2.4 **hsa_round_method_t**

enum [hsa_round_method_t](#)

Round methods.

Enumerator

HSA_ROUND_METHOD_SINGLE	Single round method.
HSA_ROUND_METHOD_DOUBLE	Double round method.

Definition at line [3853](#) of file [hsa.h](#).

5.7.2.5 **hsa_wavefront_info_t**

enum [hsa_wavefront_info_t](#)

Wavefront attributes.

Enumerator

HSA_WAVEFRONT_INFO_SIZE	Number of work-items in the wavefront. Must be a power of 2 in the range [1,256]. The type of this attribute is uint32_t .
-------------------------	--

Definition at line [3911](#) of file [hsa.h](#).

5.7.3 **Function Documentation**

5.7.3.1 hsa_agent_iterate_isas()

```
hsa_status_t HSA_API hsa_agent_iterate_isas (
    hsa_agent_t agent,
    hsa_status_t(*) (hsa_isa_t isa, void *data) callback,
    void * data )
```

Iterate over the instruction sets supported by the given agent, and invoke an application-defined callback on every iteration. The iterator is deterministic: if an agent supports several instruction set architectures, they are traversed in the same order in every invocation of this function.

Parameters

in	<i>agent</i>	A valid agent.
in	<i>callback</i>	Callback to be invoked once per instruction set architecture. The HSA runtime passes two arguments to the callback: the ISA and the application data. If <i>callback</i> returns a status other than HSA_STATUS_SUCCESS for a particular iteration, the traversal stops and that status value is returned.
in	<i>data</i>	Application data that is passed to <i>callback</i> on every iteration. May be NULL.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_AGENT	The agent is invalid.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>callback</i> is NULL.

5.7.3.2 hsa_isa_compatible()

```
hsa_status_t HSA_API HSA_DEPRECATED hsa_isa_compatible (
    hsa_isa_t code_object_isa,
    hsa_isa_t agent_isa,
    bool * result )
```

Check if the instruction set architecture of a code object can be executed on an agent associated with another architecture.

Deprecated Use [hsa_agent_iterate_isas](#) to query which instructions set architectures are supported by a given agent.

Parameters

in	<i>code_object_isa</i>	Instruction set architecture associated with a code object.
in	<i>agent_isa</i>	Instruction set architecture associated with an agent.
out	<i>result</i>	Pointer to a memory location where the HSA runtime stores the result of the check. If the two architectures are compatible, the result is true; if they are incompatible, the result is false.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_ISA</i>	<code>code_object_isa</code> or <code>agent_isa</code> are invalid.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<code>result</code> is NULL.

5.7.3.3 `hsa_isa_from_name()`

```

hsa_status_t HSA_API hsa_isa_from_name (
    const char * name,
    hsa_isa_t * isa )

```

Retrieve a reference to an instruction set architecture handle out of a symbolic name.

Parameters

in	<i>name</i>	Vendor-specific name associated with a particular instruction set architecture. <i>name</i> must start with the vendor name and a colon (for example, "AMD:"). The rest of the name is vendor-specific. Must be a NUL-terminated string.
out	<i>isa</i>	Memory location where the HSA runtime stores the ISA handle corresponding to the given name. Must not be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_ISA_NAME</i>	The given name does not correspond to any instruction set architecture.
<i>HSA_STATUS_ERROR_OUT_OF_RESOURCES</i>	The HSA runtime failed to allocate the required resources.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>name</i> is NULL, or <i>isa</i> is NULL.

5.7.3.4 `hsa_isa_get_exception_policies()`

```

hsa_status_t HSA_API hsa_isa_get_exception_policies (
    hsa_isa_t isa,
    hsa_profile_t profile,
    uint16_t * mask )

```

Retrieve the exception policy support for a given combination of instruction set architecture and profile.

Parameters

in	<i>isa</i>	A valid instruction set architecture.
in	<i>profile</i>	Profile.
out	<i>mask</i>	Pointer to a memory location where the HSA runtime stores a mask of <i>hsa_exception_policy_t</i> values. Must not be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_ISA</i>	The instruction set architecture is invalid.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<code>profile</code> is not a valid profile, or <code>mask</code> is NULL.

5.7.3.5 `hsa_isa_get_info()`

```

hsa_status_t HSA_API HSA_DEPRECATED hsa_isa_get_info (
    hsa_isa_t isa,
    hsa_isa_info_t attribute,
    uint32_t index,
    void * value )

```

Get the current value of an attribute for a given instruction set architecture (ISA).

Deprecated The concept of call convention has been deprecated. If the application wants to query the value of an attribute for a given instruction set architecture, use [`hsa_isa_get_info_alt`](#) instead. If the application wants to query an attribute that is specific to a given combination of ISA and wavefront, use [`hsa_wavefront_get_info`](#).

Parameters

in	<i>isa</i>	A valid instruction set architecture.
in	<i>attribute</i>	Attribute to query.
in	<i>index</i>	Call convention index. Used only for call convention attributes, otherwise ignored. Must have a value between 0 (inclusive) and the value of the attribute <code>HSA_ISA_INFO_CALL_CONVENTION_COUNT</code> (not inclusive) in <i>isa</i> .
out	<i>value</i>	Pointer to an application-allocated buffer where to store the value of the attribute. If the buffer passed by the application is not large enough to hold the value of <i>attribute</i> , the behavior is undefined.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_ISA</i>	The instruction set architecture is invalid.
<i>HSA_STATUS_ERROR_INVALID_INDEX</i>	The index is out of range.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>attribute</i> is an invalid instruction set architecture attribute, or <i>value</i> is NULL.

5.7.3.6 `hsa_isa_get_info_alt()`

```

hsa_status_t HSA_API hsa_isa_get_info_alt (

```

```

hsa_isa_t isa,
hsa_isa_info_t attribute,
void * value )

```

Get the current value of an attribute for a given instruction set architecture (ISA).

Parameters

in	<i>isa</i>	A valid instruction set architecture.
in	<i>attribute</i>	Attribute to query.
out	<i>value</i>	Pointer to an application-allocated buffer where to store the value of the attribute. If the buffer passed by the application is not large enough to hold the value of <i>attribute</i> , the behavior is undefined.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_ISA</i>	The instruction set architecture is invalid.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>attribute</i> is an invalid instruction set architecture attribute, or <i>value</i> is NULL.

5.7.3.7 hsa_isa_get_round_method()

```

hsa_status_t HSA_API hsa_isa_get_round_method (
    hsa_isa_t isa,
    hsa_fp_type_t fp_type,
    hsa_flush_mode_t flush_mode,
    hsa_round_method_t * round_method )

```

Retrieve the round method (single or double) used to implement the floating-point multiply add instruction (mad) for a given combination of instruction set architecture, floating-point type, and flush to zero modifier.

Parameters

in	<i>isa</i>	Instruction set architecture.
in	<i>fp_type</i>	Floating-point type.
in	<i>flush_mode</i>	Flush to zero modifier.
out	<i>round_method</i>	Pointer to a memory location where the HSA runtime stores the round method used by the implementation. Must not be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_ISA</i>	The instruction set architecture is invalid.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>fp_type</i> is not a valid floating-point type, or <i>flush_mode</i> is not a valid flush to zero modifier, or <i>round_method</i> is NULL.

5.7.3.8 hsa_isa_iterate_wavefronts()

```
hsa_status_t HSA_API hsa_isa_iterate_wavefronts (
    hsa_isa_t isa,
    hsa_status_t(*) (hsa_wavefront_t wavefront, void *data) callback,
    void * data )
```

Iterate over the different wavefronts supported by an instruction set architecture, and invoke an application-defined callback on every iteration.

Parameters

in	<i>isa</i>	Instruction set architecture.
in	<i>callback</i>	Callback to be invoked once per wavefront that is supported by the agent. The HSA runtime passes two arguments to the callback: the wavefront handle and the application data. If <i>callback</i> returns a status other than HSA_STATUS_SUCCESS for a particular iteration, the traversal stops and that value is returned.
in	<i>data</i>	Application data that is passed to <i>callback</i> on every iteration. May be NULL.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_ISA	The instruction set architecture is invalid.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>callback</i> is NULL.

5.7.3.9 hsa_wavefront_get_info()

```
hsa_status_t HSA_API hsa_wavefront_get_info (
    hsa_wavefront_t wavefront,
    hsa_wavefront_info_t attribute,
    void * value )
```

Get the current value of a wavefront attribute.

Parameters

in	<i>wavefront</i>	A wavefront.
in	<i>attribute</i>	Attribute to query.
out	<i>value</i>	Pointer to an application-allocated buffer where to store the value of the attribute. If the buffer passed by the application is not large enough to hold the value of <i>attribute</i> , the behavior is undefined.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
------------------------------------	--

Return values

<code>HSA_STATUS_ERROR_NOT_INITIALIZED</code>	The HSA runtime has not been initialized.
<code>HSA_STATUS_ERROR_INVALID_WAVEFRONT</code>	The wavefront is invalid.
<code>HSA_STATUS_ERROR_INVALID_ARGUMENT</code>	<code>attribute</code> is an invalid wavefront attribute, or <code>value</code> is NULL.

5.8 Executable

Classes

- struct [`hsa_code_object_reader_s`](#)
Code object reader handle. A code object reader is used to load a code object from file (when created using [`hsa_code_object_reader_create_from_file`](#)), or from memory (if created using [`hsa_code_object_reader_create_from_memory`](#)).
- struct [`hsa_executable_s`](#)
Struct containing an opaque handle to an executable, which contains ISA for finalized kernels and indirect functions together with the allocated global or readonly segment variables they reference.
- struct [`hsa_loaded_code_object_s`](#)
Loaded code object handle.
- struct [`hsa_executable_symbol_s`](#)
Executable symbol handle.

Typedefs

- typedef struct [`hsa_code_object_reader_s`](#) [`hsa_code_object_reader_t`](#)
Code object reader handle. A code object reader is used to load a code object from file (when created using [`hsa_code_object_reader_create_from_file`](#)), or from memory (if created using [`hsa_code_object_reader_create_from_memory`](#)).
- typedef struct [`hsa_executable_s`](#) [`hsa_executable_t`](#)
Struct containing an opaque handle to an executable, which contains ISA for finalized kernels and indirect functions together with the allocated global or readonly segment variables they reference.
- typedef struct [`hsa_loaded_code_object_s`](#) [`hsa_loaded_code_object_t`](#)
Loaded code object handle.
- typedef struct [`hsa_executable_symbol_s`](#) [`hsa_executable_symbol_t`](#)
Executable symbol handle.

Enumerations

- enum [`hsa_executable_state_t`](#) { [`HSA_EXECUTABLE_STATE_UNFROZEN`](#) = 0 , [`HSA_EXECUTABLE_STATE_FROZEN`](#) = 1 }
- enum [`hsa_executable_info_t`](#) { [`HSA_EXECUTABLE_INFO_PROFILE`](#) = 1 , [`HSA_EXECUTABLE_INFO_STATE`](#) = 2 , [`HSA_EXECUTABLE_INFO_DEFAULT_FLOAT_ROUNDING_MODE`](#) = 3 }
- enum [`hsa_symbol_kind_t`](#) { [`HSA_SYMBOL_KIND_VARIABLE`](#) = 0 , [`HSA_SYMBOL_KIND_KERNEL`](#) = 1 , [`HSA_SYMBOL_KIND_INDIRECT_FUNCTION`](#) = 2 }
- enum [`hsa_symbol_linkage_t`](#) { [`HSA_SYMBOL_LINKAGE_MODULE`](#) = 0 , [`HSA_SYMBOL_LINKAGE_PROGRAM`](#) = 1 }

Linkage type of a symbol.

- enum `hsa_variable_allocation_t` { `HSA_VARIABLE_ALLOCATION_AGENT` = 0 , `HSA_VARIABLE_ALLOCATION_PROGRAM` = 1 }

Allocation type of a variable.

- enum `hsa_variable_segment_t` { `HSA_VARIABLE_SEGMENT_GLOBAL` = 0 , `HSA_VARIABLE_SEGMENT_READONLY` = 1 }

Memory segment associated with a variable.

- enum `hsa_executable_symbol_info_t` {
`HSA_EXECUTABLE_SYMBOL_INFO_TYPE` = 0 , `HSA_EXECUTABLE_SYMBOL_INFO_NAME_LENGTH`
= 1 , `HSA_EXECUTABLE_SYMBOL_INFO_NAME` = 2 , `HSA_EXECUTABLE_SYMBOL_INFO_MODULE_NAME_LENGTH`
= 3 ,
`HSA_EXECUTABLE_SYMBOL_INFO_MODULE_NAME` = 4 , `HSA_EXECUTABLE_SYMBOL_INFO_AGENT`
= 20 , `HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_ADDRESS` = 21 , `HSA_EXECUTABLE_SYMBOL_INFO_LINKAGE`
= 5 ,
`HSA_EXECUTABLE_SYMBOL_INFO_IS_DEFINITION` = 17 , `HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_ALLOCATION`
= 6 , `HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_SEGMENT` = 7 , `HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_ALIGN`
= 8 ,
`HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_SIZE` = 9 , `HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_IS_CONST`
= 10 , `HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_OBJECT` = 22 , `HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_KERNEL_OBJECT_NAME_LENGTH`
= 11 ,
`HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_KERNEL_ARG_SEGMENT_ALIGNMENT` = 12 , `HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_KERNEL_ARG_SEGMENT_SIZE`
= 13 , `HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_PRIVATE_SEGMENT_SIZE` = 14 , `HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_PRIVATE_SEGMENT_ALIGNMENT`
= 15 ,
`HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_CALL_CONVENTION` = 18 , `HSA_EXECUTABLE_SYMBOL_INFO_INDIRECT_FUNCTION_CALL_CONVENTION`
= 23 , `HSA_EXECUTABLE_SYMBOL_INFO_INDIRECT_FUNCTION_CALL_CONVENTION` = 16 }

Executable symbol attributes.

Functions

- `hsa_status_t` HSA_API `hsa_code_object_reader_create_from_file` (`hsa_file_t` file, `hsa_code_object_reader_t` *code_object_reader)
Create a code object reader to operate on a file.
- `hsa_status_t` HSA_API `hsa_code_object_reader_create_from_memory` (const void *code_object, size_t size, `hsa_code_object_reader_t` *code_object_reader)
Create a code object reader to operate on memory.
- `hsa_status_t` HSA_API `hsa_code_object_reader_destroy` (`hsa_code_object_reader_t` code_object_reader)
Destroy a code object reader.
- `hsa_status_t` HSA_API HSA_DEPRECATED `hsa_executable_create` (`hsa_profile_t` profile, `hsa_executable_state_t` executable_state, const char *options, `hsa_executable_t` *executable)
Create an empty executable.
- `hsa_status_t` HSA_API `hsa_executable_create_alt` (`hsa_profile_t` profile, `hsa_default_float_rounding_mode_t` default_float_rounding_mode, const char *options, `hsa_executable_t` *executable)
Create an empty executable.
- `hsa_status_t` HSA_API `hsa_executable_destroy` (`hsa_executable_t` executable)
Destroy an executable.
- `hsa_status_t` HSA_API `hsa_executable_load_program_code_object` (`hsa_executable_t` executable, `hsa_code_object_reader_t` code_object_reader, const char *options, `hsa_loaded_code_object_t` *loaded_code_object)
Load a program code object into an executable.
- `hsa_status_t` HSA_API `hsa_executable_load_agent_code_object` (`hsa_executable_t` executable, `hsa_agent_t` agent, `hsa_code_object_reader_t` code_object_reader, const char *options, `hsa_loaded_code_object_t` *loaded_code_object)
Load an agent code object into an executable.
- `hsa_status_t` HSA_API `hsa_executable_freeze` (`hsa_executable_t` executable, const char *options)

Freeze the executable.

- `hsa_status_t` HSA_API `hsa_executable_get_info` (`hsa_executable_t` executable, `hsa_executable_info_t` attribute, void *value)

Get the current value of an attribute for a given executable.

- `hsa_status_t` HSA_API `hsa_executable_global_variable_define` (`hsa_executable_t` executable, const char *variable_name, void *address)

Define an external global variable with program allocation.

- `hsa_status_t` HSA_API `hsa_executable_agent_global_variable_define` (`hsa_executable_t` executable, `hsa_agent_t` agent, const char *variable_name, void *address)

Define an external global variable with agent allocation.

- `hsa_status_t` HSA_API `hsa_executable_readonly_variable_define` (`hsa_executable_t` executable, `hsa_agent_t` agent, const char *variable_name, void *address)

Define an external readonly variable.

- `hsa_status_t` HSA_API `hsa_executable_validate` (`hsa_executable_t` executable, uint32_t *result)

Validate an executable. Checks that all code objects have matching machine model, profile, and default floating-point rounding mode. Checks that all declarations have definitions. Checks declaration-definition compatibility (see the HSA Programming Reference Manual for compatibility rules). Invoking this function is equivalent to invoking `hsa_executable_validate_alt` with no options.

- `hsa_status_t` HSA_API `hsa_executable_validate_alt` (`hsa_executable_t` executable, const char *options, uint32_t *result)

Validate an executable. Checks that all code objects have matching machine model, profile, and default floating-point rounding mode. Checks that all declarations have definitions. Checks declaration-definition compatibility (see the HSA Programming Reference Manual for compatibility rules).

- `hsa_status_t` HSA_API HSA_DEPRECATED `hsa_executable_get_symbol` (`hsa_executable_t` executable, const char *module_name, const char *symbol_name, `hsa_agent_t` agent, int32_t call_convention, `hsa_executable_symbol_t` *symbol)

Get the symbol handle for a given a symbol name.

- `hsa_status_t` HSA_API `hsa_executable_get_symbol_by_name` (`hsa_executable_t` executable, const char *symbol_name, const `hsa_agent_t` *agent, `hsa_executable_symbol_t` *symbol)

Retrieve the symbol handle corresponding to a given a symbol name.

- `hsa_status_t` HSA_API `hsa_executable_symbol_get_info` (`hsa_executable_symbol_t` executable_symbol, `hsa_executable_symbol_info_t` attribute, void *value)

Get the current value of an attribute for a given executable symbol.

- `hsa_status_t` HSA_API HSA_DEPRECATED `hsa_executable_iterate_symbols` (`hsa_executable_t` executable, `hsa_status_t`(*callback)(`hsa_executable_t` exec, `hsa_executable_symbol_t` symbol, void *data), void *data)

Iterate over the symbols in a executable, and invoke an application-defined callback on every iteration.

- `hsa_status_t` HSA_API `hsa_executable_iterate_agent_symbols` (`hsa_executable_t` executable, `hsa_agent_t` agent, `hsa_status_t`(*callback)(`hsa_executable_t` exec, `hsa_agent_t` agent, `hsa_executable_symbol_t` symbol, void *data), void *data)

Iterate over the kernels, indirect functions, and agent allocation variables in an executable for a given agent, and invoke an application-defined callback on every iteration.

- `hsa_status_t` HSA_API `hsa_executable_iterate_program_symbols` (`hsa_executable_t` executable, `hsa_status_t`(*callback)(`hsa_executable_t` exec, `hsa_executable_symbol_t` symbol, void *data), void *data)

Iterate over the program allocation variables in an executable, and invoke an application-defined callback on every iteration.

5.8.1 Detailed Description

5.8.2 Typedef Documentation

5.8.2.1 hsa_executable_symbol_t

```
typedef struct hsa_executable_symbol_s hsa_executable_symbol_t
```

Executable symbol handle.

The lifetime of an executable object symbol matches that of the executable associated with it. An operation on a symbol whose associated executable has been destroyed results in undefined behavior.

5.8.3 Enumeration Type Documentation

5.8.3.1 hsa_executable_info_t

```
enum hsa_executable_info_t
```

Executable attributes.

Enumerator

HSA_EXECUTABLE_INFO_PROFILE	Profile this executable is created for. The type of this attribute is hsa_profile_t .
HSA_EXECUTABLE_INFO_STATE	Executable state. The type of this attribute is hsa_executable_state_t .
HSA_EXECUTABLE_INFO_DEFAULT_FLOAT_ROUNDING_MODE	Default floating-point rounding mode specified when executable was created. The type of this attribute is hsa_default_float_rounding_mode_t .

Definition at line 4401 of file [hsa.h](#).

5.8.3.2 hsa_executable_state_t

```
enum hsa_executable_state_t
```

Executable state.

Enumerator

HSA_EXECUTABLE_STATE_UNFROZEN	Executable state, which allows the user to load code objects and define external variables. Variable addresses, kernel code handles, and indirect function code handles are not available in query operations until the executable is frozen (zero always returned).
HSA_EXECUTABLE_STATE_FROZEN	Executable state, which allows the user to query variable addresses, kernel code handles, and indirect function code handles using query operations. Loading new code objects, as well as defining external variables, is not allowed in this state.

Definition at line 4121 of file [hsa.h](#).

5.8.3.3 hsa_executable_symbol_info_t

enum [hsa_executable_symbol_info_t](#)

Executable symbol attributes.

Enumerator

HSA_EXECUTABLE_SYMBOL_INFO_TYPE	The kind of the symbol. The type of this attribute is hsa_symbol_kind_t .
HSA_EXECUTABLE_SYMBOL_INFO_NAME_LENGTH	The length of the symbol name in bytes, not including the NUL terminator. The type of this attribute is uint32_t .
HSA_EXECUTABLE_SYMBOL_INFO_NAME	The name of the symbol. The type of this attribute is character array with the length equal to the value of HSA_EXECUTABLE_SYMBOL_INFO_NAME_LENGTH attribute.
HSA_EXECUTABLE_SYMBOL_INFO_MODULE_NAME_LENGTH	Deprecated The length of the module name in bytes (not including the NUL terminator) to which this symbol belongs if this symbol has module linkage, otherwise 0 is returned. The type of this attribute is uint32_t .
HSA_EXECUTABLE_SYMBOL_INFO_MODULE_NAME	Deprecated The module name to which this symbol belongs if this symbol has module linkage, otherwise an empty string is returned. The type of this attribute is character array with the length equal to the value of HSA_EXECUTABLE_SYMBOL_INFO_MODULE_NAME_LENGTH attribute.
HSA_EXECUTABLE_SYMBOL_INFO_AGENT	Deprecated Agent associated with this symbol. If the symbol is a variable, the value of this attribute is only defined if HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_ALLOCATION is HSA_VARIABLE_ALLOCATION_AGENT . The type of this attribute is hsa_agent_t .
HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_ADDRESS	The address of the variable. The value of this attribute is undefined if the symbol is not a variable. The type of this attribute is uint64_t . If executable's state is HSA_EXECUTABLE_STATE_UNFROZEN , then 0 is returned.
HSA_EXECUTABLE_SYMBOL_INFO_LINKAGE	The linkage kind of the symbol. The type of this attribute is hsa_symbol_linkage_t .
HSA_EXECUTABLE_SYMBOL_INFO_IS_DEFINITION	Indicates whether the symbol corresponds to a definition. The type of this attribute is bool .

Enumerator

HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE↔ _ALLOCATION	Deprecated The allocation kind of the variable. The value of this attribute is undefined if the symbol is not a variable. The type of this attribute is hsa_variable_allocation_t .
HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE↔ _SEGMENT	Deprecated The segment kind of the variable. The value of this attribute is undefined if the symbol is not a variable. The type of this attribute is hsa_variable_segment_t .
HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE↔ _ALIGNMENT	Deprecated Alignment of the symbol in memory. The value of this attribute is undefined if the symbol is not a variable. The type of this attribute is uint32_t . The current alignment of the variable in memory may be greater than the value specified in the source program variable declaration.
HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE↔ _SIZE	Deprecated Size of the variable. The value of this attribute is undefined if the symbol is not a variable. The type of this attribute is uint32_t . A value of 0 is returned if the variable is an external variable and has an unknown dimension.
HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE↔ _IS_CONST	Deprecated Indicates whether the variable is constant. The value of this attribute is undefined if the symbol is not a variable. The type of this attribute is bool .
HSA_EXECUTABLE_SYMBOL_INFO_KERNEL↔ OBJECT	Kernel object handle, used in the kernel dispatch packet. The value of this attribute is undefined if the symbol is not a kernel. The type of this attribute is uint64_t . If the state of the executable is HSA_EXECUTABLE_STATE_UNFROZEN , then 0 is returned.
HSA_EXECUTABLE_SYMBOL_INFO_KERNEL↔ KERNARG_SEGMENT_SIZE	Size of kernarg segment memory that is required to hold the values of the kernel arguments, in bytes. Must be a multiple of 16. The value of this attribute is undefined if the symbol is not a kernel. The type of this attribute is uint32_t .
HSA_EXECUTABLE_SYMBOL_INFO_KERNEL↔ KERNARG_SEGMENT_ALIGNMENT	Alignment (in bytes) of the buffer used to pass arguments to the kernel, which is the maximum of 16 and the maximum alignment of any of the kernel arguments. The value of this attribute is undefined if the symbol is not a kernel. The type of this attribute is uint32_t .

Enumerator

HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_↔ GROUP_SEGMENT_SIZE	Size of static group segment memory required by the kernel (per work-group), in bytes. The value of this attribute is undefined if the symbol is not a kernel. The type of this attribute is uint32_t. The reported amount does not include any dynamically allocated group segment memory that may be requested by the application when a kernel is dispatched.
HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_↔ PRIVATE_SEGMENT_SIZE	Size of static private, spill, and arg segment memory required by this kernel (per work-item), in bytes. The value of this attribute is undefined if the symbol is not a kernel. The type of this attribute is uint32_t. If the value of HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_DYNAMIC_CALLSTACK is true, the kernel may use more private memory than the reported value, and the application must add the dynamic call stack usage to <i>private_segment_size</i> when populating a kernel dispatch packet.
HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_↔ DYNAMIC_CALLSTACK	Dynamic callstack flag. The value of this attribute is undefined if the symbol is not a kernel. The type of this attribute is bool. If this flag is set (the value is true), the kernel uses a dynamically sized call stack. This can happen if recursive calls, calls to indirect functions, or the HSAIL alloca instruction are present in the kernel.
HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_↔ CALL_CONVENTION	Deprecated Call convention of the kernel. The value of this attribute is undefined if the symbol is not a kernel. The type of this attribute is uint32_t.
HSA_EXECUTABLE_SYMBOL_INFO_INDIRECT_↔ FUNCTION_OBJECT	Indirect function object handle. The value of this attribute is undefined if the symbol is not an indirect function, or the associated agent does not support the Full Profile. The type of this attribute depends on the machine model: the type is uint32_t for small machine model, and uint64_t for large model. If the state of the executable is HSA_EXECUTABLE_STATE_UNFROZEN , then 0 is returned.
HSA_EXECUTABLE_SYMBOL_INFO_INDIRECT_↔ FUNCTION_CALL_CONVENTION	Deprecated Call convention of the indirect function. The value of this attribute is undefined if the symbol is not an indirect function, or the associated agent does not support the Full Profile. The type of this attribute is uint32_t.

Definition at line 4801 of file [hsa.h](#).

5.8.3.4 hsa_symbol_kind_t

enum [hsa_symbol_kind_t](#)

Symbol type.

Enumerator

HSA_SYMBOL_KIND_VARIABLE	Variable.
HSA_SYMBOL_KIND_KERNEL	Kernel.
HSA_SYMBOL_KIND_INDIRECT_FUNCTION	Indirect function.

Definition at line 4741 of file [hsa.h](#).

5.8.3.5 hsa_symbol_linkage_t

enum [hsa_symbol_linkage_t](#)

Linkage type of a symbol.

Enumerator

HSA_SYMBOL_LINKAGE_MODULE	Module linkage.
HSA_SYMBOL_LINKAGE_PROGRAM	Program linkage.

Definition at line 4759 of file [hsa.h](#).

5.8.3.6 hsa_variable_allocation_t

enum [hsa_variable_allocation_t](#)

Allocation type of a variable.

Enumerator

HSA_VARIABLE_ALLOCATION_AGENT	Agent allocation.
HSA_VARIABLE_ALLOCATION_PROGRAM	Program allocation.

Definition at line 4773 of file [hsa.h](#).

5.8.3.7 hsa_variable_segment_t

enum [hsa_variable_segment_t](#)

Memory segment associated with a variable.

Enumerator

HSA_VARIABLE_SEGMENT_GLOBAL	Global memory segment.
HSA_VARIABLE_SEGMENT_READONLY	Readonly memory segment.

Definition at line 4787 of file [hsa.h](#).

5.8.4 Function Documentation

5.8.4.1 hsa_code_object_reader_create_from_file()

```
hsa_status_t HSA_API hsa_code_object_reader_create_from_file (
    hsa_file_t file,
    hsa_code_object_reader_t * code_object_reader )
```

Create a code object reader to operate on a file.

Parameters

in	<i>file</i>	File descriptor. The file must have been opened by application with at least read permissions prior calling this function. The file must contain a vendor-specific code object.
----	-------------	---

The file is owned and managed by the application; the lifetime of the file descriptor must exceed that of any associated code object reader.

Parameters

out	<i>code_object_reader</i>	Memory location to store the newly created code object reader handle. Must not be NULL.
-----	---------------------------	---

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_FILE	<i>file</i> is invalid.
HSA_STATUS_ERROR_OUT_OF_RESOURCES	The HSA runtime failed to allocate the required resources.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>code_object_reader</i> is NULL.

5.8.4.2 hsa_code_object_reader_create_from_memory()

```
hsa_status_t HSA_API hsa_code_object_reader_create_from_memory (
    const void * code_object,
```

```
size_t size,
hsa_code_object_reader_t * code_object_reader )
```

Create a code object reader to operate on memory.

Parameters

in	<i>code_object</i>	Memory buffer that contains a vendor-specific code object. The buffer is owned and managed by the application; the lifetime of the buffer must exceed that of any associated code object reader.
in	<i>size</i>	Size of the buffer pointed to by <i>code_object</i> . Must not be 0.
out	<i>code_object_reader</i>	Memory location to store newly created code object reader handle. Must not be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_OUT_OF_RESOURCES</i>	The HSA runtime failed to allocate the required resources.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>code_object</i> is NULL, <i>size</i> is zero, or <i>code_object_reader</i> is NULL.

5.8.4.3 hsa_code_object_reader_destroy()

```
hsa_status_t HSA_API hsa_code_object_reader_destroy (
    hsa_code_object_reader_t code_object_reader )
```

Destroy a code object reader.

The code object reader handle becomes invalid after completion of this function. Any file or memory used to create the code object read is not closed, removed, or deallocated by this function.

Parameters

in	<i>code_object_reader</i>	Code object reader to destroy.
----	---------------------------	--------------------------------

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_CODE_OBJECT_READER</i>	<i>code_object_reader</i> is invalid.

5.8.4.4 hsa_executable_agent_global_variable_define()

```
hsa_status_t HSA_API hsa_executable_agent_global_variable_define (
    hsa_executable_t executable,
```

```

hsa_agent_t agent,
const char * variable_name,
void * address )

```

Define an external global variable with agent allocation.

This function allows the application to provide the definition of a variable in the global segment memory with agent allocation. The variable must be defined before loading a code object into an executable. In addition, code objects loaded must not define the variable.

Parameters

in	<i>executable</i>	Executable. Must not be in frozen state.
in	<i>agent</i>	Agent for which the variable is being defined.
in	<i>variable_name</i>	Name of the variable. The Programmer's Reference Manual describes the standard name mangling scheme.
in	<i>address</i>	Address where the variable is defined. This address must have been previously allocated using hsa_memory_allocate in a global region that is only visible to <i>agent</i> . The application cannot deallocate the buffer pointed by <i>address</i> before <i>executable</i> is destroyed.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_OUT_OF_RESOURCES	The HSA runtime failed to allocate the required resources.
HSA_STATUS_ERROR_INVALID_EXECUTABLE	The executable is invalid.
HSA_STATUS_ERROR_INVALID_AGENT	<i>agent</i> is invalid.
HSA_STATUS_ERROR_VARIABLE_ALREADY_DEFINED	The variable is already defined.
HSA_STATUS_ERROR_INVALID_SYMBOL_NAME	There is no variable with the <i>variable_name</i> .
HSA_STATUS_ERROR_FROZEN_EXECUTABLE	<i>executable</i> is frozen.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>variable_name</i> is NULL.

5.8.4.5 hsa_executable_create()

```

hsa_status_t HSA_API HSA_DEPRECATED hsa_executable_create (
    hsa_profile_t profile,
    hsa_executable_state_t executable_state,
    const char * options,
    hsa_executable_t * executable )

```

Create an empty executable.

Deprecated Use [hsa_executable_create_alt](#) instead, which allows the application to specify the default floating-point rounding mode of the executable and assumes an unfrozen initial state.

Parameters

in	<i>profile</i>	Profile used in the executable.
in	<i>executable_state</i>	Executable state. If the state is HSA_EXECUTABLE_STATE_FROZEN , the resulting executable is useless because no code objects can be loaded, and no variables can be defined.
in	<i>options</i>	Standard and vendor-specific options. Unknown options are ignored. A standard option begins with the "-hsa_" prefix. Options beginning with the "-hsa_ext_<extension_name>_" prefix are reserved for extensions. A vendor-specific option begins with the "-<vendor_name>_" prefix. Must be a NUL-terminated string. May be NULL.
out	<i>executable</i>	Memory location where the HSA runtime stores the newly created executable handle.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_OUT_OF_RESOURCES	The HSA runtime failed to allocate the required resources.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>profile</i> is invalid, or <i>executable</i> is NULL.

5.8.4.6 hsa_executable_create_alt()

```

hsa_status_t HSA_API hsa_executable_create_alt (
    hsa_profile_t profile,
    hsa_default_float_rounding_mode_t default_float_rounding_mode,
    const char * options,
    hsa_executable_t * executable )

```

Create an empty executable.

Parameters

in	<i>profile</i>	Profile used in the executable.
in	<i>default_float_rounding_mode</i>	Default floating-point rounding mode used in the executable. Allowed rounding modes are near and zero (default is not allowed).
in	<i>options</i>	Standard and vendor-specific options. Unknown options are ignored. A standard option begins with the "-hsa_" prefix. Options beginning with the "-hsa_ext_<extension_name>_" prefix are reserved for extensions. A vendor-specific option begins with the "-<vendor_name>_" prefix. Must be a NUL-terminated string. May be NULL.
out	<i>executable</i>	Memory location where the HSA runtime stores newly created executable handle. The initial state of the executable is HSA_EXECUTABLE_STATE_UNFROZEN .

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
------------------------------------	--

Return values

<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_OUT_OF_RESOURCES</i>	The HSA runtime failed to allocate the required resources.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	profile is invalid, or executable is NULL.

5.8.4.7 hsa_executable_destroy()

```
hsa_status_t HSA_API hsa_executable_destroy (
    hsa_executable_t executable )
```

Destroy an executable.

An executable handle becomes invalid after the executable has been destroyed. Code object handles that were loaded into this executable are still valid after the executable has been destroyed, and can be used as intended. Resources allocated outside and associated with this executable (such as external global or readonly variables) can be released after the executable has been destroyed.

Executable should not be destroyed while kernels are in flight.

Parameters

in	<i>executable</i>	Executable.
----	-------------------	-------------

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_EXECUTABLE</i>	The executable is invalid.

5.8.4.8 hsa_executable_freeze()

```
hsa_status_t HSA_API hsa_executable_freeze (
    hsa_executable_t executable,
    const char * options )
```

Freeze the executable.

No modifications to executable can be made after freezing: no code objects can be loaded to the executable, and no external variables can be defined. Freezing the executable does not prevent querying the executable's attributes. The application must define all the external variables in an executable before freezing it.

Parameters

in	<i>executable</i>	Executable.
in	<i>options</i>	Standard and vendor-specific options. Unknown options are ignored. A standard option begins with the "-hsa_" prefix. Options beginning with the
Generated by Doxygen		"-hsa_ext_<extension_name>_" prefix are reserved for extensions. A vendor-specific option begins with the "-<vendor_name>_" prefix. Must be a NUL-terminated string. May be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_EXECUTABLE</i>	The executable is invalid.
<i>HSA_STATUS_ERROR_VARIABLE_UNDEFINED</i>	One or more variables are undefined in the executable.
<i>HSA_STATUS_ERROR_FROZEN_EXECUTABLE</i>	<code>executable</code> is already frozen.

5.8.4.9 `hsa_executable_get_info()`

```

hsa_status_t HSA_API hsa_executable_get_info (
    hsa_executable_t executable,
    hsa_executable_info_t attribute,
    void * value )

```

Get the current value of an attribute for a given executable.

Parameters

in	<i>executable</i>	Executable.
in	<i>attribute</i>	Attribute to query.
out	<i>value</i>	Pointer to an application-allocated buffer where to store the value of the attribute. If the buffer passed by the application is not large enough to hold the value of <i>attribute</i> , the behavior is undefined.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_EXECUTABLE</i>	The executable is invalid.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>attribute</i> is an invalid executable attribute, or <i>value</i> is NULL.

5.8.4.10 `hsa_executable_get_symbol()`

```

hsa_status_t HSA_API HSA_DEPRECATED hsa_executable_get_symbol (
    hsa_executable_t executable,
    const char * module_name,
    const char * symbol_name,
    hsa_agent_t agent,
    int32_t call_convention,
    hsa_executable_symbol_t * symbol )

```

Get the symbol handle for a given a symbol name.

Deprecated Use [`hsa_executable_get_symbol_by_name`](#) instead.

Parameters

in	<i>executable</i>	Executable.
in	<i>module_name</i>	Module name. Must be NULL if the symbol has program linkage.
in	<i>symbol_name</i>	Symbol name.
in	<i>agent</i>	Agent associated with the symbol. If the symbol is independent of any agent (for example, a variable with program allocation), this argument is ignored.
in	<i>call_convention</i>	Call convention associated with the symbol. If the symbol does not correspond to an indirect function, this argument is ignored.
out	<i>symbol</i>	Memory location where the HSA runtime stores the symbol handle.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_EXECUTABLE</i>	The executable is invalid.
<i>HSA_STATUS_ERROR_INVALID_SYMBOL_NAME</i>	There is no symbol with a name that matches <i>symbol_name</i> .
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>symbol_name</i> is NULL, or <i>symbol</i> is NULL.

5.8.4.11 hsa_executable_get_symbol_by_name()

```

hsa_status_t HSA_API hsa_executable_get_symbol_by_name (
    hsa_executable_t executable,
    const char * symbol_name,
    const hsa_agent_t * agent,
    hsa_executable_symbol_t * symbol )

```

Retrieve the symbol handle corresponding to a given a symbol name.

Parameters

in	<i>executable</i>	Executable.
in	<i>symbol_name</i>	Symbol name. Must be a NUL-terminated character array. The Programmer's Reference Manual describes the standard name mangling scheme.
in	<i>agent</i>	Pointer to the agent for which the symbol with the given name is defined. If the symbol corresponding to the given name has program allocation, <i>agent</i> must be NULL.
out	<i>symbol</i>	Memory location where the HSA runtime stores the symbol handle. Must not be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_EXECUTABLE</i>	The executable is invalid.
<i>HSA_STATUS_ERROR_INVALID_SYMBOL_NAME</i>	There is no symbol with a name that matches <i>symbol_name</i> .

Return values

<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	symbol_name is NULL, or symbol is NULL.
--	---

5.8.4.12 hsa_executable_global_variable_define()

```

hsa_status_t HSA_API hsa_executable_global_variable_define (
    hsa_executable_t executable,
    const char * variable_name,
    void * address )

```

Define an external global variable with program allocation.

This function allows the application to provide the definition of a variable in the global segment memory with program allocation. The variable must be defined before loading a code object into an executable. In addition, code objects loaded must not define the variable.

Parameters

in	<i>executable</i>	Executable. Must not be in frozen state.
in	<i>variable_name</i>	Name of the variable. The Programmer's Reference Manual describes the standard name mangling scheme.
in	<i>address</i>	Address where the variable is defined. This address must be in global memory and can be read and written by any agent in the system. The application cannot deallocate the buffer pointed by <i>address</i> before <i>executable</i> is destroyed.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_OUT_OF_RESOURCES</i>	The HSA runtime failed to allocate the required resources.
<i>HSA_STATUS_ERROR_INVALID_EXECUTABLE</i>	The executable is invalid.
<i>HSA_STATUS_ERROR_VARIABLE_ALREADY_DEFINED</i>	The variable is already defined.
<i>HSA_STATUS_ERROR_INVALID_SYMBOL_NAME</i>	There is no variable with the <i>variable_name</i> .
<i>HSA_STATUS_ERROR_FROZEN_EXECUTABLE</i>	<i>executable</i> is frozen.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>variable_name</i> is NULL.

5.8.4.13 hsa_executable_iterate_agent_symbols()

```

hsa_status_t HSA_API hsa_executable_iterate_agent_symbols (
    hsa_executable_t executable,
    hsa_agent_t agent,
    hsa_status_t(*) (hsa_executable_t exec, hsa_agent_t agent, hsa_executable_symbol_t
symbol, void *data) callback,
    void * data )

```

Iterate over the kernels, indirect functions, and agent allocation variables in an executable for a given agent, and invoke an application- defined callback on every iteration.

Parameters

in	<i>executable</i>	Executable.
in	<i>agent</i>	Agent.
in	<i>callback</i>	Callback to be invoked once per executable symbol. The HSA runtime passes three arguments to the callback: the executable, a symbol, and the application data. If <i>callback</i> returns a status other than HSA_STATUS_SUCCESS for a particular iteration, the traversal stops and hsa_executable_iterate_symbols returns that status value.
in	<i>data</i>	Application data that is passed to <i>callback</i> on every iteration. May be NULL.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_EXECUTABLE	The executable is invalid.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>callback</i> is NULL.

5.8.4.14 hsa_executable_iterate_program_symbols()

```

hsa_status_t HSA_API hsa_executable_iterate_program_symbols (
    hsa_executable_t executable,
    hsa_status_t (*) (hsa_executable_t exec, hsa_executable_symbol_t symbol, void *data)
    callback,
    void * data )

```

Iterate over the program allocation variables in an executable, and invoke an application-defined callback on every iteration.

Parameters

in	<i>executable</i>	Executable.
in	<i>callback</i>	Callback to be invoked once per executable symbol. The HSA runtime passes three arguments to the callback: the executable, a symbol, and the application data. If <i>callback</i> returns a status other than HSA_STATUS_SUCCESS for a particular iteration, the traversal stops and hsa_executable_iterate_symbols returns that status value.
in	<i>data</i>	Application data that is passed to <i>callback</i> on every iteration. May be NULL.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_EXECUTABLE	The executable is invalid.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>callback</i> is NULL.

5.8.4.15 hsa_executable_iterate_symbols()

```

hsa_status_t HSA_API HSA_DEPRECATED hsa_executable_iterate_symbols (
    hsa_executable_t executable,
    hsa_status_t (*) (hsa_executable_t exec, hsa_executable_symbol_t symbol, void *data)
    callback,
    void * data )

```

Iterate over the symbols in a executable, and invoke an application-defined callback on every iteration.

Deprecated

Parameters

in	<i>executable</i>	Executable.
in	<i>callback</i>	Callback to be invoked once per executable symbol. The HSA runtime passes three arguments to the callback: the executable, a symbol, and the application data. If <i>callback</i> returns a status other than HSA_STATUS_SUCCESS for a particular iteration, the traversal stops and hsa_executable_iterate_symbols returns that status value.
in	<i>data</i>	Application data that is passed to <i>callback</i> on every iteration. May be NULL.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_EXECUTABLE	The executable is invalid.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>callback</i> is NULL.

5.8.4.16 hsa_executable_load_agent_code_object()

```

hsa_status_t HSA_API hsa_executable_load_agent_code_object (
    hsa_executable_t executable,
    hsa_agent_t agent,
    hsa_code_object_reader_t code_object_reader,
    const char * options,
    hsa_loaded_code_object_t * loaded_code_object )

```

Load an agent code object into an executable.

The agent code object contains all defined agent allocation variables, functions, indirect functions, and kernels in a given program for a given instruction set architecture.

Any module linkage declaration must have been defined either by a define variable or by loading a code object that has a symbol with module linkage definition.

The default floating-point rounding mode of the code object associated with `code_object_reader` must match that of the executable ([HSA_EXECUTABLE_INFO_DEFAULT_FLOAT_ROUNDING_MODE](#)), or be default (in which case the value of [HSA_EXECUTABLE_INFO_DEFAULT_FLOAT_ROUNDING_MODE](#) is used). If the agent code object uses extensions, the implementation and the agent must support them for this operation to return successfully.

Parameters

in	<i>executable</i>	Executable.
in	<i>agent</i>	Agent to load code object for. A code object can be loaded into an executable at most once for a given agent. The instruction set architecture of the code object must be supported by the agent.
in	<i>code_object_reader</i>	A code object reader that holds the code object to load. If a code object reader is destroyed before all the associated executables are destroyed, the behavior is undefined.
in	<i>options</i>	Standard and vendor-specific options. Unknown options are ignored. A standard option begins with the "-hsa_" prefix. Options beginning with the "-hsa_ext_<extension_name>_" prefix are reserved for extensions. A vendor-specific option begins with the "-<vendor_name>_" prefix. Must be a NUL-terminated string. May be NULL.
out	<i>loaded_code_object</i>	Pointer to a memory location where the HSA runtime stores the loaded code object handle. May be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_OUT_OF_RESOURCES</i>	The HSA runtime failed to allocate the required resources.
<i>HSA_STATUS_ERROR_INVALID_EXECUTABLE</i>	The executable is invalid.
<i>HSA_STATUS_ERROR_FROZEN_EXECUTABLE</i>	The executable is frozen.
<i>HSA_STATUS_ERROR_INVALID_AGENT</i>	The agent is invalid.
<i>HSA_STATUS_ERROR_INVALID_CODE_OBJECT_READER</i>	<i>code_object_reader</i> is invalid.
<i>HSA_STATUS_ERROR_INCOMPATIBLE_ARGUMENTS</i>	The code object read by <i>code_object_reader</i> is not compatible with the agent (for example, the agent does not support the instruction set architecture of the code object), the executable (for example, there is a default floating-point mode mismatch between the two), or the implementation.

5.8.4.17 hsa_executable_load_program_code_object()

```

hsa_status_t HSA_API hsa_executable_load_program_code_object (
    hsa_executable_t executable,
    hsa_code_object_reader_t code_object_reader,
    const char * options,
    hsa_loaded_code_object_t * loaded_code_object )

```

Load a program code object into an executable.

A program code object contains information about resources that are accessible by all kernel agents that run the executable, and can be loaded at most once into an executable.

If the program code object uses extensions, the implementation must support them for this operation to return successfully.

Parameters

in	<i>executable</i>	Executable.
in	<i>code_object_reader</i>	A code object reader that holds the program code object to load. If a code object reader is destroyed before all the associated executables are destroyed, the behavior is undefined.
in	<i>options</i>	Standard and vendor-specific options. Unknown options are ignored. A standard option begins with the "-hsa_" prefix. Options beginning with the "-hsa_ext_<extension_name>_" prefix are reserved for extensions. A vendor-specific option begins with the "-<vendor_name>_" prefix. Must be a NUL-terminated string. May be NULL.
out	<i>loaded_code_object</i>	Pointer to a memory location where the HSA runtime stores the loaded code object handle. May be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_OUT_OF_RESOURCES</i>	The HSA runtime failed to allocate the required resources.
<i>HSA_STATUS_ERROR_INVALID_EXECUTABLE</i>	The executable is invalid.
<i>HSA_STATUS_ERROR_FROZEN_EXECUTABLE</i>	The executable is frozen.
<i>HSA_STATUS_ERROR_INVALID_CODE_OBJECT_READER</i>	<i>code_object_reader</i> is invalid.
<i>HSA_STATUS_ERROR_INCOMPATIBLE_ARGUMENTS</i>	The program code object is not compatible with the executable or the implementation (for example, the code object uses an extension that is not supported by the implementation).

5.8.4.18 hsa_executable_readonly_variable_define()

```

hsa_status_t HSA_API hsa_executable_readonly_variable_define (
    hsa_executable_t executable,
    hsa_agent_t agent,
    const char * variable_name,
    void * address )

```

Define an external readonly variable.

This function allows the application to provide the definition of a variable in the readonly segment memory. The variable must be defined before loading a code object into an executable. In addition, code objects loaded must not define the variable.

Parameters

in	<i>executable</i>	Executable. Must not be in frozen state.
in	<i>agent</i>	Agent for which the variable is being defined.
in	<i>variable_name</i>	Name of the variable. The Programmer's Reference Manual describes the standard name mangling scheme.
in	<i>address</i>	Address where the variable is defined. This address must have been previously allocated using <i>hsa_memory_allocate</i> in a readonly region associated with <i>agent</i> . The application cannot deallocate the buffer pointed by <i>address</i> before <i>executable</i> is destroyed.
Generated by Doxygen	<i>address</i>	Address where the variable is defined. The buffer pointed by <i>address</i> is owned by the application, and cannot be deallocated before <i>executable</i> is destroyed.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_OUT_OF_RESOURCES</i>	The HSA runtime failed to allocate the required resources.
<i>HSA_STATUS_ERROR_INVALID_EXECUTABLE</i>	Executable is invalid.
<i>HSA_STATUS_ERROR_INVALID_AGENT</i>	<code>agent</code> is invalid.
<i>HSA_STATUS_ERROR_VARIABLE_ALREADY_DEFINED</i>	The variable is already defined.
<i>HSA_STATUS_ERROR_INVALID_SYMBOL_NAME</i>	There is no variable with the <code>variable_name</code> .
<i>HSA_STATUS_ERROR_FROZEN_EXECUTABLE</i>	<code>executable</code> is frozen.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<code>variable_name</code> is NULL.

5.8.4.19 `hsa_executable_symbol_get_info()`

```

hsa_status_t HSA_API hsa_executable_symbol_get_info (
    hsa_executable_symbol_t executable_symbol,
    hsa_executable_symbol_info_t attribute,
    void * value )

```

Get the current value of an attribute for a given executable symbol.

Parameters

in	<i>executable_symbol</i>	Executable symbol.
in	<i>attribute</i>	Attribute to query.
out	<i>value</i>	Pointer to an application-allocated buffer where to store the value of the attribute. If the buffer passed by the application is not large enough to hold the value of <code>attribute</code> , the behavior is undefined.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_EXECUTABLE_SYMBOL</i>	The executable symbol is invalid.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<code>attribute</code> is an invalid executable symbol attribute, or <code>value</code> is NULL.

5.8.4.20 `hsa_executable_validate()`

```

hsa_status_t HSA_API hsa_executable_validate (
    hsa_executable_t executable,
    uint32_t * result )

```

Validate an executable. Checks that all code objects have matching machine model, profile, and default floating-point rounding mode. Checks that all declarations have definitions. Checks declaration-definition compatibility (see the HSA Programming Reference Manual for compatibility rules). Invoking this function is equivalent to invoking [hsa_executable_validate_alt](#) with no options.

Parameters

in	<i>executable</i>	Executable. Must be in frozen state.
out	<i>result</i>	Memory location where the HSA runtime stores the validation result. If the executable passes validation, the result is 0.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_EXECUTABLE	<i>executable</i> is invalid.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>result</i> is NULL.

5.8.4.21 hsa_executable_validate_alt()

```
hsa_status_t HSA_API hsa_executable_validate_alt (
    hsa_executable_t executable,
    const char * options,
    uint32_t * result )
```

Validate an executable. Checks that all code objects have matching machine model, profile, and default floating-point rounding mode. Checks that all declarations have definitions. Checks declaration-definition compatibility (see the HSA Programming Reference Manual for compatibility rules).

Parameters

in	<i>executable</i>	Executable. Must be in frozen state.
in	<i>options</i>	Standard and vendor-specific options. Unknown options are ignored. A standard option begins with the "-hsa_" prefix. Options beginning with the "-hsa_ext_<extension_name>_" prefix are reserved for extensions. A vendor-specific option begins with the "-<vendor_name>_" prefix. Must be a NUL-terminated string. May be NULL.
out	<i>result</i>	Memory location where the HSA runtime stores the validation result. If the executable passes validation, the result is 0.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_EXECUTABLE	<i>executable</i> is invalid.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>result</i> is NULL.

5.9 Code Objects (deprecated).

Classes

- struct [hsa_code_object_s](#)
Struct containing an opaque handle to a code object, which contains ISA for finalized kernels and indirect functions together with information about the global or readonly segment variables they reference.
- struct [hsa_callback_data_s](#)
Application data handle that is passed to the serialization and deserialization functions.
- struct [hsa_code_symbol_s](#)
Code object symbol handle.

Typedefs

- typedef struct [hsa_code_object_s](#) [hsa_code_object_t](#)
Struct containing an opaque handle to a code object, which contains ISA for finalized kernels and indirect functions together with information about the global or readonly segment variables they reference.
- typedef struct [hsa_callback_data_s](#) [hsa_callback_data_t](#)
Application data handle that is passed to the serialization and deserialization functions.
- typedef struct [hsa_code_symbol_s](#) [hsa_code_symbol_t](#)
Code object symbol handle.

Enumerations

- enum [hsa_code_object_type_t](#) { [HSA_CODE_OBJECT_TYPE_PROGRAM](#) = 0 }
Code object type.
- enum [hsa_code_object_info_t](#) {
[HSA_CODE_OBJECT_INFO_VERSION](#) = 0, [HSA_CODE_OBJECT_INFO_TYPE](#) = 1, [HSA_CODE_OBJECT_INFO_ISA](#) = 2, [HSA_CODE_OBJECT_INFO_MACHINE_MODEL](#) = 3, [HSA_CODE_OBJECT_INFO_PROFILE](#) = 4, [HSA_CODE_OBJECT_INFO_DEFAULT_FLOAT_ROUNDING_MODE](#) = 5 }
Code object attributes.
- enum [hsa_code_symbol_info_t](#) {
[HSA_CODE_SYMBOL_INFO_TYPE](#) = 0, [HSA_CODE_SYMBOL_INFO_NAME_LENGTH](#) = 1, [HSA_CODE_SYMBOL_INFO_NAME](#) = 2, [HSA_CODE_SYMBOL_INFO_MODULE_NAME_LENGTH](#) = 3, [HSA_CODE_SYMBOL_INFO_MODULE_NAME](#) = 4, [HSA_CODE_SYMBOL_INFO_LINKAGE](#) = 5, [HSA_CODE_SYMBOL_INFO_IS_DEFINITION](#) = 17, [HSA_CODE_SYMBOL_INFO_VARIABLE_ALLOCATION](#) = 6, [HSA_CODE_SYMBOL_INFO_VARIABLE_SEGMENT](#) = 7, [HSA_CODE_SYMBOL_INFO_VARIABLE_ALIGNMENT](#) = 8, [HSA_CODE_SYMBOL_INFO_VARIABLE_SIZE](#) = 9, [HSA_CODE_SYMBOL_INFO_VARIABLE_IS_CONST](#) = 10, [HSA_CODE_SYMBOL_INFO_KERNEL_KERNARG_SEGMENT_SIZE](#) = 11, [HSA_CODE_SYMBOL_INFO_KERNEL_KERNARG_SEGMENT](#) = 12, [HSA_CODE_SYMBOL_INFO_KERNEL_GROUP_SEGMENT_SIZE](#) = 13, [HSA_CODE_SYMBOL_INFO_KERNEL_GROUP_SEGMENT](#) = 14, [HSA_CODE_SYMBOL_INFO_KERNEL_DYNAMIC_CALLSTACK](#) = 15, [HSA_CODE_SYMBOL_INFO_KERNEL_CALL_CONVENTION](#) = 16, [HSA_CODE_SYMBOL_INFO_INDIRECT_FUNCTION_CALL_CONVENTION](#) = 18 }
Code object symbol attributes.

Functions

- `hsa_status_t` HSA_API HSA_DEPRECATED `hsa_code_object_serialize` (`hsa_code_object_t` code_object, `hsa_status_t`(*alloc_callback)(size_t size, `hsa_callback_data_t` data, void **address), `hsa_callback_data_t` callback_data, const char *options, void **serialized_code_object, size_t *serialized_code_object_size)
Serialize a code object. Can be used for offline finalization, install-time finalization, disk code caching, etc.
- `hsa_status_t` HSA_API HSA_DEPRECATED `hsa_code_object_deserialize` (void *serialized_code_object, size_t serialized_code_object_size, const char *options, `hsa_code_object_t` *code_object)
Deserialize a code object.
- `hsa_status_t` HSA_API HSA_DEPRECATED `hsa_code_object_destroy` (`hsa_code_object_t` code_object)
Destroy a code object.
- `hsa_status_t` HSA_API HSA_DEPRECATED `hsa_code_object_get_info` (`hsa_code_object_t` code_object, `hsa_code_object_info_t` attribute, void *value)
Get the current value of an attribute for a given code object.
- `hsa_status_t` HSA_API HSA_DEPRECATED `hsa_executable_load_code_object` (`hsa_executable_t` executable, `hsa_agent_t` agent, `hsa_code_object_t` code_object, const char *options)
Load code object into the executable.
- `hsa_status_t` HSA_API HSA_DEPRECATED `hsa_code_object_get_symbol` (`hsa_code_object_t` code_↔object, const char *symbol_name, `hsa_code_symbol_t` *symbol)
Get the symbol handle within a code object for a given a symbol name.
- `hsa_status_t` HSA_API HSA_DEPRECATED `hsa_code_object_get_symbol_from_name` (`hsa_code_object_t` code_object, const char *module_name, const char *symbol_name, `hsa_code_symbol_t` *symbol)
Get the symbol handle within a code object for a given a symbol name.
- `hsa_status_t` HSA_API HSA_DEPRECATED `hsa_code_symbol_get_info` (`hsa_code_symbol_t` code_↔symbol, `hsa_code_symbol_info_t` attribute, void *value)
Get the current value of an attribute for a given code symbol.
- `hsa_status_t` HSA_API HSA_DEPRECATED `hsa_code_object_iterate_symbols` (`hsa_code_object_t` code_↔object, `hsa_status_t`(*callback)(`hsa_code_object_t` code_object, `hsa_code_symbol_t` symbol, void *data), void *data)
Iterate over the symbols in a code object, and invoke an application-defined callback on every iteration.

5.9.1 Detailed Description

5.9.2 Typedef Documentation

5.9.2.1 `hsa_callback_data_t`

```
typedef struct hsa_callback_data_s hsa_callback_data_t
```

Application data handle that is passed to the serialization and deserialization functions.

Deprecated

5.9.2.2 hsa_code_object_t

```
typedef struct hsa_code_object_s hsa_code_object_t
```

Struct containing an opaque handle to a code object, which contains ISA for finalized kernels and indirect functions together with information about the global or readonly segment variables they reference.

Deprecated

5.9.2.3 hsa_code_symbol_t

```
typedef struct hsa_code_symbol_s hsa_code_symbol_t
```

Code object symbol handle.

Deprecated

The lifetime of a code object symbol matches that of the code object associated with it. An operation on a symbol whose associated code object has been destroyed results in undefined behavior.

5.9.3 Enumeration Type Documentation

5.9.3.1 hsa_code_object_info_t

```
enum hsa_code_object_info_t
```

Code object attributes.

Deprecated

Enumerator

HSA_CODE_OBJECT_INFO_VERSION	The version of the code object. The type of this attribute is a NUL-terminated char[64]. The name must be at most 63 characters long (not including the NUL terminator) and all array elements not used for the name must be NUL.
HSA_CODE_OBJECT_INFO_TYPE	Type of code object. The type of this attribute is hsa_code_object_type_t .
HSA_CODE_OBJECT_INFO_ISA	Instruction set architecture this code object is produced for. The type of this attribute is hsa_isa_t .
HSA_CODE_OBJECT_INFO_MACHINE_MODEL	Machine model this code object is produced for. The type of this attribute is hsa_machine_model_t .
HSA_CODE_OBJECT_INFO_PROFILE	Profile this code object is produced for. The type of this attribute is hsa_profile_t .
HSA_CODE_OBJECT_INFO_DEFAULT_FLOAT_ROUNDING_MODE	Default floating-point rounding mode used when the code object is produced. The type of this attribute is hsa_default_float_rounding_mode_t .

Definition at line 5282 of file [hsa.h](#).

5.9.3.2 hsa_code_object_type_t

enum [hsa_code_object_type_t](#)

Code object type.

Deprecated

Enumerator

HSA_CODE_OBJECT_TYPE_PROGRAM	Produces code object that contains ISA for all kernels and indirect functions in HSA source.
------------------------------	--

Definition at line 5269 of file [hsa.h](#).

5.9.3.3 hsa_code_symbol_info_t

enum [hsa_code_symbol_info_t](#)

Code object symbol attributes.

Deprecated

Enumerator

HSA_CODE_SYMBOL_INFO_TYPE	The type of the symbol. The type of this attribute is hsa_symbol_kind_t .
HSA_CODE_SYMBOL_INFO_NAME_LENGTH	The length of the symbol name in bytes, not including the NUL terminator. The type of this attribute is uint32_t .
HSA_CODE_SYMBOL_INFO_NAME	The name of the symbol. The type of this attribute is character array with the length equal to the value of HSA_CODE_SYMBOL_INFO_NAME_LENGTH attribute.
HSA_CODE_SYMBOL_INFO_MODULE_NAME_LENGTH	The length of the module name in bytes (not including the NUL terminator) to which this symbol belongs if this symbol has module linkage, otherwise 0 is returned. The type of this attribute is uint32_t .
HSA_CODE_SYMBOL_INFO_MODULE_NAME	The module name to which this symbol belongs if this symbol has module linkage, otherwise an empty string is returned. The type of this attribute is character array with the length equal to the value of HSA_CODE_SYMBOL_INFO_MODULE_NAME_LENGTH attribute.

Enumerator

HSA_CODE_SYMBOL_INFO_LINKAGE	The linkage kind of the symbol. The type of this attribute is hsa_symbol_linkage_t .
HSA_CODE_SYMBOL_INFO_IS_DEFINITION	Indicates whether the symbol corresponds to a definition. The type of this attribute is bool.
HSA_CODE_SYMBOL_INFO_VARIABLE_↔ ALLOCATION	The allocation kind of the variable. The value of this attribute is undefined if the symbol is not a variable. The type of this attribute is hsa_variable_allocation_t .
HSA_CODE_SYMBOL_INFO_VARIABLE_↔ SEGMENT	The segment kind of the variable. The value of this attribute is undefined if the symbol is not a variable. The type of this attribute is hsa_variable_segment_t .
HSA_CODE_SYMBOL_INFO_VARIABLE_↔ ALIGNMENT	Alignment of the symbol in memory. The value of this attribute is undefined if the symbol is not a variable. The type of this attribute is <code>uint32_t</code> . The current alignment of the variable in memory may be greater than the value specified in the source program variable declaration.
HSA_CODE_SYMBOL_INFO_VARIABLE_SIZE	Size of the variable. The value of this attribute is undefined if the symbol is not a variable. The type of this attribute is <code>uint32_t</code> . A size of 0 is returned if the variable is an external variable and has an unknown dimension.
HSA_CODE_SYMBOL_INFO_VARIABLE_IS_↔ CONST	Indicates whether the variable is constant. The value of this attribute is undefined if the symbol is not a variable. The type of this attribute is bool.
HSA_CODE_SYMBOL_INFO_KERNEL_↔ KERNARG_SEGMENT_SIZE	Size of kernarg segment memory that is required to hold the values of the kernel arguments, in bytes. Must be a multiple of 16. The value of this attribute is undefined if the symbol is not a kernel. The type of this attribute is <code>uint32_t</code> .
HSA_CODE_SYMBOL_INFO_KERNEL_↔ KERNARG_SEGMENT_ALIGNMENT	Alignment (in bytes) of the buffer used to pass arguments to the kernel, which is the maximum of 16 and the maximum alignment of any of the kernel arguments. The value of this attribute is undefined if the symbol is not a kernel. The type of this attribute is <code>uint32_t</code> .
HSA_CODE_SYMBOL_INFO_KERNEL_GROUP_↔ SEGMENT_SIZE	Size of static group segment memory required by the kernel (per work-group), in bytes. The value of this attribute is undefined if the symbol is not a kernel. The type of this attribute is <code>uint32_t</code> . The reported amount does not include any dynamically allocated group segment memory that may be requested by the application when a kernel is dispatched.
HSA_CODE_SYMBOL_INFO_KERNEL_PRIVATE_↔ _SEGMENT_SIZE	Size of static private, spill, and arg segment memory required by this kernel (per work-item), in bytes. The value of this attribute is undefined if the symbol is not a kernel. The type of this attribute is <code>uint32_t</code> . If the value of HSA_CODE_SYMBOL_INFO_KERNEL_DYNAMIC_CALLSTACK is true, the kernel may use more private memory than the reported value, and the application must add the dynamic call stack usage to <i>private_segment_size</i> when populating a kernel dispatch packet.

Enumerator

HSA_CODE_SYMBOL_INFO_KERNEL_↔ DYNAMIC_CALLSTACK	Dynamic callstack flag. The value of this attribute is undefined if the symbol is not a kernel. The type of this attribute is bool. If this flag is set (the value is true), the kernel uses a dynamically sized call stack. This can happen if recursive calls, calls to indirect functions, or the HSAIL alloca instruction are present in the kernel.
HSA_CODE_SYMBOL_INFO_KERNEL_CALL_↔ CONVENTION	Call convention of the kernel. The value of this attribute is undefined if the symbol is not a kernel. The type of this attribute is uint32_t.
HSA_CODE_SYMBOL_INFO_INDIRECT_↔ FUNCTION_CALL_CONVENTION	Call convention of the indirect function. The value of this attribute is undefined if the symbol is not an indirect function. The type of this attribute is uint32_t.

Definition at line 5489 of file [hsa.h](#).

5.9.4 Function Documentation

5.9.4.1 hsa_code_object_deserialize()

```

hsa_status_t HSA_API HSA_DEPRECATED hsa_code_object_deserialize (
    void * serialized_code_object,
    size_t serialized_code_object_size,
    const char * options,
    hsa_code_object_t * code_object )

```

Deserialize a code object.

Deprecated

Parameters

in	<i>serialized_code_object</i>	A serialized code object. Must not be NULL.
in	<i>serialized_code_object_size</i>	The size (in bytes) of <i>serialized_code_object</i> . Must not be 0.
in	<i>options</i>	Standard and vendor-specific options. Unknown options are ignored. A standard option begins with the "-hsa_" prefix. Options beginning with the "-hsa_ext_<extension_name>_" prefix are reserved for extensions. A vendor-specific option begins with the "-<vendor_name>_" prefix. Must be a NUL-terminated string. May be NULL.
out	<i>code_object</i>	Memory location where the HSA runtime stores the deserialized code object.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_OUT_OF_RESOURCES</i>	The HSA runtime failed to allocate the required resources.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<code>serialized_code_object</code> , or <code>code_object</code> are NULL, or <code>serialized_code_object_size</code> is 0.

5.9.4.2 `hsa_code_object_destroy()`

```
hsa_status_t HSA_API HSA_DEPRECATED hsa_code_object_destroy (
    hsa_code_object_t code_object )
```

Destroy a code object.

Deprecated

The lifetime of a code object must exceed that of any executable where it has been loaded. If an executable that loaded `code_object` has not been destroyed, the behavior is undefined.

Parameters

in	<code>code_object</code>	Code object. The handle becomes invalid after it has been destroyed.
----	--------------------------	--

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_CODE_OBJECT</i>	<code>code_object</code> is invalid.

5.9.4.3 `hsa_code_object_get_info()`

```
hsa_status_t HSA_API HSA_DEPRECATED hsa_code_object_get_info (
    hsa_code_object_t code_object,
    hsa_code_object_info_t attribute,
    void * value )
```

Get the current value of an attribute for a given code object.

Deprecated

Parameters

in	<i>code_object</i>	Code object.
in	<i>attribute</i>	Attribute to query.
out	<i>value</i>	Pointer to an application-allocated buffer where to store the value of the attribute. If the buffer passed by the application is not large enough to hold the value of <i>attribute</i> , the behavior is undefined.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_CODE_OBJECT</i>	<i>code_object</i> is invalid.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>attribute</i> is an invalid code object attribute, or <i>value</i> is NULL.

5.9.4.4 hsa_code_object_get_symbol()

```

hsa_status_t HSA_API HSA_DEPRECATED hsa_code_object_get_symbol (
    hsa_code_object_t code_object,
    const char * symbol_name,
    hsa_code_symbol_t * symbol )

```

Get the symbol handle within a code object for a given a symbol name.

Deprecated

Parameters

in	<i>code_object</i>	Code object.
in	<i>symbol_name</i>	Symbol name.
out	<i>symbol</i>	Memory location where the HSA runtime stores the symbol handle.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_CODE_OBJECT</i>	<i>code_object</i> is invalid.
<i>HSA_STATUS_ERROR_INVALID_SYMBOL_NAME</i>	There is no symbol with a name that matches <i>symbol_name</i> .
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>symbol_name</i> is NULL, or <i>symbol</i> is NULL.

5.9.4.5 hsa_code_object_get_symbol_from_name()

```
hsa_status_t HSA_API HSA_DEPRECATED hsa_code_object_get_symbol_from_name (
    hsa_code_object_t code_object,
    const char * module_name,
    const char * symbol_name,
    hsa_code_symbol_t * symbol )
```

Get the symbol handle within a code object for a given a symbol name.

Deprecated

Parameters

in	<i>code_object</i>	Code object.
in	<i>module_name</i>	Module name. Must be NULL if the symbol has program linkage.
in	<i>symbol_name</i>	Symbol name.
out	<i>symbol</i>	Memory location where the HSA runtime stores the symbol handle.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_CODE_OBJECT</i>	<i>code_object</i> is invalid.
<i>HSA_STATUS_ERROR_INVALID_SYMBOL_NAME</i>	There is no symbol with a name that matches <i>symbol_name</i> .
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>symbol_name</i> is NULL, or <i>symbol</i> is NULL.

5.9.4.6 hsa_code_object_iterate_symbols()

```
hsa_status_t HSA_API HSA_DEPRECATED hsa_code_object_iterate_symbols (
    hsa_code_object_t code_object,
    hsa_status_t(*) (hsa_code_object_t code_object, hsa_code_symbol_t symbol, void
*data) callback,
    void * data )
```

Iterate over the symbols in a code object, and invoke an application-defined callback on every iteration.

Deprecated

Parameters

in	<i>code_object</i>	Code object.
in	<i>callback</i>	Callback to be invoked once per code object symbol. The HSA runtime passes three arguments to the callback: the code object, a symbol, and the application data. If <i>callback</i> returns a status other than <i>HSA_STATUS_SUCCESS</i> for a particular iteration, the traversal stops and <i>hsa_code_object_iterate_symbols</i> returns that status value.
in	<i>data</i>	Application data that is passed to <i>callback</i> on every iteration. May be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_CODE_OBJECT</i>	<code>code_object</code> is invalid.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<code>callback</code> is NULL.

5.9.4.7 `hsa_code_object_serialize()`

```

hsa_status_t HSA_API HSA_DEPRECATED hsa_code_object_serialize (
    hsa_code_object_t code_object,
    hsa_status_t(*) (size_t size, hsa_callback_data_t data, void **address) alloc_↵
callback,
    hsa_callback_data_t callback_data,
    const char * options,
    void ** serialized_code_object,
    size_t * serialized_code_object_size )

```

Serialize a code object. Can be used for offline finalization, install-time finalization, disk code caching, etc.

Deprecated

Parameters

in	<i>code_object</i>	Code object.
in	<i>alloc_callback</i>	Callback function for memory allocation. Must not be NULL. The HSA runtime passes three arguments to the callback: the allocation size, the application data, and a pointer to a memory location where the application stores the allocation result. The HSA runtime invokes <code>alloc_callback</code> once to allocate a buffer that contains the serialized version of <code>code_object</code> . If the callback returns a status code other than <i>HSA_STATUS_SUCCESS</i> , this function returns the same code.
in	<i>callback_data</i>	Application data that is passed to <code>alloc_callback</code> . May be NULL.
in	<i>options</i>	Standard and vendor-specific options. Unknown options are ignored. A standard option begins with the "-hsa_" prefix. Options beginning with the "-hsa_ext_<extension_name>_" prefix are reserved for extensions. A vendor-specific option begins with the "-<vendor_name>_" prefix. Must be a NUL-terminated string. May be NULL.
out	<i>serialized_code_object</i>	Memory location where the HSA runtime stores a pointer to the serialized code object. Must not be NULL.
out	<i>serialized_code_object_size</i>	Memory location where the HSA runtime stores the size (in bytes) of <code>serialized_code_object</code> . The returned value matches the allocation size passed by the HSA runtime to <code>alloc_callback</code> . Must not be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_OUT_OF_RESOURCES</i>	The HSA runtime failed to allocate the required resources.
<i>HSA_STATUS_ERROR_INVALID_CODE_OBJECT</i>	<code>code_object</code> is invalid.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<code>alloc_callback</code> , <code>serialized_code_object</code> , or <code>serialized_code_object_size</code> are NULL.

5.9.4.8 `hsa_code_symbol_get_info()`

```

hsa_status_t HSA_API HSA_DEPRECATED hsa_code_symbol_get_info (
    hsa_code_symbol_t code_symbol,
    hsa_code_symbol_info_t attribute,
    void * value )

```

Get the current value of an attribute for a given code symbol.

Deprecated

Parameters

in	<i>code_symbol</i>	Code symbol.
in	<i>attribute</i>	Attribute to query.
out	<i>value</i>	Pointer to an application-allocated buffer where to store the value of the attribute. If the buffer passed by the application is not large enough to hold the value of <code>attribute</code> , the behavior is undefined.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_CODE_SYMBOL</i>	The code symbol is invalid.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<code>attribute</code> is an invalid code symbol attribute, or <code>value</code> is NULL.

5.9.4.9 `hsa_executable_load_code_object()`

```

hsa_status_t HSA_API HSA_DEPRECATED hsa_executable_load_code_object (
    hsa_executable_t executable,
    hsa_agent_t agent,

```

```

hsa_code_object_t code_object,
const char * options )

```

Load code object into the executable.

Deprecated

Every global or readonly variable that is external must be defined before loading the code object. An internal global or readonly variable is allocated once the code object, that is being loaded, references this variable and this variable is not allocated.

Any module linkage declaration must have been defined either by a define variable or by loading a code object that has a symbol with module linkage definition.

Parameters

in	<i>executable</i>	Executable.
in	<i>agent</i>	Agent to load code object for. The agent must support the default floating-point rounding mode used by <i>code_object</i> .
in	<i>code_object</i>	Code object to load. The lifetime of the code object must exceed that of the executable: if <i>code_object</i> is destroyed before <i>executable</i> , the behavior is undefined.
in	<i>options</i>	Standard and vendor-specific options. Unknown options are ignored. A standard option begins with the "-hsa_" prefix. Options beginning with the "-hsa_ext_<extension_name>_" prefix are reserved for extensions. A vendor-specific option begins with the "-<vendor_name>_" prefix. Must be a NUL-terminated string. May be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_OUT_OF_RESOURCES</i>	The HSA runtime failed to allocate the required resources.
<i>HSA_STATUS_ERROR_INVALID_EXECUTABLE</i>	The executable is invalid.
<i>HSA_STATUS_ERROR_INVALID_AGENT</i>	The agent is invalid.
<i>HSA_STATUS_ERROR_INVALID_CODE_OBJECT</i>	<i>code_object</i> is invalid.
<i>HSA_STATUS_ERROR_INCOMPATIBLE_ARGUMENTS</i>	<i>agent</i> is not compatible with <i>code_object</i> (for example, <i>agent</i> does not support the default floating-point rounding mode specified by <i>code_object</i>), or <i>code_object</i> is not compatible with <i>executable</i> (for example, <i>code_object</i> and <i>executable</i> have different machine models or profiles).
<i>HSA_STATUS_ERROR_FROZEN_EXECUTABLE</i>	<i>executable</i> is frozen.

5.10 Finalization Extensions

Enumerations

- enum {
[*HSA_EXT_STATUS_ERROR_INVALID_PROGRAM*](#) = 0x2000 , [*HSA_EXT_STATUS_ERROR_INVALID_MODULE*](#)

```
= 0x2001 , HSA_EXT_STATUS_ERROR_INCOMPATIBLE_MODULE = 0x2002 , HSA_EXT_STATUS_ERROR_MODULE_ALREADY_INCLUDED = 0x2003 ,
HSA_EXT_STATUS_ERROR_SYMBOL_MISMATCH = 0x2004 , HSA_EXT_STATUS_ERROR_FINALIZATION_FAILED = 0x2005 , HSA_EXT_STATUS_ERROR_DIRECTIVE_MISMATCH = 0x2006 }
```

Enumeration constants added to [hsa_status_t](#) by this extension.

5.10.1 Detailed Description

5.10.2 Enumeration Type Documentation

5.10.2.1 anonymous enum

anonymous enum

Enumeration constants added to [hsa_status_t](#) by this extension.

Enumerator

HSA_EXT_STATUS_ERROR_INVALID_PROGRAM	The HSAIL program is invalid.
HSA_EXT_STATUS_ERROR_INVALID_MODULE	The HSAIL module is invalid.
HSA_EXT_STATUS_ERROR_INCOMPATIBLE_MODULE	Machine model or profile of the HSAIL module do not match the machine model or profile of the HSAIL program.
HSA_EXT_STATUS_ERROR_MODULE_ALREADY_INCLUDED	The HSAIL module is already a part of the HSAIL program.
HSA_EXT_STATUS_ERROR_SYMBOL_MISMATCH	Compatibility mismatch between symbol declaration and symbol definition.
HSA_EXT_STATUS_ERROR_FINALIZATION_FAILED	The finalization encountered an error while finalizing a kernel or indirect function.
HSA_EXT_STATUS_ERROR_DIRECTIVE_MISMATCH	Mismatch between a directive in the control directive structure and in the HSAIL kernel.

Definition at line 69 of file [hsa_ext_finalize.h](#).

5.11 Finalization Program

Classes

- struct [hsa_ext_program_s](#)

An opaque handle to a HSAIL program, which groups a set of HSAIL modules that collectively define functions and variables used by kernels and indirect functions.

- struct [hsa_ext_control_directives_s](#)

Control directives specify low-level information about the finalization process.

Typedefs

- typedef [BrigModule_t](#) [hsa_ext_module_t](#)
HSAIL (BRIG) module. The HSA Programmer's Reference Manual contains the definition of the BrigModule_t type.
- typedef struct [hsa_ext_program_s](#) [hsa_ext_program_t](#)
An opaque handle to a HSAIL program, which groups a set of HSAIL modules that collectively define functions and variables used by kernels and indirect functions.
- typedef struct [hsa_ext_control_directives_s](#) [hsa_ext_control_directives_t](#)
Control directives specify low-level information about the finalization process.

Enumerations

- enum [hsa_ext_program_info_t](#) { [HSA_EXT_PROGRAM_INFO_MACHINE_MODEL](#) = 0, [HSA_EXT_PROGRAM_INFO_PROFILE](#) = 1, [HSA_EXT_PROGRAM_INFO_DEFAULT_FLOAT_ROUNDING_MODE](#) = 2 }
HSAIL program attributes.
- enum [hsa_ext_finalizer_call_convention_t](#) { [HSA_EXT_FINALIZER_CALL_CONVENTION_AUTO](#) = -1 }
Finalizer-determined call convention.

Functions

- [hsa_status_t](#) HSA_API [hsa_ext_program_create](#) ([hsa_machine_model_t](#) machine_model, [hsa_profile_t](#) profile, [hsa_default_float_rounding_mode_t](#) default_float_rounding_mode, const char *options, [hsa_ext_program_t](#) *program)
Create an empty HSAIL program.
- [hsa_status_t](#) HSA_API [hsa_ext_program_destroy](#) ([hsa_ext_program_t](#) program)
Destroy a HSAIL program.
- [hsa_status_t](#) HSA_API [hsa_ext_program_add_module](#) ([hsa_ext_program_t](#) program, [hsa_ext_module_t](#) module)
Add a HSAIL module to an existing HSAIL program.
- [hsa_status_t](#) HSA_API [hsa_ext_program_iterate_modules](#) ([hsa_ext_program_t](#) program, [hsa_status_t](#)(*callback)([hsa_ext_program_t](#) program, [hsa_ext_module_t](#) module, void *data), void *data)
Iterate over the HSAIL modules in a program, and invoke an application-defined callback on every iteration.
- [hsa_status_t](#) HSA_API [hsa_ext_program_get_info](#) ([hsa_ext_program_t](#) program, [hsa_ext_program_info_t](#) attribute, void *value)
Get the current value of an attribute for a given HSAIL program.
- [hsa_status_t](#) HSA_API [hsa_ext_program_finalize](#) ([hsa_ext_program_t](#) program, [hsa_isa_t](#) isa, [int32_t](#) call_convention, [hsa_ext_control_directives_t](#) control_directives, const char *options, [hsa_code_object_type_t](#) code_object_type, [hsa_code_object_t](#) *code_object)
Finalize an HSAIL program for a given instruction set architecture.

5.11.1 Detailed Description

5.11.2 Typedef Documentation

5.11.2.1 hsa_ext_module_t

```
typedef BrigModule_t hsa_ext_module_t
```

HSAIL (BRIG) module. The HSA Programmer's Reference Manual contains the definition of the BrigModule_t type.

Definition at line 113 of file [hsa_ext_finalize.h](#).

5.11.3 Enumeration Type Documentation

5.11.3.1 hsa_ext_finalizer_call_convention_t

```
enum hsa_ext_finalizer_call_convention_t
```

Finalizer-determined call convention.

Enumerator

HSA_EXT_FINALIZER_CALL_CONVENTION_AUTO	Finalizer-determined call convention.
--	---------------------------------------

Definition at line 311 of file [hsa_ext_finalize.h](#).

5.11.3.2 hsa_ext_program_info_t

```
enum hsa_ext_program_info_t
```

HSAIL program attributes.

Enumerator

HSA_EXT_PROGRAM_INFO_MACHINE_MODEL	Machine model specified when the HSAIL program was created. The type of this attribute is hsa_machine_model_t .
HSA_EXT_PROGRAM_INFO_PROFILE	Profile specified when the HSAIL program was created. The type of this attribute is hsa_profile_t .
HSA_EXT_PROGRAM_INFO_DEFAULT_FLOAT_ROUNDING_MODE	Default float rounding mode specified when the HSAIL program was created. The type of this attribute is hsa_default_float_rounding_mode_t .

Definition at line 264 of file [hsa_ext_finalize.h](#).

5.11.4 Function Documentation

5.11.4.1 hsa_ext_program_add_module()

```
hsa_status_t HSA_API hsa_ext_program_add_module (
    hsa_ext_program_t program,
    hsa_ext_module_t module )
```

Add a HSAIL module to an existing HSAIL program.

The HSA runtime does not perform a deep copy of the HSAIL module upon addition. Instead, it stores a pointer to the HSAIL module. The ownership of the HSAIL module belongs to the application, which must ensure that `module` is not released before destroying the HSAIL program.

The HSAIL module is successfully added to the HSAIL program if `module` is valid, if all the declarations and definitions for the same symbol are compatible, and if `module` specify machine model and profile that matches the HSAIL program.

Parameters

in	<i>program</i>	HSAIL program.
in	<i>module</i>	HSAIL module. The application can add the same HSAIL module to <code>program</code> at most once. The HSAIL module must specify the same machine model and profile as <code>program</code> . If the floating-mode rounding mode of <code>module</code> is not default, then it should match that of <code>program</code> .

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_OUT_OF_RESOURCES</i>	There is a failure to allocate resources required for the operation.
<i>HSA_EXT_STATUS_ERROR_INVALID_PROGRAM</i>	The HSAIL program is invalid.
<i>HSA_EXT_STATUS_ERROR_INVALID_MODULE</i>	The HSAIL module is invalid.
<i>HSA_EXT_STATUS_ERROR_INCOMPATIBLE_MODULE</i>	The machine model of <code>module</code> does not match machine model of <code>program</code> , or the profile of <code>module</code> does not match profile of <code>program</code> .
<i>HSA_EXT_STATUS_ERROR_MODULE_ALREADY_IN_PROGRAM</i>	The HSAIL module is already a part of the HSAIL program.
<i>HSA_EXT_STATUS_ERROR_SYMBOL_MISMATCH</i>	Symbol declaration and symbol definition compatibility mismatch. See the symbol compatibility rules in the HSA Programming Reference Manual.

5.11.4.2 hsa_ext_program_create()

```
hsa_status_t HSA_API hsa_ext_program_create (
    hsa_machine_model_t machine_model,
    hsa_profile_t profile,
    hsa_default_float_rounding_mode_t default_float_rounding_mode,
    const char * options,
    hsa_ext_program_t * program )
```

Create an empty HSAIL program.

Parameters

in	<i>machine_model</i>	Machine model used in the HSAIL program.
in	<i>profile</i>	Profile used in the HSAIL program.
in	<i>default_float_rounding_mode</i>	Default float rounding mode used in the HSAIL program.
in	<i>options</i>	Vendor-specific options. May be NULL.
out	<i>program</i>	Memory location where the HSA runtime stores the newly created HSAIL program handle.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_OUT_OF_RESOURCES</i>	There is a failure to allocate resources required for the operation.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>machine_model</i> is invalid, <i>profile</i> is invalid, <i>default_float_rounding_mode</i> is invalid, or <i>program</i> is NULL.

5.11.4.3 `hsa_ext_program_destroy()`

```
hsa_status_t HSA_API hsa_ext_program_destroy (
    hsa_ext_program_t program )
```

Destroy a HSAIL program.

The HSAIL program handle becomes invalid after it has been destroyed. Code object handles produced by [`hsa_ext_program_finalize`](#) are still valid after the HSAIL program has been destroyed, and can be used as intended. Resources allocated outside and associated with the HSAIL program (such as HSAIL modules that are added to the HSAIL program) can be released after the finalization program has been destroyed.

Parameters

in	<i>program</i>	HSAIL program.
----	----------------	----------------

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_EXT_STATUS_ERROR_INVALID_PROGRAM</i>	The HSAIL program is invalid.

5.11.4.4 `hsa_ext_program_finalize()`

```
hsa_status_t HSA_API hsa_ext_program_finalize (
    hsa_ext_program_t program,
```

```

hsa_isa_t isa,
int32_t call_convention,
hsa_ext_control_directives_t control_directives,
const char * options,
hsa_code_object_type_t code_object_type,
hsa_code_object_t * code_object )

```

Finalize an HSAIL program for a given instruction set architecture.

Finalize all of the kernels and indirect functions that belong to the same HSAIL program for a specific instruction set architecture (ISA). The transitive closure of all functions specified by call or scall must be defined. Kernels and indirect functions that are being finalized must be defined. Kernels and indirect functions that are referenced in kernels and indirect functions being finalized may or may not be defined, but must be declared. All the global/readonly segment variables that are referenced in kernels and indirect functions being finalized may or may not be defined, but must be declared.

Parameters

in	<i>program</i>	HSAIL program.
in	<i>isa</i>	Instruction set architecture to finalize for.
in	<i>call_convention</i>	A call convention used in a finalization. Must have a value between HSA_EXT_FINALIZER_CALL_CONVENTION_AUTO (inclusive) and the value of the attribute HSA_ISA_INFO_CALL_CONVENTION_COUNT in <i>isa</i> (not inclusive).
in	<i>control_directives</i>	Low-level control directives that influence the finalization process.
in	<i>options</i>	Vendor-specific options. May be NULL.
in	<i>code_object_type</i>	Type of code object to produce.
out	<i>code_object</i>	Code object generated by the Finalizer, which contains the machine code for the kernels and indirect functions in the HSAIL program. The code object is independent of the HSAIL module that was used to generate it.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_OUT_OF_RESOURCES	There is a failure to allocate resources required for the operation.
HSA_EXT_STATUS_ERROR_INVALID_PROGRAM	The HSAIL program is invalid.
HSA_STATUS_ERROR_INVALID_ISA	<i>isa</i> is invalid.
HSA_EXT_STATUS_ERROR_DIRECTIVE_MISMATCH	The directive in the control directive structure and in the HSAIL kernel mismatch, or if the same directive is used with a different value in one of the functions used by this kernel.
HSA_EXT_STATUS_ERROR_FINALIZATION_FAILED	The Finalizer encountered an error while compiling a kernel or an indirect function.

5.11.4.5 hsa_ext_program_get_info()

```

hsa_status_t HSA_API hsa_ext_program_get_info (
    hsa_ext_program_t program,

```

```

    hsa_ext_program_info_t attribute,
    void * value )

```

Get the current value of an attribute for a given HSAIL program.

Parameters

in	<i>program</i>	HSAIL program.
in	<i>attribute</i>	Attribute to query.
out	<i>value</i>	Pointer to an application-allocated buffer where to store the value of the attribute. If the buffer passed by the application is not large enough to hold the value of <i>attribute</i> , the behaviour is undefined.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_EXT_STATUS_ERROR_INVALID_PROGRAM	The HSAIL program is invalid.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>attribute</i> is an invalid HSAIL program attribute, or <i>value</i> is NULL.

5.11.4.6 hsa_ext_program_iterate_modules()

```

hsa_status_t HSA_API hsa_ext_program_iterate_modules (
    hsa_ext_program_t program,
    hsa_status_t (*) (hsa_ext_program_t program, hsa_ext_module_t module, void *data)
    callback,
    void * data )

```

Iterate over the HSAIL modules in a program, and invoke an application-defined callback on every iteration.

Parameters

in	<i>program</i>	HSAIL program.
in	<i>callback</i>	Callback to be invoked once per HSAIL module in the program. The HSA runtime passes three arguments to the callback: the program, a HSAIL module, and the application data. If <i>callback</i> returns a status other than HSA_STATUS_SUCCESS for a particular iteration, the traversal stops and hsa_ext_program_iterate_modules returns that status value.
in	<i>data</i>	Application data that is passed to <i>callback</i> on every iteration. May be NULL.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_EXT_STATUS_ERROR_INVALID_PROGRAM	The program is invalid.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>callback</i> is NULL.

5.12 Images and Samplers

Classes

- struct [hsa_ext_image_s](#)
Image handle, populated by [hsa_ext_image_create](#) or [hsa_ext_image_create_with_layout](#). Image handles are only unique within an agent, not across agents.
- struct [hsa_ext_image_format_s](#)
Image format.
- struct [hsa_ext_image_descriptor_s](#)
Implementation independent image descriptor.
- struct [hsa_ext_image_data_info_s](#)
Agent specific image size and alignment requirements, populated by [hsa_ext_image_data_get_info](#) and [hsa_ext_image_data_get_info_with_layout](#).
- struct [hsa_ext_image_region_s](#)
Image region.
- struct [hsa_ext_sampler_s](#)
Sampler handle. Samplers are populated by [hsa_ext_sampler_create](#). Sampler handles are only unique within an agent, not across agents.
- struct [hsa_ext_sampler_descriptor_s](#)
Implementation independent sampler descriptor.
- struct [hsa_ext_images_v1_00_pfn_s](#)
The function pointer table for the images v1.00 extension. Can be returned by [hsa_system_get_extension_table](#) or [hsa_system_get_major_extension_table](#).
- struct [hsa_ext_images_v1_pfn_s](#)
The function pointer table for the images v1 extension. Can be returned by [hsa_system_get_extension_table](#) or [hsa_system_get_major_extension_table](#).

Typedefs

- typedef struct [hsa_ext_image_s](#) [hsa_ext_image_t](#)
Image handle, populated by [hsa_ext_image_create](#) or [hsa_ext_image_create_with_layout](#). Image handles are only unique within an agent, not across agents.
- typedef uint32_t [hsa_ext_image_channel_type32_t](#)
A fixed-size type used to represent [hsa_ext_image_channel_type_t](#) constants.
- typedef uint32_t [hsa_ext_image_channel_order32_t](#)
A fixed-size type used to represent [hsa_ext_image_channel_order_t](#) constants.
- typedef struct [hsa_ext_image_format_s](#) [hsa_ext_image_format_t](#)
Image format.
- typedef struct [hsa_ext_image_descriptor_s](#) [hsa_ext_image_descriptor_t](#)
Implementation independent image descriptor.
- typedef struct [hsa_ext_image_data_info_s](#) [hsa_ext_image_data_info_t](#)
Agent specific image size and alignment requirements, populated by [hsa_ext_image_data_get_info](#) and [hsa_ext_image_data_get_info_with_layout](#).
- typedef struct [hsa_ext_image_region_s](#) [hsa_ext_image_region_t](#)
Image region.
- typedef struct [hsa_ext_sampler_s](#) [hsa_ext_sampler_t](#)
Sampler handle. Samplers are populated by [hsa_ext_sampler_create](#). Sampler handles are only unique within an agent, not across agents.
- typedef uint32_t [hsa_ext_sampler_addressing_mode32_t](#)
A fixed-size type used to represent [hsa_ext_sampler_addressing_mode_t](#) constants.

- typedef uint32_t [hsa_ext_sampler_coordinate_mode32_t](#)
A fixed-size type used to represent [hsa_ext_sampler_coordinate_mode_t](#) constants.
- typedef uint32_t [hsa_ext_sampler_filter_mode32_t](#)
A fixed-size type used to represent [hsa_ext_sampler_filter_mode_t](#) constants.
- typedef struct [hsa_ext_sampler_descriptor_s](#) [hsa_ext_sampler_descriptor_t](#)
Implementation independent sampler descriptor.
- typedef struct [hsa_ext_images_1_00_pfn_s](#) [hsa_ext_images_1_00_pfn_t](#)
The function pointer table for the images v1.00 extension. Can be returned by [hsa_system_get_extension_table](#) or [hsa_system_get_major_extension_table](#).
- typedef struct [hsa_ext_images_1_pfn_s](#) [hsa_ext_images_1_pfn_t](#)
The function pointer table for the images v1 extension. Can be returned by [hsa_system_get_extension_table](#) or [hsa_system_get_major_extension_table](#).

Enumerations

- enum { [HSA_EXT_STATUS_ERROR_IMAGE_FORMAT_UNSUPPORTED](#) = 0x3000 , [HSA_EXT_STATUS_ERROR_IMAGE_S](#) = 0x3001 , [HSA_EXT_STATUS_ERROR_IMAGE_PITCH_UNSUPPORTED](#) = 0x3002 , [HSA_EXT_STATUS_ERROR_SAMPLE](#) = 0x3003 }
Enumeration constants added to [hsa_status_t](#) by this extension.
- enum {
[HSA_EXT_AGENT_INFO_IMAGE_1D_MAX_ELEMENTS](#) = 0x3000 , [HSA_EXT_AGENT_INFO_IMAGE_1DA_MAX_ELEMEN](#) = 0x3001 , [HSA_EXT_AGENT_INFO_IMAGE_1DB_MAX_ELEMENTS](#) = 0x3002 , [HSA_EXT_AGENT_INFO_IMAGE_2D_MAX](#) = 0x3003 ,
[HSA_EXT_AGENT_INFO_IMAGE_2DA_MAX_ELEMENTS](#) = 0x3004 , [HSA_EXT_AGENT_INFO_IMAGE_2DDEPTH_MAX_E](#) = 0x3005 , [HSA_EXT_AGENT_INFO_IMAGE_2DADEPTH_MAX_ELEMENTS](#) = 0x3006 , [HSA_EXT_AGENT_INFO_IMAGE_3](#) = 0x3007 ,
[HSA_EXT_AGENT_INFO_IMAGE_ARRAY_MAX_LAYERS](#) = 0x3008 , [HSA_EXT_AGENT_INFO_MAX_IMAGE_RD_HANDLE](#) = 0x3009 , [HSA_EXT_AGENT_INFO_MAX_IMAGE_RORW_HANDLES](#) = 0x300A , [HSA_EXT_AGENT_INFO_MAX_SAMPLE](#) = 0x300B ,
[HSA_EXT_AGENT_INFO_IMAGE_LINEAR_ROW_PITCH_ALIGNMENT](#) = 0x300C }
Enumeration constants added to [hsa_agent_info_t](#) by this extension.
- enum [hsa_ext_image_geometry_t](#) {
[HSA_EXT_IMAGE_GEOMETRY_1D](#) = 0 , [HSA_EXT_IMAGE_GEOMETRY_2D](#) = 1 , [HSA_EXT_IMAGE_GEOMETRY_3D](#) = 2 , [HSA_EXT_IMAGE_GEOMETRY_1DA](#) = 3 ,
[HSA_EXT_IMAGE_GEOMETRY_2DA](#) = 4 , [HSA_EXT_IMAGE_GEOMETRY_1DB](#) = 5 , [HSA_EXT_IMAGE_GEOMETRY_2DD](#) = 6 , [HSA_EXT_IMAGE_GEOMETRY_2DADEPTH](#) = 7 }
Geometry associated with the image. This specifies the number of image dimensions and whether the image is an image array. See the Image Geometry section in the HSA Programming Reference Manual for definitions on each geometry. The enumeration values match the BRIG type [hsa_ext_brig_image_geometry_t](#).
- enum [hsa_ext_image_channel_type_t](#) {
[HSA_EXT_IMAGE_CHANNEL_TYPE_SNORM_INT8](#) = 0 , [HSA_EXT_IMAGE_CHANNEL_TYPE_↵](#)
[SNORM_INT16](#) = 1 , [HSA_EXT_IMAGE_CHANNEL_TYPE_UNORM_INT8](#) = 2 , [HSA_EXT_IMAGE_↵](#)
[CHANNEL_TYPE_UNORM_INT16](#) = 3 ,
[HSA_EXT_IMAGE_CHANNEL_TYPE_UNORM_INT24](#) = 4 , [HSA_EXT_IMAGE_CHANNEL_TYPE_↵](#)
[UNORM_SHORT_555](#) = 5 , [HSA_EXT_IMAGE_CHANNEL_TYPE_UNORM_SHORT_565](#) = 6 , [HSA_↵](#)
[EXT_IMAGE_CHANNEL_TYPE_UNORM_SHORT_101010](#) = 7 ,
[HSA_EXT_IMAGE_CHANNEL_TYPE_SIGNED_INT8](#) = 8 , [HSA_EXT_IMAGE_CHANNEL_TYPE_↵](#)
[SIGNED_INT16](#) = 9 , [HSA_EXT_IMAGE_CHANNEL_TYPE_SIGNED_INT32](#) = 10 , [HSA_EXT_IMAGE_↵](#)
[CHANNEL_TYPE_UNSIGNED_INT8](#) = 11 ,
[HSA_EXT_IMAGE_CHANNEL_TYPE_UNSIGNED_INT16](#) = 12 , [HSA_EXT_IMAGE_CHANNEL_TYPE_↵](#)
[UNSIGNED_INT32](#) = 13 , [HSA_EXT_IMAGE_CHANNEL_TYPE_HALF_FLOAT](#) = 14 , [HSA_EXT_IMAGE_↵](#)
[_CHANNEL_TYPE_FLOAT](#) = 15 }
Channel type associated with the elements of an image. See the Channel Type section in the HSA Programming Reference Manual for definitions on each channel type. The enumeration values and definition match the BRIG type [hsa_ext_brig_image_channel_type_t](#).

- enum [hsa_ext_image_channel_order_t](#) {
[HSA_EXT_IMAGE_CHANNEL_ORDER_A](#) = 0 , [HSA_EXT_IMAGE_CHANNEL_ORDER_R](#) = 1 , [HSA_EXT_IMAGE_CHANNEL_ORDER_RX](#) = 2 , [HSA_EXT_IMAGE_CHANNEL_ORDER_RG](#) = 3 ,
[HSA_EXT_IMAGE_CHANNEL_ORDER_RGX](#) = 4 , [HSA_EXT_IMAGE_CHANNEL_ORDER_RA](#) = 5 ,
[HSA_EXT_IMAGE_CHANNEL_ORDER_RGB](#) = 6 , [HSA_EXT_IMAGE_CHANNEL_ORDER_RGBX](#) = 7 ,
[HSA_EXT_IMAGE_CHANNEL_ORDER_RGBA](#) = 8 , [HSA_EXT_IMAGE_CHANNEL_ORDER_BGRA](#) = 9 ,
[HSA_EXT_IMAGE_CHANNEL_ORDER_ARGB](#) = 10 , [HSA_EXT_IMAGE_CHANNEL_ORDER_ABGR](#) = 11 ,
[HSA_EXT_IMAGE_CHANNEL_ORDER_SRGB](#) = 12 , [HSA_EXT_IMAGE_CHANNEL_ORDER_SRGBX](#) = 13 , [HSA_EXT_IMAGE_CHANNEL_ORDER_SRGBA](#) = 14 , [HSA_EXT_IMAGE_CHANNEL_ORDER_SBGRA](#) = 15 ,
[HSA_EXT_IMAGE_CHANNEL_ORDER_INTENSITY](#) = 16 , [HSA_EXT_IMAGE_CHANNEL_ORDER_LUMINANCE](#) = 17 , [HSA_EXT_IMAGE_CHANNEL_ORDER_DEPTH](#) = 18 , [HSA_EXT_IMAGE_CHANNEL_ORDER_DEPTH_STENCIL](#) = 19 }
Channel order associated with the elements of an image. See the Channel Order section in the HSA Programming Reference Manual for definitions on each channel order. The enumeration values match the BRIG type [hsa_ext_brig_image_channel_order_t](#).
- enum [hsa_ext_image_capability_t](#) {
[HSA_EXT_IMAGE_CAPABILITY_NOT_SUPPORTED](#) = 0x0 , [HSA_EXT_IMAGE_CAPABILITY_READ_ONLY](#) = 0x1 , [HSA_EXT_IMAGE_CAPABILITY_WRITE_ONLY](#) = 0x2 , [HSA_EXT_IMAGE_CAPABILITY_READ_WRITE](#) = 0x4 ,
[HSA_EXT_IMAGE_CAPABILITY_READ_MODIFY_WRITE](#) = 0x8 , [HSA_EXT_IMAGE_CAPABILITY_ACCESS_INVARIANT_D](#) = 0x10 }
Image capability.
- enum [hsa_ext_image_data_layout_t](#) { [HSA_EXT_IMAGE_DATA_LAYOUT_OPAQUE](#) = 0x0 , [HSA_EXT_IMAGE_DATA_LAYOUT](#) = 0x1 }
Image data layout.
- enum [hsa_ext_sampler_addressing_mode_t](#) {
[HSA_EXT_SAMPLER_ADDRESSING_MODE_UNDEFINED](#) = 0 , [HSA_EXT_SAMPLER_ADDRESSING_MODE_CLAMP_TO](#) = 1 , [HSA_EXT_SAMPLER_ADDRESSING_MODE_CLAMP_TO_BORDER](#) = 2 , [HSA_EXT_SAMPLER_ADDRESSING_MODE](#) = 3 ,
[HSA_EXT_SAMPLER_ADDRESSING_MODE_MIRRORED_REPEAT](#) = 4 }
Sampler address modes. The sampler address mode describes the processing of out-of-range image coordinates. See the Addressing Mode section in the HSA Programming Reference Manual for definitions on each address mode. The values match the BRIG type [hsa_ext_brig_sampler_addressing_t](#).
- enum [hsa_ext_sampler_coordinate_mode_t](#) { [HSA_EXT_SAMPLER_COORDINATE_MODE_UNNORMALIZED](#) = 0 , [HSA_EXT_SAMPLER_COORDINATE_MODE_NORMALIZED](#) = 1 }
Sampler coordinate normalization modes. See the Coordinate Normalization Mode section in the HSA Programming Reference Manual for definitions on each coordinate normalization mode. The values match the BRIG type [hsa_ext_brig_sampler_coord_normalization_t](#).
- enum [hsa_ext_sampler_filter_mode_t](#) { [HSA_EXT_SAMPLER_FILTER_MODE_NEAREST](#) = 0 , [HSA_EXT_SAMPLER_FILTER](#) = 1 }
Sampler filter modes. See the Filter Mode section in the HSA Programming Reference Manual for definitions on each address mode. The enumeration values match the BRIG type [hsa_ext_brig_sampler_filter_t](#).

Functions

- [hsa_status_t](#) HSA_API [hsa_ext_image_get_capability](#) ([hsa_agent_t](#) agent, [hsa_ext_image_geometry_t](#) geometry, const [hsa_ext_image_format_t](#) *image_format, uint32_t *capability_mask)
Retrieve the supported image capabilities for a given combination of agent, geometry, and image format for an image created with an opaque image data layout.
- [hsa_status_t](#) HSA_API [hsa_ext_image_get_capability_with_layout](#) ([hsa_agent_t](#) agent, [hsa_ext_image_geometry_t](#) geometry, const [hsa_ext_image_format_t](#) *image_format, [hsa_ext_image_data_layout_t](#) image_data_layout, uint32_t *capability_mask)
Retrieve the supported image capabilities for a given combination of agent, geometry, image format, and image layout for an image created with an explicit image data layout.

- [hsa_status_t](#) HSA_API [hsa_ext_image_data_get_info](#) ([hsa_agent_t](#) agent, const [hsa_ext_image_descriptor_t](#) *image_descriptor, [hsa_access_permission_t](#) access_permission, [hsa_ext_image_data_info_t](#) *image_data_info)
Retrieve the image data requirements for a given combination of agent, image descriptor, and access permission for an image created with an opaque image data layout.
- [hsa_status_t](#) HSA_API [hsa_ext_image_data_get_info_with_layout](#) ([hsa_agent_t](#) agent, const [hsa_ext_image_descriptor_t](#) *image_descriptor, [hsa_access_permission_t](#) access_permission, [hsa_ext_image_data_layout_t](#) image_data_layout, [size_t](#) image_data_row_pitch, [size_t](#) image_data_slice_pitch, [hsa_ext_image_data_info_t](#) *image_data_info)
Retrieve the image data requirements for a given combination of image descriptor, access permission, image data layout, image data row pitch, and image data slice pitch for an image created with an explicit image data layout.
- [hsa_status_t](#) HSA_API [hsa_ext_image_create](#) ([hsa_agent_t](#) agent, const [hsa_ext_image_descriptor_t](#) *image_descriptor, const void *image_data, [hsa_access_permission_t](#) access_permission, [hsa_ext_image_t](#) *image)
Creates an agent specific image handle to an image with an opaque image data layout.
- [hsa_status_t](#) HSA_API [hsa_ext_image_create_with_layout](#) ([hsa_agent_t](#) agent, const [hsa_ext_image_descriptor_t](#) *image_descriptor, const void *image_data, [hsa_access_permission_t](#) access_permission, [hsa_ext_image_data_layout_t](#) image_data_layout, [size_t](#) image_data_row_pitch, [size_t](#) image_data_slice_pitch, [hsa_ext_image_t](#) *image)
Creates an agent specific image handle to an image with an explicit image data layout.
- [hsa_status_t](#) HSA_API [hsa_ext_image_destroy](#) ([hsa_agent_t](#) agent, [hsa_ext_image_t](#) image)
Destroy an image handle previously created using [hsa_ext_image_create](#) or [hsa_ext_image_create_with_layout](#).
- [hsa_status_t](#) HSA_API [hsa_ext_image_copy](#) ([hsa_agent_t](#) agent, [hsa_ext_image_t](#) src_image, const [hsa_dim3_t](#) *src_offset, [hsa_ext_image_t](#) dst_image, const [hsa_dim3_t](#) *dst_offset, const [hsa_dim3_t](#) *range)
Copies a portion of one image (the source) to another image (the destination).
- [hsa_status_t](#) HSA_API [hsa_ext_image_import](#) ([hsa_agent_t](#) agent, const void *src_memory, [size_t](#) src_row_pitch, [size_t](#) src_slice_pitch, [hsa_ext_image_t](#) dst_image, const [hsa_ext_image_region_t](#) *image_region)
Import a linearly organized image data from memory directly to an image handle.
- [hsa_status_t](#) HSA_API [hsa_ext_image_export](#) ([hsa_agent_t](#) agent, [hsa_ext_image_t](#) src_image, void *dst_memory, [size_t](#) dst_row_pitch, [size_t](#) dst_slice_pitch, const [hsa_ext_image_region_t](#) *image_region)
Export the image data to linearly organized memory.
- [hsa_status_t](#) HSA_API [hsa_ext_image_clear](#) ([hsa_agent_t](#) agent, [hsa_ext_image_t](#) image, const void *data, const [hsa_ext_image_region_t](#) *image_region)
Clear a region of an image so that every image element has the specified value.
- [hsa_status_t](#) HSA_API [hsa_ext_sampler_create](#) ([hsa_agent_t](#) agent, const [hsa_ext_sampler_descriptor_t](#) *sampler_descriptor, [hsa_ext_sampler_t](#) *sampler)
Create an agent specific sampler handle for a given agent independent sampler descriptor and agent.
- [hsa_status_t](#) HSA_API [hsa_ext_sampler_destroy](#) ([hsa_agent_t](#) agent, [hsa_ext_sampler_t](#) sampler)
Destroy a sampler handle previously created using [hsa_ext_sampler_create](#).

5.12.1 Detailed Description

5.12.2 Macro Definition Documentation

5.12.2.1 hsa_ext_images_1

```
#define hsa_ext_images_1
```

Definition at line 1352 of file [hsa_ext_image.h](#).

5.12.2.2 hsa_ext_images_1_00

```
#define hsa_ext_images_1_00
```

Definition at line 1281 of file [hsa_ext_image.h](#).

5.12.3 Typedef Documentation

5.12.3.1 hsa_ext_image_channel_order32_t

```
typedef uint32_t hsa_ext_image_channel_order32_t
```

A fixed-size type used to represent [hsa_ext_image_channel_order_t](#) constants.

Definition at line 305 of file [hsa_ext_image.h](#).

5.12.3.2 hsa_ext_image_channel_type32_t

```
typedef uint32_t hsa_ext_image_channel_type32_t
```

A fixed-size type used to represent [hsa_ext_image_channel_type_t](#) constants.

Definition at line 269 of file [hsa_ext_image.h](#).

5.12.3.3 hsa_ext_image_t

```
typedef struct hsa_ext_image_s hsa_ext_image_t
```

Image handle, populated by [hsa_ext_image_create](#) or [hsa_ext_image_create_with_layout](#). Image handles are only unique within an agent, not across agents.

5.12.3.4 hsa_ext_sampler_addressing_mode32_t

```
typedef uint32_t hsa_ext_sampler_addressing_mode32_t
```

A fixed-size type used to represent [hsa_ext_sampler_addressing_mode_t](#) constants.

Definition at line 1145 of file [hsa_ext_image.h](#).

5.12.3.5 hsa_ext_sampler_coordinate_mode32_t

```
typedef uint32_t hsa_ext_sampler_coordinate_mode32_t
```

A fixed-size type used to represent [hsa_ext_sampler_coordinate_mode_t](#) constants.

Definition at line 1172 of file [hsa_ext_image.h](#).

5.12.3.6 hsa_ext_sampler_filter_mode32_t

```
typedef uint32_t hsa_ext_sampler_filter_mode32_t
```

A fixed-size type used to represent [hsa_ext_sampler_filter_mode_t](#) constants.

Definition at line 1200 of file [hsa_ext_image.h](#).

5.12.4 Enumeration Type Documentation

5.12.4.1 anonymous enum

```
anonymous enum
```

Enumeration constants added to [hsa_status_t](#) by this extension.

Remarks

Additions to [hsa_status_t](#)

Enumerator

HSA_EXT_STATUS_ERROR_IMAGE_FORMAT_↔ UNSUPPORTED	Image format is not supported.
HSA_EXT_STATUS_ERROR_IMAGE_SIZE_↔ UNSUPPORTED	Image size is not supported.
HSA_EXT_STATUS_ERROR_IMAGE_PITCH_↔ UNSUPPORTED	Image pitch is not supported or invalid.
HSA_EXT_STATUS_ERROR_SAMPLER_DESCRIPTOR_↔ UNSUPPORTED	Sampler descriptor is not supported or invalid.

Definition at line 68 of file [hsa_ext_image.h](#).

5.12.4.2 anonymous enum

```
anonymous enum
```

Enumeration constants added to [hsa_agent_info_t](#) by this extension.

Remarks

Additions to [hsa_agent_info_t](#)

Enumerator

HSA_EXT_AGENT_INFO_IMAGE_1D_MAX_↔ ELEMENTS	Maximum number of elements in 1D images. Must be at least 16384. The type of this attribute is size_t .
HSA_EXT_AGENT_INFO_IMAGE_1DA_MAX_↔ ELEMENTS	Maximum number of elements in 1DA images. Must be at least 16384. The type of this attribute is size_t .
HSA_EXT_AGENT_INFO_IMAGE_1DB_MAX_↔ ELEMENTS	Maximum number of elements in 1DB images. Must be at least 65536. The type of this attribute is size_t .
HSA_EXT_AGENT_INFO_IMAGE_2D_MAX_↔ ELEMENTS	Maximum dimensions (width, height) of 2D images, in image elements. The X and Y maximums must be at least 16384. The type of this attribute is size_t[2] .
HSA_EXT_AGENT_INFO_IMAGE_2DA_MAX_↔ ELEMENTS	Maximum dimensions (width, height) of 2DA images, in image elements. The X and Y maximums must be at least 16384. The type of this attribute is size_t[2] .
HSA_EXT_AGENT_INFO_IMAGE_2DDEPTH_↔ MAX_ELEMENTS	Maximum dimensions (width, height) of 2DDEPTH images, in image elements. The X and Y maximums must be at least 16384. The type of this attribute is size_t[2] .
HSA_EXT_AGENT_INFO_IMAGE_2DADEPTH_↔ MAX_ELEMENTS	Maximum dimensions (width, height) of 2DADEPTH images, in image elements. The X and Y maximums must be at least 16384. The type of this attribute is size_t[2] .
HSA_EXT_AGENT_INFO_IMAGE_3D_MAX_↔ ELEMENTS	Maximum dimensions (width, height, depth) of 3D images, in image elements. The maximum along any dimension must be at least 2048. The type of this attribute is size_t[3] .
HSA_EXT_AGENT_INFO_IMAGE_ARRAY_MAX_↔ LAYERS	Maximum number of image layers in a image array. Must be at least 2048. The type of this attribute is size_t .
HSA_EXT_AGENT_INFO_MAX_IMAGE_RD_↔ HANDLES	Maximum number of read-only image handles that can be created for an agent at any one time. Must be at least 128. The type of this attribute is size_t .
HSA_EXT_AGENT_INFO_MAX_IMAGE_RORW_↔ HANDLES	Maximum number of write-only and read-write image handles (combined) that can be created for an agent at any one time. Must be at least 64. The type of this attribute is size_t .
HSA_EXT_AGENT_INFO_MAX_SAMPLER_↔ HANDLERS	Maximum number of sampler handlers that can be created for an agent at any one time. Must be at least 16. The type of this attribute is size_t .
HSA_EXT_AGENT_INFO_IMAGE_LINEAR_ROW_↔ _PITCH_ALIGNMENT	Image pitch alignment. The agent only supports linear image data layouts with a row pitch that is a multiple of this value. Must be a power of 2. The type of this attribute is size_t .

Definition at line 93 of file [hsa_ext_image.h](#).

5.12.4.3 hsa_ext_image_capability_t

enum `hsa_ext_image_capability_t`

Image capability.

Enumerator

HSA_EXT_IMAGE_CAPABILITY_NOT_SUPPORTED	Images of this geometry, format, and layout are not supported by the agent.
HSA_EXT_IMAGE_CAPABILITY_READ_ONLY	Read-only images of this geometry, format, and layout are supported by the agent.
HSA_EXT_IMAGE_CAPABILITY_WRITE_ONLY	Write-only images of this geometry, format, and layout are supported by the agent.
HSA_EXT_IMAGE_CAPABILITY_READ_WRITE	Read-write images of this geometry, format, and layout are supported by the agent.
HSA_EXT_IMAGE_CAPABILITY_READ_MODIFY_WRITE	Deprecated Images of this geometry, format, and layout can be accessed from read-modify-write atomic operations in the agent.
HSA_EXT_IMAGE_CAPABILITY_ACCESS_INVARIANT_DATA_LAYOUT	Images of this geometry, format, and layout are guaranteed to have a consistent data layout regardless of how they are accessed by the associated agent.

Definition at line 362 of file `hsa_ext_image.h`.

5.12.4.4 hsa_ext_image_channel_order_t

enum `hsa_ext_image_channel_order_t`

Channel order associated with the elements of an image. See the *Channel Order* section in the *HSA Programming Reference Manual* for definitions on each channel order. The enumeration values match the BRIG type `hsa_ext_brig_image_channel_order_t`.

Definition at line 279 of file `hsa_ext_image.h`.

5.12.4.5 hsa_ext_image_channel_type_t

enum `hsa_ext_image_channel_type_t`

Channel type associated with the elements of an image. See the *Channel Type* section in the *HSA Programming Reference Manual* for definitions on each channel type. The enumeration values and definition match the BRIG type `hsa_ext_brig_image_channel_type_t`.

Definition at line 247 of file `hsa_ext_image.h`.

5.12.4.6 hsa_ext_image_data_layout_t

```
enum hsa_ext_image_data_layout_t
```

Image data layout.

An image data layout denotes such aspects of image data layout as tiling and organization of channels in memory. Some image data layouts may only apply to specific image geometries, formats, and access permissions. Different agents may support different image layout identifiers, including vendor specific layouts. Note that an agent may not support the same image data layout for different access permissions to images with the same image geometry, size, and format. If multiple agents support the same image data layout then it is possible to use separate image handles for each agent that references the same image data.

Enumerator

HSA_EXT_IMAGE_DATA_LAYOUT_OPAQUE	An implementation specific opaque image data layout which can vary depending on the agent, geometry, image format, image size, and access permissions.
HSA_EXT_IMAGE_DATA_LAYOUT_LINEAR	The image data layout is specified by the following rules in ascending byte address order. For a 3D image, 2DA image array, or 1DA image array, the image data is stored as a linear sequence of adjacent 2D image slices, 2D images, or 1D images respectively, spaced according to the slice pitch. Each 2D image is stored as a linear sequence of adjacent image rows, spaced according to the row pitch. Each 1D or 1DB image is stored as a single image row. Each image row is stored as a linear sequence of image elements. Each image element is stored as a linear sequence of image components specified by the left to right channel order definition. Each image component is stored using the memory type specified by the channel type. The 1DB image geometry always uses the linear image data layout.

Definition at line 411 of file [hsa_ext_image.h](#).

5.12.4.7 hsa_ext_image_geometry_t

```
enum hsa_ext_image_geometry_t
```

Geometry associated with the image. This specifies the number of image dimensions and whether the image is an image array. See the *Image Geometry* section in the *HSA Programming Reference Manual* for definitions on each geometry. The enumeration values match the BRIG type `hsa_ext_brig_image_geometry_t`.

Enumerator

HSA_EXT_IMAGE_GEOMETRY_1D	One-dimensional image addressed by width coordinate.
HSA_EXT_IMAGE_GEOMETRY_2D	Two-dimensional image addressed by width and height coordinates.
HSA_EXT_IMAGE_GEOMETRY_3D	Three-dimensional image addressed by width, height, and depth coordinates.

Enumerator

HSA_EXT_IMAGE_GEOMETRY_1DA	Array of one-dimensional images with the same size and format. 1D arrays are addressed by width and index coordinate.
HSA_EXT_IMAGE_GEOMETRY_2DA	Array of two-dimensional images with the same size and format. 2D arrays are addressed by width, height, and index coordinates.
HSA_EXT_IMAGE_GEOMETRY_1DB	One-dimensional image addressed by width coordinate. It has specific restrictions compared to HSA_EXT_IMAGE_GEOMETRY_1D . An image with an opaque image data layout will always use a linear image data layout, and one with an explicit image data layout must specify HSA_EXT_IMAGE_DATA_LAYOUT_LINEAR .
HSA_EXT_IMAGE_GEOMETRY_2DDEPTH	Two-dimensional depth image addressed by width and height coordinates.
HSA_EXT_IMAGE_GEOMETRY_2DADEPTH	Array of two-dimensional depth images with the same size and format. 2D arrays are addressed by width, height, and index coordinates.

Definition at line 191 of file [hsa_ext_image.h](#).

5.12.4.8 hsa_ext_sampler_addressing_mode_t

```
enum hsa_ext_sampler_addressing_mode_t
```

Sampler address modes. The sampler address mode describes the processing of out-of-range image coordinates. See the *Addressing Mode* section in the *HSA Programming Reference Manual* for definitions on each address mode. The values match the BRIG type `hsa_ext_brig_sampler_addressing_t`.

Enumerator

HSA_EXT_SAMPLER_ADDRESSING_MODE_↔ UNDEFINED	Out-of-range coordinates are not handled.
HSA_EXT_SAMPLER_ADDRESSING_MODE_↔ CLAMP_TO_EDGE	Clamp out-of-range coordinates to the image edge.
HSA_EXT_SAMPLER_ADDRESSING_MODE_↔ CLAMP_TO_BORDER	Clamp out-of-range coordinates to the image border color.
HSA_EXT_SAMPLER_ADDRESSING_MODE_↔ REPEAT	Wrap out-of-range coordinates back into the valid coordinate range so the image appears as repeated tiles.
HSA_EXT_SAMPLER_ADDRESSING_MODE_↔ MIRRORED_REPEAT	Mirror out-of-range coordinates back into the valid coordinate range so the image appears as repeated tiles with every other tile a reflection.

Definition at line 1111 of file [hsa_ext_image.h](#).

5.12.4.9 hsa_ext_sampler_coordinate_mode_t

```
enum hsa_ext_sampler_coordinate_mode_t
```


Sampler coordinate normalization modes. See the *Coordinate Normalization Mode* section in the *HSA Programming Reference Manual* for definitions on each coordinate normalization mode. The values match the BRIG type `hsa_ext_brig_sampler_coord_normalization_t`.

Enumerator

HSA_EXT_SAMPLER_COORDINATE_MODE_UNNORMALIZED	Coordinates are used to directly address an image element.
HSA_EXT_SAMPLER_COORDINATE_MODE_NORMALIZED	Coordinates are scaled by the image dimension size before being used to address an image element.

Definition at line 1154 of file [hsa_ext_image.h](#).

5.12.4.10 hsa_ext_sampler_filter_mode_t

```
enum hsa_ext_sampler_filter_mode_t
```

Sampler filter modes. See the *Filter Mode* section in the *HSA Programming Reference Manual* for definitions on each address mode. The enumeration values match the BRIG type `hsa_ext_brig_sampler_filter_t`.

Enumerator

HSA_EXT_SAMPLER_FILTER_MODE_NEAREST	Filter to the image element nearest (in Manhattan distance) to the specified coordinate.
HSA_EXT_SAMPLER_FILTER_MODE_LINEAR	Filter to the image element calculated by combining the elements in a 2x2 square block or 2x2x2 cube block around the specified coordinate. The elements are combined using linear interpolation.

Definition at line 1181 of file [hsa_ext_image.h](#).

5.12.5 Function Documentation

5.12.5.1 hsa_ext_image_clear()

```
hsa_status_t HSA_API hsa_ext_image_clear (
    hsa_agent_t agent,
    hsa_ext_image_t image,
    const void * data,
    const hsa_ext_image_region_t * image_region )
```

Clear a region of an image so that every image element has the specified value.

Parameters

in	<i>agent</i>	Agent associated with the image handle.
----	--------------	---

Parameters

in	<i>image</i>	Image handle for image to be cleared.
in	<i>data</i>	The value to which to set each image element being cleared. It is specified as an array of image component values. The number of array elements must match the number of access components for the image channel order. The type of each array element must match the image access type of the image channel type. When the value is used to set the value of an image element, the conversion method corresponding to the image channel type is used. See the <i>Channel Order</i> section and <i>Channel Type</i> section in the <i>HSA Programming Reference Manual</i> for more information. Must not be NULL.
in	<i>image_region</i>	Pointer to the image region to clear. Must not be NULL. If the region references an out-of-bounds element, the behavior is undefined.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_AGENT</i>	The agent is invalid.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>data</i> is NULL, or <i>image_region</i> is NULL.

5.12.5.2 hsa_ext_image_copy()

```

hsa_status_t HSA_API hsa_ext_image_copy (
    hsa_agent_t agent,
    hsa_ext_image_t src_image,
    const hsa_dim3_t * src_offset,
    hsa_ext_image_t dst_image,
    const hsa_dim3_t * dst_offset,
    const hsa_dim3_t * range )

```

Copies a portion of one image (the source) to another image (the destination).

The source and destination image formats should be the same, with the exception that s-form channel orders match the corresponding non-s-form channel order and vice versa. For example, it is allowed to copy a source image with a channel order of HSA_EXT_IMAGE_CHANNEL_ORDER_SRGB to a destination image with a channel order of HSA_EXT_IMAGE_CHANNEL_ORDER_RGB.

The source and destination images do not have to be of the same geometry and appropriate scaling is performed by the HSA runtime. It is possible to copy subregions between any combinations of source and destination geometries, provided that the dimensions of the subregions are the same. For example, it is allowed to copy a rectangular region from a 2D image to a slice of a 3D image.

If the source and destination image data overlap, or the combination of offset and range references an out-of-bounds element in any of the images, the behavior is undefined.

Parameters

in	<i>agent</i>	Agent associated with both the source and destination image handles.
in	<i>src_image</i>	Image handle of source image. The agent associated with the source image handle must be identical to that of the destination image.

Parameters

in	<i>src_offset</i>	Pointer to the offset within the source image where to copy the data from. Must not be NULL.
in	<i>dst_image</i>	Image handle of destination image.
in	<i>dst_offset</i>	Pointer to the offset within the destination image where to copy the data. Must not be NULL.
in	<i>range</i>	Dimensions of the image portion to be copied. The HSA runtime computes the size of the image data to be copied using this argument. Must not be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_AGENT</i>	The agent is invalid.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>src_offset</i> is NULL, <i>dst_offset</i> is NULL, or <i>range</i> is NULL.

5.12.5.3 hsa_ext_image_create()

```

hsa_status_t HSA_API hsa_ext_image_create (
    hsa_agent_t agent,
    const hsa_ext_image_descriptor_t * image_descriptor,
    const void * image_data,
    hsa_access_permission_t access_permission,
    hsa_ext_image_t * image )

```

Creates an agent specific image handle to an image with an opaque image data layout.

Images with an opaque image data layout created with different access permissions but matching image descriptors and same agent can share the same image data if [*HSA_EXT_IMAGE_CAPABILITY_ACCESS_INVARIANT_DATA_LAYOUT*](#) is reported by [*hsa_ext_image_get_capability*](#) for the image format specified in the image descriptor. Image descriptors match if they have the same values, with the exception that s-form channel orders match the corresponding non-s-form channel order and vice versa.

If necessary, an application can use image operations (import, export, copy, clear) to prepare the image for the intended use regardless of the access permissions.

Parameters

in	<i>agent</i>	agent to be associated with the image handle created.
in	<i>image_descriptor</i>	Pointer to an image descriptor. Must not be NULL.
in	<i>image_data</i>	Image data buffer that must have been allocated according to the size and alignment requirements dictated by <i>hsa_ext_image_data_get_info</i> . Must not be NULL.

Any previous memory contents are preserved upon creation. The application is responsible for ensuring that the lifetime of the image data exceeds that of all the associated images.

Parameters

in	<i>access_permission</i>	Access permission of the image when accessed by agent. The access permission defines how the agent is allowed to access the image using the image handle created and must match the corresponding HSAIL image handle type. The agent must support the image format specified in <i>image_descriptor</i> for the given <i>access_permission</i> .
out	<i>image</i>	Pointer to a memory location where the HSA runtime stores the newly created image handle. Must not be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_AGENT</i>	The agent is invalid.
<i>HSA_EXT_STATUS_ERROR_IMAGE_FORMAT_UNSUPPORTED</i>	The agent does not have the capability to support the image format contained in <i>image_descriptor</i> using the specified <i>access_permission</i> .
<i>HSA_EXT_STATUS_ERROR_IMAGE_SIZE_UNSUPPORTED</i>	The agent does not support the image dimensions specified by <i>image_descriptor</i> using the specified <i>access_permission</i> .
<i>HSA_STATUS_ERROR_OUT_OF_RESOURCES</i>	The HSA runtime failed to allocate the required resources.

support the creation of more image handles with the given *access_permission*).

Return values

<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>image_descriptor</i> is NULL, <i>image_data</i> is NULL, <i>image_data</i> does not have a valid alignment, <i>access_permission</i> is not a valid access permission value, or <i>image</i> is NULL.
--	--

5.12.5.4 `hsa_ext_image_create_with_layout()`

```

hsa_status_t HSA_API hsa_ext_image_create_with_layout (
    hsa_agent_t agent,
    const hsa_ext_image_descriptor_t * image_descriptor,
    const void * image_data,
    hsa_access_permission_t access_permission,
    hsa_ext_image_data_layout_t image_data_layout,
    size_t image_data_row_pitch,
    size_t image_data_slice_pitch,
    hsa_ext_image_t * image )

```

Creates an agent specific image handle to an image with an explicit image data layout.

Images with an explicit image data layout created with different access permissions but matching image descriptors and matching image layout can share the same image data if [*HSA_EXT_IMAGE_CAPABILITY_ACCESS_INVARIANT_DATA_LAYOUT*](#) is reported by [*hsa_ext_image_get_capability_with_layout*](#) for the image format specified in the image descriptor

and specified image data layout. Image descriptors match if they have the same values, with the exception that s-form channel orders match the corresponding non-s-form channel order and vice versa. Image layouts match if they are the same image data layout and use the same image row and slice values.

If necessary, an application can use image operations (import, export, copy, clear) to prepare the image for the intended use regardless of the access permissions.

Parameters

in	<i>agent</i>	agent to be associated with the image handle created.
in	<i>image_descriptor</i>	Pointer to an image descriptor. Must not be NULL.
in	<i>image_data</i>	Image data buffer that must have been allocated according to the size and alignment requirements dictated by hsa_ext_image_data_get_info_with_layout . Must not be NULL.

Any previous memory contents are preserved upon creation. The application is responsible for ensuring that the lifetime of the image data exceeds that of all the associated images.

Parameters

in	<i>access_permission</i>	Access permission of the image when accessed by the agent. The access permission defines how the agent is allowed to access the image and must match the corresponding HSAIL image handle type. The agent must support the image format specified in <i>image_descriptor</i> for the given <i>access_permission</i> and <i>image_data_layout</i> .
in	<i>image_data_layout</i>	The image data layout to use for the <i>image_data</i> . It is invalid to use HSA_EXT_IMAGE_DATA_LAYOUT_OPAQUE ; use hsa_ext_image_create instead.
in	<i>image_data_row_pitch</i>	The size in bytes for a single row of the image in the image data. If 0 is specified then the default row pitch value is used: image width * image element byte size. The value used must be greater than or equal to the default row pitch, and be a multiple of the image element byte size. For the linear image layout it must also be a multiple of the image linear row pitch alignment for the agents that will access the image data using image instructions.
in	<i>image_data_slice_pitch</i>	The size in bytes of a single slice of a 3D image, or the size in bytes of each image layer in an image array in the image data. If 0 is specified then the default slice pitch value is used: row pitch * height if geometry is HSA_EXT_IMAGE_GEOMETRY_3D , HSA_EXT_IMAGE_GEOMETRY_2DA , or HSA_EXT_IMAGE_GEOMETRY_2DADEPTH ; row pitch if geometry is HSA_EXT_IMAGE_GEOMETRY_1DA ; and 0 otherwise. The value used must be 0 if the default slice pitch is 0, be greater than or equal to the default slice pitch, and be a multiple of the row pitch.
out	<i>image</i>	Pointer to a memory location where the HSA runtime stores the newly created image handle. Must not be NULL.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_AGENT	The agent is invalid.

Return values

HSA_EXT_STATUS_ERROR_IMAGE_FORMAT_UNSUPPORTED	The agent does not have the capability to support the image format contained in the image descriptor using the specified <code>access_permission</code> and <code>image_data_layout</code> .
HSA_EXT_STATUS_ERROR_IMAGE_SIZE_UNSUPPORTED	The agent does not support the image dimensions specified by <code>image_descriptor</code> using the specified <code>access_permission</code> and <code>image_data_layout</code> .
HSA_EXT_STATUS_ERROR_IMAGE_PITCH_UNSUPPORTED	The agent does not support the row and slice pitch specified by <code>image_data_row_pitch</code> and <code>image_data_slice_pitch</code> , or the values are invalid.
HSA_STATUS_ERROR_OUT_OF_RESOURCES	The HSA runtime failed to allocate the required resources.

support the creation of more image handles with the given `access_permission`).

Return values

HSA_STATUS_ERROR_INVALID_ARGUMENT	<code>image_descriptor</code> is NULL, <code>image_data</code> is NULL, <code>image_data</code> does not have a valid alignment, <code>image_data_layout</code> is HSA_EXT_IMAGE_DATA_LAYOUT_OPAQUE , or image is NULL.
---	---

5.12.5.5 `hsa_ext_image_data_get_info()`

```

hsa_status_t HSA_API hsa_ext_image_data_get_info (
    hsa_agent_t agent,
    const hsa_ext_image_descriptor_t * image_descriptor,
    hsa_access_permission_t access_permission,
    hsa_ext_image_data_info_t * image_data_info )

```

Retrieve the image data requirements for a given combination of agent, image descriptor, and access permission for an image created with an opaque image data layout.

The optimal image data size and alignment requirements may vary depending on the image attributes specified in `image_descriptor`, the `access_permission`, and the agent. Also, different implementations of the HSA runtime may return different requirements for the same input values.

The implementation must return the same image data requirements for different access permissions with matching image descriptors as long as [hsa_ext_image_get_capability](#) reports [HSA_EXT_IMAGE_CAPABILITY_ACCESS_INVARIANT_DATA_LAYOUT](#). Image descriptors match if they have the same values, with the exception that s-form channel orders match the corresponding non-s-form channel order and vice versa.

Parameters

in	<i>agent</i>	Agent to be associated with the image handle.
in	<i>image_descriptor</i>	Pointer to an image descriptor. Must not be NULL.

Parameters

in	<i>access_permission</i>	Access permission of the image when accessed by <i>agent</i> . The access permission defines how the agent is allowed to access the image and must match the corresponding HSAIL image handle type. The <i>agent</i> must support the image format specified in <i>image_descriptor</i> for the given <i>access_permission</i> .
out	<i>image_data_info</i>	Memory location where the runtime stores the size and alignment requirements. Must not be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_AGENT</i>	The agent is invalid.
<i>HSA_EXT_STATUS_ERROR_IMAGE_FORMAT_UNSUPPORTED</i>	The agent does not support the image format specified by <i>image_descriptor</i> with the specified <i>access_permission</i> .
<i>HSA_EXT_STATUS_ERROR_IMAGE_SIZE_UNSUPPORTED</i>	The agent does not support the image dimensions specified by <i>image_descriptor</i> with the specified <i>access_permission</i> .
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<i>image_descriptor</i> is NULL, <i>access_permission</i> is not a valid access permission value, or <i>image_data_info</i> is NULL.

5.12.5.6 `hsa_ext_image_data_get_info_with_layout()`

```

hsa_status_t HSA_API hsa_ext_image_data_get_info_with_layout (
    hsa_agent_t agent,
    const hsa_ext_image_descriptor_t * image_descriptor,
    hsa_access_permission_t access_permission,
    hsa_ext_image_data_layout_t image_data_layout,
    size_t image_data_row_pitch,
    size_t image_data_slice_pitch,
    hsa_ext_image_data_info_t * image_data_info )

```

Retrieve the image data requirements for a given combination of image descriptor, access permission, image data layout, image data row pitch, and image data slice pitch for an image created with an explicit image data layout.

The image data size and alignment requirements may vary depending on the image attributes specified in *image_descriptor*, the *access_permission*, and the image layout. However, different implementations of the HSA runtime will return the same requirements for the same input values.

The implementation must return the same image data requirements for different access permissions with matching image descriptors and matching image layouts as long as [`hsa_ext_image_get_capability`](#) reports [`HSA_EXT_IMAGE_CAPABILITY_ACCESS_INVARIANT_DATA_LAYOUT`](#). Image descriptors match if they have the same values, with the exception that s-form channel orders match the corresponding non-s-form channel order and vice versa. Image layouts match if they are the same image data layout and use the same image row and slice pitch values.

Parameters

in	<i>image_descriptor</i>	Pointer to an image descriptor. Must not be NULL.
in	<i>access_permission</i>	Access permission of the image when accessed by an agent. The access permission defines how the agent is allowed to access the image and must match the corresponding HSAIL image handle type.
in	<i>image_data_layout</i>	The image data layout to use. It is invalid to use HSA_EXT_IMAGE_DATA_LAYOUT_OPAQUE ; use hsa_ext_image_data_get_info instead.
in	<i>image_data_row_pitch</i>	The size in bytes for a single row of the image in the image data. If 0 is specified then the default row pitch value is used: image width * image element byte size. The value used must be greater than or equal to the default row pitch, and be a multiple of the image element byte size. For the linear image layout it must also be a multiple of the image linear row pitch alignment for the agents that will access the image data using image instructions.
in	<i>image_data_slice_pitch</i>	The size in bytes of a single slice of a 3D image, or the size in bytes of each image layer in an image array in the image data. If 0 is specified then the default slice pitch value is used: row pitch * height if geometry is HSA_EXT_IMAGE_GEOMETRY_3D , HSA_EXT_IMAGE_GEOMETRY_2DA , or HSA_EXT_IMAGE_GEOMETRY_2DADEPTH ; row pitch if geometry is HSA_EXT_IMAGE_GEOMETRY_1DA ; and 0 otherwise. The value used must be 0 if the default slice pitch is 0, be greater than or equal to the default slice pitch, and be a multiple of the row pitch.
out	<i>image_data_info</i>	Memory location where the runtime stores the size and alignment requirements. Must not be NULL.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_EXT_STATUS_ERROR_IMAGE_FORMAT_UNSUPPORTED	The image format specified by <i>image_descriptor</i> is not supported for the <i>access_permission</i> and <i>image_data_layout</i> specified.
HSA_EXT_STATUS_ERROR_IMAGE_SIZE_UNSUPPORTED	The image dimensions specified by <i>image_descriptor</i> are not supported for the <i>access_permission</i> and <i>image_data_layout</i> specified.
HSA_EXT_STATUS_ERROR_IMAGE_PITCH_UNSUPPORTED	The row and slice pitch specified by <i>image_data_row_pitch</i> and <i>image_data_slice_pitch</i> are invalid or not supported.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>image_descriptor</i> is NULL, <i>image_data_layout</i> is HSA_EXT_IMAGE_DATA_LAYOUT_OPAQUE , or <i>image_data_info</i> is NULL.

5.12.5.7 hsa_ext_image_destroy()

```
hsa_status_t HSA_API hsa_ext_image_destroy (
```



```

hsa_agent_t agent,
hsa_ext_image_t image )

```

Destroy an image handle previously created using [hsa_ext_image_create](#) or [hsa_ext_image_create_with_layout](#).

Destroying the image handle does not free the associated image data, or modify its contents. The application should not destroy an image handle while there are references to it queued for execution or currently being used in a kernel dispatch.

Parameters

in	<i>agent</i>	Agent associated with the image handle.
in	<i>image</i>	Image handle to destroy.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_AGENT	The agent is invalid.

5.12.5.8 hsa_ext_image_export()

```

hsa_status_t HSA_API hsa_ext_image_export (
    hsa_agent_t agent,
    hsa_ext_image_t src_image,
    void * dst_memory,
    size_t dst_row_pitch,
    size_t dst_slice_pitch,
    const hsa_ext_image_region_t * image_region )

```

Export the image data to linearly organized memory.

The operation updates the destination memory with the image data of `src_image`. The size of the data exported to memory is implicitly derived from the image region.

It is the application's responsibility to avoid out of bounds memory access.

None of the destination memory or source image data memory can overlap. Overlapping of any of the source and destination image data memory within the export operation produces undefined results.

Parameters

in	<i>agent</i>	Agent associated with the image handle.
in	<i>src_image</i>	Image handle of source image.
in	<i>dst_memory</i>	Destination memory. Must not be NULL.
in	<i>dst_row_pitch</i>	The size in bytes of a single row of the image in the destination memory. If the value is smaller than the source image region width * image element byte size, then region width * image element byte size is used.

Parameters

in	<i>dst_slice_pitch</i>	The size in bytes of a single 2D slice of a 3D image, or the size in bytes of each image in an image array in the destination memory. If the geometry is HSA_EXT_IMAGE_GEOMETRY_1DA and the value is smaller than the value used for <i>dst_row_pitch</i> , then the value used for <i>dst_row_pitch</i> is used. If the geometry is HSA_EXT_IMAGE_GEOMETRY_3D , HSA_EXT_IMAGE_GEOMETRY_2DA , or HSA_EXT_IMAGE_GEOMETRY_2DADEPTH and the value is smaller than the value used for <i>dst_row_pitch</i> * source image region height, then the value used for <i>dst_row_pitch</i> * source image region height is used. Otherwise, the value is not used.
in	<i>image_region</i>	Pointer to the image region to be exported. Must not be NULL.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_AGENT	The agent is invalid.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>dst_memory</i> is NULL, or <i>image_region</i> is NULL.

5.12.5.9 `hsa_ext_image_get_capability()`

```

hsa_status_t HSA_API hsa_ext_image_get_capability (
    hsa_agent_t agent,
    hsa_ext_image_geometry_t geometry,
    const hsa_ext_image_format_t * image_format,
    uint32_t * capability_mask )

```

Retrieve the supported image capabilities for a given combination of agent, geometry, and image format for an image created with an opaque image data layout.

Parameters

in	<i>agent</i>	Agent to be associated with the image handle.
in	<i>geometry</i>	Geometry.
in	<i>image_format</i>	Pointer to an image format. Must not be NULL.
out	<i>capability_mask</i>	Pointer to a memory location where the HSA runtime stores a bit-mask of supported image capability (hsa_ext_image_capability_t) values. Must not be NULL.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_AGENT	The agent is invalid.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>image_format</i> is NULL, or <i>capability_mask</i> is NULL.

5.12.5.10 hsa_ext_image_get_capability_with_layout()

```
hsa_status_t HSA_API hsa_ext_image_get_capability_with_layout (
    hsa_agent_t agent,
    hsa_ext_image_geometry_t geometry,
    const hsa_ext_image_format_t * image_format,
    hsa_ext_image_data_layout_t image_data_layout,
    uint32_t * capability_mask )
```

Retrieve the supported image capabilities for a given combination of agent, geometry, image format, and image layout for an image created with an explicit image data layout.

Parameters

in	<i>agent</i>	Agent to be associated with the image handle.
in	<i>geometry</i>	Geometry.
in	<i>image_format</i>	Pointer to an image format. Must not be NULL.
in	<i>image_data_layout</i>	The image data layout. It is invalid to use HSA_EXT_IMAGE_DATA_LAYOUT_OPAQUE ; use hsa_ext_image_get_capability instead.
out	<i>capability_mask</i>	Pointer to a memory location where the HSA runtime stores a bit-mask of supported image capability (hsa_ext_image_capability_t) values. Must not be NULL.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_AGENT	The agent is invalid.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>image_format</i> is NULL, <i>image_data_layout</i> is HSA_EXT_IMAGE_DATA_LAYOUT_OPAQUE , or <i>capability_mask</i> is NULL.

5.12.5.11 hsa_ext_image_import()

```
hsa_status_t HSA_API hsa_ext_image_import (
    hsa_agent_t agent,
    const void * src_memory,
    size_t src_row_pitch,
    size_t src_slice_pitch,
    hsa_ext_image_t dst_image,
    const hsa_ext_image_region_t * image_region )
```

Import a linearly organized image data from memory directly to an image handle.

This operation updates the image data referenced by the image handle from the source memory. The size of the data imported from memory is implicitly derived from the image region.

It is the application's responsibility to avoid out of bounds memory access.

None of the source memory or destination image data memory can overlap. Overlapping of any of the source and destination image data memory within the import operation produces undefined results.

Parameters

in	<i>agent</i>	Agent associated with the image handle.
in	<i>src_memory</i>	Source memory. Must not be NULL.
in	<i>src_row_pitch</i>	The size in bytes of a single row of the image in the source memory. If the value is smaller than the destination image region width * image element byte size, then region width * image element byte size is used.
in	<i>src_slice_pitch</i>	The size in bytes of a single 2D slice of a 3D image, or the size in bytes of each image layer in an image array in the source memory. If the geometry is HSA_EXT_IMAGE_GEOMETRY_1DA and the value is smaller than the value used for <i>src_row_pitch</i> , then the value used for <i>src_row_pitch</i> is used. If the geometry is HSA_EXT_IMAGE_GEOMETRY_3D , HSA_EXT_IMAGE_GEOMETRY_2DA , or HSA_EXT_IMAGE_GEOMETRY_2DADEPTH and the value is smaller than the value used for <i>src_row_pitch</i> * destination image region height, then the value used for <i>src_row_pitch</i> * destination image region height is used. Otherwise, the value is not used.
in	<i>dst_image</i>	Image handle of destination image.
in	<i>image_region</i>	Pointer to the image region to be updated. Must not be NULL.

Return values

HSA_STATUS_SUCCESS	The function has been executed successfully.
HSA_STATUS_ERROR_NOT_INITIALIZED	The HSA runtime has not been initialized.
HSA_STATUS_ERROR_INVALID_AGENT	The agent is invalid.
HSA_STATUS_ERROR_INVALID_ARGUMENT	<i>src_memory</i> is NULL, or <i>image_region</i> is NULL.

5.12.5.12 hsa_ext_sampler_create()

```

hsa_status_t HSA_API hsa_ext_sampler_create (
    hsa_agent_t agent,
    const hsa_ext_sampler_descriptor_t * sampler_descriptor,
    hsa_ext_sampler_t * sampler )

```

Create an agent specific sampler handle for a given agent independent sampler descriptor and agent.

Parameters

in	<i>agent</i>	Agent to be associated with the sampler handle created.
in	<i>sampler_descriptor</i>	Pointer to a sampler descriptor. Must not be NULL.
out	<i>sampler</i>	Memory location where the HSA runtime stores the newly created sampler handle. Must not be NULL.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_AGENT</i>	The agent is invalid.
<i>HSA_EXT_STATUS_ERROR_SAMPLER_DESCRIPTOR_INVALID</i>	The agent does not have the capability to support the properties specified by <code>sampler_descriptor</code> or it is invalid.
<i>HSA_STATUS_ERROR_OUT_OF_RESOURCES</i>	The HSA runtime failed to allocate the required resources.
<i>HSA_STATUS_ERROR_INVALID_ARGUMENT</i>	<code>sampler_descriptor</code> is NULL, or <code>sampler</code> is NULL.

5.12.5.13 `hsa_ext_sampler_destroy()`

```

hsa_status_t HSA_API hsa_ext_sampler_destroy (
    hsa_agent_t agent,
    hsa_ext_sampler_t sampler )

```

Destroy a sampler handle previously created using [`hsa_ext_sampler_create`](#).

The sampler handle should not be destroyed while there are references to it queued for execution or currently being used in a kernel dispatch.

Parameters

in	<i>agent</i>	Agent associated with the sampler handle.
in	<i>sampler</i>	Sampler handle to destroy.

Return values

<i>HSA_STATUS_SUCCESS</i>	The function has been executed successfully.
<i>HSA_STATUS_ERROR_NOT_INITIALIZED</i>	The HSA runtime has not been initialized.
<i>HSA_STATUS_ERROR_INVALID_AGENT</i>	The agent is invalid.

Chapter 6

Class Documentation

6.1 `amd_control_directives_s` Struct Reference

Public Attributes

- `amd_enabled_control_directive64_t` [enabled_control_directives](#)
- `uint16_t` [enable_break_exceptions](#)
- `uint16_t` [enable_detect_exceptions](#)
- `uint32_t` [max_dynamic_group_size](#)
- `uint64_t` [max_flat_grid_size](#)
- `uint32_t` [max_flat_workgroup_size](#)
- `uint8_t` [required_dim](#)
- `uint8_t` [reserved1](#) [3]
- `uint64_t` [required_grid_size](#) [3]
- `uint32_t` [required_workgroup_size](#) [3]
- `uint8_t` [reserved2](#) [60]

6.1.1 Detailed Description

Definition at line 205 of file [amd_hsa_kernel_code.h](#).

6.1.2 Member Data Documentation

6.1.2.1 `enable_break_exceptions`

```
uint16_t amd_control_directives_s::enable_break_exceptions
```

Definition at line 207 of file [amd_hsa_kernel_code.h](#).

6.1.2.2 enable_detect_exceptions

```
uint16_t amd_control_directives_s::enable_detect_exceptions
```

Definition at line 208 of file [amd_hsa_kernel_code.h](#).

6.1.2.3 enabled_control_directives

```
amd_enabled_control_directive64_t amd_control_directives_s::enabled_control_directives
```

Definition at line 206 of file [amd_hsa_kernel_code.h](#).

6.1.2.4 max_dynamic_group_size

```
uint32_t amd_control_directives_s::max_dynamic_group_size
```

Definition at line 209 of file [amd_hsa_kernel_code.h](#).

6.1.2.5 max_flat_grid_size

```
uint64_t amd_control_directives_s::max_flat_grid_size
```

Definition at line 210 of file [amd_hsa_kernel_code.h](#).

6.1.2.6 max_flat_workgroup_size

```
uint32_t amd_control_directives_s::max_flat_workgroup_size
```

Definition at line 211 of file [amd_hsa_kernel_code.h](#).

6.1.2.7 required_dim

```
uint8_t amd_control_directives_s::required_dim
```

Definition at line 212 of file [amd_hsa_kernel_code.h](#).

6.1.2.8 required_grid_size

```
uint64_t amd_control_directives_s::required_grid_size[3]
```

Definition at line 214 of file [amd_hsa_kernel_code.h](#).

6.1.2.9 required_workgroup_size

```
uint32_t amd_control_directives_s::required_workgroup_size[3]
```

Definition at line 215 of file [amd_hsa_kernel_code.h](#).

6.1.2.10 reserved1

```
uint8_t amd_control_directives_s::reserved1[3]
```

Definition at line 213 of file [amd_hsa_kernel_code.h](#).

6.1.2.11 reserved2

```
uint8_t amd_control_directives_s::reserved2[60]
```

Definition at line 216 of file [amd_hsa_kernel_code.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/amd_hsa_kernel_code.h](#)

6.2 amd_kernel_code_s Struct Reference

Collaboration diagram for amd_kernel_code_s:

Public Attributes

- `amd_kernel_code_version32_t` `amd_kernel_code_version_major`
- `amd_kernel_code_version32_t` `amd_kernel_code_version_minor`
- `amd_machine_kind16_t` `amd_machine_kind`
- `amd_machine_version16_t` `amd_machine_version_major`
- `amd_machine_version16_t` `amd_machine_version_minor`
- `amd_machine_version16_t` `amd_machine_version_stepping`
- `int64_t` `kernel_code_entry_byte_offset`
- `int64_t` `kernel_code_prefetch_byte_offset`
- `uint64_t` `kernel_code_prefetch_byte_size`
- `uint64_t` `max_scratch_backing_memory_byte_size`
- `amd_compute_pgm_rsrc_one32_t` `compute_pgm_rsrc1`
- `amd_compute_pgm_rsrc_two32_t` `compute_pgm_rsrc2`
- `amd_kernel_code_properties32_t` `kernel_code_properties`
- `uint32_t` `workitem_private_segment_byte_size`
- `uint32_t` `workgroup_group_segment_byte_size`
- `uint32_t` `gds_segment_byte_size`
- `uint64_t` `kernarg_segment_byte_size`
- `uint32_t` `workgroup_fbarrier_count`
- `uint16_t` `wavefront_sgpr_count`
- `uint16_t` `workitem_vgpr_count`
- `uint16_t` `reserved_vgpr_first`
- `uint16_t` `reserved_vgpr_count`
- `uint16_t` `reserved_sgpr_first`
- `uint16_t` `reserved_sgpr_count`
- `uint16_t` `debug_wavefront_private_segment_offset_sgpr`
- `uint16_t` `debug_private_segment_buffer_sgpr`
- `amd_powertwo8_t` `kernarg_segment_alignment`
- `amd_powertwo8_t` `group_segment_alignment`
- `amd_powertwo8_t` `private_segment_alignment`
- `amd_powertwo8_t` `wavefront_size`
- `int32_t` `call_convention`
- `uint8_t` `reserved1` [12]
- `uint64_t` `runtime_loader_kernel_symbol`
- `amd_control_directives_t` `control_directives`

6.2.1 Detailed Description

Definition at line 223 of file `amd_hsa_kernel_code.h`.

6.2.2 Member Data Documentation

6.2.2.1 `amd_kernel_code_version_major`

```
amd_kernel_code_version32_t amd_kernel_code_s::amd_kernel_code_version_major
```

Definition at line 224 of file `amd_hsa_kernel_code.h`.

6.2.2.2 amd_kernel_code_version_minor

```
amd_kernel_code_version32_t amd_kernel_code_s::amd_kernel_code_version_minor
```

Definition at line 225 of file [amd_hsa_kernel_code.h](#).

6.2.2.3 amd_machine_kind

```
amd_machine_kind16_t amd_kernel_code_s::amd_machine_kind
```

Definition at line 226 of file [amd_hsa_kernel_code.h](#).

6.2.2.4 amd_machine_version_major

```
amd_machine_version16_t amd_kernel_code_s::amd_machine_version_major
```

Definition at line 227 of file [amd_hsa_kernel_code.h](#).

6.2.2.5 amd_machine_version_minor

```
amd_machine_version16_t amd_kernel_code_s::amd_machine_version_minor
```

Definition at line 228 of file [amd_hsa_kernel_code.h](#).

6.2.2.6 amd_machine_version_stepping

```
amd_machine_version16_t amd_kernel_code_s::amd_machine_version_stepping
```

Definition at line 229 of file [amd_hsa_kernel_code.h](#).

6.2.2.7 call_convention

```
int32_t amd_kernel_code_s::call_convention
```

Definition at line 254 of file [amd_hsa_kernel_code.h](#).

6.2.2.8 compute_pgm_rsrc1

`amd_compute_pgm_rsrc_one32_t amd_kernel_code_s::compute_pgm_rsrc1`

Definition at line 234 of file [amd_hsa_kernel_code.h](#).

6.2.2.9 compute_pgm_rsrc2

`amd_compute_pgm_rsrc_two32_t amd_kernel_code_s::compute_pgm_rsrc2`

Definition at line 235 of file [amd_hsa_kernel_code.h](#).

6.2.2.10 control_directives

`amd_control_directives_t amd_kernel_code_s::control_directives`

Definition at line 257 of file [amd_hsa_kernel_code.h](#).

6.2.2.11 debug_private_segment_buffer_sgpr

`uint16_t amd_kernel_code_s::debug_private_segment_buffer_sgpr`

Definition at line 249 of file [amd_hsa_kernel_code.h](#).

6.2.2.12 debug_wavefront_private_segment_offset_sgpr

`uint16_t amd_kernel_code_s::debug_wavefront_private_segment_offset_sgpr`

Definition at line 248 of file [amd_hsa_kernel_code.h](#).

6.2.2.13 gds_segment_byte_size

`uint32_t amd_kernel_code_s::gds_segment_byte_size`

Definition at line 239 of file [amd_hsa_kernel_code.h](#).

6.2.2.14 group_segment_alignment

```
amd_powertwo8_t amd_kernel_code_s::group_segment_alignment
```

Definition at line 251 of file [amd_hsa_kernel_code.h](#).

6.2.2.15 kernarg_segment_alignment

```
amd_powertwo8_t amd_kernel_code_s::kernarg_segment_alignment
```

Definition at line 250 of file [amd_hsa_kernel_code.h](#).

6.2.2.16 kernarg_segment_byte_size

```
uint64_t amd_kernel_code_s::kernarg_segment_byte_size
```

Definition at line 240 of file [amd_hsa_kernel_code.h](#).

6.2.2.17 kernel_code_entry_byte_offset

```
int64_t amd_kernel_code_s::kernel_code_entry_byte_offset
```

Definition at line 230 of file [amd_hsa_kernel_code.h](#).

6.2.2.18 kernel_code_prefetch_byte_offset

```
int64_t amd_kernel_code_s::kernel_code_prefetch_byte_offset
```

Definition at line 231 of file [amd_hsa_kernel_code.h](#).

6.2.2.19 kernel_code_prefetch_byte_size

```
uint64_t amd_kernel_code_s::kernel_code_prefetch_byte_size
```

Definition at line 232 of file [amd_hsa_kernel_code.h](#).

6.2.2.20 kernel_code_properties

```
amd_kernel_code_properties32_t amd_kernel_code_s::kernel_code_properties
```

Definition at line 236 of file [amd_hsa_kernel_code.h](#).

6.2.2.21 max_scratch_backing_memory_byte_size

```
uint64_t amd_kernel_code_s::max_scratch_backing_memory_byte_size
```

Definition at line 233 of file [amd_hsa_kernel_code.h](#).

6.2.2.22 private_segment_alignment

```
amd_powertwo8_t amd_kernel_code_s::private_segment_alignment
```

Definition at line 252 of file [amd_hsa_kernel_code.h](#).

6.2.2.23 reserved1

```
uint8_t amd_kernel_code_s::reserved1[12]
```

Definition at line 255 of file [amd_hsa_kernel_code.h](#).

6.2.2.24 reserved_sgpr_count

```
uint16_t amd_kernel_code_s::reserved_sgpr_count
```

Definition at line 247 of file [amd_hsa_kernel_code.h](#).

6.2.2.25 reserved_sgpr_first

```
uint16_t amd_kernel_code_s::reserved_sgpr_first
```

Definition at line 246 of file [amd_hsa_kernel_code.h](#).

6.2.2.26 reserved_vgpr_count

```
uint16_t amd_kernel_code_s::reserved_vgpr_count
```

Definition at line 245 of file [amd_hsa_kernel_code.h](#).

6.2.2.27 reserved_vgpr_first

```
uint16_t amd_kernel_code_s::reserved_vgpr_first
```

Definition at line 244 of file [amd_hsa_kernel_code.h](#).

6.2.2.28 runtime_loader_kernel_symbol

```
uint64_t amd_kernel_code_s::runtime_loader_kernel_symbol
```

Definition at line 256 of file [amd_hsa_kernel_code.h](#).

6.2.2.29 wavefront_sgpr_count

```
uint16_t amd_kernel_code_s::wavefront_sgpr_count
```

Definition at line 242 of file [amd_hsa_kernel_code.h](#).

6.2.2.30 wavefront_size

```
amd_powertwo8_t amd_kernel_code_s::wavefront_size
```

Definition at line 253 of file [amd_hsa_kernel_code.h](#).

6.2.2.31 workgroup_fbarrier_count

```
uint32_t amd_kernel_code_s::workgroup_fbarrier_count
```

Definition at line 241 of file [amd_hsa_kernel_code.h](#).

6.2.2.32 workgroup_group_segment_byte_size

uint32_t amd_kernel_code_s::workgroup_group_segment_byte_size

Definition at line 238 of file [amd_hsa_kernel_code.h](#).

6.2.2.33 workitem_private_segment_byte_size

uint32_t amd_kernel_code_s::workitem_private_segment_byte_size

Definition at line 237 of file [amd_hsa_kernel_code.h](#).

6.2.2.34 workitem_vgpr_count

uint16_t amd_kernel_code_s::workitem_vgpr_count

Definition at line 243 of file [amd_hsa_kernel_code.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/amd_hsa_kernel_code.h](#)

6.3 amd_queue_s Struct Reference

Collaboration diagram for amd_queue_s:

Public Attributes

- [hsa_queue_t](#) hsa_queue
- uint32_t reserved1 [4]
- volatile uint64_t write_dispatch_id
- uint32_t group_segment_aperture_base_hi
- uint32_t private_segment_aperture_base_hi
- uint32_t max_cu_id
- uint32_t max_wave_id
- volatile uint64_t max_legacy_doorbell_dispatch_id_plus_1
- volatile uint32_t legacy_doorbell_lock
- uint32_t reserved2 [9]
- volatile uint64_t read_dispatch_id
- uint32_t read_dispatch_id_field_base_byte_offset
- uint32_t compute_tmpring_size
- uint32_t scratch_resource_descriptor [4]
- uint64_t scratch_backing_memory_location
- uint64_t scratch_backing_memory_byte_size
- uint32_t scratch_wave64_lane_byte_size
- amd_queue_properties32_t queue_properties
- uint32_t reserved3 [2]
- [hsa_signal_t](#) queue_inactive_signal
- uint32_t reserved4 [14]

6.3.1 Detailed Description

Definition at line 63 of file [amd_hsa_queue.h](#).

6.3.2 Member Data Documentation

6.3.2.1 compute_tmpring_size

```
uint32_t amd_queue_s::compute_tmpring_size
```

Definition at line 76 of file [amd_hsa_queue.h](#).

6.3.2.2 group_segment_aperture_base_hi

```
uint32_t amd_queue_s::group_segment_aperture_base_hi
```

Definition at line 67 of file [amd_hsa_queue.h](#).

6.3.2.3 hsa_queue

```
hsa_queue_t amd_queue_s::hsa_queue
```

Definition at line 64 of file [amd_hsa_queue.h](#).

6.3.2.4 legacy_doorbell_lock

```
volatile uint32_t amd_queue_s::legacy_doorbell_lock
```

Definition at line 72 of file [amd_hsa_queue.h](#).

6.3.2.5 max_cu_id

```
uint32_t amd_queue_s::max_cu_id
```

Definition at line 69 of file [amd_hsa_queue.h](#).

6.3.2.6 max_legacy_doorbell_dispatch_id_plus_1

```
volatile uint64_t amd_queue_s::max_legacy_doorbell_dispatch_id_plus_1
```

Definition at line 71 of file [amd_hsa_queue.h](#).

6.3.2.7 max_wave_id

```
uint32_t amd_queue_s::max_wave_id
```

Definition at line 70 of file [amd_hsa_queue.h](#).

6.3.2.8 private_segment_aperture_base_hi

```
uint32_t amd_queue_s::private_segment_aperture_base_hi
```

Definition at line 68 of file [amd_hsa_queue.h](#).

6.3.2.9 queue_inactive_signal

```
hsa_signal_t amd_queue_s::queue_inactive_signal
```

Definition at line 83 of file [amd_hsa_queue.h](#).

6.3.2.10 queue_properties

```
amd_queue_properties32_t amd_queue_s::queue_properties
```

Definition at line 81 of file [amd_hsa_queue.h](#).

6.3.2.11 read_dispatch_id

```
volatile uint64_t amd_queue_s::read_dispatch_id
```

Definition at line 74 of file [amd_hsa_queue.h](#).

6.3.2.12 read_dispatch_id_field_base_byte_offset

```
uint32_t amd_queue_s::read_dispatch_id_field_base_byte_offset
```

Definition at line 75 of file [amd_hsa_queue.h](#).

6.3.2.13 reserved1

```
uint32_t amd_queue_s::reserved1[4]
```

Definition at line 65 of file [amd_hsa_queue.h](#).

6.3.2.14 reserved2

```
uint32_t amd_queue_s::reserved2[9]
```

Definition at line 73 of file [amd_hsa_queue.h](#).

6.3.2.15 reserved3

```
uint32_t amd_queue_s::reserved3[2]
```

Definition at line 82 of file [amd_hsa_queue.h](#).

6.3.2.16 reserved4

```
uint32_t amd_queue_s::reserved4[14]
```

Definition at line 84 of file [amd_hsa_queue.h](#).

6.3.2.17 scratch_backing_memory_byte_size

```
uint64_t amd_queue_s::scratch_backing_memory_byte_size
```

Definition at line 79 of file [amd_hsa_queue.h](#).

6.3.2.18 scratch_backing_memory_location

```
uint64_t amd_queue_s::scratch_backing_memory_location
```

Definition at line 78 of file [amd_hsa_queue.h](#).

6.3.2.19 scratch_resource_descriptor

```
uint32_t amd_queue_s::scratch_resource_descriptor[4]
```

Definition at line 77 of file [amd_hsa_queue.h](#).

6.3.2.20 scratch_wave64_lane_byte_size

```
uint32_t amd_queue_s::scratch_wave64_lane_byte_size
```

Definition at line 80 of file [amd_hsa_queue.h](#).

6.3.2.21 write_dispatch_id

```
volatile uint64_t amd_queue_s::write_dispatch_id
```

Definition at line 66 of file [amd_hsa_queue.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/amd_hsa_queue.h](#)

6.4 amd_runtime_loader_debug_info_s Struct Reference

Public Attributes

- const void * [elf_raw](#)
- size_t [elf_size](#)
- const char * [kernel_name](#)
- const void * [owning_segment](#)

6.4.1 Detailed Description

Definition at line 262 of file [amd_hsa_kernel_code.h](#).

6.4.2 Member Data Documentation

6.4.2.1 elf_raw

```
const void* amd_runtime_loader_debug_info_s::elf_raw
```

Definition at line 263 of file [amd_hsa_kernel_code.h](#).

6.4.2.2 elf_size

```
size_t amd_runtime_loader_debug_info_s::elf_size
```

Definition at line 264 of file [amd_hsa_kernel_code.h](#).

6.4.2.3 kernel_name

```
const char* amd_runtime_loader_debug_info_s::kernel_name
```

Definition at line 265 of file [amd_hsa_kernel_code.h](#).

6.4.2.4 owning_segment

```
const void* amd_runtime_loader_debug_info_s::owning_segment
```

Definition at line 266 of file [amd_hsa_kernel_code.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/amd_hsa_kernel_code.h](#)

6.5 amd_signal_s Struct Reference

Public Attributes

- `amd_signal_kind64_t` [kind](#)
- - union {
 - `volatile int64_t` [value](#)
 - `volatile uint32_t` * [legacy_hardware_doorbell_ptr](#)
 - `volatile uint64_t` * [hardware_doorbell_ptr](#)
- `uint64_t` [event_mailbox_ptr](#)
- `uint32_t` [event_id](#)
- `uint32_t` [reserved1](#)
- `uint64_t` [start_ts](#)
- `uint64_t` [end_ts](#)
- - union {
 - `amd_queue_t` * [queue_ptr](#)
 - `uint64_t` [reserved2](#)
- `uint32_t` [reserved3](#) [2]

6.5.1 Detailed Description

Definition at line 61 of file [amd_hsa_signal.h](#).

6.5.2 Member Data Documentation

6.5.2.1 end_ts

```
uint64_t amd_signal_s::end_ts
```

Definition at line 72 of file [amd_hsa_signal.h](#).

6.5.2.2 event_id

```
uint32_t amd_signal_s::event_id
```

Definition at line 69 of file [amd_hsa_signal.h](#).

6.5.2.3 event_mailbox_ptr

```
uint64_t amd_signal_s::event_mailbox_ptr
```

Definition at line 68 of file [amd_hsa_signal.h](#).

6.5.2.4 hardware_doorbell_ptr

```
volatile uint64_t* amd_signal_s::hardware_doorbell_ptr
```

Definition at line 66 of file [amd_hsa_signal.h](#).

6.5.2.5 kind

```
amd_signal_kind64_t amd_signal_s::kind
```

Definition at line 62 of file [amd_hsa_signal.h](#).

6.5.2.6 legacy_hardware_doorbell_ptr

```
volatile uint32_t* amd_signal_s::legacy_hardware_doorbell_ptr
```

Definition at line 65 of file [amd_hsa_signal.h](#).

6.5.2.7 queue_ptr

```
amd_queue_t* amd_signal_s::queue_ptr
```

Definition at line 74 of file [amd_hsa_signal.h](#).

6.5.2.8 reserved1

```
uint32_t amd_signal_s::reserved1
```

Definition at line 70 of file [amd_hsa_signal.h](#).

6.5.2.9 reserved2

```
uint64_t amd_signal_s::reserved2
```

Definition at line 75 of file [amd_hsa_signal.h](#).

6.5.2.10 reserved3

```
uint32_t amd_signal_s::reserved3[2]
```

Definition at line 77 of file [amd_hsa_signal.h](#).

6.5.2.11 start_ts

```
uint64_t amd_signal_s::start_ts
```

Definition at line 71 of file [amd_hsa_signal.h](#).

6.5.2.12 value

```
volatile int64_t amd_signal_s::value
```

Definition at line 64 of file [amd_hsa_signal.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/amd_hsa_signal.h](#)

6.6 AmdExtTable Struct Reference

Collaboration diagram for AmdExtTable:

6.7 amdgpu_hsa_image_descriptor_s Struct Reference

Public Attributes

- uint16_t [size](#)
- amdgpu_hsa_metadata_kind16_t [kind](#)
- amdgpu_hsa_image_geometry8_t [geometry](#)
- amdgpu_hsa_image_channel_order8_t [channel_order](#)
- amdgpu_hsa_image_channel_type8_t [channel_type](#)
- uint8_t [reserved1](#)
- uint64_t [width](#)
- uint64_t [height](#)
- uint64_t [depth](#)
- uint64_t [array](#)

6.7.1 Detailed Description

Definition at line 367 of file [amd_hsa_elf.h](#).

6.7.2 Member Data Documentation

6.7.2.1 array

```
uint64_t amdgpu_hsa_image_descriptor_s::array
```

Definition at line 377 of file [amd_hsa_elf.h](#).

6.7.2.2 channel_order

```
amdgpu_hsa_image_channel_order8_t amdgpu_hsa_image_descriptor_s::channel_order
```

Definition at line 371 of file [amd_hsa_elf.h](#).

6.7.2.3 channel_type

```
amdgpu_hsa_image_channel_type8_t amdgpu_hsa_image_descriptor_s::channel_type
```

Definition at line 372 of file [amd_hsa_elf.h](#).

6.7.2.4 depth

```
uint64_t amdgpu_hsa_image_descriptor_s::depth
```

Definition at line 376 of file [amd_hsa_elf.h](#).

6.7.2.5 geometry

```
amdgpu_hsa_image_geometry8_t amdgpu_hsa_image_descriptor_s::geometry
```

Definition at line 370 of file [amd_hsa_elf.h](#).

6.7.2.6 height

```
uint64_t amdgpu_hsa_image_descriptor_s::height
```

Definition at line 375 of file [amd_hsa_elf.h](#).

6.7.2.7 kind

```
amdgpu_hsa_metadata_kind16_t amdgpu_hsa_image_descriptor_s::kind
```

Definition at line 369 of file [amd_hsa_elf.h](#).

6.7.2.8 reserved1

```
uint8_t amdgpu_hsa_image_descriptor_s::reserved1
```

Definition at line 373 of file [amd_hsa_elf.h](#).

6.7.2.9 size

```
uint16_t amdgpu_hsa_image_descriptor_s::size
```

Definition at line 368 of file [amd_hsa_elf.h](#).

6.7.2.10 width

```
uint64_t amdgpu_hsa_image_descriptor_s::width
```

Definition at line 374 of file [amd_hsa_elf.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/amd_hsa_elf.h](#)

6.8 amdgpu_hsa_note_code_object_version_s Struct Reference

Public Attributes

- uint32_t [major_version](#)
- uint32_t [minor_version](#)

6.8.1 Detailed Description

Definition at line 380 of file [amd_hsa_elf.h](#).

6.8.2 Member Data Documentation

6.8.2.1 major_version

```
uint32_t amdgpu_hsa_note_code_object_version_s::major_version
```

Definition at line 381 of file [amd_hsa_elf.h](#).

6.8.2.2 minor_version

```
uint32_t amdgpu_hsa_note_code_object_version_s::minor_version
```

Definition at line 382 of file [amd_hsa_elf.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/amd_hsa_elf.h](#)

6.9 amdgpu_hsa_note_hsail_s Struct Reference

Public Attributes

- uint32_t [hsail_major_version](#)
- uint32_t [hsail_minor_version](#)
- uint8_t [profile](#)
- uint8_t [machine_model](#)
- uint8_t [default_float_round](#)

6.9.1 Detailed Description

Definition at line 385 of file [amd_hsa_elf.h](#).

6.9.2 Member Data Documentation

6.9.2.1 default_float_round

```
uint8_t amdgpu_hsa_note_hsail_s::default_float_round
```

Definition at line 390 of file [amd_hsa_elf.h](#).

6.9.2.2 hsail_major_version

```
uint32_t amdgpu_hsa_note_hsail_s::hsail_major_version
```

Definition at line 386 of file [amd_hsa_elf.h](#).

6.9.2.3 hsail_minor_version

```
uint32_t amdgpu_hsa_note_hsail_s::hsail_minor_version
```

Definition at line 387 of file [amd_hsa_elf.h](#).

6.9.2.4 machine_model

```
uint8_t amdgpu_hsa_note_hsail_s::machine_model
```

Definition at line 389 of file [amd_hsa_elf.h](#).

6.9.2.5 profile

```
uint8_t amdgpu_hsa_note_hsail_s::profile
```

Definition at line 388 of file [amd_hsa_elf.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/amd_hsa_elf.h](#)

6.10 amdgpu_hsa_note_isa_s Struct Reference

Public Attributes

- uint16_t [vendor_name_size](#)
- uint16_t [architecture_name_size](#)
- uint32_t [major](#)
- uint32_t [minor](#)
- uint32_t [stepping](#)
- char [vendor_and_architecture_name](#) [1]

6.10.1 Detailed Description

Definition at line 393 of file [amd_hsa_elf.h](#).

6.10.2 Member Data Documentation

6.10.2.1 architecture_name_size

```
uint16_t amdgpu_hsa_note_isa_s::architecture_name_size
```

Definition at line 395 of file [amd_hsa_elf.h](#).

6.10.2.2 major

```
uint32_t amdgpu_hsa_note_isa_s::major
```

Definition at line 396 of file [amd_hsa_elf.h](#).

6.10.2.3 minor

```
uint32_t amdgpu_hsa_note_isa_s::minor
```

Definition at line 397 of file [amd_hsa_elf.h](#).

6.10.2.4 stepping

```
uint32_t amdgpu_hsa_note_isa_s::stepping
```

Definition at line 398 of file [amd_hsa_elf.h](#).

6.10.2.5 vendor_and_architecture_name

```
char amdgpu_hsa_note_isa_s::vendor_and_architecture_name[1]
```

Definition at line 399 of file [amd_hsa_elf.h](#).

6.10.2.6 vendor_name_size

```
uint16_t amdgpu_hsa_note_isa_s::vendor_name_size
```

Definition at line 394 of file [amd_hsa_elf.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/amd_hsa_elf.h](#)

6.11 amdgpu_hsa_note_producer_options_s Struct Reference

Public Attributes

- uint16_t [producer_options_size](#)
- char [producer_options](#) [1]

6.11.1 Detailed Description

Definition at line 410 of file [amd_hsa_elf.h](#).

6.11.2 Member Data Documentation

6.11.2.1 producer_options

```
char amdgpu_hsa_note_producer_options_s::producer_options[1]
```

Definition at line 412 of file [amd_hsa_elf.h](#).

6.11.2.2 producer_options_size

```
uint16_t amdgpu_hsa_note_producer_options_s::producer_options_size
```

Definition at line 411 of file [amd_hsa_elf.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/amd_hsa_elf.h](#)

6.12 amdgpu_hsa_note_producer_s Struct Reference

Public Attributes

- uint16_t [producer_name_size](#)
- uint16_t [reserved](#)
- uint32_t [producer_major_version](#)
- uint32_t [producer_minor_version](#)
- char [producer_name](#) [1]

6.12.1 Detailed Description

Definition at line [402](#) of file [amd_hsa_elf.h](#).

6.12.2 Member Data Documentation

6.12.2.1 producer_major_version

```
uint32_t amdgpu_hsa_note_producer_s::producer_major_version
```

Definition at line [405](#) of file [amd_hsa_elf.h](#).

6.12.2.2 producer_minor_version

```
uint32_t amdgpu_hsa_note_producer_s::producer_minor_version
```

Definition at line [406](#) of file [amd_hsa_elf.h](#).

6.12.2.3 producer_name

```
char amdgpu_hsa_note_producer_s::producer_name[1]
```

Definition at line [407](#) of file [amd_hsa_elf.h](#).

6.12.2.4 producer_name_size

```
uint16_t amdgpu_hsa_note_producer_s::producer_name_size
```

Definition at line [403](#) of file [amd_hsa_elf.h](#).

6.12.2.5 reserved

```
uint16_t amdgpu_hsa_note_producer_s::reserved
```

Definition at line 404 of file [amd_hsa_elf.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/amd_hsa_elf.h](#)

6.13 amdgpu_hsa_sampler_descriptor_s Struct Reference

Public Attributes

- [uint16_t size](#)
- [amdgpu_hsa_metadata_kind16_t kind](#)
- [amdgpu_hsa_sampler_coord8_t coord](#)
- [amdgpu_hsa_sampler_filter8_t filter](#)
- [amdgpu_hsa_sampler_addressing8_t addressing](#)
- [uint8_t reserved1](#)

6.13.1 Detailed Description

Definition at line 298 of file [amd_hsa_elf.h](#).

6.13.2 Member Data Documentation

6.13.2.1 addressing

```
amdgpu_hsa_sampler_addressing8_t amdgpu_hsa_sampler_descriptor_s::addressing
```

Definition at line 303 of file [amd_hsa_elf.h](#).

6.13.2.2 coord

```
amdgpu_hsa_sampler_coord8_t amdgpu_hsa_sampler_descriptor_s::coord
```

Definition at line 301 of file [amd_hsa_elf.h](#).

6.13.2.3 filter

```
amdgpu_hsa_sampler_filter8_t amdgpu_hsa_sampler_descriptor_s::filter
```

Definition at line 302 of file [amd_hsa_elf.h](#).

6.13.2.4 kind

```
amdgpu_hsa_metadata_kind16_t amdgpu_hsa_sampler_descriptor_s::kind
```

Definition at line 300 of file [amd_hsa_elf.h](#).

6.13.2.5 reserved1

```
uint8_t amdgpu_hsa_sampler_descriptor_s::reserved1
```

Definition at line 304 of file [amd_hsa_elf.h](#).

6.13.2.6 size

```
uint16_t amdgpu_hsa_sampler_descriptor_s::size
```

Definition at line 299 of file [amd_hsa_elf.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/amd_hsa_elf.h](#)

6.14 ApiTableVersion Struct Reference

Public Attributes

- uint32_t [major_id](#)
- uint32_t [minor_id](#)
- uint32_t [step_id](#)
- uint32_t [reserved](#)

6.14.1 Detailed Description

Definition at line 103 of file [hsa_api_trace.h](#).

6.14.2 Member Data Documentation

6.14.2.1 major_id

```
uint32_t ApiTableVersion::major_id
```

Definition at line 104 of file [hsa_api_trace.h](#).

6.14.2.2 minor_id

```
uint32_t ApiTableVersion::minor_id
```

Definition at line 105 of file [hsa_api_trace.h](#).

6.14.2.3 reserved

```
uint32_t ApiTableVersion::reserved
```

Definition at line 107 of file [hsa_api_trace.h](#).

6.14.2.4 step_id

```
uint32_t ApiTableVersion::step_id
```

Definition at line 106 of file [hsa_api_trace.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa_api_trace.h](#)

6.15 BrigBase Struct Reference

Public Attributes

- `uint16_t` [byteCount](#)
- `BrigKind16_t` [kind](#)

6.15.1 Detailed Description

Definition at line 782 of file [Brig.h](#).

6.15.2 Member Data Documentation

6.15.2.1 byteCount

```
uint16_t BrigBase::byteCount
```

Definition at line 783 of file [Brig.h](#).

6.15.2.2 kind

```
BrigKind16_t BrigBase::kind
```

Definition at line 784 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.16 BrigData Struct Reference

Public Attributes

- `uint32_t` [byteCount](#)
- `uint8_t` [bytes](#)[1]

6.16.1 Detailed Description

Definition at line 787 of file [Brig.h](#).

6.16.2 Member Data Documentation

6.16.2.1 `byteCount`

```
uint32_t BrigData::byteCount
```

Definition at line 788 of file [Brig.h](#).

6.16.2.2 `bytes`

```
uint8_t BrigData::bytes[1]
```

Definition at line 789 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.17 `BrigDirectiveArgBlock` Struct Reference

Collaboration diagram for `BrigDirectiveArgBlock`:

Public Attributes

- [BrigBase](#) `base`

6.17.1 Detailed Description

Definition at line 792 of file [Brig.h](#).

6.17.2 Member Data Documentation

6.17.2.1 `base`

```
BrigBase BrigDirectiveArgBlock::base
```

Definition at line 793 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.18 BrigDirectiveComment Struct Reference

Collaboration diagram for BrigDirectiveComment:

Public Attributes

- [BrigBase](#) `base`
- `BrigDataOffsetString32_t` `name`

6.18.1 Detailed Description

Definition at line 796 of file [Brig.h](#).

6.18.2 Member Data Documentation

6.18.2.1 `base`

[BrigBase](#) `BrigDirectiveComment::base`

Definition at line 797 of file [Brig.h](#).

6.18.2.2 `name`

`BrigDataOffsetString32_t` `BrigDirectiveComment::name`

Definition at line 798 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/Brig.h`

6.19 BrigDirectiveControl Struct Reference

Collaboration diagram for BrigDirectiveControl:

Public Attributes

- [BrigBase](#) `base`
- `BrigControlDirective16_t` `control`
- `uint16_t` `reserved`
- `BrigDataOffsetOperandList32_t` `operands`

6.19.1 Detailed Description

Definition at line 801 of file [Brig.h](#).

6.19.2 Member Data Documentation

6.19.2.1 base

[BrigBase](#) BrigDirectiveControl::base

Definition at line 802 of file [Brig.h](#).

6.19.2.2 control

BrigControlDirective16_t BrigDirectiveControl::control

Definition at line 803 of file [Brig.h](#).

6.19.2.3 operands

BrigDataOffsetOperandList32_t BrigDirectiveControl::operands

Definition at line 805 of file [Brig.h](#).

6.19.2.4 reserved

uint16_t BrigDirectiveControl::reserved

Definition at line 804 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.20 BrigDirectiveExecutable Struct Reference

Collaboration diagram for BrigDirectiveExecutable:

Public Attributes

- [BrigBase](#) `base`
- `BrigDataOffsetString32_t` `name`
- `uint16_t` `outArgCount`
- `uint16_t` `inArgCount`
- `BrigCodeOffset32_t` `firstInArg`
- `BrigCodeOffset32_t` `firstCodeBlockEntry`
- `BrigCodeOffset32_t` `nextModuleEntry`
- `BrigExecutableModifier8_t` `modifier`
- `BrigLinkage8_t` `linkage`
- `uint16_t` `reserved`

6.20.1 Detailed Description

Definition at line 808 of file [Brig.h](#).

6.20.2 Member Data Documentation

6.20.2.1 `base`

[BrigBase](#) `BrigDirectiveExecutable::base`

Definition at line 809 of file [Brig.h](#).

6.20.2.2 `firstCodeBlockEntry`

`BrigCodeOffset32_t` `BrigDirectiveExecutable::firstCodeBlockEntry`

Definition at line 814 of file [Brig.h](#).

6.20.2.3 `firstInArg`

`BrigCodeOffset32_t` `BrigDirectiveExecutable::firstInArg`

Definition at line 813 of file [Brig.h](#).

6.20.2.4 inArgCount

```
uint16_t BrigDirectiveExecutable::inArgCount
```

Definition at line 812 of file [Brig.h](#).

6.20.2.5 linkage

```
BrigLinkage8_t BrigDirectiveExecutable::linkage
```

Definition at line 817 of file [Brig.h](#).

6.20.2.6 modifier

```
BrigExecutableModifier8_t BrigDirectiveExecutable::modifier
```

Definition at line 816 of file [Brig.h](#).

6.20.2.7 name

```
BrigDataOffsetString32_t BrigDirectiveExecutable::name
```

Definition at line 810 of file [Brig.h](#).

6.20.2.8 nextModuleEntry

```
BrigCodeOffset32_t BrigDirectiveExecutable::nextModuleEntry
```

Definition at line 815 of file [Brig.h](#).

6.20.2.9 outArgCount

```
uint16_t BrigDirectiveExecutable::outArgCount
```

Definition at line 811 of file [Brig.h](#).

6.20.2.10 reserved

```
uint16_t BrigDirectiveExecutable::reserved
```

Definition at line 818 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.21 BrigDirectiveExtension Struct Reference

Collaboration diagram for BrigDirectiveExtension:

Public Attributes

- [BrigBase](#) base
- BrigDataOffsetString32_t name

6.21.1 Detailed Description

Definition at line 821 of file [Brig.h](#).

6.21.2 Member Data Documentation

6.21.2.1 base

```
BrigBase BrigDirectiveExtension::base
```

Definition at line 822 of file [Brig.h](#).

6.21.2.2 name

```
BrigDataOffsetString32_t BrigDirectiveExtension::name
```

Definition at line 823 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.22 BrigDirectiveFbarrier Struct Reference

Collaboration diagram for BrigDirectiveFbarrier:

Public Attributes

- [BrigBase](#) `base`
- `BrigDataOffsetString32_t` `name`
- `BrigVariableModifier8_t` `modifier`
- `BrigLinkage8_t` `linkage`
- `uint16_t` `reserved`

6.22.1 Detailed Description

Definition at line 826 of file [Brig.h](#).

6.22.2 Member Data Documentation

6.22.2.1 `base`

[BrigBase](#) `BrigDirectiveFbarrier::base`

Definition at line 827 of file [Brig.h](#).

6.22.2.2 `linkage`

`BrigLinkage8_t` `BrigDirectiveFbarrier::linkage`

Definition at line 830 of file [Brig.h](#).

6.22.2.3 `modifier`

`BrigVariableModifier8_t` `BrigDirectiveFbarrier::modifier`

Definition at line 829 of file [Brig.h](#).

6.22.2.4 name

```
BrigDataOffsetString32_t BrigDirectiveFbarrier::name
```

Definition at line 828 of file [Brig.h](#).

6.22.2.5 reserved

```
uint16_t BrigDirectiveFbarrier::reserved
```

Definition at line 831 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.23 BrigDirectiveLabel Struct Reference

Collaboration diagram for BrigDirectiveLabel:

Public Attributes

- [BrigBase](#) base
- BrigDataOffsetString32_t [name](#)

6.23.1 Detailed Description

Definition at line 834 of file [Brig.h](#).

6.23.2 Member Data Documentation

6.23.2.1 base

```
BrigBase BrigDirectiveLabel::base
```

Definition at line 835 of file [Brig.h](#).

6.23.2.2 name

`BrigDataOffsetString32_t BrigDirectiveLabel::name`

Definition at line 836 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/Brig.h`

6.24 BrigDirectiveLoc Struct Reference

Collaboration diagram for BrigDirectiveLoc:

Public Attributes

- [BrigBase](#) `base`
- `BrigDataOffsetString32_t` `filename`
- `uint32_t` `line`
- `uint32_t` `column`

6.24.1 Detailed Description

Definition at line 839 of file [Brig.h](#).

6.24.2 Member Data Documentation

6.24.2.1 base

`BrigBase` `BrigDirectiveLoc::base`

Definition at line 840 of file [Brig.h](#).

6.24.2.2 column

`uint32_t` `BrigDirectiveLoc::column`

Definition at line 843 of file [Brig.h](#).

6.24.2.3 filename

BrigDataOffsetString32_t BrigDirectiveLoc::filename

Definition at line 841 of file [Brig.h](#).

6.24.2.4 line

uint32_t BrigDirectiveLoc::line

Definition at line 842 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.25 BrigDirectiveModule Struct Reference

Collaboration diagram for BrigDirectiveModule:

Public Attributes

- [BrigBase](#) base
- BrigDataOffsetString32_t name
- BrigVersion32_t hsailMajor
- BrigVersion32_t hsailMinor
- BrigProfile8_t profile
- BrigMachineModel8_t machineModel
- BrigRound8_t defaultFloatRound
- uint8_t reserved

6.25.1 Detailed Description

Definition at line 869 of file [Brig.h](#).

6.25.2 Member Data Documentation

6.25.2.1 base

[BrigBase](#) BrigDirectiveModule::base

Definition at line 870 of file [Brig.h](#).

6.25.2.2 defaultFloatRound

BrigRound8_t BrigDirectiveModule::defaultFloatRound

Definition at line 876 of file [Brig.h](#).

6.25.2.3 hsailMajor

BrigVersion32_t BrigDirectiveModule::hsailMajor

Definition at line 872 of file [Brig.h](#).

6.25.2.4 hsailMinor

BrigVersion32_t BrigDirectiveModule::hsailMinor

Definition at line 873 of file [Brig.h](#).

6.25.2.5 machineModel

BrigMachineModel8_t BrigDirectiveModule::machineModel

Definition at line 875 of file [Brig.h](#).

6.25.2.6 name

BrigDataOffsetString32_t BrigDirectiveModule::name

Definition at line 871 of file [Brig.h](#).

6.25.2.7 profile

BrigProfile8_t BrigDirectiveModule::profile

Definition at line 874 of file [Brig.h](#).

6.25.2.8 reserved

```
uint8_t BrigDirectiveModule::reserved
```

Definition at line 877 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.26 BrigDirectiveNone Struct Reference

Collaboration diagram for BrigDirectiveNone:

Public Attributes

- [BrigBase base](#)

6.26.1 Detailed Description

Definition at line 846 of file [Brig.h](#).

6.26.2 Member Data Documentation

6.26.2.1 base

```
BrigBase BrigDirectiveNone::base
```

Definition at line 847 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.27 BrigDirectivePragma Struct Reference

Collaboration diagram for BrigDirectivePragma:

Public Attributes

- [BrigBase base](#)
- [BrigDataOffsetOperandList32_t operands](#)

6.27.1 Detailed Description

Definition at line 850 of file [Brig.h](#).

6.27.2 Member Data Documentation

6.27.2.1 base

[BrigBase](#) BrigDirectivePragma::base

Definition at line 851 of file [Brig.h](#).

6.27.2.2 operands

BrigDataOffsetOperandList32_t BrigDirectivePragma::operands

Definition at line 852 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.28 BrigDirectiveVariable Struct Reference

Collaboration diagram for BrigDirectiveVariable:

Public Attributes

- [BrigBase](#) base
- BrigDataOffsetString32_t name
- BrigOperandOffset32_t init
- BrigType16_t type
- BrigSegment8_t segment
- BrigAlignment8_t align
- [BrigUInt64](#) dim
- BrigVariableModifier8_t modifier
- BrigLinkage8_t linkage
- BrigAllocation8_t allocation
- uint8_t reserved

6.28.1 Detailed Description

Definition at line 855 of file [Brig.h](#).

6.28.2 Member Data Documentation

6.28.2.1 align

```
BrigAlignment8_t BrigDirectiveVariable::align
```

Definition at line 861 of file [Brig.h](#).

6.28.2.2 allocation

```
BrigAllocation8_t BrigDirectiveVariable::allocation
```

Definition at line 865 of file [Brig.h](#).

6.28.2.3 base

```
BrigBase BrigDirectiveVariable::base
```

Definition at line 856 of file [Brig.h](#).

6.28.2.4 dim

```
BrigUInt64 BrigDirectiveVariable::dim
```

Definition at line 862 of file [Brig.h](#).

6.28.2.5 init

```
BrigOperandOffset32_t BrigDirectiveVariable::init
```

Definition at line 858 of file [Brig.h](#).

6.28.2.6 linkage

`BrigLinkage8_t BrigDirectiveVariable::linkage`

Definition at line 864 of file [Brig.h](#).

6.28.2.7 modifier

`BrigVariableModifier8_t BrigDirectiveVariable::modifier`

Definition at line 863 of file [Brig.h](#).

6.28.2.8 name

`BrigDataOffsetString32_t BrigDirectiveVariable::name`

Definition at line 857 of file [Brig.h](#).

6.28.2.9 reserved

`uint8_t BrigDirectiveVariable::reserved`

Definition at line 866 of file [Brig.h](#).

6.28.2.10 segment

`BrigSegment8_t BrigDirectiveVariable::segment`

Definition at line 860 of file [Brig.h](#).

6.28.2.11 type

`BrigType16_t BrigDirectiveVariable::type`

Definition at line 859 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/Brig.h`

6.29 BrigInstAddr Struct Reference

Collaboration diagram for BrigInstAddr:

Public Attributes

- [BrigInstBase](#) `base`
- [BrigSegment8_t](#) `segment`
- [uint8_t](#) `reserved` [3]

6.29.1 Detailed Description

Definition at line 887 of file [Brig.h](#).

6.29.2 Member Data Documentation

6.29.2.1 `base`

[BrigInstBase](#) BrigInstAddr::base

Definition at line 888 of file [Brig.h](#).

6.29.2.2 `reserved`

[uint8_t](#) BrigInstAddr::reserved[3]

Definition at line 890 of file [Brig.h](#).

6.29.2.3 `segment`

[BrigSegment8_t](#) BrigInstAddr::segment

Definition at line 889 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/Brig.h`

6.30 BrigInstAtomic Struct Reference

Collaboration diagram for BrigInstAtomic:

Public Attributes

- [BrigInstBase](#) `base`
- `BrigSegment8_t` [segment](#)
- `BrigMemoryOrder8_t` [memoryOrder](#)
- `BrigMemoryScope8_t` [memoryScope](#)
- `BrigAtomicOperation8_t` [atomicOperation](#)
- `uint8_t` [equivClass](#)
- `uint8_t` [reserved](#) [3]

6.30.1 Detailed Description

Definition at line [893](#) of file [Brig.h](#).

6.30.2 Member Data Documentation

6.30.2.1 `atomicOperation`

```
BrigAtomicOperation8_t BrigInstAtomic::atomicOperation
```

Definition at line [898](#) of file [Brig.h](#).

6.30.2.2 `base`

```
BrigInstBase BrigInstAtomic::base
```

Definition at line [894](#) of file [Brig.h](#).

6.30.2.3 `equivClass`

```
uint8_t BrigInstAtomic::equivClass
```

Definition at line [899](#) of file [Brig.h](#).

6.30.2.4 memoryOrder

```
BrigMemoryOrder8_t BrigInstAtomic::memoryOrder
```

Definition at line 896 of file [Brig.h](#).

6.30.2.5 memoryScope

```
BrigMemoryScope8_t BrigInstAtomic::memoryScope
```

Definition at line 897 of file [Brig.h](#).

6.30.2.6 reserved

```
uint8_t BrigInstAtomic::reserved[3]
```

Definition at line 900 of file [Brig.h](#).

6.30.2.7 segment

```
BrigSegment8_t BrigInstAtomic::segment
```

Definition at line 895 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.31 BrigInstBase Struct Reference

Collaboration diagram for BrigInstBase:

Public Attributes

- [BrigBase](#) base
- BrigOpcode16_t opcode
- BrigType16_t type
- BrigDataOffsetOperandList32_t operands

6.31.1 Detailed Description

Definition at line 880 of file [Brig.h](#).

6.31.2 Member Data Documentation

6.31.2.1 base

```
BrigBase BrigInstBase::base
```

Definition at line 881 of file [Brig.h](#).

6.31.2.2 opcode

```
BrigOpcode16_t BrigInstBase::opcode
```

Definition at line 882 of file [Brig.h](#).

6.31.2.3 operands

```
BrigDataOffsetOperandList32_t BrigInstBase::operands
```

Definition at line 884 of file [Brig.h](#).

6.31.2.4 type

```
BrigType16_t BrigInstBase::type
```

Definition at line 883 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/Brig.h`

6.32 BrigInstBasic Struct Reference

Collaboration diagram for BrigInstBasic:

Public Attributes

- [BrigInstBase](#) base

6.32.1 Detailed Description

Definition at line 903 of file [Brig.h](#).

6.32.2 Member Data Documentation

6.32.2.1 base

[BrigInstBase](#) BrigInstBasic::base

Definition at line 904 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- /home/alexv/Programming/ROCR-Runtime/include/Brig.h

6.33 BrigInstBr Struct Reference

Collaboration diagram for BrigInstBr:

Public Attributes

- [BrigInstBase](#) base
- BrigWidth8_t [width](#)
- uint8_t [reserved](#) [3]

6.33.1 Detailed Description

Definition at line 907 of file [Brig.h](#).

6.33.2 Member Data Documentation

6.33.2.1 base

`BrigInstBase` `BrigInstBr::base`

Definition at line 908 of file [Brig.h](#).

6.33.2.2 reserved

`uint8_t` `BrigInstBr::reserved[3]`

Definition at line 910 of file [Brig.h](#).

6.33.2.3 width

`BrigWidth8_t` `BrigInstBr::width`

Definition at line 909 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/Brig.h`

6.34 BrigInstCmp Struct Reference

Collaboration diagram for `BrigInstCmp`:

Public Attributes

- [BrigInstBase](#) `base`
- `BrigType16_t` `sourceType`
- `BrigAluModifier8_t` `modifier`
- `BrigCompareOperation8_t` `compare`
- `BrigPack8_t` `pack`
- `uint8_t` `reserved` [3]

6.34.1 Detailed Description

Definition at line 913 of file [Brig.h](#).

6.34.2 Member Data Documentation

6.34.2.1 base

`BrigInstBase` BrigInstCmp::base

Definition at line 914 of file [Brig.h](#).

6.34.2.2 compare

`BrigCompareOperation8_t` BrigInstCmp::compare

Definition at line 917 of file [Brig.h](#).

6.34.2.3 modifier

`BrigAluModifier8_t` BrigInstCmp::modifier

Definition at line 916 of file [Brig.h](#).

6.34.2.4 pack

`BrigPack8_t` BrigInstCmp::pack

Definition at line 918 of file [Brig.h](#).

6.34.2.5 reserved

`uint8_t` BrigInstCmp::reserved[3]

Definition at line 919 of file [Brig.h](#).

6.34.2.6 sourceType

`BrigType16_t` BrigInstCmp::sourceType

Definition at line 915 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/Brig.h`

6.35 BrigInstCvt Struct Reference

Collaboration diagram for BrigInstCvt:

Public Attributes

- [BrigInstBase](#) `base`
- `BrigType16_t` `sourceType`
- `BrigAluModifier8_t` `modifier`
- `BrigRound8_t` `round`

6.35.1 Detailed Description

Definition at line 922 of file [Brig.h](#).

6.35.2 Member Data Documentation

6.35.2.1 `base`

`BrigInstBase` `BrigInstCvt::base`

Definition at line 923 of file [Brig.h](#).

6.35.2.2 `modifier`

`BrigAluModifier8_t` `BrigInstCvt::modifier`

Definition at line 925 of file [Brig.h](#).

6.35.2.3 `round`

`BrigRound8_t` `BrigInstCvt::round`

Definition at line 926 of file [Brig.h](#).

6.35.2.4 sourceType

`BrigType16_t BrigInstCvt::sourceType`

Definition at line 924 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/Brig.h`

6.36 BrigInstImage Struct Reference

Collaboration diagram for BrigInstImage:

Public Attributes

- [BrigInstBase](#) `base`
- `BrigType16_t` `imageType`
- `BrigType16_t` `coordType`
- `BrigImageGeometry8_t` `geometry`
- `uint8_t` `equivClass`
- `uint16_t` `reserved`

6.36.1 Detailed Description

Definition at line 929 of file [Brig.h](#).

6.36.2 Member Data Documentation

6.36.2.1 base

`BrigInstBase` `BrigInstImage::base`

Definition at line 930 of file [Brig.h](#).

6.36.2.2 coordType

`BrigType16_t` `BrigInstImage::coordType`

Definition at line 932 of file [Brig.h](#).

6.36.2.3 equivClass

```
uint8_t BrigInstImage::equivClass
```

Definition at line 934 of file [Brig.h](#).

6.36.2.4 geometry

```
BrigImageGeometry8_t BrigInstImage::geometry
```

Definition at line 933 of file [Brig.h](#).

6.36.2.5 imageType

```
BrigType16_t BrigInstImage::imageType
```

Definition at line 931 of file [Brig.h](#).

6.36.2.6 reserved

```
uint16_t BrigInstImage::reserved
```

Definition at line 935 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.37 BrigInstLane Struct Reference

Collaboration diagram for BrigInstLane:

Public Attributes

- [BrigInstBase](#) base
- BrigType16_t [sourceType](#)
- BrigWidth8_t [width](#)
- uint8_t [reserved](#)

6.37.1 Detailed Description

Definition at line 938 of file [Brig.h](#).

6.37.2 Member Data Documentation

6.37.2.1 base

```
BrigInstBase BrigInstLane::base
```

Definition at line 939 of file [Brig.h](#).

6.37.2.2 reserved

```
uint8_t BrigInstLane::reserved
```

Definition at line 942 of file [Brig.h](#).

6.37.2.3 sourceType

```
BrigType16_t BrigInstLane::sourceType
```

Definition at line 940 of file [Brig.h](#).

6.37.2.4 width

```
BrigWidth8_t BrigInstLane::width
```

Definition at line 941 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.38 BrigInstMem Struct Reference

Collaboration diagram for BrigInstMem:

Public Attributes

- [BrigInstBase](#) `base`
- `BrigSegment8_t` `segment`
- `BrigAlignment8_t` `align`
- `uint8_t` `equivClass`
- `BrigWidth8_t` `width`
- `BrigMemoryModifier8_t` `modifier`
- `uint8_t` `reserved` [3]

6.38.1 Detailed Description

Definition at line 945 of file [Brig.h](#).

6.38.2 Member Data Documentation

6.38.2.1 align

```
BrigAlignment8_t BrigInstMem::align
```

Definition at line 948 of file [Brig.h](#).

6.38.2.2 base

```
BrigInstBase BrigInstMem::base
```

Definition at line 946 of file [Brig.h](#).

6.38.2.3 equivClass

```
uint8_t BrigInstMem::equivClass
```

Definition at line 949 of file [Brig.h](#).

6.38.2.4 modifier

```
BrigMemoryModifier8_t BrigInstMem::modifier
```

Definition at line 951 of file [Brig.h](#).

6.38.2.5 reserved

```
uint8_t BrigInstMem::reserved[3]
```

Definition at line 952 of file [Brig.h](#).

6.38.2.6 segment

```
BrigSegment8_t BrigInstMem::segment
```

Definition at line 947 of file [Brig.h](#).

6.38.2.7 width

```
BrigWidth8_t BrigInstMem::width
```

Definition at line 950 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.39 BrigInstMemFence Struct Reference

Collaboration diagram for BrigInstMemFence:

Public Attributes

- [BrigInstBase](#) base
- BrigMemoryOrder8_t [memoryOrder](#)
- BrigMemoryScope8_t [globalSegmentMemoryScope](#)
- BrigMemoryScope8_t [groupSegmentMemoryScope](#)
- BrigMemoryScope8_t [imageSegmentMemoryScope](#)

6.39.1 Detailed Description

Definition at line 955 of file [Brig.h](#).

6.39.2 Member Data Documentation

6.39.2.1 base

`BrigInstBase` `BrigInstMemFence::base`

Definition at line 956 of file [Brig.h](#).

6.39.2.2 globalSegmentMemoryScope

`BrigMemoryScope8_t` `BrigInstMemFence::globalSegmentMemoryScope`

Definition at line 958 of file [Brig.h](#).

6.39.2.3 groupSegmentMemoryScope

`BrigMemoryScope8_t` `BrigInstMemFence::groupSegmentMemoryScope`

Definition at line 959 of file [Brig.h](#).

6.39.2.4 imageSegmentMemoryScope

`BrigMemoryScope8_t` `BrigInstMemFence::imageSegmentMemoryScope`

Definition at line 960 of file [Brig.h](#).

6.39.2.5 memoryOrder

`BrigMemoryOrder8_t` `BrigInstMemFence::memoryOrder`

Definition at line 957 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/Brig.h`

6.40 BrigInstMod Struct Reference

Collaboration diagram for `BrigInstMod`:

Public Attributes

- [BrigInstBase](#) `base`
- `BrigAluModifier8_t` `modifier`
- `BrigRound8_t` `round`
- `BrigPack8_t` `pack`
- `uint8_t` `reserved`

6.40.1 Detailed Description

Definition at line 963 of file [Brig.h](#).

6.40.2 Member Data Documentation

6.40.2.1 `base`

`BrigInstBase` `BrigInstMod::base`

Definition at line 964 of file [Brig.h](#).

6.40.2.2 `modifier`

`BrigAluModifier8_t` `BrigInstMod::modifier`

Definition at line 965 of file [Brig.h](#).

6.40.2.3 `pack`

`BrigPack8_t` `BrigInstMod::pack`

Definition at line 967 of file [Brig.h](#).

6.40.2.4 `reserved`

`uint8_t` `BrigInstMod::reserved`

Definition at line 968 of file [Brig.h](#).

6.40.2.5 round

`BrigRound8_t BrigInstMod::round`

Definition at line 966 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/Brig.h`

6.41 BrigInstQueryImage Struct Reference

Collaboration diagram for BrigInstQueryImage:

Public Attributes

- [BrigInstBase](#) `base`
- `BrigType16_t` `imageType`
- `BrigImageGeometry8_t` `geometry`
- `BrigImageQuery8_t` `query`

6.41.1 Detailed Description

Definition at line 971 of file [Brig.h](#).

6.41.2 Member Data Documentation

6.41.2.1 base

`BrigInstBase` `BrigInstQueryImage::base`

Definition at line 972 of file [Brig.h](#).

6.41.2.2 geometry

`BrigImageGeometry8_t` `BrigInstQueryImage::geometry`

Definition at line 974 of file [Brig.h](#).

6.41.2.3 imageType

```
BrigType16_t BrigInstQueryImage::imageType
```

Definition at line 973 of file [Brig.h](#).

6.41.2.4 query

```
BrigImageQuery8_t BrigInstQueryImage::query
```

Definition at line 975 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.42 BrigInstQuerySampler Struct Reference

Collaboration diagram for BrigInstQuerySampler:

Public Attributes

- [BrigInstBase](#) base
- BrigSamplerQuery8_t [query](#)
- uint8_t [reserved](#) [3]

6.42.1 Detailed Description

Definition at line 978 of file [Brig.h](#).

6.42.2 Member Data Documentation

6.42.2.1 base

```
BrigInstBase BrigInstQuerySampler::base
```

Definition at line 979 of file [Brig.h](#).

6.42.2.2 query

```
BrigSamplerQuery8_t BrigInstQuerySampler::query
```

Definition at line 980 of file [Brig.h](#).

6.42.2.3 reserved

```
uint8_t BrigInstQuerySampler::reserved[3]
```

Definition at line 981 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.43 BrigInstQueue Struct Reference

Collaboration diagram for BrigInstQueue:

Public Attributes

- [BrigInstBase](#) [base](#)
- BrigSegment8_t [segment](#)
- BrigMemoryOrder8_t [memoryOrder](#)
- uint16_t [reserved](#)

6.43.1 Detailed Description

Definition at line 984 of file [Brig.h](#).

6.43.2 Member Data Documentation

6.43.2.1 base

```
BrigInstBase BrigInstQueue::base
```

Definition at line 985 of file [Brig.h](#).

6.43.2.2 memoryOrder

```
BrigMemoryOrder8_t BrigInstQueue::memoryOrder
```

Definition at line 987 of file [Brig.h](#).

6.43.2.3 reserved

```
uint16_t BrigInstQueue::reserved
```

Definition at line 988 of file [Brig.h](#).

6.43.2.4 segment

```
BrigSegment8_t BrigInstQueue::segment
```

Definition at line 986 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.44 BrigInstSeg Struct Reference

Collaboration diagram for BrigInstSeg:

Public Attributes

- [BrigInstBase](#) base
- [BrigSegment8_t](#) [segment](#)
- [uint8_t](#) [reserved](#) [3]

6.44.1 Detailed Description

Definition at line 991 of file [Brig.h](#).

6.44.2 Member Data Documentation

6.44.2.1 base

`BrigInstBase` `BrigInstSeg::base`

Definition at line 992 of file [Brig.h](#).

6.44.2.2 reserved

`uint8_t` `BrigInstSeg::reserved[3]`

Definition at line 994 of file [Brig.h](#).

6.44.2.3 segment

`BrigSegment8_t` `BrigInstSeg::segment`

Definition at line 993 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/Brig.h`

6.45 BrigInstSegCvt Struct Reference

Collaboration diagram for `BrigInstSegCvt`:

Public Attributes

- [BrigInstBase](#) `base`
- `BrigType16_t` `sourceType`
- `BrigSegment8_t` `segment`
- `BrigSegCvtModifier8_t` `modifier`

6.45.1 Detailed Description

Definition at line 997 of file [Brig.h](#).

6.45.2 Member Data Documentation

6.45.2.1 base

`BrigInstBase` `BrigInstSegCvt::base`

Definition at line 998 of file [Brig.h](#).

6.45.2.2 modifier

`BrigSegCvtModifier8_t` `BrigInstSegCvt::modifier`

Definition at line 1001 of file [Brig.h](#).

6.45.2.3 segment

`BrigSegment8_t` `BrigInstSegCvt::segment`

Definition at line 1000 of file [Brig.h](#).

6.45.2.4 sourceType

`BrigType16_t` `BrigInstSegCvt::sourceType`

Definition at line 999 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.46 BrigInstSignal Struct Reference

Collaboration diagram for BrigInstSignal:

Public Attributes

- [BrigInstBase](#) `base`
- `BrigType16_t` [signalType](#)
- `BrigMemoryOrder8_t` [memoryOrder](#)
- `BrigAtomicOperation8_t` [signalOperation](#)

6.46.1 Detailed Description

Definition at line 1004 of file [Brig.h](#).

6.46.2 Member Data Documentation

6.46.2.1 base

`BrigInstBase` `BrigInstSignal::base`

Definition at line 1005 of file [Brig.h](#).

6.46.2.2 memoryOrder

`BrigMemoryOrder8_t` `BrigInstSignal::memoryOrder`

Definition at line 1007 of file [Brig.h](#).

6.46.2.3 signalOperation

`BrigAtomicOperation8_t` `BrigInstSignal::signalOperation`

Definition at line 1008 of file [Brig.h](#).

6.46.2.4 signalType

`BrigType16_t` `BrigInstSignal::signalType`

Definition at line 1006 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/Brig.h`

6.47 BrigInstSourceType Struct Reference

Collaboration diagram for `BrigInstSourceType`:

Public Attributes

- [BrigInstBase](#) `base`
- `BrigType16_t` `sourceType`
- `uint16_t` `reserved`

6.47.1 Detailed Description

Definition at line 1011 of file [Brig.h](#).

6.47.2 Member Data Documentation

6.47.2.1 `base`

```
BrigInstBase BrigInstSourceType::base
```

Definition at line 1012 of file [Brig.h](#).

6.47.2.2 `reserved`

```
uint16_t BrigInstSourceType::reserved
```

Definition at line 1014 of file [Brig.h](#).

6.47.2.3 `sourceType`

```
BrigType16_t BrigInstSourceType::sourceType
```

Definition at line 1013 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/Brig.h`

6.48 BrigModuleHeader Struct Reference

Public Attributes

- char [identification](#) [8]
- BrigVersion32_t [brigMajor](#)
- BrigVersion32_t [brigMinor](#)
- uint64_t [byteCount](#)
- uint8_t [hash](#) [64]
- uint32_t [reserved](#)
- uint32_t [sectionCount](#)
- uint64_t [sectionIndex](#)

6.48.1 Detailed Description

Definition at line [1114](#) of file [Brig.h](#).

6.48.2 Member Data Documentation

6.48.2.1 brigMajor

```
BrigVersion32_t BrigModuleHeader::brigMajor
```

Definition at line [1116](#) of file [Brig.h](#).

6.48.2.2 brigMinor

```
BrigVersion32_t BrigModuleHeader::brigMinor
```

Definition at line [1117](#) of file [Brig.h](#).

6.48.2.3 byteCount

```
uint64_t BrigModuleHeader::byteCount
```

Definition at line [1118](#) of file [Brig.h](#).

6.48.2.4 hash

```
uint8_t BrigModuleHeader::hash[64]
```

Definition at line 1119 of file [Brig.h](#).

6.48.2.5 identification

```
char BrigModuleHeader::identification[8]
```

Definition at line 1115 of file [Brig.h](#).

6.48.2.6 reserved

```
uint32_t BrigModuleHeader::reserved
```

Definition at line 1120 of file [Brig.h](#).

6.48.2.7 sectionCount

```
uint32_t BrigModuleHeader::sectionCount
```

Definition at line 1121 of file [Brig.h](#).

6.48.2.8 sectionIndex

```
uint64_t BrigModuleHeader::sectionIndex
```

Definition at line 1122 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.49 BrigOperandAddress Struct Reference

Collaboration diagram for BrigOperandAddress:

Public Attributes

- [BrigBase](#) `base`
- `BrigCodeOffset32_t` `symbol`
- `BrigOperandOffset32_t` `reg`
- `BrigUInt64` `offset`

6.49.1 Detailed Description

Definition at line 1017 of file [Brig.h](#).

6.49.2 Member Data Documentation

6.49.2.1 `base`

```
BrigBase BrigOperandAddress::base
```

Definition at line 1018 of file [Brig.h](#).

6.49.2.2 `offset`

```
BrigUInt64 BrigOperandAddress::offset
```

Definition at line 1021 of file [Brig.h](#).

6.49.2.3 `reg`

```
BrigOperandOffset32_t BrigOperandAddress::reg
```

Definition at line 1020 of file [Brig.h](#).

6.49.2.4 `symbol`

```
BrigCodeOffset32_t BrigOperandAddress::symbol
```

Definition at line 1019 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/Brig.h`

6.50 BrigOperandAlign Struct Reference

Collaboration diagram for BrigOperandAlign:

Public Attributes

- [BrigBase](#) `base`
- [BrigAlignment8_t](#) `align`
- [uint8_t](#) `reserved` [3]

6.50.1 Detailed Description

Definition at line [1024](#) of file [Brig.h](#).

6.50.2 Member Data Documentation

6.50.2.1 align

```
BrigAlignment8_t BrigOperandAlign::align
```

Definition at line [1026](#) of file [Brig.h](#).

6.50.2.2 base

```
BrigBase BrigOperandAlign::base
```

Definition at line [1025](#) of file [Brig.h](#).

6.50.2.3 reserved

```
uint8_t BrigOperandAlign::reserved[3]
```

Definition at line [1027](#) of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/Brig.h`

6.51 BrigOperandCodeList Struct Reference

Collaboration diagram for BrigOperandCodeList:

Public Attributes

- [BrigBase](#) [base](#)
- [BrigDataOffsetCodeList32_t](#) [elements](#)

6.51.1 Detailed Description

Definition at line [1030](#) of file [Brig.h](#).

6.51.2 Member Data Documentation

6.51.2.1 base

[BrigBase](#) [BrigOperandCodeList::base](#)

Definition at line [1031](#) of file [Brig.h](#).

6.51.2.2 elements

[BrigDataOffsetCodeList32_t](#) [BrigOperandCodeList::elements](#)

Definition at line [1032](#) of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.52 BrigOperandCodeRef Struct Reference

Collaboration diagram for BrigOperandCodeRef:

Public Attributes

- [BrigBase](#) [base](#)
- [BrigCodeOffset32_t](#) [ref](#)

6.52.1 Detailed Description

Definition at line 1035 of file [Brig.h](#).

6.52.2 Member Data Documentation

6.52.2.1 base

[BrigBase](#) BrigOperandCodeRef::base

Definition at line 1036 of file [Brig.h](#).

6.52.2.2 ref

[BrigCodeOffset32_t](#) BrigOperandCodeRef::ref

Definition at line 1037 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.53 BrigOperandConstantBytes Struct Reference

Collaboration diagram for BrigOperandConstantBytes:

Public Attributes

- [BrigBase](#) base
- [BrigType16_t](#) type
- [uint16_t](#) reserved
- [BrigDataOffsetString32_t](#) bytes

6.53.1 Detailed Description

Definition at line 1040 of file [Brig.h](#).

6.53.2 Member Data Documentation

6.53.2.1 base

`BrigBase` `BrigOperandConstantBytes::base`

Definition at line 1041 of file [Brig.h](#).

6.53.2.2 bytes

`BrigDataOffsetString32_t` `BrigOperandConstantBytes::bytes`

Definition at line 1044 of file [Brig.h](#).

6.53.2.3 reserved

`uint16_t` `BrigOperandConstantBytes::reserved`

Definition at line 1043 of file [Brig.h](#).

6.53.2.4 type

`BrigType16_t` `BrigOperandConstantBytes::type`

Definition at line 1042 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.54 BrigOperandConstantImage Struct Reference

Collaboration diagram for `BrigOperandConstantImage`:

Public Attributes

- [BrigBase](#) `base`
- `BrigType16_t` `type`
- `BrigImageGeometry8_t` `geometry`
- `BrigImageChannelOrder8_t` `channelOrder`
- `BrigImageChannelType8_t` `channelType`
- `uint8_t` `reserved` [3]
- `BrigUInt64` `width`
- `BrigUInt64` `height`
- `BrigUInt64` `depth`
- `BrigUInt64` `array`

6.54.1 Detailed Description

Definition at line 1054 of file [Brig.h](#).

6.54.2 Member Data Documentation

6.54.2.1 array

[BrigUInt64](#) BrigOperandConstantImage::array

Definition at line 1064 of file [Brig.h](#).

6.54.2.2 base

[BrigBase](#) BrigOperandConstantImage::base

Definition at line 1055 of file [Brig.h](#).

6.54.2.3 channelOrder

[BrigImageChannelOrder8_t](#) BrigOperandConstantImage::channelOrder

Definition at line 1058 of file [Brig.h](#).

6.54.2.4 channelType

[BrigImageChannelType8_t](#) BrigOperandConstantImage::channelType

Definition at line 1059 of file [Brig.h](#).

6.54.2.5 depth

[BrigUInt64](#) BrigOperandConstantImage::depth

Definition at line 1063 of file [Brig.h](#).

6.54.2.6 geometry

```
BrigImageGeometry8_t BrigOperandConstantImage::geometry
```

Definition at line 1057 of file [Brig.h](#).

6.54.2.7 height

```
BrigUInt64 BrigOperandConstantImage::height
```

Definition at line 1062 of file [Brig.h](#).

6.54.2.8 reserved

```
uint8_t BrigOperandConstantImage::reserved[3]
```

Definition at line 1060 of file [Brig.h](#).

6.54.2.9 type

```
BrigType16_t BrigOperandConstantImage::type
```

Definition at line 1056 of file [Brig.h](#).

6.54.2.10 width

```
BrigUInt64 BrigOperandConstantImage::width
```

Definition at line 1061 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.55 BrigOperandConstantOperandList Struct Reference

Collaboration diagram for BrigOperandConstantOperandList:

Public Attributes

- [BrigBase](#) `base`
- `BrigType16_t` `type`
- `uint16_t` `reserved`
- `BrigDataOffsetOperandList32_t` `elements`

6.55.1 Detailed Description

Definition at line 1047 of file [Brig.h](#).

6.55.2 Member Data Documentation

6.55.2.1 `base`

```
BrigBase BrigOperandConstantOperandList::base
```

Definition at line 1048 of file [Brig.h](#).

6.55.2.2 `elements`

```
BrigDataOffsetOperandList32_t BrigOperandConstantOperandList::elements
```

Definition at line 1051 of file [Brig.h](#).

6.55.2.3 `reserved`

```
uint16_t BrigOperandConstantOperandList::reserved
```

Definition at line 1050 of file [Brig.h](#).

6.55.2.4 `type`

```
BrigType16_t BrigOperandConstantOperandList::type
```

Definition at line 1049 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/Brig.h`

6.56 BrigOperandConstantSampler Struct Reference

Collaboration diagram for BrigOperandConstantSampler:

Public Attributes

- [BrigBase](#) [base](#)
- BrigType16_t [type](#)
- BrigSamplerCoordNormalization8_t [coord](#)
- BrigSamplerFilter8_t [filter](#)
- BrigSamplerAddressing8_t [addressing](#)
- uint8_t [reserved](#) [3]

6.56.1 Detailed Description

Definition at line 1078 of file [Brig.h](#).

6.56.2 Member Data Documentation

6.56.2.1 addressing

BrigSamplerAddressing8_t BrigOperandConstantSampler::addressing

Definition at line 1083 of file [Brig.h](#).

6.56.2.2 base

[BrigBase](#) BrigOperandConstantSampler::base

Definition at line 1079 of file [Brig.h](#).

6.56.2.3 coord

BrigSamplerCoordNormalization8_t BrigOperandConstantSampler::coord

Definition at line 1081 of file [Brig.h](#).

6.56.2.4 filter

```
BrigSamplerFilter8_t BrigOperandConstantSampler::filter
```

Definition at line 1082 of file [Brig.h](#).

6.56.2.5 reserved

```
uint8_t BrigOperandConstantSampler::reserved[3]
```

Definition at line 1084 of file [Brig.h](#).

6.56.2.6 type

```
BrigType16_t BrigOperandConstantSampler::type
```

Definition at line 1080 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.57 BrigOperandOperandList Struct Reference

Collaboration diagram for BrigOperandOperandList:

Public Attributes

- [BrigBase base](#)
- [BrigDataOffsetOperandList32_t elements](#)

6.57.1 Detailed Description

Definition at line 1067 of file [Brig.h](#).

6.57.2 Member Data Documentation

6.57.2.1 base

[BrigBase](#) BrigOperandOperandList::base

Definition at line 1068 of file [Brig.h](#).

6.57.2.2 elements

BrigDataOffsetOperandList32_t BrigOperandOperandList::elements

Definition at line 1069 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.58 BrigOperandRegister Struct Reference

Collaboration diagram for BrigOperandRegister:

Public Attributes

- [BrigBase](#) base
- BrigRegisterKind16_t regKind
- uint16_t regNum

6.58.1 Detailed Description

Definition at line 1072 of file [Brig.h](#).

6.58.2 Member Data Documentation

6.58.2.1 base

[BrigBase](#) BrigOperandRegister::base

Definition at line 1073 of file [Brig.h](#).

6.58.2.2 regKind

```
BrigRegisterKind16_t BrigOperandRegister::regKind
```

Definition at line 1074 of file [Brig.h](#).

6.58.2.3 regNum

```
uint16_t BrigOperandRegister::regNum
```

Definition at line 1075 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.59 BrigOperandString Struct Reference

Collaboration diagram for BrigOperandString:

Public Attributes

- [BrigBase](#) base
- BrigDataOffsetString32_t [string](#)

6.59.1 Detailed Description

Definition at line 1087 of file [Brig.h](#).

6.59.2 Member Data Documentation

6.59.2.1 base

```
BrigBase BrigOperandString::base
```

Definition at line 1088 of file [Brig.h](#).

6.59.2.2 string

`BrigDataOffsetString32_t BrigOperandString::string`

Definition at line 1089 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.60 BrigOperandWavesize Struct Reference

Collaboration diagram for BrigOperandWavesize:

Public Attributes

- [BrigBase](#) `base`

6.60.1 Detailed Description

Definition at line 1092 of file [Brig.h](#).

6.60.2 Member Data Documentation

6.60.2.1 base

[BrigBase](#) `BrigOperandWavesize::base`

Definition at line 1093 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.61 BrigSectionHeader Struct Reference

Public Attributes

- `uint64_t` `byteCount`
- `uint32_t` `headerByteCount`
- `uint32_t` `nameLength`
- `uint8_t` `name` [1]

6.61.1 Detailed Description

Definition at line 1107 of file [Brig.h](#).

6.61.2 Member Data Documentation

6.61.2.1 byteCount

```
uint64_t BrigSectionHeader::byteCount
```

Definition at line 1108 of file [Brig.h](#).

6.61.2.2 headerByteCount

```
uint32_t BrigSectionHeader::headerByteCount
```

Definition at line 1109 of file [Brig.h](#).

6.61.2.3 name

```
uint8_t BrigSectionHeader::name[1]
```

Definition at line 1111 of file [Brig.h](#).

6.61.2.4 nameLength

```
uint32_t BrigSectionHeader::nameLength
```

Definition at line 1110 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.62 BrigUInt64 Struct Reference

Public Attributes

- [uint32_t lo](#)
- [uint32_t hi](#)

6.62.1 Detailed Description

Definition at line 777 of file [Brig.h](#).

6.62.2 Member Data Documentation

6.62.2.1 hi

```
uint32_t BrigUInt64::hi
```

Definition at line 779 of file [Brig.h](#).

6.62.2.2 lo

```
uint32_t BrigUInt64::lo
```

Definition at line 778 of file [Brig.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/Brig.h](#)

6.63 CoreApiTable Struct Reference

Collaboration diagram for CoreApiTable:

Public Attributes

- [ApiTableVersion](#) version
- `decltype(hsa_init) * hsa_init_fn`
- `decltype(hsa_shut_down) * hsa_shut_down_fn`
- `decltype(hsa_system_get_info) * hsa_system_get_info_fn`
- `decltype(hsa_system_extension_supported) * hsa_system_extension_supported_fn`
- `decltype(hsa_system_get_extension_table) * hsa_system_get_extension_table_fn`
- `decltype(hsa_iterate_agents) * hsa_iterate_agents_fn`
- `decltype(hsa_agent_get_info) * hsa_agent_get_info_fn`
- `decltype(hsa_queue_create) * hsa_queue_create_fn`
- `decltype(hsa_soft_queue_create) * hsa_soft_queue_create_fn`
- `decltype(hsa_queue_destroy) * hsa_queue_destroy_fn`
- `decltype(hsa_queue_inactivate) * hsa_queue_inactivate_fn`
- `decltype(hsa_queue_load_read_index_scacquire) * hsa_queue_load_read_index_scacquire_fn`
- `decltype(hsa_queue_load_read_index_relaxed) * hsa_queue_load_read_index_relaxed_fn`
- `decltype(hsa_queue_load_write_index_scacquire) * hsa_queue_load_write_index_scacquire_fn`

- `decltype(hsa_queue_load_write_index_relaxed) * hsa_queue_load_write_index_relaxed_fn`
- `decltype(hsa_queue_store_write_index_relaxed) * hsa_queue_store_write_index_relaxed_fn`
- `decltype(hsa_queue_store_write_index_screlease) * hsa_queue_store_write_index_screlease_fn`
- `decltype(hsa_queue_cas_write_index_scacq_screl) * hsa_queue_cas_write_index_scacq_screl_fn`
- `decltype(hsa_queue_cas_write_index_scacquire) * hsa_queue_cas_write_index_scacquire_fn`
- `decltype(hsa_queue_cas_write_index_relaxed) * hsa_queue_cas_write_index_relaxed_fn`
- `decltype(hsa_queue_cas_write_index_screlease) * hsa_queue_cas_write_index_screlease_fn`
- `decltype(hsa_queue_add_write_index_scacq_screl) * hsa_queue_add_write_index_scacq_screl_fn`
- `decltype(hsa_queue_add_write_index_scacquire) * hsa_queue_add_write_index_scacquire_fn`
- `decltype(hsa_queue_add_write_index_relaxed) * hsa_queue_add_write_index_relaxed_fn`
- `decltype(hsa_queue_add_write_index_screlease) * hsa_queue_add_write_index_screlease_fn`
- `decltype(hsa_queue_store_read_index_relaxed) * hsa_queue_store_read_index_relaxed_fn`
- `decltype(hsa_queue_store_read_index_screlease) * hsa_queue_store_read_index_screlease_fn`
- `decltype(hsa_agent_iterate_regions) * hsa_agent_iterate_regions_fn`
- `decltype(hsa_region_get_info) * hsa_region_get_info_fn`
- `decltype(hsa_agent_get_exception_policies) * hsa_agent_get_exception_policies_fn`
- `decltype(hsa_agent_extension_supported) * hsa_agent_extension_supported_fn`
- `decltype(hsa_memory_register) * hsa_memory_register_fn`
- `decltype(hsa_memory_deregister) * hsa_memory_deregister_fn`
- `decltype(hsa_memory_allocate) * hsa_memory_allocate_fn`
- `decltype(hsa_memory_free) * hsa_memory_free_fn`
- `decltype(hsa_memory_copy) * hsa_memory_copy_fn`
- `decltype(hsa_memory_assign_agent) * hsa_memory_assign_agent_fn`
- `decltype(hsa_signal_create) * hsa_signal_create_fn`
- `decltype(hsa_signal_destroy) * hsa_signal_destroy_fn`
- `decltype(hsa_signal_load_relaxed) * hsa_signal_load_relaxed_fn`
- `decltype(hsa_signal_load_scacquire) * hsa_signal_load_scacquire_fn`
- `decltype(hsa_signal_store_relaxed) * hsa_signal_store_relaxed_fn`
- `decltype(hsa_signal_store_screlease) * hsa_signal_store_screlease_fn`
- `decltype(hsa_signal_wait_relaxed) * hsa_signal_wait_relaxed_fn`
- `decltype(hsa_signal_wait_scacquire) * hsa_signal_wait_scacquire_fn`
- `decltype(hsa_signal_and_relaxed) * hsa_signal_and_relaxed_fn`
- `decltype(hsa_signal_and_scacquire) * hsa_signal_and_scacquire_fn`
- `decltype(hsa_signal_and_screlease) * hsa_signal_and_screlease_fn`
- `decltype(hsa_signal_and_scacq_screl) * hsa_signal_and_scacq_screl_fn`
- `decltype(hsa_signal_or_relaxed) * hsa_signal_or_relaxed_fn`
- `decltype(hsa_signal_or_scacquire) * hsa_signal_or_scacquire_fn`
- `decltype(hsa_signal_or_screlease) * hsa_signal_or_screlease_fn`
- `decltype(hsa_signal_or_scacq_screl) * hsa_signal_or_scacq_screl_fn`
- `decltype(hsa_signal_xor_relaxed) * hsa_signal_xor_relaxed_fn`
- `decltype(hsa_signal_xor_scacquire) * hsa_signal_xor_scacquire_fn`
- `decltype(hsa_signal_xor_screlease) * hsa_signal_xor_screlease_fn`
- `decltype(hsa_signal_xor_scacq_screl) * hsa_signal_xor_scacq_screl_fn`
- `decltype(hsa_signal_exchange_relaxed) * hsa_signal_exchange_relaxed_fn`
- `decltype(hsa_signal_exchange_scacquire) * hsa_signal_exchange_scacquire_fn`
- `decltype(hsa_signal_exchange_screlease) * hsa_signal_exchange_screlease_fn`
- `decltype(hsa_signal_exchange_scacq_screl) * hsa_signal_exchange_scacq_screl_fn`
- `decltype(hsa_signal_add_relaxed) * hsa_signal_add_relaxed_fn`
- `decltype(hsa_signal_add_scacquire) * hsa_signal_add_scacquire_fn`
- `decltype(hsa_signal_add_screlease) * hsa_signal_add_screlease_fn`
- `decltype(hsa_signal_add_scacq_screl) * hsa_signal_add_scacq_screl_fn`
- `decltype(hsa_signal_subtract_relaxed) * hsa_signal_subtract_relaxed_fn`
- `decltype(hsa_signal_subtract_scacquire) * hsa_signal_subtract_scacquire_fn`
- `decltype(hsa_signal_subtract_screlease) * hsa_signal_subtract_screlease_fn`
- `decltype(hsa_signal_subtract_scacq_screl) * hsa_signal_subtract_scacq_screl_fn`

- `decltype(hsa_signal_cas_relaxed) * hsa_signal_cas_relaxed_fn`
- `decltype(hsa_signal_cas_scacquire) * hsa_signal_cas_scacquire_fn`
- `decltype(hsa_signal_cas_screlease) * hsa_signal_cas_screlease_fn`
- `decltype(hsa_signal_cas_scacq_screl) * hsa_signal_cas_scacq_screl_fn`
- `decltype(hsa_isa_from_name) * hsa_isa_from_name_fn`
- `decltype(hsa_isa_get_info) * hsa_isa_get_info_fn`
- `decltype(hsa_isa_compatible) * hsa_isa_compatible_fn`
- `decltype(hsa_code_object_serialize) * hsa_code_object_serialize_fn`
- `decltype(hsa_code_object_deserialize) * hsa_code_object_deserialize_fn`
- `decltype(hsa_code_object_destroy) * hsa_code_object_destroy_fn`
- `decltype(hsa_code_object_get_info) * hsa_code_object_get_info_fn`
- `decltype(hsa_code_object_get_symbol) * hsa_code_object_get_symbol_fn`
- `decltype(hsa_code_symbol_get_info) * hsa_code_symbol_get_info_fn`
- `decltype(hsa_code_object_iterate_symbols) * hsa_code_object_iterate_symbols_fn`
- `decltype(hsa_executable_create) * hsa_executable_create_fn`
- `decltype(hsa_executable_destroy) * hsa_executable_destroy_fn`
- `decltype(hsa_executable_load_code_object) * hsa_executable_load_code_object_fn`
- `decltype(hsa_executable_freeze) * hsa_executable_freeze_fn`
- `decltype(hsa_executable_get_info) * hsa_executable_get_info_fn`
- `decltype(hsa_executable_global_variable_define) * hsa_executable_global_variable_define_fn`
- `decltype(hsa_executable_agent_global_variable_define) * hsa_executable_agent_global_variable_define_fn`
- `decltype(hsa_executable_readonly_variable_define) * hsa_executable_readonly_variable_define_fn`
- `decltype(hsa_executable_validate) * hsa_executable_validate_fn`
- `decltype(hsa_executable_get_symbol) * hsa_executable_get_symbol_fn`
- `decltype(hsa_executable_symbol_get_info) * hsa_executable_symbol_get_info_fn`
- `decltype(hsa_executable_iterate_symbols) * hsa_executable_iterate_symbols_fn`
- `decltype(hsa_status_string) * hsa_status_string_fn`
- `decltype(hsa_extension_get_name) * hsa_extension_get_name_fn`
- `decltype(hsa_system_major_extension_supported) * hsa_system_major_extension_supported_fn`
- `decltype(hsa_system_get_major_extension_table) * hsa_system_get_major_extension_table_fn`
- `decltype(hsa_agent_major_extension_supported) * hsa_agent_major_extension_supported_fn`
- `decltype(hsa_cache_get_info) * hsa_cache_get_info_fn`
- `decltype(hsa_agent_iterate_caches) * hsa_agent_iterate_caches_fn`
- `decltype(hsa_signal_silent_store_relaxed) * hsa_signal_silent_store_relaxed_fn`
- `decltype(hsa_signal_silent_store_screlease) * hsa_signal_silent_store_screlease_fn`
- `decltype(hsa_signal_group_create) * hsa_signal_group_create_fn`
- `decltype(hsa_signal_group_destroy) * hsa_signal_group_destroy_fn`
- `decltype(hsa_signal_group_wait_any_scacquire) * hsa_signal_group_wait_any_scacquire_fn`
- `decltype(hsa_signal_group_wait_any_relaxed) * hsa_signal_group_wait_any_relaxed_fn`
- `decltype(hsa_agent_iterate_isas) * hsa_agent_iterate_isas_fn`
- `decltype(hsa_isa_get_info_alt) * hsa_isa_get_info_alt_fn`
- `decltype(hsa_isa_get_exception_policies) * hsa_isa_get_exception_policies_fn`
- `decltype(hsa_isa_get_round_method) * hsa_isa_get_round_method_fn`
- `decltype(hsa_wavefront_get_info) * hsa_wavefront_get_info_fn`
- `decltype(hsa_isa_iterate_wavefronts) * hsa_isa_iterate_wavefronts_fn`
- `decltype(hsa_code_object_get_symbol_from_name) * hsa_code_object_get_symbol_from_name_fn`
- `decltype(hsa_code_object_reader_create_from_file) * hsa_code_object_reader_create_from_file_fn`
- `decltype(hsa_code_object_reader_create_from_memory) * hsa_code_object_reader_create_from_memory_fn`
- `decltype(hsa_code_object_reader_destroy) * hsa_code_object_reader_destroy_fn`
- `decltype(hsa_executable_create_alt) * hsa_executable_create_alt_fn`
- `decltype(hsa_executable_load_program_code_object) * hsa_executable_load_program_code_object_fn`
- `decltype(hsa_executable_load_agent_code_object) * hsa_executable_load_agent_code_object_fn`
- `decltype(hsa_executable_validate_alt) * hsa_executable_validate_alt_fn`
- `decltype(hsa_executable_get_symbol_by_name) * hsa_executable_get_symbol_by_name_fn`
- `decltype(hsa_executable_iterate_agent_symbols) * hsa_executable_iterate_agent_symbols_fn`
- `decltype(hsa_executable_iterate_program_symbols) * hsa_executable_iterate_program_symbols_fn`

6.63.1 Detailed Description

Definition at line 193 of file [hsa_api_trace.h](#).

6.63.2 Member Data Documentation

6.63.2.1 hsa_agent_extension_supported_fn

```
decltype(hsa_agent_extension_supported) * CoreApiTable::hsa_agent_extension_supported_fn
```

Definition at line 225 of file [hsa_api_trace.h](#).

6.63.2.2 hsa_agent_get_exception_policies_fn

```
decltype(hsa_agent_get_exception_policies) * CoreApiTable::hsa_agent_get_exception_policies_fn
```

Definition at line 224 of file [hsa_api_trace.h](#).

6.63.2.3 hsa_agent_get_info_fn

```
decltype(hsa_agent_get_info) * CoreApiTable::hsa_agent_get_info_fn
```

Definition at line 201 of file [hsa_api_trace.h](#).

6.63.2.4 hsa_agent_iterate_caches_fn

```
decltype(hsa_agent_iterate_caches) * CoreApiTable::hsa_agent_iterate_caches_fn
```

Definition at line 326 of file [hsa_api_trace.h](#).

6.63.2.5 hsa_agent_iterate_isas_fn

```
decltype(hsa_agent_iterate_isas) * CoreApiTable::hsa_agent_iterate_isas_fn
```

Definition at line 336 of file [hsa_api_trace.h](#).

6.63.2.6 hsa_agent_iterate_regions_fn

```
decltype(hsa_agent_iterate_regions) * CoreApiTable::hsa_agent_iterate_regions_fn
```

Definition at line 222 of file [hsa_api_trace.h](#).

6.63.2.7 hsa_agent_major_extension_supported_fn

```
decltype(hsa_agent_major_extension_supported) * CoreApiTable::hsa_agent_major_extension_↵  
supported_fn
```

Definition at line 324 of file [hsa_api_trace.h](#).

6.63.2.8 hsa_cache_get_info_fn

```
decltype(hsa_cache_get_info) * CoreApiTable::hsa_cache_get_info_fn
```

Definition at line 325 of file [hsa_api_trace.h](#).

6.63.2.9 hsa_code_object_deserialize_fn

```
decltype(hsa_code_object_deserialize) * CoreApiTable::hsa_code_object_deserialize_fn
```

Definition at line 282 of file [hsa_api_trace.h](#).

6.63.2.10 hsa_code_object_destroy_fn

```
decltype(hsa_code_object_destroy) * CoreApiTable::hsa_code_object_destroy_fn
```

Definition at line 284 of file [hsa_api_trace.h](#).

6.63.2.11 hsa_code_object_get_info_fn

```
decltype(hsa_code_object_get_info) * CoreApiTable::hsa_code_object_get_info_fn
```

Definition at line 286 of file [hsa_api_trace.h](#).

6.63.2.12 hsa_code_object_get_symbol_fn

```
decltype(hsa_code_object_get_symbol) * CoreApiTable::hsa_code_object_get_symbol_fn
```

Definition at line 288 of file [hsa_api_trace.h](#).

6.63.2.13 hsa_code_object_get_symbol_from_name_fn

```
decltype(hsa_code_object_get_symbol_from_name) * CoreApiTable::hsa_code_object_get_symbol_↵  
from_name_fn
```

Definition at line 347 of file [hsa_api_trace.h](#).

6.63.2.14 hsa_code_object_iterate_symbols_fn

```
decltype(hsa_code_object_iterate_symbols) * CoreApiTable::hsa_code_object_iterate_symbols_fn
```

Definition at line 292 of file [hsa_api_trace.h](#).

6.63.2.15 hsa_code_object_reader_create_from_file_fn

```
decltype(hsa_code_object_reader_create_from_file) * CoreApiTable::hsa_code_object_reader_↵  
create_from_file_fn
```

Definition at line 352 of file [hsa_api_trace.h](#).

6.63.2.16 hsa_code_object_reader_create_from_memory_fn

```
decltype(hsa_code_object_reader_create_from_memory) * CoreApiTable::hsa_code_object_reader_↵  
create_from_memory_fn
```

Definition at line 354 of file [hsa_api_trace.h](#).

6.63.2.17 hsa_code_object_reader_destroy_fn

```
decltype(hsa_code_object_reader_destroy) * CoreApiTable::hsa_code_object_reader_destroy_fn
```

Definition at line 355 of file [hsa_api_trace.h](#).

6.63.2.18 hsa_code_object_serialize_fn

```
decltype(hsa_code_object_serialize) * CoreApiTable::hsa_code_object_serialize_fn
```

Definition at line 280 of file [hsa_api_trace.h](#).

6.63.2.19 hsa_code_symbol_get_info_fn

```
decltype(hsa_code_symbol_get_info) * CoreApiTable::hsa_code_symbol_get_info_fn
```

Definition at line 290 of file [hsa_api_trace.h](#).

6.63.2.20 hsa_executable_agent_global_variable_define_fn

```
decltype(hsa_executable_agent_global_variable_define) * CoreApiTable::hsa_executable_agent_↵  
global_variable_define_fn
```

Definition at line 306 of file [hsa_api_trace.h](#).

6.63.2.21 hsa_executable_create_alt_fn

```
decltype(hsa_executable_create_alt) * CoreApiTable::hsa_executable_create_alt_fn
```

Definition at line 356 of file [hsa_api_trace.h](#).

6.63.2.22 hsa_executable_create_fn

```
decltype(hsa_executable_create) * CoreApiTable::hsa_executable_create_fn
```

Definition at line 297 of file [hsa_api_trace.h](#).

6.63.2.23 hsa_executable_destroy_fn

```
decltype(hsa_executable_destroy) * CoreApiTable::hsa_executable_destroy_fn
```

Definition at line 298 of file [hsa_api_trace.h](#).

6.63.2.24 hsa_executable_freeze_fn

```
decltype(hsa_executable_freeze) * CoreApiTable::hsa_executable_freeze_fn
```

Definition at line 301 of file [hsa_api_trace.h](#).

6.63.2.25 hsa_executable_get_info_fn

```
decltype(hsa_executable_get_info) * CoreApiTable::hsa_executable_get_info_fn
```

Definition at line 302 of file [hsa_api_trace.h](#).

6.63.2.26 hsa_executable_get_symbol_by_name_fn

```
decltype(hsa_executable_get_symbol_by_name) * CoreApiTable::hsa_executable_get_symbol_by_name↔  
_fn
```

Definition at line 363 of file [hsa_api_trace.h](#).

6.63.2.27 hsa_executable_get_symbol_fn

```
decltype(hsa_executable_get_symbol) * CoreApiTable::hsa_executable_get_symbol_fn
```

Definition at line 311 of file [hsa_api_trace.h](#).

6.63.2.28 hsa_executable_global_variable_define_fn

```
decltype(hsa_executable_global_variable_define) * CoreApiTable::hsa_executable_global_variable↔  
_define_fn
```

Definition at line 304 of file [hsa_api_trace.h](#).

6.63.2.29 hsa_executable_iterate_agent_symbols_fn

```
decltype(hsa_executable_iterate_agent_symbols) * CoreApiTable::hsa_executable_iterate_agent↔  
symbols_fn
```

Definition at line 365 of file [hsa_api_trace.h](#).

6.63.2.30 hsa_executable_iterate_program_symbols_fn

```
decltype(hsa_executable_iterate_program_symbols) * CoreApiTable::hsa_executable_iterate_↔  
program_symbols_fn
```

Definition at line 367 of file [hsa_api_trace.h](#).

6.63.2.31 hsa_executable_iterate_symbols_fn

```
decltype(hsa_executable_iterate_symbols) * CoreApiTable::hsa_executable_iterate_symbols_fn
```

Definition at line 314 of file [hsa_api_trace.h](#).

6.63.2.32 hsa_executable_load_agent_code_object_fn

```
decltype(hsa_executable_load_agent_code_object) * CoreApiTable::hsa_executable_load_agent_↔  
code_object_fn
```

Definition at line 360 of file [hsa_api_trace.h](#).

6.63.2.33 hsa_executable_load_code_object_fn

```
decltype(hsa_executable_load_code_object) * CoreApiTable::hsa_executable_load_code_object_fn
```

Definition at line 300 of file [hsa_api_trace.h](#).

6.63.2.34 hsa_executable_load_program_code_object_fn

```
decltype(hsa_executable_load_program_code_object) * CoreApiTable::hsa_executable_load_program_↔  
_code_object_fn
```

Definition at line 358 of file [hsa_api_trace.h](#).

6.63.2.35 hsa_executable_readonly_variable_define_fn

```
decltype(hsa_executable_readonly_variable_define) * CoreApiTable::hsa_executable_readonly_↔  
variable_define_fn
```

Definition at line 308 of file [hsa_api_trace.h](#).

6.63.2.36 hsa_executable_symbol_get_info_fn

```
decltype(hsa_executable_symbol_get_info) * CoreApiTable::hsa_executable_symbol_get_info_fn
```

Definition at line 312 of file [hsa_api_trace.h](#).

6.63.2.37 hsa_executable_validate_alt_fn

```
decltype(hsa_executable_validate_alt) * CoreApiTable::hsa_executable_validate_alt_fn
```

Definition at line 361 of file [hsa_api_trace.h](#).

6.63.2.38 hsa_executable_validate_fn

```
decltype(hsa_executable_validate) * CoreApiTable::hsa_executable_validate_fn
```

Definition at line 309 of file [hsa_api_trace.h](#).

6.63.2.39 hsa_extension_get_name_fn

```
decltype(hsa_extension_get_name) * CoreApiTable::hsa_extension_get_name_fn
```

Definition at line 321 of file [hsa_api_trace.h](#).

6.63.2.40 hsa_init_fn

```
decltype(hsa_init) * CoreApiTable::hsa_init_fn
```

Definition at line 195 of file [hsa_api_trace.h](#).

6.63.2.41 hsa_isa_compatible_fn

```
decltype(hsa_isa_compatible) * CoreApiTable::hsa_isa_compatible_fn
```

Definition at line 275 of file [hsa_api_trace.h](#).

6.63.2.42 hsa_isa_from_name_fn

```
decltype(hsa_isa_from_name) * CoreApiTable::hsa_isa_from_name_fn
```

Definition at line 271 of file [hsa_api_trace.h](#).

6.63.2.43 hsa_isa_get_exception_policies_fn

```
decltype(hsa_isa_get_exception_policies) * CoreApiTable::hsa_isa_get_exception_policies_fn
```

Definition at line 338 of file [hsa_api_trace.h](#).

6.63.2.44 hsa_isa_get_info_alt_fn

```
decltype(hsa_isa_get_info_alt) * CoreApiTable::hsa_isa_get_info_alt_fn
```

Definition at line 337 of file [hsa_api_trace.h](#).

6.63.2.45 hsa_isa_get_info_fn

```
decltype(hsa_isa_get_info) * CoreApiTable::hsa_isa_get_info_fn
```

Definition at line 273 of file [hsa_api_trace.h](#).

6.63.2.46 hsa_isa_get_round_method_fn

```
decltype(hsa_isa_get_round_method) * CoreApiTable::hsa_isa_get_round_method_fn
```

Definition at line 339 of file [hsa_api_trace.h](#).

6.63.2.47 hsa_isa_iterate_wavefronts_fn

```
decltype(hsa_isa_iterate_wavefronts) * CoreApiTable::hsa_isa_iterate_wavefronts_fn
```

Definition at line 341 of file [hsa_api_trace.h](#).

6.63.2.48 hsa_iterate_agents_fn

```
decltype(hsa_iterate_agents) * CoreApiTable::hsa_iterate_agents_fn
```

Definition at line 200 of file [hsa_api_trace.h](#).

6.63.2.49 hsa_memory_allocate_fn

```
decltype(hsa_memory_allocate) * CoreApiTable::hsa_memory_allocate_fn
```

Definition at line 228 of file [hsa_api_trace.h](#).

6.63.2.50 hsa_memory_assign_agent_fn

```
decltype(hsa_memory_assign_agent) * CoreApiTable::hsa_memory_assign_agent_fn
```

Definition at line 231 of file [hsa_api_trace.h](#).

6.63.2.51 hsa_memory_copy_fn

```
decltype(hsa_memory_copy) * CoreApiTable::hsa_memory_copy_fn
```

Definition at line 230 of file [hsa_api_trace.h](#).

6.63.2.52 hsa_memory_deregister_fn

```
decltype(hsa_memory_deregister) * CoreApiTable::hsa_memory_deregister_fn
```

Definition at line 227 of file [hsa_api_trace.h](#).

6.63.2.53 hsa_memory_free_fn

```
decltype(hsa_memory_free) * CoreApiTable::hsa_memory_free_fn
```

Definition at line 229 of file [hsa_api_trace.h](#).

6.63.2.54 hsa_memory_register_fn

```
decltype(hsa_memory_register) * CoreApiTable::hsa_memory_register_fn
```

Definition at line 226 of file [hsa_api_trace.h](#).

6.63.2.55 hsa_queue_add_write_index_relaxed_fn

```
decltype(hsa_queue_add_write_index_relaxed) * CoreApiTable::hsa_queue_add_write_index_relaxed↵  
_fn
```

Definition at line 218 of file [hsa_api_trace.h](#).

6.63.2.56 hsa_queue_add_write_index_scacq_screl_fn

```
decltype(hsa_queue_add_write_index_scacq_screl) * CoreApiTable::hsa_queue_add_write_index_↵  
scacq_screl_fn
```

Definition at line 216 of file [hsa_api_trace.h](#).

6.63.2.57 hsa_queue_add_write_index_scacquire_fn

```
decltype(hsa_queue_add_write_index_scacquire) * CoreApiTable::hsa_queue_add_write_index_↵  
scacquire_fn
```

Definition at line 217 of file [hsa_api_trace.h](#).

6.63.2.58 hsa_queue_add_write_index_screlease_fn

```
decltype(hsa_queue_add_write_index_screlease) * CoreApiTable::hsa_queue_add_write_index_↵  
screlease_fn
```

Definition at line 219 of file [hsa_api_trace.h](#).

6.63.2.59 hsa_queue_cas_write_index_relaxed_fn

```
decltype(hsa_queue_cas_write_index_relaxed) * CoreApiTable::hsa_queue_cas_write_index_relaxed↵  
_fn
```

Definition at line 214 of file [hsa_api_trace.h](#).

6.63.2.60 hsa_queue_cas_write_index_scacq_screl_fn

```
decltype(hsa_queue_cas_write_index_scacq_screl) * CoreApiTable::hsa_queue_cas_write_index_↵  
scacq_screl_fn
```

Definition at line 212 of file [hsa_api_trace.h](#).

6.63.2.61 hsa_queue_cas_write_index_scacquire_fn

```
decltype(hsa_queue_cas_write_index_scacquire) * CoreApiTable::hsa_queue_cas_write_index_↵  
scacquire_fn
```

Definition at line 213 of file [hsa_api_trace.h](#).

6.63.2.62 hsa_queue_cas_write_index_screlease_fn

```
decltype(hsa_queue_cas_write_index_screlease) * CoreApiTable::hsa_queue_cas_write_index_↵  
screlease_fn
```

Definition at line 215 of file [hsa_api_trace.h](#).

6.63.2.63 hsa_queue_create_fn

```
decltype(hsa_queue_create) * CoreApiTable::hsa_queue_create_fn
```

Definition at line 202 of file [hsa_api_trace.h](#).

6.63.2.64 hsa_queue_destroy_fn

```
decltype(hsa_queue_destroy) * CoreApiTable::hsa_queue_destroy_fn
```

Definition at line 204 of file [hsa_api_trace.h](#).

6.63.2.65 hsa_queue_inactivate_fn

```
decltype(hsa_queue_inactivate) * CoreApiTable::hsa_queue_inactivate_fn
```

Definition at line 205 of file [hsa_api_trace.h](#).

6.63.2.66 hsa_queue_load_read_index_relaxed_fn

```
decltype(hsa_queue_load_read_index_relaxed) * CoreApiTable::hsa_queue_load_read_index_relaxed↔  
_fn
```

Definition at line 207 of file [hsa_api_trace.h](#).

6.63.2.67 hsa_queue_load_read_index_scacquire_fn

```
decltype(hsa_queue_load_read_index_scacquire) * CoreApiTable::hsa_queue_load_read_index_↔  
scacquire_fn
```

Definition at line 206 of file [hsa_api_trace.h](#).

6.63.2.68 hsa_queue_load_write_index_relaxed_fn

```
decltype(hsa_queue_load_write_index_relaxed) * CoreApiTable::hsa_queue_load_write_index_↔  
relaxed_fn
```

Definition at line 209 of file [hsa_api_trace.h](#).

6.63.2.69 hsa_queue_load_write_index_scacquire_fn

```
decltype(hsa_queue_load_write_index_scacquire) * CoreApiTable::hsa_queue_load_write_index_↔  
scacquire_fn
```

Definition at line 208 of file [hsa_api_trace.h](#).

6.63.2.70 hsa_queue_store_read_index_relaxed_fn

```
decltype(hsa_queue_store_read_index_relaxed) * CoreApiTable::hsa_queue_store_read_index_↔  
relaxed_fn
```

Definition at line 220 of file [hsa_api_trace.h](#).

6.63.2.71 hsa_queue_store_read_index_screlease_fn

```
decltype(hsa_queue_store_read_index_screlease) * CoreApiTable::hsa_queue_store_read_index_↔  
screlease_fn
```

Definition at line 221 of file [hsa_api_trace.h](#).

6.63.2.72 hsa_queue_store_write_index_relaxed_fn

```
decltype(hsa_queue_store_write_index_relaxed) * CoreApiTable::hsa_queue_store_write_index_↵  
relaxed_fn
```

Definition at line 210 of file [hsa_api_trace.h](#).

6.63.2.73 hsa_queue_store_write_index_screlease_fn

```
decltype(hsa_queue_store_write_index_screlease) * CoreApiTable::hsa_queue_store_write_index_↵  
screlease_fn
```

Definition at line 211 of file [hsa_api_trace.h](#).

6.63.2.74 hsa_region_get_info_fn

```
decltype(hsa_region_get_info) * CoreApiTable::hsa_region_get_info_fn
```

Definition at line 223 of file [hsa_api_trace.h](#).

6.63.2.75 hsa_shut_down_fn

```
decltype(hsa_shut_down) * CoreApiTable::hsa_shut_down_fn
```

Definition at line 196 of file [hsa_api_trace.h](#).

6.63.2.76 hsa_signal_add_relaxed_fn

```
decltype(hsa_signal_add_relaxed) * CoreApiTable::hsa_signal_add_relaxed_fn
```

Definition at line 256 of file [hsa_api_trace.h](#).

6.63.2.77 hsa_signal_add_scacq_screl_fn

```
decltype(hsa_signal_add_scacq_screl) * CoreApiTable::hsa_signal_add_scacq_screl_fn
```

Definition at line 259 of file [hsa_api_trace.h](#).

6.63.2.78 hsa_signal_add_scacquire_fn

```
decltype(hsa_signal_add_scacquire) * CoreApiTable::hsa_signal_add_scacquire_fn
```

Definition at line 257 of file [hsa_api_trace.h](#).

6.63.2.79 hsa_signal_add_screlease_fn

```
decltype(hsa_signal_add_screlease) * CoreApiTable::hsa_signal_add_screlease_fn
```

Definition at line 258 of file [hsa_api_trace.h](#).

6.63.2.80 hsa_signal_and_relaxed_fn

```
decltype(hsa_signal_and_relaxed) * CoreApiTable::hsa_signal_and_relaxed_fn
```

Definition at line 240 of file [hsa_api_trace.h](#).

6.63.2.81 hsa_signal_and_scacq_screl_fn

```
decltype(hsa_signal_and_scacq_screl) * CoreApiTable::hsa_signal_and_scacq_screl_fn
```

Definition at line 243 of file [hsa_api_trace.h](#).

6.63.2.82 hsa_signal_and_scacquire_fn

```
decltype(hsa_signal_and_scacquire) * CoreApiTable::hsa_signal_and_scacquire_fn
```

Definition at line 241 of file [hsa_api_trace.h](#).

6.63.2.83 hsa_signal_and_screlease_fn

```
decltype(hsa_signal_and_screlease) * CoreApiTable::hsa_signal_and_screlease_fn
```

Definition at line 242 of file [hsa_api_trace.h](#).

6.63.2.84 hsa_signal_cas_relaxed_fn

```
decltype(hsa_signal_cas_relaxed) * CoreApiTable::hsa_signal_cas_relaxed_fn
```

Definition at line 264 of file [hsa_api_trace.h](#).

6.63.2.85 hsa_signal_cas_scacq_screl_fn

```
decltype(hsa_signal_cas_scacq_screl) * CoreApiTable::hsa_signal_cas_scacq_screl_fn
```

Definition at line 267 of file [hsa_api_trace.h](#).

6.63.2.86 hsa_signal_cas_scacquire_fn

```
decltype(hsa_signal_cas_scacquire) * CoreApiTable::hsa_signal_cas_scacquire_fn
```

Definition at line 265 of file [hsa_api_trace.h](#).

6.63.2.87 hsa_signal_cas_screlease_fn

```
decltype(hsa_signal_cas_screlease) * CoreApiTable::hsa_signal_cas_screlease_fn
```

Definition at line 266 of file [hsa_api_trace.h](#).

6.63.2.88 hsa_signal_create_fn

```
decltype(hsa_signal_create) * CoreApiTable::hsa_signal_create_fn
```

Definition at line 232 of file [hsa_api_trace.h](#).

6.63.2.89 hsa_signal_destroy_fn

```
decltype(hsa_signal_destroy) * CoreApiTable::hsa_signal_destroy_fn
```

Definition at line 233 of file [hsa_api_trace.h](#).

6.63.2.90 hsa_signal_exchange_relaxed_fn

```
decltype(hsa_signal_exchange_relaxed) * CoreApiTable::hsa_signal_exchange_relaxed_fn
```

Definition at line 252 of file [hsa_api_trace.h](#).

6.63.2.91 hsa_signal_exchange_scacq_screl_fn

```
decltype(hsa_signal_exchange_scacq_screl) * CoreApiTable::hsa_signal_exchange_scacq_screl_fn
```

Definition at line 255 of file [hsa_api_trace.h](#).

6.63.2.92 hsa_signal_exchange_scacquire_fn

```
decltype(hsa_signal_exchange_scacquire) * CoreApiTable::hsa_signal_exchange_scacquire_fn
```

Definition at line 253 of file [hsa_api_trace.h](#).

6.63.2.93 hsa_signal_exchange_screlease_fn

```
decltype(hsa_signal_exchange_screlease) * CoreApiTable::hsa_signal_exchange_screlease_fn
```

Definition at line 254 of file [hsa_api_trace.h](#).

6.63.2.94 hsa_signal_group_create_fn

```
decltype(hsa_signal_group_create) * CoreApiTable::hsa_signal_group_create_fn
```

Definition at line 329 of file [hsa_api_trace.h](#).

6.63.2.95 hsa_signal_group_destroy_fn

```
decltype(hsa_signal_group_destroy) * CoreApiTable::hsa_signal_group_destroy_fn
```

Definition at line 330 of file [hsa_api_trace.h](#).

6.63.2.96 hsa_signal_group_wait_any_relaxed_fn

```
decltype(hsa_signal_group_wait_any_relaxed) * CoreApiTable::hsa_signal_group_wait_any_relaxed↵  
_fn
```

Definition at line 332 of file [hsa_api_trace.h](#).

6.63.2.97 hsa_signal_group_wait_any_scacquire_fn

```
decltype(hsa_signal_group_wait_any_scacquire) * CoreApiTable::hsa_signal_group_wait_any_↵  
scacquire_fn
```

Definition at line 331 of file [hsa_api_trace.h](#).

6.63.2.98 hsa_signal_load_relaxed_fn

```
decltype(hsa_signal_load_relaxed) * CoreApiTable::hsa_signal_load_relaxed_fn
```

Definition at line 234 of file [hsa_api_trace.h](#).

6.63.2.99 hsa_signal_load_scacquire_fn

```
decltype(hsa_signal_load_scacquire) * CoreApiTable::hsa_signal_load_scacquire_fn
```

Definition at line 235 of file [hsa_api_trace.h](#).

6.63.2.100 hsa_signal_or_relaxed_fn

```
decltype(hsa_signal_or_relaxed) * CoreApiTable::hsa_signal_or_relaxed_fn
```

Definition at line 244 of file [hsa_api_trace.h](#).

6.63.2.101 hsa_signal_or_scacq_screl_fn

```
decltype(hsa_signal_or_scacq_screl) * CoreApiTable::hsa_signal_or_scacq_screl_fn
```

Definition at line 247 of file [hsa_api_trace.h](#).

6.63.2.102 hsa_signal_or_scacquire_fn

```
decltype(hsa_signal_or_scacquire) * CoreApiTable::hsa_signal_or_scacquire_fn
```

Definition at line 245 of file [hsa_api_trace.h](#).

6.63.2.103 hsa_signal_or_screlease_fn

```
decltype(hsa_signal_or_screlease) * CoreApiTable::hsa_signal_or_screlease_fn
```

Definition at line 246 of file [hsa_api_trace.h](#).

6.63.2.104 hsa_signal_silent_store_relaxed_fn

```
decltype(hsa_signal_silent_store_relaxed) * CoreApiTable::hsa_signal_silent_store_relaxed_fn
```

Definition at line 327 of file [hsa_api_trace.h](#).

6.63.2.105 hsa_signal_silent_store_screlease_fn

```
decltype(hsa_signal_silent_store_screlease) * CoreApiTable::hsa_signal_silent_store_screlease↵_fn
```

Definition at line 328 of file [hsa_api_trace.h](#).

6.63.2.106 hsa_signal_store_relaxed_fn

```
decltype(hsa_signal_store_relaxed) * CoreApiTable::hsa_signal_store_relaxed_fn
```

Definition at line 236 of file [hsa_api_trace.h](#).

6.63.2.107 hsa_signal_store_screlease_fn

```
decltype(hsa_signal_store_screlease) * CoreApiTable::hsa_signal_store_screlease_fn
```

Definition at line 237 of file [hsa_api_trace.h](#).

6.63.2.108 hsa_signal_subtract_relaxed_fn

```
decltype(hsa_signal_subtract_relaxed) * CoreApiTable::hsa_signal_subtract_relaxed_fn
```

Definition at line 260 of file [hsa_api_trace.h](#).

6.63.2.109 hsa_signal_subtract_scacq_screl_fn

```
decltype(hsa_signal_subtract_scacq_screl) * CoreApiTable::hsa_signal_subtract_scacq_screl_fn
```

Definition at line 263 of file [hsa_api_trace.h](#).

6.63.2.110 hsa_signal_subtract_scacquire_fn

```
decltype(hsa_signal_subtract_scacquire) * CoreApiTable::hsa_signal_subtract_scacquire_fn
```

Definition at line 261 of file [hsa_api_trace.h](#).

6.63.2.111 hsa_signal_subtract_screlease_fn

```
decltype(hsa_signal_subtract_screlease) * CoreApiTable::hsa_signal_subtract_screlease_fn
```

Definition at line 262 of file [hsa_api_trace.h](#).

6.63.2.112 hsa_signal_wait_relaxed_fn

```
decltype(hsa_signal_wait_relaxed) * CoreApiTable::hsa_signal_wait_relaxed_fn
```

Definition at line 238 of file [hsa_api_trace.h](#).

6.63.2.113 hsa_signal_wait_scacquire_fn

```
decltype(hsa_signal_wait_scacquire) * CoreApiTable::hsa_signal_wait_scacquire_fn
```

Definition at line 239 of file [hsa_api_trace.h](#).

6.63.2.114 hsa_signal_xor_relaxed_fn

```
decltype(hsa_signal_xor_relaxed) * CoreApiTable::hsa_signal_xor_relaxed_fn
```

Definition at line 248 of file [hsa_api_trace.h](#).

6.63.2.115 hsa_signal_xor_scacq_screl_fn

```
decltype(hsa_signal_xor_scacq_screl) * CoreApiTable::hsa_signal_xor_scacq_screl_fn
```

Definition at line 251 of file [hsa_api_trace.h](#).

6.63.2.116 hsa_signal_xor_scacquire_fn

```
decltype(hsa_signal_xor_scacquire) * CoreApiTable::hsa_signal_xor_scacquire_fn
```

Definition at line 249 of file [hsa_api_trace.h](#).

6.63.2.117 hsa_signal_xor_screlease_fn

```
decltype(hsa_signal_xor_screlease) * CoreApiTable::hsa_signal_xor_screlease_fn
```

Definition at line 250 of file [hsa_api_trace.h](#).

6.63.2.118 hsa_soft_queue_create_fn

```
decltype(hsa_soft_queue_create) * CoreApiTable::hsa_soft_queue_create_fn
```

Definition at line 203 of file [hsa_api_trace.h](#).

6.63.2.119 hsa_status_string_fn

```
decltype(hsa_status_string) * CoreApiTable::hsa_status_string_fn
```

Definition at line 318 of file [hsa_api_trace.h](#).

6.63.2.120 hsa_system_extension_supported_fn

```
decltype(hsa_system_extension_supported) * CoreApiTable::hsa_system_extension_supported_fn
```

Definition at line 198 of file [hsa_api_trace.h](#).

6.63.2.121 hsa_system_get_extension_table_fn

```
decltype(hsa_system_get_extension_table) * CoreApiTable::hsa_system_get_extension_table_fn
```

Definition at line 199 of file [hsa_api_trace.h](#).

6.63.2.122 hsa_system_get_info_fn

```
decltype(hsa_system_get_info) * CoreApiTable::hsa_system_get_info_fn
```

Definition at line 197 of file [hsa_api_trace.h](#).

6.63.2.123 hsa_system_get_major_extension_table_fn

```
decltype(hsa_system_get_major_extension_table) * CoreApiTable::hsa_system_get_major_extension↵  
_table_fn
```

Definition at line 323 of file [hsa_api_trace.h](#).

6.63.2.124 hsa_system_major_extension_supported_fn

```
decltype(hsa_system_major_extension_supported) * CoreApiTable::hsa_system_major_extension↵  
supported_fn
```

Definition at line 322 of file [hsa_api_trace.h](#).

6.63.2.125 hsa_wavefront_get_info_fn

```
decltype(hsa_wavefront_get_info) * CoreApiTable::hsa_wavefront_get_info_fn
```

Definition at line 340 of file [hsa_api_trace.h](#).

6.63.2.126 version

`ApiTableVersion` `CoreApiTable::version`

Definition at line 194 of file [hsa_api_trace.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/hsa_api_trace.h`

6.64 FinalizerExtTable Struct Reference

Collaboration diagram for FinalizerExtTable:

Public Attributes

- [ApiTableVersion](#) `version`
- `decltype(hsa_ext_program_create) * hsa_ext_program_create_fn`
- `decltype(hsa_ext_program_destroy) * hsa_ext_program_destroy_fn`
- `decltype(hsa_ext_program_add_module) * hsa_ext_program_add_module_fn`
- `decltype(hsa_ext_program_iterate_modules) * hsa_ext_program_iterate_modules_fn`
- `decltype(hsa_ext_program_get_info) * hsa_ext_program_get_info_fn`
- `decltype(hsa_ext_program_finalize) * hsa_ext_program_finalize_fn`

6.64.1 Detailed Description

Definition at line 111 of file [hsa_api_trace.h](#).

6.64.2 Member Data Documentation

6.64.2.1 hsa_ext_program_add_module_fn

`decltype(hsa_ext_program_add_module) * FinalizerExtTable::hsa_ext_program_add_module_fn`

Definition at line 115 of file [hsa_api_trace.h](#).

6.64.2.2 hsa_ext_program_create_fn

`decltype(hsa_ext_program_create) * FinalizerExtTable::hsa_ext_program_create_fn`

Definition at line 113 of file [hsa_api_trace.h](#).

6.64.2.3 hsa_ext_program_destroy_fn

```
decltype(hsa_ext_program_destroy) * FinalizerExtTable::hsa_ext_program_destroy_fn
```

Definition at line 114 of file [hsa_api_trace.h](#).

6.64.2.4 hsa_ext_program_finalize_fn

```
decltype(hsa_ext_program_finalize) * FinalizerExtTable::hsa_ext_program_finalize_fn
```

Definition at line 118 of file [hsa_api_trace.h](#).

6.64.2.5 hsa_ext_program_get_info_fn

```
decltype(hsa_ext_program_get_info) * FinalizerExtTable::hsa_ext_program_get_info_fn
```

Definition at line 117 of file [hsa_api_trace.h](#).

6.64.2.6 hsa_ext_program_iterate_modules_fn

```
decltype(hsa_ext_program_iterate_modules) * FinalizerExtTable::hsa_ext_program_iterate_↵  
modules_fn
```

Definition at line 116 of file [hsa_api_trace.h](#).

6.64.2.7 version

```
ApiTableVersion FinalizerExtTable::version
```

Definition at line 112 of file [hsa_api_trace.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa_api_trace.h](#)

6.65 hsa_agent_dispatch_packet_s Struct Reference

Agent dispatch packet.

```
#include <hsa.h>
```

Collaboration diagram for hsa_agent_dispatch_packet_s:

Public Attributes

- `uint16_t` [header](#)
- `uint16_t` [type](#)
- `uint32_t` [reserved0](#)
- `uint32_t` [reserved1](#)
- `void *` [return_address](#)
- `uint64_t` [arg](#) [4]
- `uint64_t` [reserved2](#)
- `hsa_signal_t` [completion_signal](#)

6.65.1 Detailed Description

Agent dispatch packet.

Definition at line [3029](#) of file [hsa.h](#).

6.65.2 Member Data Documentation

6.65.2.1 `arg`

```
uint64_t hsa_agent_dispatch_packet_s::arg[4]
```

Function arguments.

Definition at line [3065](#) of file [hsa.h](#).

6.65.2.2 `completion_signal`

```
hsa_signal_t hsa_agent_dispatch_packet_s::completion_signal
```

Signal used to indicate completion of the job. The application can use the special signal handle 0 to indicate that no signal is used.

Definition at line [3076](#) of file [hsa.h](#).

6.65.2.3 `header`

```
uint16_t hsa_agent_dispatch_packet_s::header
```

Packet header. Used to configure multiple packet parameters such as the packet type. The parameters are described by [hsa_packet_header_t](#).

Definition at line [3034](#) of file [hsa.h](#).

6.65.2.4 reserved0

```
uint32_t hsa_agent_dispatch_packet_s::reserved0
```

Reserved. Must be 0.

Definition at line 3044 of file [hsa.h](#).

6.65.2.5 reserved1

```
uint32_t hsa_agent_dispatch_packet_s::reserved1
```

Definition at line 3058 of file [hsa.h](#).

6.65.2.6 reserved2

```
uint64_t hsa_agent_dispatch_packet_s::reserved2
```

Reserved. Must be 0.

Definition at line 3070 of file [hsa.h](#).

6.65.2.7 return_address

```
void* hsa_agent_dispatch_packet_s::return_address
```

Definition at line 3059 of file [hsa.h](#).

6.65.2.8 type

```
uint16_t hsa_agent_dispatch_packet_s::type
```

Application-defined function to be performed by the destination agent.

Definition at line 3039 of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/hsa.h`

6.66 hsa_agent_s Struct Reference

Struct containing an opaque handle to an agent, a device that participates in the HSA memory model. An agent can submit AQL packets for execution, and may also accept AQL packets for execution (agent dispatch packets or kernel dispatch packets launching HSAIL-derived binaries).

```
#include <hsa.h>
```

Public Attributes

- uint64_t [handle](#)

6.66.1 Detailed Description

Struct containing an opaque handle to an agent, a device that participates in the HSA memory model. An agent can submit AQL packets for execution, and may also accept AQL packets for execution (agent dispatch packets or kernel dispatch packets launching HSAIL-derived binaries).

Definition at line [741](#) of file [hsa.h](#).

6.66.2 Member Data Documentation

6.66.2.1 handle

```
uint64_t hsa_agent_s::handle
```

Opaque handle. Two handles reference the same object of the enclosing type if and only if they are equal.

Definition at line [746](#) of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa.h](#)

6.67 hsa_amd_barrier_value_packet_s Struct Reference

AMD barrier value packet. Halts packet processing and waits for (signal_value & [mask](#)) [cond value](#) to be satisfied, where signal_value is the value of the signal [signal](#).

```
#include <hsa_ext_amd.h>
```

Collaboration diagram for hsa_amd_barrier_value_packet_s:

Public Attributes

- [hsa_amd_vendor_packet_header_t](#) header
- [uint32_t](#) reserved0
- [hsa_signal_t](#) signal
- [hsa_signal_value_t](#) value
- [hsa_signal_value_t](#) mask
- [hsa_signal_condition32_t](#) cond
- [uint32_t](#) reserved1
- [uint64_t](#) reserved2
- [uint64_t](#) reserved3
- [hsa_signal_t](#) completion_signal

6.67.1 Detailed Description

AMD barrier value packet. Halts packet processing and waits for (signal_value & [mask](#)) [cond value](#) to be satisfied, where signal_value is the value of the signal [signal](#).

Definition at line 110 of file [hsa_ext_amd.h](#).

6.67.2 Member Data Documentation

6.67.2.1 completion_signal

```
hsa\_signal\_t hsa_amd_barrier_value_packet_s::completion_signal
```

Signal used to indicate completion of the job. The application can use the special signal handle 0 to indicate that no signal is used.

Definition at line 162 of file [hsa_ext_amd.h](#).

6.67.2.2 cond

```
hsa\_signal\_condition32\_t hsa_amd_barrier_value_packet_s::cond
```

Comparison operation. See [hsa_signal_condition_t](#).

Definition at line 141 of file [hsa_ext_amd.h](#).

6.67.2.3 header

```
hsa_amd_vendor_packet_header_t hsa_amd_barrier_value_packet_s::header
```

AMD vendor specific packet header.

Definition at line 114 of file [hsa_ext_amd.h](#).

6.67.2.4 mask

```
hsa_signal_value_t hsa_amd_barrier_value_packet_s::mask
```

Bit mask to be combined by bitwise AND with [signal](#)'s value.

Definition at line 136 of file [hsa_ext_amd.h](#).

6.67.2.5 reserved0

```
uint32_t hsa_amd_barrier_value_packet_s::reserved0
```

Reserved. Must be 0.

Definition at line 119 of file [hsa_ext_amd.h](#).

6.67.2.6 reserved1

```
uint32_t hsa_amd_barrier_value_packet_s::reserved1
```

Reserved. Must be 0.

Definition at line 146 of file [hsa_ext_amd.h](#).

6.67.2.7 reserved2

```
uint64_t hsa_amd_barrier_value_packet_s::reserved2
```

Reserved. Must be 0.

Definition at line 151 of file [hsa_ext_amd.h](#).

6.67.2.8 reserved3

```
uint64_t hsa_amd_barrier_value_packet_s::reserved3
```

Reserved. Must be 0.

Definition at line 156 of file [hsa_ext_amd.h](#).

6.67.2.9 signal

```
hsa_signal_t hsa_amd_barrier_value_packet_s::signal
```

Dependent signal object. A signal with a handle value of 0 is allowed and is interpreted by the packet processor as a satisfied dependency.

Definition at line 126 of file [hsa_ext_amd.h](#).

6.67.2.10 value

```
hsa_signal_value_t hsa_amd_barrier_value_packet_s::value
```

Value to compare against.

Definition at line 131 of file [hsa_ext_amd.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa_ext_amd.h](#)

6.68 hsa_amd_event_s Struct Reference

AMD GPU event data passed to event handler.

```
#include <hsa_ext_amd.h>
```

Collaboration diagram for `hsa_amd_event_s`:

Public Attributes

- `hsa_amd_event_type_t` [event_type](#)
- ```
union {
 hsa_amd_gpu_memory_fault_info_t memory_fault
};
```

### 6.68.1 Detailed Description

AMD GPU event data passed to event handler.

Definition at line 2099 of file [hsa\\_ext\\_amd.h](#).

### 6.68.2 Member Data Documentation

#### 6.68.2.1 event\_type

`hsa_amd_event_type_t hsa_amd_event_s::event_type`

Definition at line 2103 of file [hsa\\_ext\\_amd.h](#).

#### 6.68.2.2 memory\_fault

`hsa_amd_gpu_memory_fault_info_t hsa_amd_event_s::memory_fault`

Definition at line 2108 of file [hsa\\_ext\\_amd.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/hsa_ext_amd.h`

## 6.69 hsa\_amd\_gpu\_memory\_fault\_info\_s Struct Reference

AMD GPU memory fault event data.

```
#include <hsa_ext_amd.h>
```

Collaboration diagram for `hsa_amd_gpu_memory_fault_info_s`:

### Public Attributes

- [hsa\\_agent\\_t](#) `agent`
- [uint64\\_t](#) `virtual_address`
- [uint32\\_t](#) `fault_reason_mask`

### 6.69.1 Detailed Description

AMD GPU memory fault event data.

Definition at line 2080 of file [hsa\\_ext\\_amd.h](#).

## 6.69.2 Member Data Documentation

### 6.69.2.1 agent

[hsa\\_agent\\_t](#) hsa\_amd\_gpu\_memory\_fault\_info\_s::agent

Definition at line 2084 of file [hsa\\_ext\\_amd.h](#).

### 6.69.2.2 fault\_reason\_mask

uint32\_t hsa\_amd\_gpu\_memory\_fault\_info\_s::fault\_reason\_mask

Definition at line 2093 of file [hsa\\_ext\\_amd.h](#).

### 6.69.2.3 virtual\_address

uint64\_t hsa\_amd\_gpu\_memory\_fault\_info\_s::virtual\_address

Definition at line 2088 of file [hsa\\_ext\\_amd.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_amd.h](#)

## 6.70 hsa\_amd\_hdp\_flush\_s Struct Reference

### Public Attributes

- [uint32\\_t](#) \* [HDP\\_MEM\\_FLUSH\\_CNTL](#)
- [uint32\\_t](#) \* [HDP\\_REG\\_FLUSH\\_CNTL](#)

### 6.70.1 Detailed Description

Definition at line 342 of file [hsa\\_ext\\_amd.h](#).

### 6.70.2 Member Data Documentation

### 6.70.2.1 HDP\_MEM\_FLUSH\_CNTL

```
uint32_t* hsa_amd_hdp_flush_s::HDP_MEM_FLUSH_CNTL
```

Definition at line 343 of file [hsa\\_ext\\_amd.h](#).

### 6.70.2.2 HDP\_REG\_FLUSH\_CNTL

```
uint32_t* hsa_amd_hdp_flush_s::HDP_REG_FLUSH_CNTL
```

Definition at line 344 of file [hsa\\_ext\\_amd.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_amd.h](#)

## 6.71 hsa\_amd\_image\_descriptor\_s Struct Reference

Encodes an opaque vendor specific image format. The length of data depends on the underlying format. This structure must not be copied as its true length can not be determined.

```
#include <hsa_ext_amd.h>
```

### Public Attributes

- uint32\_t [version](#)
- uint32\_t [deviceId](#)
- uint32\_t [data](#) [1]

### 6.71.1 Detailed Description

Encodes an opaque vendor specific image format. The length of data depends on the underlying format. This structure must not be copied as its true length can not be determined.

Definition at line 1709 of file [hsa\\_ext\\_amd.h](#).

### 6.71.2 Member Data Documentation

#### 6.71.2.1 data

```
uint32_t hsa_amd_image_descriptor_s::data[1]
```

Definition at line 1723 of file [hsa\\_ext\\_amd.h](#).

### 6.71.2.2 deviceID

```
uint32_t hsa_amd_image_descriptor_s::deviceID
```

Definition at line 1718 of file [hsa\\_ext\\_amd.h](#).

### 6.71.2.3 version

```
uint32_t hsa_amd_image_descriptor_s::version
```

Definition at line 1713 of file [hsa\\_ext\\_amd.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_amd.h](#)

## 6.72 hsa\_amd\_ipc\_memory\_s Struct Reference

256-bit process independent identifier for a ROCr shared memory allocation.

```
#include <hsa_ext_amd.h>
```

### Public Attributes

- `uint32_t` [handle](#) [8]

### 6.72.1 Detailed Description

256-bit process independent identifier for a ROCr shared memory allocation.

Definition at line 1901 of file [hsa\\_ext\\_amd.h](#).

### 6.72.2 Member Data Documentation

#### 6.72.2.1 handle

```
uint32_t hsa_amd_ipc_memory_s::handle[8]
```

Definition at line 1902 of file [hsa\\_ext\\_amd.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_amd.h](#)

## 6.73 hsa\_amd\_memory\_pool\_link\_info\_s Struct Reference

Link properties when accessing the memory pool from the specified agent.

```
#include <hsa_ext_amd.h>
```

### Public Attributes

- uint32\_t [min\\_latency](#)
- uint32\_t [max\\_latency](#)
- uint32\_t [min\\_bandwidth](#)
- uint32\_t [max\\_bandwidth](#)
- bool [atomic\\_support\\_32bit](#)
- bool [atomic\\_support\\_64bit](#)
- bool [coherent\\_support](#)
- hsa\_amd\_link\_info\_type\_t [link\\_type](#)
- uint32\_t [numa\\_distance](#)

### 6.73.1 Detailed Description

Link properties when accessing the memory pool from the specified agent.

Definition at line [1297](#) of file [hsa\\_ext\\_amd.h](#).

### 6.73.2 Member Data Documentation

#### 6.73.2.1 atomic\_support\_32bit

```
bool hsa_amd_memory_pool_link_info_s::atomic_support_32bit
```

Support for 32-bit atomic transactions.

Definition at line [1321](#) of file [hsa\\_ext\\_amd.h](#).

#### 6.73.2.2 atomic\_support\_64bit

```
bool hsa_amd_memory_pool_link_info_s::atomic_support_64bit
```

Support for 64-bit atomic transactions.

Definition at line [1326](#) of file [hsa\\_ext\\_amd.h](#).

### 6.73.2.3 coherent\_support

```
bool hsa_amd_memory_pool_link_info_s::coherent_support
```

Support for cache coherent transactions.

Definition at line 1331 of file [hsa\\_ext\\_amd.h](#).

### 6.73.2.4 link\_type

```
hsa_amd_link_info_type_t hsa_amd_memory_pool_link_info_s::link_type
```

The type of bus/link.

Definition at line 1336 of file [hsa\\_ext\\_amd.h](#).

### 6.73.2.5 max\_bandwidth

```
uint32_t hsa_amd_memory_pool_link_info_s::max_bandwidth
```

Maximum link interface bandwidth in MB/s.

Definition at line 1316 of file [hsa\\_ext\\_amd.h](#).

### 6.73.2.6 max\_latency

```
uint32_t hsa_amd_memory_pool_link_info_s::max_latency
```

Maximum transfer latency (rounded to ns).

Definition at line 1306 of file [hsa\\_ext\\_amd.h](#).

### 6.73.2.7 min\_bandwidth

```
uint32_t hsa_amd_memory_pool_link_info_s::min_bandwidth
```

Minimum link interface bandwidth in MB/s.

Definition at line 1311 of file [hsa\\_ext\\_amd.h](#).

### 6.73.2.8 min\_latency

```
uint32_t hsa_amd_memory_pool_link_info_s::min_latency
```

Minimum transfer latency (rounded to ns).

Definition at line 1301 of file [hsa\\_ext\\_amd.h](#).

### 6.73.2.9 numa\_distance

```
uint32_t hsa_amd_memory_pool_link_info_s::numa_distance
```

NUMA distance of memory pool relative to querying agent

Definition at line 1341 of file [hsa\\_ext\\_amd.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_amd.h](#)

## 6.74 hsa\_amd\_memory\_pool\_s Struct Reference

A memory pool encapsulates physical storage on an agent along with a memory access model.

```
#include <hsa_ext_amd.h>
```

### Public Attributes

- `uint64_t` [handle](#)

### 6.74.1 Detailed Description

A memory pool encapsulates physical storage on an agent along with a memory access model.

A memory pool encapsulates a physical partition of an agent's memory system along with a memory access model. Division of a single memory system into separate pools allows querying each partition's access path properties (see `hsa_amd_agent_memory_pool_get_info`). Allocations from a pool are preferentially bound to that pool's physical partition. Binding to the pool's preferential physical partition may not be possible or persistent depending on the system's memory policy and/or state which is beyond the scope of HSA APIs.

For example, a multi-node NUMA memory system may be represented by multiple pool's with each pool providing size and access path information for the partition it represents. Allocations from a pool are preferentially bound to the pool's partition (which in this example is a NUMA node) while following its memory access model. The actual placement may vary or migrate due to the system's NUMA policy and state, which is beyond the scope of HSA APIs.

Definition at line 923 of file [hsa\\_ext\\_amd.h](#).



## 6.74.2 Member Data Documentation

### 6.74.2.1 handle

```
uint64_t hsa_amd_memory_pool_s::handle
```

Opaque handle.

Definition at line 927 of file [hsa\\_ext\\_amd.h](#).

The documentation for this struct was generated from the following file:

- /home/alexv/Programming/ROCR-Runtime/include/hsa\_ext\_amd.h

## 6.75 hsa\_amd\_packet\_header\_s Struct Reference

AMD vendor specific AQL packet header.

```
#include <hsa_ext_amd.h>
```

### Public Attributes

- uint16\_t [header](#)
- [hsa\\_amd\\_packet\\_type8\\_t](#) AmdFormat
- uint8\_t [reserved](#)

### 6.75.1 Detailed Description

AMD vendor specific AQL packet header.

Definition at line 87 of file [hsa\\_ext\\_amd.h](#).

## 6.75.2 Member Data Documentation

### 6.75.2.1 AmdFormat

```
hsa_amd_packet_type8_t hsa_amd_packet_header_s::AmdFormat
```

Format of the vendor specific packet.

Definition at line 97 of file [hsa\\_ext\\_amd.h](#).

### 6.75.2.2 header

```
uint16_t hsa_amd_packet_header_s::header
```

Packet header. Used to configure multiple packet parameters such as the packet type. The parameters are described by [hsa\\_packet\\_header\\_t](#).

Definition at line 92 of file [hsa\\_ext\\_amd.h](#).

### 6.75.2.3 reserved

```
uint8_t hsa_amd_packet_header_s::reserved
```

Reserved. Must be 0.

Definition at line 102 of file [hsa\\_ext\\_amd.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_amd.h](#)

## 6.76 hsa\_amd\_pointer\_info\_s Struct Reference

Describes a memory allocation known to ROCr. Within a ROCr major version this structure can only grow.

```
#include <hsa_ext_amd.h>
```

Collaboration diagram for `hsa_amd_pointer_info_s`:

### Public Attributes

- `uint32_t` [size](#)
- `hsa_amd_pointer_type_t` [type](#)
- `void *` [agentBaseAddress](#)
- `void *` [hostBaseAddress](#)
- `size_t` [sizeInBytes](#)
- `void *` [userData](#)
- [hsa\\_agent\\_t](#) [agentOwner](#)
- `uint32_t` [global\\_flags](#)

### 6.76.1 Detailed Description

Describes a memory allocation known to ROCr. Within a ROCr major version this structure can only grow.

Definition at line 1794 of file [hsa\\_ext\\_amd.h](#).

## 6.76.2 Member Data Documentation

### 6.76.2.1 agentBaseAddress

```
void* hsa_amd_pointer_info_s::agentBaseAddress
```

Definition at line 1810 of file [hsa\\_ext\\_amd.h](#).

### 6.76.2.2 agentOwner

```
hsa_agent_t hsa_amd_pointer_info_s::agentOwner
```

Definition at line 1828 of file [hsa\\_ext\\_amd.h](#).

### 6.76.2.3 global\_flags

```
uint32_t hsa_amd_pointer_info_s::global_flags
```

Definition at line 1834 of file [hsa\\_ext\\_amd.h](#).

### 6.76.2.4 hostBaseAddress

```
void* hsa_amd_pointer_info_s::hostBaseAddress
```

Definition at line 1814 of file [hsa\\_ext\\_amd.h](#).

### 6.76.2.5 size

```
uint32_t hsa_amd_pointer_info_s::size
```

Definition at line 1802 of file [hsa\\_ext\\_amd.h](#).

### 6.76.2.6 `sizeInBytes`

```
size_t hsa_amd_pointer_info_s::sizeInBytes
```

Definition at line 1818 of file [hsa\\_ext\\_amd.h](#).

### 6.76.2.7 `type`

```
hsa_amd_pointer_type_t hsa_amd_pointer_info_s::type
```

Definition at line 1806 of file [hsa\\_ext\\_amd.h](#).

### 6.76.2.8 `userData`

```
void* hsa_amd_pointer_info_s::userData
```

Definition at line 1822 of file [hsa\\_ext\\_amd.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_amd.h](#)

## 6.77 `hsa_amd_profiling_async_copy_time_s` Struct Reference

Structure containing profiling async copy time information.

```
#include <hsa_ext_amd.h>
```

### Public Attributes

- `uint64_t` [start](#)
- `uint64_t` [end](#)

#### 6.77.1 Detailed Description

Structure containing profiling async copy time information.

Times are reported as ticks in the domain of the HSA system clock. The HSA system clock tick and frequency is obtained via `hsa_system_get_info`.

Definition at line 450 of file [hsa\\_ext\\_amd.h](#).

## 6.77.2 Member Data Documentation

### 6.77.2.1 end

```
uint64_t hsa_amd_profiling_async_copy_time_s::end
```

Async copy completion time.

Definition at line 458 of file [hsa\\_ext\\_amd.h](#).

### 6.77.2.2 start

```
uint64_t hsa_amd_profiling_async_copy_time_s::start
```

Async copy processing start time.

Definition at line 454 of file [hsa\\_ext\\_amd.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_amd.h](#)

## 6.78 hsa\_amd\_profiling\_dispatch\_time\_s Struct Reference

Structure containing profiling dispatch time information.

```
#include <hsa_ext_amd.h>
```

### Public Attributes

- uint64\_t [start](#)
- uint64\_t [end](#)

### 6.78.1 Detailed Description

Structure containing profiling dispatch time information.

Times are reported as ticks in the domain of the HSA system clock. The HSA system clock tick and frequency is obtained via [hsa\\_system\\_get\\_info](#).

Definition at line 433 of file [hsa\\_ext\\_amd.h](#).

## 6.78.2 Member Data Documentation

### 6.78.2.1 end

`uint64_t hsa_amd_profiling_dispatch_time_s::end`

Dispatch packet completion time.

Definition at line 441 of file [hsa\\_ext\\_amd.h](#).

### 6.78.2.2 start

`uint64_t hsa_amd_profiling_dispatch_time_s::start`

Dispatch packet processing start time.

Definition at line 437 of file [hsa\\_ext\\_amd.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_amd.h](#)

## 6.79 hsa\_amd\_svm\_attribute\_pair\_s Struct Reference

### Public Attributes

- `uint64_t` [attribute](#)
- `uint64_t` [value](#)

### 6.79.1 Detailed Description

Definition at line 2323 of file [hsa\\_ext\\_amd.h](#).

## 6.79.2 Member Data Documentation

### 6.79.2.1 attribute

`uint64_t hsa_amd_svm_attribute_pair_s::attribute`

Definition at line 2325 of file [hsa\\_ext\\_amd.h](#).

### 6.79.2.2 value

```
uint64_t hsa_amd_svm_attribute_pair_s::value
```

Definition at line 2328 of file [hsa\\_ext\\_amd.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_amd.h](#)

## 6.80 hsa\_barrier\_and\_packet\_s Struct Reference

Barrier-AND packet.

```
#include <hsa.h>
```

Collaboration diagram for `hsa_barrier_and_packet_s`:

### Public Attributes

- `uint16_t` [header](#)
- `uint16_t` [reserved0](#)
- `uint32_t` [reserved1](#)
- `hsa_signal_t` [dep\\_signal](#) [5]
- `uint64_t` [reserved2](#)
- `hsa_signal_t` [completion\\_signal](#)

### 6.80.1 Detailed Description

Barrier-AND packet.

Definition at line 3083 of file [hsa.h](#).

### 6.80.2 Member Data Documentation

#### 6.80.2.1 completion\_signal

```
hsa_signal_t hsa_barrier_and_packet_s::completion_signal
```

Signal used to indicate completion of the job. The application can use the special signal handle 0 to indicate that no signal is used.

Definition at line 3116 of file [hsa.h](#).

### 6.80.2.2 dep\_signal

```
hsa_signal_t hsa_barrier_and_packet_s::dep_signal[5]
```

Array of dependent signal objects. Signals with a handle value of 0 are allowed and are interpreted by the packet processor as satisfied dependencies.

Definition at line 3105 of file [hsa.h](#).

### 6.80.2.3 header

```
uint16_t hsa_barrier_and_packet_s::header
```

Packet header. Used to configure multiple packet parameters such as the packet type. The parameters are described by [hsa\\_packet\\_header\\_t](#).

Definition at line 3088 of file [hsa.h](#).

### 6.80.2.4 reserved0

```
uint16_t hsa_barrier_and_packet_s::reserved0
```

Reserved. Must be 0.

Definition at line 3093 of file [hsa.h](#).

### 6.80.2.5 reserved1

```
uint32_t hsa_barrier_and_packet_s::reserved1
```

Reserved. Must be 0.

Definition at line 3098 of file [hsa.h](#).

### 6.80.2.6 reserved2

```
uint64_t hsa_barrier_and_packet_s::reserved2
```

Reserved. Must be 0.

Definition at line 3110 of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa.h](#)



## 6.81 hsa\_barrier\_or\_packet\_s Struct Reference

Barrier-OR packet.

```
#include <hsa.h>
```

Collaboration diagram for hsa\_barrier\_or\_packet\_s:

### Public Attributes

- `uint16_t` [header](#)
- `uint16_t` [reserved0](#)
- `uint32_t` [reserved1](#)
- `hsa_signal_t` [dep\\_signal](#) [5]
- `uint64_t` [reserved2](#)
- `hsa_signal_t` [completion\\_signal](#)

### 6.81.1 Detailed Description

Barrier-OR packet.

Definition at line [3123](#) of file [hsa.h](#).

### 6.81.2 Member Data Documentation

#### 6.81.2.1 completion\_signal

```
hsa_signal_t hsa_barrier_or_packet_s::completion_signal
```

Signal used to indicate completion of the job. The application can use the special signal handle 0 to indicate that no signal is used.

Definition at line [3156](#) of file [hsa.h](#).

#### 6.81.2.2 dep\_signal

```
hsa_signal_t hsa_barrier_or_packet_s::dep_signal[5]
```

Array of dependent signal objects. Signals with a handle value of 0 are allowed and are interpreted by the packet processor as dependencies not satisfied.

Definition at line [3145](#) of file [hsa.h](#).

### 6.81.2.3 header

```
uint16_t hsa_barrier_or_packet_s::header
```

Packet header. Used to configure multiple packet parameters such as the packet type. The parameters are described by [hsa\\_packet\\_header\\_t](#).

Definition at line [3128](#) of file [hsa.h](#).

### 6.81.2.4 reserved0

```
uint16_t hsa_barrier_or_packet_s::reserved0
```

Reserved. Must be 0.

Definition at line [3133](#) of file [hsa.h](#).

### 6.81.2.5 reserved1

```
uint32_t hsa_barrier_or_packet_s::reserved1
```

Reserved. Must be 0.

Definition at line [3138](#) of file [hsa.h](#).

### 6.81.2.6 reserved2

```
uint64_t hsa_barrier_or_packet_s::reserved2
```

Reserved. Must be 0.

Definition at line [3150](#) of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa.h](#)

## 6.82 hsa\_cache\_s Struct Reference

Cache handle.

```
#include <hsa.h>
```

## Public Attributes

- uint64\_t [handle](#)

### 6.82.1 Detailed Description

Cache handle.

Definition at line 1149 of file [hsa.h](#).

### 6.82.2 Member Data Documentation

#### 6.82.2.1 handle

```
uint64_t hsa_cache_s::handle
```

Opaque handle. Two handles reference the same object of the enclosing type if and only if they are equal.

Definition at line 1154 of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- /home/alexv/Programming/ROCR-Runtime/include/hsa.h

## 6.83 hsa\_callback\_data\_s Struct Reference

Application data handle that is passed to the serialization and deserialization functions.

```
#include <hsa.h>
```

## Public Attributes

- uint64\_t [handle](#)

### 6.83.1 Detailed Description

Application data handle that is passed to the serialization and deserialization functions.

## Deprecated

Definition at line 5143 of file [hsa.h](#).

## 6.83.2 Member Data Documentation

### 6.83.2.1 handle

```
uint64_t hsa_callback_data_s::handle
```

Opaque handle.

Definition at line 5147 of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa.h](#)

## 6.84 hsa\_code\_object\_reader\_s Struct Reference

Code object reader handle. A code object reader is used to load a code object from file (when created using [hsa\\_code\\_object\\_reader\\_create\\_from\\_file](#)), or from memory (if created using [hsa\\_code\\_object\\_reader\\_create\\_from\\_memory](#)).

```
#include <hsa.h>
```

### Public Attributes

- uint64\_t [handle](#)

### 6.84.1 Detailed Description

Code object reader handle. A code object reader is used to load a code object from file (when created using [hsa\\_code\\_object\\_reader\\_create\\_from\\_file](#)), or from memory (if created using [hsa\\_code\\_object\\_reader\\_create\\_from\\_memory](#)).

Definition at line 4019 of file [hsa.h](#).

## 6.84.2 Member Data Documentation

### 6.84.2.1 handle

```
uint64_t hsa_code_object_reader_s::handle
```

Opaque handle. Two handles reference the same object of the enclosing type if and only if they are equal.

Definition at line 4024 of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa.h](#)

## 6.85 hsa\_code\_object\_s Struct Reference

Struct containing an opaque handle to a code object, which contains ISA for finalized kernels and indirect functions together with information about the global or readonly segment variables they reference.

```
#include <hsa.h>
```

### Public Attributes

- uint64\_t [handle](#)

### 6.85.1 Detailed Description

Struct containing an opaque handle to a code object, which contains ISA for finalized kernels and indirect functions together with information about the global or readonly segment variables they reference.

#### Deprecated

Definition at line 5129 of file [hsa.h](#).

### 6.85.2 Member Data Documentation

#### 6.85.2.1 handle

```
uint64_t hsa_code_object_s::handle
```

Opaque handle. Two handles reference the same object of the enclosing type if and only if they are equal.

Definition at line 5134 of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- /home/alexv/Programming/ROCR-Runtime/include/hsa.h

## 6.86 hsa\_code\_symbol\_s Struct Reference

Code object symbol handle.

```
#include <hsa.h>
```

### Public Attributes

- uint64\_t [handle](#)

### 6.86.1 Detailed Description

Code object symbol handle.

#### Deprecated

The lifetime of a code object symbol matches that of the code object associated with it. An operation on a symbol whose associated code object has been destroyed results in undefined behavior.

Definition at line 5412 of file [hsa.h](#).

### 6.86.2 Member Data Documentation

#### 6.86.2.1 handle

```
uint64_t hsa_code_symbol_s::handle
```

Opaque handle. Two handles reference the same object of the enclosing type if and only if they are equal.

Definition at line 5417 of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa.h](#)

## 6.87 hsa\_dim3\_s Struct Reference

Three-dimensional coordinate.

```
#include <hsa.h>
```

### Public Attributes

- [uint32\\_t x](#)
- [uint32\\_t y](#)
- [uint32\\_t z](#)

#### 6.87.1 Detailed Description

Three-dimensional coordinate.

Definition at line 298 of file [hsa.h](#).

## 6.87.2 Member Data Documentation

### 6.87.2.1 x

```
uint32_t hsa_dim3_s::x
```

X dimension.

Definition at line 302 of file [hsa.h](#).

### 6.87.2.2 y

```
uint32_t hsa_dim3_s::y
```

Y dimension.

Definition at line 307 of file [hsa.h](#).

### 6.87.2.3 z

```
uint32_t hsa_dim3_s::z
```

Z dimension.

Definition at line 312 of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa.h](#)

## 6.88 hsa\_executable\_s Struct Reference

Struct containing an opaque handle to an executable, which contains ISA for finalized kernels and indirect functions together with the allocated global or readonly segment variables they reference.

```
#include <hsa.h>
```

### Public Attributes

- uint64\_t [handle](#)

### 6.88.1 Detailed Description

Struct containing an opaque handle to an executable, which contains ISA for finalized kernels and indirect functions together with the allocated global or readonly segment variables they reference.

Definition at line 4110 of file [hsa.h](#).

### 6.88.2 Member Data Documentation

#### 6.88.2.1 handle

```
uint64_t hsa_executable_s::handle
```

Opaque handle. Two handles reference the same object of the enclosing type if and only if they are equal.

Definition at line 4115 of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa.h](#)

## 6.89 hsa\_executable\_symbol\_s Struct Reference

Executable symbol handle.

```
#include <hsa.h>
```

### Public Attributes

- uint64\_t [handle](#)

### 6.89.1 Detailed Description

Executable symbol handle.

The lifetime of an executable object symbol matches that of the executable associated with it. An operation on a symbol whose associated executable has been destroyed results in undefined behavior.

Definition at line 4652 of file [hsa.h](#).

### 6.89.2 Member Data Documentation



### 6.89.2.1 handle

```
uint64_t hsa_executable_symbol_s::handle
```

Opaque handle. Two handles reference the same object of the enclosing type if and only if they are equal.

Definition at line 4657 of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa.h](#)

## 6.90 hsa\_ext\_amd\_aql\_pm4\_packet\_t Struct Reference

Collaboration diagram for hsa\_ext\_amd\_aql\_pm4\_packet\_t:

### Public Attributes

- [uint16\\_t](#) [header](#)
- [uint16\\_t](#) [pm4\\_command](#) [27]
- [hsa\\_signal\\_t](#) [completion\\_signal](#)

### 6.90.1 Detailed Description

Definition at line 202 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.90.2 Member Data Documentation

#### 6.90.2.1 completion\_signal

```
hsa_signal_t hsa_ext_amd_aql_pm4_packet_t::completion_signal
```

Definition at line 205 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

#### 6.90.2.2 header

```
uint16_t hsa_ext_amd_aql_pm4_packet_t::header
```

Definition at line 203 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.90.2.3 pm4\_command

```
uint16_t hsa_ext_amd_aql_pm4_packet_t::pm4_command[27]
```

Definition at line 204 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ven\\_amd\\_aqlprofile.h](#)

## 6.91 hsa\_ext\_control\_directives\_s Struct Reference

Control directives specify low-level information about the finalization process.

```
#include <hsa_ext_finalize.h>
```

Collaboration diagram for `hsa_ext_control_directives_s`:

### Public Attributes

- `uint64_t` [control\\_directives\\_mask](#)
- `uint16_t` [break\\_exceptions\\_mask](#)
- `uint16_t` [detect\\_exceptions\\_mask](#)
- `uint32_t` [max\\_dynamic\\_group\\_size](#)
- `uint64_t` [max\\_flat\\_grid\\_size](#)
- `uint32_t` [max\\_flat\\_workgroup\\_size](#)
- `uint32_t` [reserved1](#)
- `uint64_t` [required\\_grid\\_size](#) [3]
- `hsa_dim3_t` [required\\_workgroup\\_size](#)
- `uint8_t` [required\\_dim](#)
- `uint8_t` [reserved2](#) [75]

### 6.91.1 Detailed Description

Control directives specify low-level information about the finalization process.

Definition at line 322 of file [hsa\\_ext\\_finalize.h](#).

### 6.91.2 Member Data Documentation

### 6.91.2.1 break\_exceptions\_mask

```
uint16_t hsa_ext_control_directives_s::break_exceptions_mask
```

Bitset of HSAIL exceptions that must have the BREAK policy enabled. The bit assigned to an HSAIL exception is determined by the corresponding value in BrigExceptionsMask. If the kernel contains a enablebreakexceptions control directive, the finalizer uses the union of the two masks.

Definition at line 340 of file [hsa\\_ext\\_finalize.h](#).

### 6.91.2.2 control\_directives\_mask

```
uint64_t hsa_ext_control_directives_s::control_directives_mask
```

Bitset indicating which control directives are enabled. The bit assigned to a control directive is determined by the corresponding value in BrigControlDirective.

If a control directive is disabled, its corresponding field value (if any) must be 0. Control directives that are only present or absent (such as partial workgroups) have no corresponding field as the presence of the bit in this mask is sufficient.

Definition at line 333 of file [hsa\\_ext\\_finalize.h](#).

### 6.91.2.3 detect\_exceptions\_mask

```
uint16_t hsa_ext_control_directives_s::detect_exceptions_mask
```

Bitset of HSAIL exceptions that must have the DETECT policy enabled. The bit assigned to an HSAIL exception is determined by the corresponding value in BrigExceptionsMask. If the kernel contains a enabledetectexceptions control directive, the finalizer uses the union of the two masks.

Definition at line 347 of file [hsa\\_ext\\_finalize.h](#).

### 6.91.2.4 max\_dynamic\_group\_size

```
uint32_t hsa_ext_control_directives_s::max_dynamic_group_size
```

Maximum size (in bytes) of dynamic group memory that will be allocated by the application for any dispatch of the kernel. If the kernel contains a maxdynamicssize control directive, the two values should match.

Definition at line 353 of file [hsa\\_ext\\_finalize.h](#).

### 6.91.2.5 max\_flat\_grid\_size

```
uint64_t hsa_ext_control_directives_s::max_flat_grid_size
```

Maximum number of grid work-items that will be used by the application to launch the kernel. If the kernel contains a `maxflatgridsize` control directive, the value of *max\_flat\_grid\_size* must not be greater than the value of the directive, and takes precedence.

The value specified for maximum absolute grid size must be greater than or equal to the product of the values specified by *required\_grid\_size*.

If the bit at position `BRIG_CONTROL_MAXFLATGRIDSIZE` is set in *control\_directives\_mask*, this field must be greater than 0.

Definition at line 366 of file [hsa\\_ext\\_finalize.h](#).

### 6.91.2.6 max\_flat\_workgroup\_size

```
uint32_t hsa_ext_control_directives_s::max_flat_workgroup_size
```

Maximum number of work-group work-items that will be used by the application to launch the kernel. If the kernel contains a `maxflatworkgroupsize` control directive, the value of *max\_flat\_workgroup\_size* must not be greater than the value of the directive, and takes precedence.

The value specified for maximum absolute grid size must be greater than or equal to the product of the values specified by *required\_workgroup\_size*.

If the bit at position `BRIG_CONTROL_MAXFLATWORKGROUPSIZE` is set in *control\_directives\_mask*, this field must be greater than 0.

Definition at line 380 of file [hsa\\_ext\\_finalize.h](#).

### 6.91.2.7 required\_dim

```
uint8_t hsa_ext_control_directives_s::required_dim
```

Number of dimensions that will be used by the application to launch the kernel. If the kernel contains a `requireddim` control directive, the two values should match.

The specified dimensions must be consistent with *required\_grid\_size* and *required\_workgroup\_size*. This invariant must hold only if all the corresponding control directives are enabled.

If the bit at position `BRIG_CONTROL_REQUIREDDIM` is set in *control\_directives\_mask*, this field must be 1, 2, or 3.

Definition at line 425 of file [hsa\\_ext\\_finalize.h](#).

### 6.91.2.8 required\_grid\_size

```
uint64_t hsa_ext_control_directives_s::required_grid_size[3]
```

Grid size that will be used by the application in any dispatch of the kernel. If the kernel contains a requiredgridsizes control directive, the dimensions should match.

The specified grid size must be consistent with *required\_workgroup\_size* and *required\_dim*. Also, the product of the three dimensions must not exceed *max\_flat\_grid\_size*. Note that the listed invariants must hold only if all the corresponding control directives are enabled.

If the bit at position BRIG\_CONTROL\_REQUIREDGRIDSIZESIZE is set in *control\_directives\_mask*, the three dimension values must be greater than 0.

Definition at line 398 of file [hsa\\_ext\\_finalize.h](#).

### 6.91.2.9 required\_workgroup\_size

```
hsa_dim3_t hsa_ext_control_directives_s::required_workgroup_size
```

Work-group size that will be used by the application in any dispatch of the kernel. If the kernel contains a required-workgroupsize control directive, the dimensions should match.

The specified work-group size must be consistent with *required\_grid\_size* and *required\_dim*. Also, the product of the three dimensions must not exceed *max\_flat\_workgroup\_size*. Note that the listed invariants must hold only if all the corresponding control directives are enabled.

If the bit at position BRIG\_CONTROL\_REQUIREDWORKGROUPSIZE is set in *control\_directives\_mask*, the three dimension values must be greater than 0.

Definition at line 412 of file [hsa\\_ext\\_finalize.h](#).

### 6.91.2.10 reserved1

```
uint32_t hsa_ext_control_directives_s::reserved1
```

Reserved. Must be 0.

Definition at line 384 of file [hsa\\_ext\\_finalize.h](#).

### 6.91.2.11 reserved2

```
uint8_t hsa_ext_control_directives_s::reserved2[75]
```

Reserved. Must be 0.

Definition at line 429 of file [hsa\\_ext\\_finalize.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_finalize.h](#)

## 6.92 hsa\_ext\_finalizer\_1\_00\_pfn\_s Struct Reference

### Public Attributes

- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_program\\_create](#) )( [hsa\\_machine\\_model\\_t](#) machine\_model, [hsa\\_profile\\_t](#) profile, [hsa\\_default\\_float\\_rounding\\_mode\\_t](#) default\_float\_rounding\_mode, const char \*options, [hsa\\_ext\\_program\\_t](#) \*program)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_program\\_destroy](#) )( [hsa\\_ext\\_program\\_t](#) program)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_program\\_add\\_module](#) )( [hsa\\_ext\\_program\\_t](#) program, [hsa\\_ext\\_module\\_t](#) module)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_program\\_iterate\\_modules](#) )( [hsa\\_ext\\_program\\_t](#) program, [hsa\\_status\\_t](#)(\*callback)([hsa\\_ext\\_program\\_t](#) program, [hsa\\_ext\\_module\\_t](#) module, void \*data), void \*data)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_program\\_get\\_info](#) )( [hsa\\_ext\\_program\\_t](#) program, [hsa\\_ext\\_program\\_info\\_t](#) attribute, void \*value)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_program\\_finalize](#) )( [hsa\\_ext\\_program\\_t](#) program, [hsa\\_isa\\_t](#) isa, int32\_t call\_↵convention, [hsa\\_ext\\_control\\_directives\\_t](#) control\_directives, const char \*options, [hsa\\_code\\_object\\_type\\_t](#) code\_object\_type, [hsa\\_code\\_object\\_t](#) \*code\_object)

### 6.92.1 Detailed Description

Definition at line 500 of file [hsa\\_ext\\_finalize.h](#).

### 6.92.2 Member Data Documentation

#### 6.92.2.1 hsa\_ext\_program\_add\_module

```
hsa_status_t(* hsa_ext_finalizer_1_00_pfn_s::hsa_ext_program_add_module) (hsa_ext_program_t
program, hsa_ext_module_t module)
```

Definition at line 508 of file [hsa\\_ext\\_finalize.h](#).

#### 6.92.2.2 hsa\_ext\_program\_create

```
hsa_status_t(* hsa_ext_finalizer_1_00_pfn_s::hsa_ext_program_create) (hsa_machine_model_t
machine_model, hsa_profile_t profile, hsa_default_float_rounding_mode_t default_float_rounding_↵
_mode, const char *options, hsa_ext_program_t *program)
```

Definition at line 501 of file [hsa\\_ext\\_finalize.h](#).

#### 6.92.2.3 hsa\_ext\_program\_destroy

```
hsa_status_t(* hsa_ext_finalizer_1_00_pfn_s::hsa_ext_program_destroy) (hsa_ext_program_t program)
```

Definition at line 506 of file [hsa\\_ext\\_finalize.h](#).

#### 6.92.2.4 hsa\_ext\_program\_finalize

```
hsa_status_t(* hsa_ext_finalizer_1_00_pfn_s::hsa_ext_program_finalize) (hsa_ext_program_t
program, hsa_isa_t isa, int32_t call_convention, hsa_ext_control_directives_t control_directives,
const char *options, hsa_code_object_type_t code_object_type, hsa_code_object_t *code_object)
```

Definition at line 521 of file [hsa\\_ext\\_finalize.h](#).

#### 6.92.2.5 hsa\_ext\_program\_get\_info

```
hsa_status_t(* hsa_ext_finalizer_1_00_pfn_s::hsa_ext_program_get_info) (hsa_ext_program_t
program, hsa_ext_program_info_t attribute, void *value)
```

Definition at line 517 of file [hsa\\_ext\\_finalize.h](#).

#### 6.92.2.6 hsa\_ext\_program\_iterate\_modules

```
hsa_status_t(* hsa_ext_finalizer_1_00_pfn_s::hsa_ext_program_iterate_modules) (hsa_ext_program_t
program, hsa_status_t(*callback) (hsa_ext_program_t program, hsa_ext_module_t module, void
*data), void *data)
```

Definition at line 511 of file [hsa\\_ext\\_finalize.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_finalize.h](#)

## 6.93 hsa\_ext\_image\_data\_info\_s Struct Reference

Agent specific image size and alignment requirements, populated by [hsa\\_ext\\_image\\_data\\_get\\_info](#) and [hsa\\_ext\\_image\\_data\\_get\\_info\\_with\\_layout](#).

```
#include <hsa_ext_image.h>
```

### Public Attributes

- `size_t` [size](#)
- `size_t` [alignment](#)

#### 6.93.1 Detailed Description

Agent specific image size and alignment requirements, populated by [hsa\\_ext\\_image\\_data\\_get\\_info](#) and [hsa\\_ext\\_image\\_data\\_get\\_info\\_with\\_layout](#).

Definition at line 509 of file [hsa\\_ext\\_image.h](#).

## 6.93.2 Member Data Documentation

### 6.93.2.1 alignment

```
size_t hsa_ext_image_data_info_s::alignment
```

Image data alignment, in bytes. Must always be a power of 2.

Definition at line 518 of file [hsa\\_ext\\_image.h](#).

### 6.93.2.2 size

```
size_t hsa_ext_image_data_info_s::size
```

Image data size, in bytes.

Definition at line 513 of file [hsa\\_ext\\_image.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_image.h](#)

## 6.94 hsa\_ext\_image\_descriptor\_s Struct Reference

Implementation independent image descriptor.

```
#include <hsa_ext_image.h>
```

Collaboration diagram for `hsa_ext_image_descriptor_s`:

### Public Attributes

- [hsa\\_ext\\_image\\_geometry\\_t](#) geometry
- [size\\_t](#) width
- [size\\_t](#) height
- [size\\_t](#) depth
- [size\\_t](#) array\_size
- [hsa\\_ext\\_image\\_format\\_t](#) format

### 6.94.1 Detailed Description

Implementation independent image descriptor.

Definition at line 326 of file [hsa\\_ext\\_image.h](#).



## 6.94.2 Member Data Documentation

### 6.94.2.1 array\_size

```
size_t hsa_ext_image_descriptor_s::array_size
```

Number of image layers in the image array. Only used if the geometry is [HSA\\_EXT\\_IMAGE\\_GEOMETRY\\_1DA](#), [HSA\\_EXT\\_IMAGE\\_GEOMETRY\\_2DA](#), or [HSA\\_EXT\\_IMAGE\\_GEOMETRY\\_2DADEPTH](#), otherwise must be 0.

Definition at line 352 of file [hsa\\_ext\\_image.h](#).

### 6.94.2.2 depth

```
size_t hsa_ext_image_descriptor_s::depth
```

Depth of the image, in components. Only used if the geometry is [HSA\\_EXT\\_IMAGE\\_GEOMETRY\\_3D](#), otherwise must be 0.

Definition at line 346 of file [hsa\\_ext\\_image.h](#).

### 6.94.2.3 format

```
hsa_ext_image_format_t hsa_ext_image_descriptor_s::format
```

Image format.

Definition at line 356 of file [hsa\\_ext\\_image.h](#).

### 6.94.2.4 geometry

```
hsa_ext_image_geometry_t hsa_ext_image_descriptor_s::geometry
```

Image geometry.

Definition at line 330 of file [hsa\\_ext\\_image.h](#).

#### 6.94.2.5 height

```
size_t hsa_ext_image_descriptor_s::height
```

Height of the image, in components. Only used if the geometry is [HSA\\_EXT\\_IMAGE\\_GEOMETRY\\_2D](#), [HSA\\_EXT\\_IMAGE\\_GEOMETRY\\_3D](#), [HSA\\_EXT\\_IMAGE\\_GEOMETRY\\_2DA](#), [HSA\\_EXT\\_IMAGE\\_GEOMETRY\\_2DDEPTH](#), or [HSA\\_EXT\\_IMAGE\\_GEOMETRY\\_2DADEPTH](#), otherwise must be 0.

Definition at line 341 of file [hsa\\_ext\\_image.h](#).

#### 6.94.2.6 width

```
size_t hsa_ext_image_descriptor_s::width
```

Width of the image, in components.

Definition at line 334 of file [hsa\\_ext\\_image.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_image.h](#)

## 6.95 hsa\_ext\_image\_format\_s Struct Reference

Image format.

```
#include <hsa_ext_image.h>
```

### Public Attributes

- [hsa\\_ext\\_image\\_channel\\_type32\\_t channel\\_type](#)
- [hsa\\_ext\\_image\\_channel\\_order32\\_t channel\\_order](#)

#### 6.95.1 Detailed Description

Image format.

Definition at line 311 of file [hsa\\_ext\\_image.h](#).

#### 6.95.2 Member Data Documentation

### 6.95.2.1 channel\_order

[hsa\\_ext\\_image\\_channel\\_order32\\_t](#) hsa\_ext\_image\_format\_s::channel\_order

Channel order.

Definition at line 320 of file [hsa\\_ext\\_image.h](#).

### 6.95.2.2 channel\_type

[hsa\\_ext\\_image\\_channel\\_type32\\_t](#) hsa\_ext\_image\_format\_s::channel\_type

Channel type.

Definition at line 315 of file [hsa\\_ext\\_image.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_image.h](#)

## 6.96 hsa\_ext\_image\_region\_s Struct Reference

Image region.

```
#include <hsa_ext_image.h>
```

Collaboration diagram for hsa\_ext\_image\_region\_s:

### Public Attributes

- [hsa\\_dim3\\_t](#) offset
- [hsa\\_dim3\\_t](#) range

### 6.96.1 Detailed Description

Image region.

Definition at line 927 of file [hsa\\_ext\\_image.h](#).

### 6.96.2 Member Data Documentation

### 6.96.2.1 offset

```
hsa_dim3_t hsa_ext_image_region_s::offset
```

Offset within an image (in coordinates).

Definition at line 931 of file [hsa\\_ext\\_image.h](#).

### 6.96.2.2 range

```
hsa_dim3_t hsa_ext_image_region_s::range
```

Dimension size of the image range (in coordinates). The x, y, and z dimensions correspond to width, height, and depth or index respectively.

Definition at line 937 of file [hsa\\_ext\\_image.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_image.h](#)

## 6.97 hsa\_ext\_image\_s Struct Reference

Image handle, populated by [hsa\\_ext\\_image\\_create](#) or [hsa\\_ext\\_image\\_create\\_with\\_layout](#). Image handles are only unique within an agent, not across agents.

```
#include <hsa_ext_image.h>
```

### Public Attributes

- `uint64_t` [handle](#)

### 6.97.1 Detailed Description

Image handle, populated by [hsa\\_ext\\_image\\_create](#) or [hsa\\_ext\\_image\\_create\\_with\\_layout](#). Image handles are only unique within an agent, not across agents.

Definition at line 174 of file [hsa\\_ext\\_image.h](#).

### 6.97.2 Member Data Documentation

### 6.97.2.1 handle

```
uint64_t hsa_ext_image_s::handle
```

Opaque handle. For a given agent, two handles reference the same object of the enclosing type if and only if they are equal.

Definition at line 179 of file [hsa\\_ext\\_image.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/hsa_ext_image.h`

## 6.98 hsa\_ext\_images\_1\_00\_pfn\_s Struct Reference

The function pointer table for the images v1.00 extension. Can be returned by [hsa\\_system\\_get\\_extension\\_table](#) or [hsa\\_system\\_get\\_major\\_extension\\_table](#).

```
#include <hsa_ext_image.h>
```

### Public Attributes

- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_get\\_capability](#) )( [hsa\\_agent\\_t](#) agent, [hsa\\_ext\\_image\\_geometry\\_t](#) geometry, const [hsa\\_ext\\_image\\_format\\_t](#) \*image\_format, [uint32\\_t](#) \*capability\_mask)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_data\\_get\\_info](#) )( [hsa\\_agent\\_t](#) agent, const [hsa\\_ext\\_image\\_descriptor\\_t](#) \*image\_descriptor, [hsa\\_access\\_permission\\_t](#) access\_permission, [hsa\\_ext\\_image\\_data\\_info\\_t](#) \*image\_data\_info)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_create](#) )( [hsa\\_agent\\_t](#) agent, const [hsa\\_ext\\_image\\_descriptor\\_t](#) \*image\_descriptor, const void \*image\_data, [hsa\\_access\\_permission\\_t](#) access\_permission, [hsa\\_ext\\_image\\_t](#) \*image)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_destroy](#) )( [hsa\\_agent\\_t](#) agent, [hsa\\_ext\\_image\\_t](#) image)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_copy](#) )( [hsa\\_agent\\_t](#) agent, [hsa\\_ext\\_image\\_t](#) src\_image, const [hsa\\_dim3\\_t](#) \*src\_offset, [hsa\\_ext\\_image\\_t](#) dst\_image, const [hsa\\_dim3\\_t](#) \*dst\_offset, const [hsa\\_dim3\\_t](#) \*range)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_import](#) )( [hsa\\_agent\\_t](#) agent, const void \*src\_memory, [size\\_t](#) src\_row\_pitch, [size\\_t](#) src\_slice\_pitch, [hsa\\_ext\\_image\\_t](#) dst\_image, const [hsa\\_ext\\_image\\_region\\_t](#) \*image\_region)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_export](#) )( [hsa\\_agent\\_t](#) agent, [hsa\\_ext\\_image\\_t](#) src\_image, void \*dst\_memory, [size\\_t](#) dst\_row\_pitch, [size\\_t](#) dst\_slice\_pitch, const [hsa\\_ext\\_image\\_region\\_t](#) \*image\_region)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_clear](#) )( [hsa\\_agent\\_t](#) agent, [hsa\\_ext\\_image\\_t](#) image, const void \*data, const [hsa\\_ext\\_image\\_region\\_t](#) \*image\_region)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_sampler\\_create](#) )( [hsa\\_agent\\_t](#) agent, const [hsa\\_ext\\_sampler\\_descriptor\\_t](#) \*sampler\_descriptor, [hsa\\_ext\\_sampler\\_t](#) \*sampler)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_sampler\\_destroy](#) )( [hsa\\_agent\\_t](#) agent, [hsa\\_ext\\_sampler\\_t](#) sampler)

### 6.98.1 Detailed Description

The function pointer table for the images v1.00 extension. Can be returned by [hsa\\_system\\_get\\_extension\\_table](#) or [hsa\\_system\\_get\\_major\\_extension\\_table](#).

Definition at line 1286 of file [hsa\\_ext\\_image.h](#).

## 6.98.2 Member Data Documentation

### 6.98.2.1 hsa\_ext\_image\_clear

```
hsa_status_t(* hsa_ext_images_1_00_pfn_s::hsa_ext_image_clear) (hsa_agent_t agent, hsa_ext_image_t image, const void *data, const hsa_ext_image_region_t *image_region)
```

Definition at line 1335 of file [hsa\\_ext\\_image.h](#).

### 6.98.2.2 hsa\_ext\_image\_copy

```
hsa_status_t(* hsa_ext_images_1_00_pfn_s::hsa_ext_image_copy) (hsa_agent_t agent, hsa_ext_image_t src_image, const hsa_dim3_t *src_offset, hsa_ext_image_t dst_image, const hsa_dim3_t *dst_offset, const hsa_dim3_t *range)
```

Definition at line 1311 of file [hsa\\_ext\\_image.h](#).

### 6.98.2.3 hsa\_ext\_image\_create

```
hsa_status_t(* hsa_ext_images_1_00_pfn_s::hsa_ext_image_create) (hsa_agent_t agent, const hsa_ext_image_descriptor_t *image_descriptor, const void *image_data, hsa_access_permission_t access_permission, hsa_ext_image_t *image)
```

Definition at line 1300 of file [hsa\\_ext\\_image.h](#).

### 6.98.2.4 hsa\_ext\_image\_data\_get\_info

```
hsa_status_t(* hsa_ext_images_1_00_pfn_s::hsa_ext_image_data_get_info) (hsa_agent_t agent, const hsa_ext_image_descriptor_t *image_descriptor, hsa_access_permission_t access_permission, hsa_ext_image_data_info_t *image_data_info)
```

Definition at line 1294 of file [hsa\\_ext\\_image.h](#).

### 6.98.2.5 hsa\_ext\_image\_destroy

```
hsa_status_t(* hsa_ext_images_1_00_pfn_s::hsa_ext_image_destroy) (hsa_agent_t agent, hsa_ext_image_t image)
```

Definition at line 1307 of file [hsa\\_ext\\_image.h](#).

### 6.98.2.6 hsa\_ext\_image\_export

```
hsa_status_t(* hsa_ext_images_1_00_pfn_s::hsa_ext_image_export) (hsa_agent_t agent, hsa_ext_image_t
src_image, void *dst_memory, size_t dst_row_pitch, size_t dst_slice_pitch, const hsa_ext_image_region_t
*image_region)
```

Definition at line 1327 of file [hsa\\_ext\\_image.h](#).

### 6.98.2.7 hsa\_ext\_image\_get\_capability

```
hsa_status_t(* hsa_ext_images_1_00_pfn_s::hsa_ext_image_get_capability) (hsa_agent_t agent,
hsa_ext_image_geometry_t geometry, const hsa_ext_image_format_t *image_format, uint32_t *capability↵
_mask)
```

Definition at line 1288 of file [hsa\\_ext\\_image.h](#).

### 6.98.2.8 hsa\_ext\_image\_import

```
hsa_status_t(* hsa_ext_images_1_00_pfn_s::hsa_ext_image_import) (hsa_agent_t agent, const void
*src_memory, size_t src_row_pitch, size_t src_slice_pitch, hsa_ext_image_t dst_image, const
hsa_ext_image_region_t *image_region)
```

Definition at line 1319 of file [hsa\\_ext\\_image.h](#).

### 6.98.2.9 hsa\_ext\_sampler\_create

```
hsa_status_t(* hsa_ext_images_1_00_pfn_s::hsa_ext_sampler_create) (hsa_agent_t agent, const
hsa_ext_sampler_descriptor_t *sampler_descriptor, hsa_ext_sampler_t *sampler)
```

Definition at line 1341 of file [hsa\\_ext\\_image.h](#).

### 6.98.2.10 hsa\_ext\_sampler\_destroy

```
hsa_status_t(* hsa_ext_images_1_00_pfn_s::hsa_ext_sampler_destroy) (hsa_agent_t agent, hsa_ext_sampler_t
sampler)
```

Definition at line 1346 of file [hsa\\_ext\\_image.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/hsa_ext_image.h`

## 6.99 hsa\_ext\_images\_1\_pfn\_s Struct Reference

The function pointer table for the images v1 extension. Can be returned by [hsa\\_system\\_get\\_extension\\_table](#) or [hsa\\_system\\_get\\_major\\_extension\\_table](#).

```
#include <hsa_ext_image.h>
```

### Public Attributes

- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_get\\_capability](#) )(hsa\_agent\_t agent, [hsa\\_ext\\_image\\_geometry\\_t](#) geometry, const [hsa\\_ext\\_image\\_format\\_t](#) \*image\_format, uint32\_t \*capability\_mask)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_data\\_get\\_info](#) )(hsa\_agent\_t agent, const [hsa\\_ext\\_image\\_descriptor\\_t](#) \*image\_descriptor, [hsa\\_access\\_permission\\_t](#) access\_permission, [hsa\\_ext\\_image\\_data\\_info\\_t](#) \*image\_data\_info)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_create](#) )(hsa\_agent\_t agent, const [hsa\\_ext\\_image\\_descriptor\\_t](#) \*image\_descriptor, const void \*image\_data, [hsa\\_access\\_permission\\_t](#) access\_permission, [hsa\\_ext\\_image\\_t](#) \*image)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_destroy](#) )(hsa\_agent\_t agent, [hsa\\_ext\\_image\\_t](#) image)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_copy](#) )(hsa\_agent\_t agent, [hsa\\_ext\\_image\\_t](#) src\_image, const [hsa\\_dim3\\_t](#) \*src\_offset, [hsa\\_ext\\_image\\_t](#) dst\_image, const [hsa\\_dim3\\_t](#) \*dst\_offset, const [hsa\\_dim3\\_t](#) \*range)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_import](#) )(hsa\_agent\_t agent, const void \*src\_memory, size\_t src\_row\_pitch, size\_t src\_slice\_pitch, [hsa\\_ext\\_image\\_t](#) dst\_image, const [hsa\\_ext\\_image\\_region\\_t](#) \*image\_region)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_export](#) )(hsa\_agent\_t agent, [hsa\\_ext\\_image\\_t](#) src\_image, void \*dst\_memory, size\_t dst\_row\_pitch, size\_t dst\_slice\_pitch, const [hsa\\_ext\\_image\\_region\\_t](#) \*image\_region)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_clear](#) )(hsa\_agent\_t agent, [hsa\\_ext\\_image\\_t](#) image, const void \*data, const [hsa\\_ext\\_image\\_region\\_t](#) \*image\_region)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_sampler\\_create](#) )(hsa\_agent\_t agent, const [hsa\\_ext\\_sampler\\_descriptor\\_t](#) \*sampler\_descriptor, [hsa\\_ext\\_sampler\\_t](#) \*sampler)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_sampler\\_destroy](#) )(hsa\_agent\_t agent, [hsa\\_ext\\_sampler\\_t](#) sampler)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_get\\_capability\\_with\\_layout](#) )(hsa\_agent\_t agent, [hsa\\_ext\\_image\\_geometry\\_t](#) geometry, const [hsa\\_ext\\_image\\_format\\_t](#) \*image\_format, [hsa\\_ext\\_image\\_data\\_layout\\_t](#) image\_data\_layout, uint32\_t \*capability\_mask)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_data\\_get\\_info\\_with\\_layout](#) )(hsa\_agent\_t agent, const [hsa\\_ext\\_image\\_descriptor\\_t](#) \*image\_descriptor, [hsa\\_access\\_permission\\_t](#) access\_permission, [hsa\\_ext\\_image\\_data\\_layout\\_t](#) image\_data\_layout, size\_t image\_data\_row\_pitch, size\_t image\_data\_slice\_pitch, [hsa\\_ext\\_image\\_data\\_info\\_t](#) \*image\_data\_info)
- [hsa\\_status\\_t](#)(\* [hsa\\_ext\\_image\\_create\\_with\\_layout](#) )(hsa\_agent\_t agent, const [hsa\\_ext\\_image\\_descriptor\\_t](#) \*image\_descriptor, const void \*image\_data, [hsa\\_access\\_permission\\_t](#) access\_permission, [hsa\\_ext\\_image\\_data\\_layout\\_t](#) image\_data\_layout, size\_t image\_data\_row\_pitch, size\_t image\_data\_slice\_pitch, [hsa\\_ext\\_image\\_t](#) \*image)

### 6.99.1 Detailed Description

The function pointer table for the images v1 extension. Can be returned by [hsa\\_system\\_get\\_extension\\_table](#) or [hsa\\_system\\_get\\_major\\_extension\\_table](#).

Definition at line 1357 of file [hsa\\_ext\\_image.h](#).

### 6.99.2 Member Data Documentation



### 6.99.2.1 hsa\_ext\_image\_clear

```
hsa_status_t(* hsa_ext_images_1_pfn_s::hsa_ext_image_clear) (hsa_agent_t agent, hsa_ext_image_t image, const void *data, const hsa_ext_image_region_t *image_region)
```

Definition at line 1406 of file [hsa\\_ext\\_image.h](#).

### 6.99.2.2 hsa\_ext\_image\_copy

```
hsa_status_t(* hsa_ext_images_1_pfn_s::hsa_ext_image_copy) (hsa_agent_t agent, hsa_ext_image_t src_image, const hsa_dim3_t *src_offset, hsa_ext_image_t dst_image, const hsa_dim3_t *dst_offset, const hsa_dim3_t *range)
```

Definition at line 1382 of file [hsa\\_ext\\_image.h](#).

### 6.99.2.3 hsa\_ext\_image\_create

```
hsa_status_t(* hsa_ext_images_1_pfn_s::hsa_ext_image_create) (hsa_agent_t agent, const hsa_ext_image_descriptor_t *image_descriptor, const void *image_data, hsa_access_permission_t access_permission, hsa_ext_image_t *image)
```

Definition at line 1371 of file [hsa\\_ext\\_image.h](#).

### 6.99.2.4 hsa\_ext\_image\_create\_with\_layout

```
hsa_status_t(* hsa_ext_images_1_pfn_s::hsa_ext_image_create_with_layout) (hsa_agent_t agent, const hsa_ext_image_descriptor_t *image_descriptor, const void *image_data, hsa_access_permission_t access_permission, hsa_ext_image_data_layout_t image_data_layout, size_t image_data_row_pitch, size_t image_data_slice_pitch, hsa_ext_image_t *image)
```

Definition at line 1437 of file [hsa\\_ext\\_image.h](#).

### 6.99.2.5 hsa\_ext\_image\_data\_get\_info

```
hsa_status_t(* hsa_ext_images_1_pfn_s::hsa_ext_image_data_get_info) (hsa_agent_t agent, const hsa_ext_image_descriptor_t *image_descriptor, hsa_access_permission_t access_permission, hsa_ext_image_data_info_t *image_data_info)
```

Definition at line 1365 of file [hsa\\_ext\\_image.h](#).

#### 6.99.2.6 hsa\_ext\_image\_data\_get\_info\_with\_layout

```
hsa_status_t(* hsa_ext_images_1_pfn_s::hsa_ext_image_data_get_info_with_layout) (hsa_agent_t agent, const hsa_ext_image_descriptor_t *image_descriptor, hsa_access_permission_t access_permission, hsa_ext_image_data_layout_t image_data_layout, size_t image_data_row_pitch, size_t image_data_slice_pitch, hsa_ext_image_data_info_t *image_data_info)
```

Definition at line 1428 of file [hsa\\_ext\\_image.h](#).

#### 6.99.2.7 hsa\_ext\_image\_destroy

```
hsa_status_t(* hsa_ext_images_1_pfn_s::hsa_ext_image_destroy) (hsa_agent_t agent, hsa_ext_image_t image)
```

Definition at line 1378 of file [hsa\\_ext\\_image.h](#).

#### 6.99.2.8 hsa\_ext\_image\_export

```
hsa_status_t(* hsa_ext_images_1_pfn_s::hsa_ext_image_export) (hsa_agent_t agent, hsa_ext_image_t src_image, void *dst_memory, size_t dst_row_pitch, size_t dst_slice_pitch, const hsa_ext_image_region_t *image_region)
```

Definition at line 1398 of file [hsa\\_ext\\_image.h](#).

#### 6.99.2.9 hsa\_ext\_image\_get\_capability

```
hsa_status_t(* hsa_ext_images_1_pfn_s::hsa_ext_image_get_capability) (hsa_agent_t agent, hsa_ext_image_geometry_t geometry, const hsa_ext_image_format_t *image_format, uint32_t *capability_mask)
```

Definition at line 1359 of file [hsa\\_ext\\_image.h](#).

#### 6.99.2.10 hsa\_ext\_image\_get\_capability\_with\_layout

```
hsa_status_t(* hsa_ext_images_1_pfn_s::hsa_ext_image_get_capability_with_layout) (hsa_agent_t agent, hsa_ext_image_geometry_t geometry, const hsa_ext_image_format_t *image_format, hsa_ext_image_data_layout_t image_data_layout, uint32_t *capability_mask)
```

Definition at line 1421 of file [hsa\\_ext\\_image.h](#).

### 6.99.2.11 hsa\_ext\_image\_import

```
hsa_status_t(* hsa_ext_images_1_pfn_s::hsa_ext_image_import) (hsa_agent_t agent, const void
*src_memory, size_t src_row_pitch, size_t src_slice_pitch, hsa_ext_image_t dst_image, const
hsa_ext_image_region_t *image_region)
```

Definition at line 1390 of file [hsa\\_ext\\_image.h](#).

### 6.99.2.12 hsa\_ext\_sampler\_create

```
hsa_status_t(* hsa_ext_images_1_pfn_s::hsa_ext_sampler_create) (hsa_agent_t agent, const hsa_ext_sampler_descr
*sampler_descriptor, hsa_ext_sampler_t *sampler)
```

Definition at line 1412 of file [hsa\\_ext\\_image.h](#).

### 6.99.2.13 hsa\_ext\_sampler\_destroy

```
hsa_status_t(* hsa_ext_images_1_pfn_s::hsa_ext_sampler_destroy) (hsa_agent_t agent, hsa_ext_sampler_t
sampler)
```

Definition at line 1417 of file [hsa\\_ext\\_image.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_image.h](#)

## 6.100 hsa\_ext\_program\_s Struct Reference

An opaque handle to a HSAIL program, which groups a set of HSAIL modules that collectively define functions and variables used by kernels and indirect functions.

```
#include <hsa_ext_finalize.h>
```

### Public Attributes

- uint64\_t [handle](#)

### 6.100.1 Detailed Description

An opaque handle to a HSAIL program, which groups a set of HSAIL modules that collectively define functions and variables used by kernels and indirect functions.

Definition at line 120 of file [hsa\\_ext\\_finalize.h](#).

## 6.100.2 Member Data Documentation

### 6.100.2.1 handle

```
uint64_t hsa_ext_program_s::handle
```

Opaque handle.

Definition at line 124 of file [hsa\\_ext\\_finalize.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_finalize.h](#)

## 6.101 hsa\_ext\_sampler\_descriptor\_s Struct Reference

Implementation independent sampler descriptor.

```
#include <hsa_ext_image.h>
```

### Public Attributes

- [hsa\\_ext\\_sampler\\_coordinate\\_mode32\\_t](#) coordinate\_mode
- [hsa\\_ext\\_sampler\\_filter\\_mode32\\_t](#) filter\_mode
- [hsa\\_ext\\_sampler\\_addressing\\_mode32\\_t](#) address\_mode

### 6.101.1 Detailed Description

Implementation independent sampler descriptor.

Definition at line 1205 of file [hsa\\_ext\\_image.h](#).

## 6.101.2 Member Data Documentation

### 6.101.2.1 address\_mode

```
hsa_ext_sampler_addressing_mode32_t hsa_ext_sampler_descriptor_s::address_mode
```

Sampler address mode describes the processing of out-of-range image coordinates.

Definition at line 1220 of file [hsa\\_ext\\_image.h](#).

### 6.101.2.2 coordinate\_mode

[hsa\\_ext\\_sampler\\_coordinate\\_mode32\\_t](#) hsa\_ext\_sampler\_descriptor\_s::coordinate\_mode

Sampler coordinate mode describes the normalization of image coordinates.

Definition at line 1209 of file [hsa\\_ext\\_image.h](#).

### 6.101.2.3 filter\_mode

[hsa\\_ext\\_sampler\\_filter\\_mode32\\_t](#) hsa\_ext\_sampler\_descriptor\_s::filter\_mode

Sampler filter type describes the type of sampling performed.

Definition at line 1214 of file [hsa\\_ext\\_image.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_image.h](#)

## 6.102 hsa\_ext\_sampler\_s Struct Reference

Sampler handle. Samplers are populated by [hsa\\_ext\\_sampler\\_create](#). Sampler handles are only unique within an agent, not across agents.

```
#include <hsa_ext_image.h>
```

### Public Attributes

- `uint64_t` [handle](#)

### 6.102.1 Detailed Description

Sampler handle. Samplers are populated by [hsa\\_ext\\_sampler\\_create](#). Sampler handles are only unique within an agent, not across agents.

Definition at line 1096 of file [hsa\\_ext\\_image.h](#).

### 6.102.2 Member Data Documentation

### 6.102.2.1 handle

```
uint64_t hsa_ext_sampler_s::handle
```

Opaque handle. For a given agent, two handles reference the same object of the enclosing type if and only if they are equal.

Definition at line 1101 of file [hsa\\_ext\\_image.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_image.h](#)

## 6.103 hsa\_isa\_s Struct Reference

Instruction set architecture.

```
#include <hsa.h>
```

### Public Attributes

- `uint64_t` [handle](#)

### 6.103.1 Detailed Description

Instruction set architecture.

Definition at line 3539 of file [hsa.h](#).

### 6.103.2 Member Data Documentation

#### 6.103.2.1 handle

```
uint64_t hsa_isa_s::handle
```

Opaque handle. Two handles reference the same object of the enclosing type if and only if they are equal.

Definition at line 3544 of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa.h](#)

## 6.104 hsa\_kernel\_dispatch\_packet\_s Struct Reference

AQL kernel dispatch packet.

```
#include <hsa.h>
```

Collaboration diagram for hsa\_kernel\_dispatch\_packet\_s:

### Public Attributes

- uint16\_t [header](#)
- uint16\_t [setup](#)
- uint16\_t [workgroup\\_size\\_x](#)
- uint16\_t [workgroup\\_size\\_y](#)
- uint16\_t [workgroup\\_size\\_z](#)
- uint16\_t [reserved0](#)
- uint32\_t [grid\\_size\\_x](#)
- uint32\_t [grid\\_size\\_y](#)
- uint32\_t [grid\\_size\\_z](#)
- uint32\_t [private\\_segment\\_size](#)
- uint32\_t [group\\_segment\\_size](#)
- uint64\_t [kernel\\_object](#)
- uint32\_t [reserved1](#)
- void \* [kernarg\\_address](#)
- uint64\_t [reserved2](#)
- [hsa\\_signal\\_t](#) [completion\\_signal](#)

### 6.104.1 Detailed Description

AQL kernel dispatch packet.

Definition at line 2918 of file [hsa.h](#).

### 6.104.2 Member Data Documentation

#### 6.104.2.1 completion\_signal

[hsa\\_signal\\_t](#) hsa\_kernel\_dispatch\_packet\_s::completion\_signal

Signal used to indicate completion of the job. The application can use the special signal handle 0 to indicate that no signal is used.

Definition at line 3022 of file [hsa.h](#).

#### 6.104.2.2 `grid_size_x`

```
uint32_t hsa_kernel_dispatch_packet_s::grid_size_x
```

X dimension of grid, in work-items. Must be greater than 0. Must not be smaller than *workgroup\_size\_x*.

Definition at line 2958 of file [hsa.h](#).

#### 6.104.2.3 `grid_size_y`

```
uint32_t hsa_kernel_dispatch_packet_s::grid_size_y
```

Y dimension of grid, in work-items. Must be greater than 0. If the grid has 1 dimension, the only valid value is 1. Must not be smaller than *workgroup\_size\_y*.

Definition at line 2965 of file [hsa.h](#).

#### 6.104.2.4 `grid_size_z`

```
uint32_t hsa_kernel_dispatch_packet_s::grid_size_z
```

Z dimension of grid, in work-items. Must be greater than 0. If the grid has 1 or 2 dimensions, the only valid value is 1. Must not be smaller than *workgroup\_size\_z*.

Definition at line 2972 of file [hsa.h](#).

#### 6.104.2.5 `group_segment_size`

```
uint32_t hsa_kernel_dispatch_packet_s::group_segment_size
```

Size in bytes of group memory allocation request (per work-group). Must not be less than the sum of the group memory used by the kernel (and the functions it calls directly or indirectly) and the dynamically allocated group segment variables.

Definition at line 2985 of file [hsa.h](#).

#### 6.104.2.6 `header`

```
uint16_t hsa_kernel_dispatch_packet_s::header
```

Packet header. Used to configure multiple packet parameters such as the packet type. The parameters are described by [hsa\\_packet\\_header\\_t](#).

Definition at line 2923 of file [hsa.h](#).



#### 6.104.2.7 kernarg\_address

```
void* hsa_kernel_dispatch_packet_s::kernarg_address
```

Definition at line 3010 of file [hsa.h](#).

#### 6.104.2.8 kernel\_object

```
uint64_t hsa_kernel_dispatch_packet_s::kernel_object
```

Opaque handle to a code object that includes an implementation-defined executable code for the kernel.

Definition at line 2991 of file [hsa.h](#).

#### 6.104.2.9 private\_segment\_size

```
uint32_t hsa_kernel_dispatch_packet_s::private_segment_size
```

Size in bytes of private memory allocation request (per work-item).

Definition at line 2977 of file [hsa.h](#).

#### 6.104.2.10 reserved0

```
uint16_t hsa_kernel_dispatch_packet_s::reserved0
```

Reserved. Must be 0.

Definition at line 2952 of file [hsa.h](#).

#### 6.104.2.11 reserved1

```
uint32_t hsa_kernel_dispatch_packet_s::reserved1
```

Definition at line 3009 of file [hsa.h](#).

#### 6.104.2.12 reserved2

```
uint64_t hsa_kernel_dispatch_packet_s::reserved2
```

Reserved. Must be 0.

Definition at line 3016 of file [hsa.h](#).

#### 6.104.2.13 setup

```
uint16_t hsa_kernel_dispatch_packet_s::setup
```

Dispatch setup parameters. Used to configure kernel dispatch parameters such as the number of dimensions in the grid. The parameters are described by [hsa\\_kernel\\_dispatch\\_packet\\_setup\\_t](#).

Definition at line 2930 of file [hsa.h](#).

#### 6.104.2.14 workgroup\_size\_x

```
uint16_t hsa_kernel_dispatch_packet_s::workgroup_size_x
```

X dimension of work-group, in work-items. Must be greater than 0.

Definition at line 2935 of file [hsa.h](#).

#### 6.104.2.15 workgroup\_size\_y

```
uint16_t hsa_kernel_dispatch_packet_s::workgroup_size_y
```

Y dimension of work-group, in work-items. Must be greater than 0. If the grid has 1 dimension, the only valid value is 1.

Definition at line 2941 of file [hsa.h](#).

#### 6.104.2.16 workgroup\_size\_z

```
uint16_t hsa_kernel_dispatch_packet_s::workgroup_size_z
```

Z dimension of work-group, in work-items. Must be greater than 0. If the grid has 1 or 2 dimensions, the only valid value is 1.

Definition at line 2947 of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa.h](#)

## 6.105 hsa\_loaded\_code\_object\_s Struct Reference

Loaded code object handle.

```
#include <hsa.h>
```

### Public Attributes

- uint64\_t [handle](#)

#### 6.105.1 Detailed Description

Loaded code object handle.

Definition at line [4240](#) of file [hsa.h](#).

#### 6.105.2 Member Data Documentation

##### 6.105.2.1 handle

```
uint64_t hsa_loaded_code_object_s::handle
```

Opaque handle. Two handles reference the same object of the enclosing type if and only if they are equal.

Definition at line [4245](#) of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa.h](#)

## 6.106 hsa\_pitched\_ptr\_s Struct Reference

### Public Attributes

- void \* [base](#)
- size\_t [pitch](#)
- size\_t [slice](#)

#### 6.106.1 Detailed Description

Definition at line [1209](#) of file [hsa\\_ext\\_amd.h](#).

## 6.106.2 Member Data Documentation

### 6.106.2.1 base

```
void* hsa_pitched_ptr_s::base
```

Definition at line 1210 of file [hsa\\_ext\\_amd.h](#).

### 6.106.2.2 pitch

```
size_t hsa_pitched_ptr_s::pitch
```

Definition at line 1211 of file [hsa\\_ext\\_amd.h](#).

### 6.106.2.3 slice

```
size_t hsa_pitched_ptr_s::slice
```

Definition at line 1212 of file [hsa\\_ext\\_amd.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ext\\_amd.h](#)

## 6.107 hsa\_queue\_s Struct Reference

User mode queue.

```
#include <hsa.h>
```

Collaboration diagram for `hsa_queue_s`:

### Public Attributes

- [hsa\\_queue\\_type32\\_t](#) type
- [uint32\\_t](#) features
- [uint32\\_t](#) reserved0
- [void \\*](#) base\_address
- [hsa\\_signal\\_t](#) doorbell\_signal
- [uint32\\_t](#) size
- [uint32\\_t](#) reserved1
- [uint64\\_t](#) id

### 6.107.1 Detailed Description

User mode queue.

The queue structure is read-only and allocated by the HSA runtime, but agents can directly modify the contents of the buffer pointed by *base\_address*, or use HSA runtime APIs to access the doorbell signal.

Definition at line 2267 of file [hsa.h](#).

### 6.107.2 Member Data Documentation

#### 6.107.2.1 base\_address

```
void* hsa_queue_s::base_address
```

Definition at line 2293 of file [hsa.h](#).

#### 6.107.2.2 doorbell\_signal

```
hsa_signal_t hsa_queue_s::doorbell_signal
```

Signal object used by the application to indicate the ID of a packet that is ready to be processed. The HSA runtime manages the doorbell signal. If the application tries to replace or destroy this signal, the behavior is undefined.

If *type* is [HSA\\_QUEUE\\_TYPE\\_SINGLE](#), the doorbell signal value must be updated in a monotonically increasing fashion. If *type* is [HSA\\_QUEUE\\_TYPE\\_MULTI](#), the doorbell signal value can be updated with any value.

Definition at line 2307 of file [hsa.h](#).

#### 6.107.2.3 features

```
uint32_t hsa_queue_s::features
```

Queue features mask. This is a bit-field of [hsa\\_queue\\_feature\\_t](#) values. Applications should ignore any unknown set bits.

Definition at line 2277 of file [hsa.h](#).

#### 6.107.2.4 id

```
uint64_t hsa_queue_s::id
```

Queue identifier, which is unique over the lifetime of the application.

Definition at line 2320 of file [hsa.h](#).

#### 6.107.2.5 reserved0

```
uint32_t hsa_queue_s::reserved0
```

Definition at line 2292 of file [hsa.h](#).

#### 6.107.2.6 reserved1

```
uint32_t hsa_queue_s::reserved1
```

Reserved. Must be 0.

Definition at line 2316 of file [hsa.h](#).

#### 6.107.2.7 size

```
uint32_t hsa_queue_s::size
```

Maximum number of packets the queue can hold. Must be a power of 2.

Definition at line 2312 of file [hsa.h](#).

#### 6.107.2.8 type

```
hsa_queue_type32_t hsa_queue_s::type
```

Queue type.

Definition at line 2271 of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa.h](#)

## 6.108 hsa\_region\_s Struct Reference

A memory region represents a block of virtual memory with certain properties. For example, the HSA runtime represents fine-grained memory in the global segment using a region. A region might be associated with more than one agent.

```
#include <hsa.h>
```

### Public Attributes

- uint64\_t [handle](#)

### 6.108.1 Detailed Description

A memory region represents a block of virtual memory with certain properties. For example, the HSA runtime represents fine-grained memory in the global segment using a region. A region might be associated with more than one agent.

Definition at line 2193 of file [hsa.h](#).

### 6.108.2 Member Data Documentation

#### 6.108.2.1 handle

```
uint64_t hsa_region_s::handle
```

Opaque handle. Two handles reference the same object of the enclosing type if and only if they are equal.

Definition at line 2198 of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- /home/alexv/Programming/ROCR-Runtime/include/hsa.h

## 6.109 hsa\_signal\_group\_s Struct Reference

Group of signals.

```
#include <hsa.h>
```

### Public Attributes

- uint64\_t [handle](#)

### 6.109.1 Detailed Description

Group of signals.

Definition at line 2053 of file [hsa.h](#).

### 6.109.2 Member Data Documentation

#### 6.109.2.1 handle

```
uint64_t hsa_signal_group_s::handle
```

Opaque handle. Two handles reference the same object of the enclosing type if and only if they are equal.

Definition at line 2058 of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa.h](#)

## 6.110 hsa\_signal\_s Struct Reference

Signal handle.

```
#include <hsa.h>
```

### Public Attributes

- `uint64_t` [handle](#)

#### 6.110.1 Detailed Description

Signal handle.

Definition at line 1325 of file [hsa.h](#).

#### 6.110.2 Member Data Documentation



### 6.110.2.1 handle

```
uint64_t hsa_signal_s::handle
```

Opaque handle. Two handles reference the same object of the enclosing type if and only if they are equal. The value 0 is reserved.

Definition at line 1330 of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- `/home/alexv/Programming/ROCR-Runtime/include/hsa.h`

## 6.111 hsa\_ven\_amd\_aqlprofile\_1\_00\_pfn\_s Struct Reference

Extension function table.

```
#include <hsa_ven_amd_aqlprofile.h>
```

### Public Attributes

- `uint32_t(* hsa_ven_amd_aqlprofile_version_major)()`
- `uint32_t(* hsa_ven_amd_aqlprofile_version_minor)()`
- `hsa_status_t(* hsa_ven_amd_aqlprofile_error_string)(const char **str)`
- `hsa_status_t(* hsa_ven_amd_aqlprofile_validate_event)(hsa_agent_t agent, const hsa_ven_amd_aqlprofile_event_t *event, bool *result)`
- `hsa_status_t(* hsa_ven_amd_aqlprofile_start)(hsa_ven_amd_aqlprofile_profile_t *profile, hsa_ext_amd_aql_pm4_packet_t *aql_start_packet)`
- `hsa_status_t(* hsa_ven_amd_aqlprofile_stop)(const hsa_ven_amd_aqlprofile_profile_t *profile, hsa_ext_amd_aql_pm4_packet_t *aql_stop_packet)`
- `hsa_status_t(* hsa_ven_amd_aqlprofile_read)(const hsa_ven_amd_aqlprofile_profile_t *profile, hsa_ext_amd_aql_pm4_packet_t *aql_read_packet)`
- `hsa_status_t(* hsa_ven_amd_aqlprofile_legacy_get_pm4)(const hsa_ext_amd_aql_pm4_packet_t *aql_packet, void *data)`
- `hsa_status_t(* hsa_ven_amd_aqlprofile_get_info)(const hsa_ven_amd_aqlprofile_profile_t *profile, hsa_ven_amd_aqlprofile_info_type_t attribute, void *value)`
- `hsa_status_t(* hsa_ven_amd_aqlprofile_iterate_data)(const hsa_ven_amd_aqlprofile_profile_t *profile, hsa_ven_amd_aqlprofile_data_callback_t callback, void *data)`

### 6.111.1 Detailed Description

Extension function table.

Definition at line 317 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.111.2 Member Data Documentation

#### 6.111.2.1 hsa\_ven\_amd\_aqlprofile\_error\_string

```
hsa_status_t(* hsa_ven_amd_aqlprofile_1_00_pfn_s::hsa_ven_amd_aqlprofile_error_string) (const char **str)
```

Definition at line 321 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

#### 6.111.2.2 hsa\_ven\_amd\_aqlprofile\_get\_info

```
hsa_status_t(* hsa_ven_amd_aqlprofile_1_00_pfn_s::hsa_ven_amd_aqlprofile_get_info) (const hsa_ven_amd_aqlprofile_profile_t *profile, hsa_ven_amd_aqlprofile_info_type_t attribute, void *value)
```

Definition at line 345 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

#### 6.111.2.3 hsa\_ven\_amd\_aqlprofile\_iterate\_data

```
hsa_status_t(* hsa_ven_amd_aqlprofile_1_00_pfn_s::hsa_ven_amd_aqlprofile_iterate_data) (const hsa_ven_amd_aqlprofile_profile_t *profile, hsa_ven_amd_aqlprofile_data_callback_t callback, void *data)
```

Definition at line 350 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

#### 6.111.2.4 hsa\_ven\_amd\_aqlprofile\_legacy\_get\_pm4

```
hsa_status_t(* hsa_ven_amd_aqlprofile_1_00_pfn_s::hsa_ven_amd_aqlprofile_legacy_get_pm4) (const hsa_ext_amd_aql_pm4_packet_t *aql_packet, void *data)
```

Definition at line 341 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

#### 6.111.2.5 hsa\_ven\_amd\_aqlprofile\_read

```
hsa_status_t(* hsa_ven_amd_aqlprofile_1_00_pfn_s::hsa_ven_amd_aqlprofile_read) (const hsa_ven_amd_aqlprofile_profile_t *profile, hsa_ext_amd_aql_pm4_packet_t *aql_read_packet)
```

Definition at line 337 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.111.2.6 hsa\_ven\_amd\_aqlprofile\_start

```
hsa_status_t (* hsa_ven_amd_aqlprofile_1_00_pfn_s::hsa_ven_amd_aqlprofile_start) (hsa_ven_amd_aqlprofile_profile_t
*profile, hsa_ext_amd_aql_pm4_packet_t *aql_start_packet)
```

Definition at line 329 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.111.2.7 hsa\_ven\_amd\_aqlprofile\_stop

```
hsa_status_t (* hsa_ven_amd_aqlprofile_1_00_pfn_s::hsa_ven_amd_aqlprofile_stop) (const hsa_ven_amd_aqlprofile_profile_t
*profile, hsa_ext_amd_aql_pm4_packet_t *aql_stop_packet)
```

Definition at line 333 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.111.2.8 hsa\_ven\_amd\_aqlprofile\_validate\_event

```
hsa_status_t (* hsa_ven_amd_aqlprofile_1_00_pfn_s::hsa_ven_amd_aqlprofile_validate_event) (hsa_agent_t
agent, const hsa_ven_amd_aqlprofile_event_t *event, bool *result)
```

Definition at line 324 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.111.2.9 hsa\_ven\_amd\_aqlprofile\_version\_major

```
uint32_t (* hsa_ven_amd_aqlprofile_1_00_pfn_s::hsa_ven_amd_aqlprofile_version_major) ()
```

Definition at line 318 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.111.2.10 hsa\_ven\_amd\_aqlprofile\_version\_minor

```
uint32_t (* hsa_ven_amd_aqlprofile_1_00_pfn_s::hsa_ven_amd_aqlprofile_version_minor) ()
```

Definition at line 319 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ven\\_amd\\_aqlprofile.h](#)

## 6.112 hsa\_ven\_amd\_aqlprofile\_descriptor\_t Struct Reference

### Public Attributes

- void \* [ptr](#)
- uint32\_t [size](#)

### 6.112.1 Detailed Description

Definition at line 176 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.112.2 Member Data Documentation

#### 6.112.2.1 ptr

```
void* hsa_ven_amd_aqlprofile_descriptor_t::ptr
```

Definition at line 177 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

#### 6.112.2.2 size

```
uint32_t hsa_ven_amd_aqlprofile_descriptor_t::size
```

Definition at line 178 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

The documentation for this struct was generated from the following file:

- /home/alexv/Programming/ROCR-Runtime/include/hsa\_ven\_amd\_aqlprofile.h

## 6.113 hsa\_ven\_amd\_aqlprofile\_event\_t Struct Reference

### Public Attributes

- hsa\_ven\_amd\_aqlprofile\_block\_name\_t [block\\_name](#)
- uint32\_t [block\\_index](#)
- uint32\_t [counter\\_id](#)

### 6.113.1 Detailed Description

Definition at line 132 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

## 6.113.2 Member Data Documentation

### 6.113.2.1 block\_index

uint32\_t hsa\_ven\_amd\_aqlprofile\_event\_t::block\_index

Definition at line 134 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.113.2.2 block\_name

hsa\_ven\_amd\_aqlprofile\_block\_name\_t hsa\_ven\_amd\_aqlprofile\_event\_t::block\_name

Definition at line 133 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.113.2.3 counter\_id

uint32\_t hsa\_ven\_amd\_aqlprofile\_event\_t::counter\_id

Definition at line 135 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

The documentation for this struct was generated from the following file:

- /home/alexv/Programming/ROCR-Runtime/include/hsa\_ven\_amd\_aqlprofile.h

## 6.114 hsa\_ven\_amd\_aqlprofile\_id\_query\_t Struct Reference

### Public Attributes

- const char \* [name](#)
- uint32\_t [id](#)
- uint32\_t [instance\\_count](#)

### 6.114.1 Detailed Description

Definition at line 255 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

## 6.114.2 Member Data Documentation

#### 6.114.2.1 id

```
uint32_t hsa_ven_amd_aqlprofile_id_query_t::id
```

Definition at line 257 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

#### 6.114.2.2 instance\_count

```
uint32_t hsa_ven_amd_aqlprofile_id_query_t::instance_count
```

Definition at line 258 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

#### 6.114.2.3 name

```
const char* hsa_ven_amd_aqlprofile_id_query_t::name
```

Definition at line 256 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ven\\_amd\\_aqlprofile.h](#)

### 6.115 hsa\_ven\_amd\_aqlprofile\_info\_data\_t Struct Reference

Collaboration diagram for [hsa\\_ven\\_amd\\_aqlprofile\\_info\\_data\\_t](#):

#### Public Attributes

- [uint32\\_t](#) [sample\\_id](#)
- - union {
    - struct {
      - [hsa\\_ven\\_amd\\_aqlprofile\\_event\\_t](#) event
      - [uint64\\_t](#) result
    - } [pmc\\_data](#)
    - [hsa\\_ven\\_amd\\_aqlprofile\\_descriptor\\_t](#) trace\_data

#### 6.115.1 Detailed Description

Definition at line 243 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

## 6.115.2 Member Data Documentation

### 6.115.2.1 event

[hsa\\_ven\\_amd\\_aqlprofile\\_event\\_t](#) [hsa\\_ven\\_amd\\_aqlprofile\\_info\\_data\\_t::event](#)

Definition at line 247 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.115.2.2 result

[uint64\\_t](#) [hsa\\_ven\\_amd\\_aqlprofile\\_info\\_data\\_t::result](#)

Definition at line 248 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.115.2.3 sample\_id

[uint32\\_t](#) [hsa\\_ven\\_amd\\_aqlprofile\\_info\\_data\\_t::sample\\_id](#)

Definition at line 244 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.115.2.4 trace\_data

[hsa\\_ven\\_amd\\_aqlprofile\\_descriptor\\_t](#) [hsa\\_ven\\_amd\\_aqlprofile\\_info\\_data\\_t::trace\\_data](#)

Definition at line 250 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ven\\_amd\\_aqlprofile.h](#)

## 6.116 hsa\_ven\_amd\_aqlprofile\_parameter\_t Struct Reference

### Public Attributes

- [hsa\\_ven\\_amd\\_aqlprofile\\_parameter\\_name\\_t](#) [parameter\\_name](#)
- [uint32\\_t](#) [value](#)

### 6.116.1 Detailed Description

Definition at line 160 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.116.2 Member Data Documentation

#### 6.116.2.1 parameter\_name

```
hsa_ven_amd_aqlprofile_parameter_name_t hsa_ven_amd_aqlprofile_parameter_t::parameter_name
```

Definition at line 161 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

#### 6.116.2.2 value

```
uint32_t hsa_ven_amd_aqlprofile_parameter_t::value
```

Definition at line 162 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ven\\_amd\\_aqlprofile.h](#)

## 6.117 hsa\_ven\_amd\_aqlprofile\_profile\_t Struct Reference

Collaboration diagram for [hsa\\_ven\\_amd\\_aqlprofile\\_profile\\_t](#):

### Public Attributes

- [hsa\\_agent\\_t](#) agent
- [hsa\\_ven\\_amd\\_aqlprofile\\_event\\_type\\_t](#) type
- const [hsa\\_ven\\_amd\\_aqlprofile\\_event\\_t](#) \* events
- [uint32\\_t](#) event\_count
- const [hsa\\_ven\\_amd\\_aqlprofile\\_parameter\\_t](#) \* parameters
- [uint32\\_t](#) parameter\_count
- [hsa\\_ven\\_amd\\_aqlprofile\\_descriptor\\_t](#) output\_buffer
- [hsa\\_ven\\_amd\\_aqlprofile\\_descriptor\\_t](#) command\_buffer

### 6.117.1 Detailed Description

Definition at line 184 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).



## 6.117.2 Member Data Documentation

### 6.117.2.1 agent

`hsa_agent_t` `hsa_ven_amd_aqlprofile_profile_t::agent`

Definition at line 185 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.117.2.2 command\_buffer

`hsa_ven_amd_aqlprofile_descriptor_t` `hsa_ven_amd_aqlprofile_profile_t::command_buffer`

Definition at line 192 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.117.2.3 event\_count

`uint32_t` `hsa_ven_amd_aqlprofile_profile_t::event_count`

Definition at line 188 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.117.2.4 events

`const` `hsa_ven_amd_aqlprofile_event_t*` `hsa_ven_amd_aqlprofile_profile_t::events`

Definition at line 187 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.117.2.5 output\_buffer

`hsa_ven_amd_aqlprofile_descriptor_t` `hsa_ven_amd_aqlprofile_profile_t::output_buffer`

Definition at line 191 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.117.2.6 parameter\_count

```
uint32_t hsa_ven_amd_aqlprofile_profile_t::parameter_count
```

Definition at line 190 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.117.2.7 parameters

```
const hsa_ven_amd_aqlprofile_parameter_t* hsa_ven_amd_aqlprofile_profile_t::parameters
```

Definition at line 189 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

### 6.117.2.8 type

```
hsa_ven_amd_aqlprofile_event_type_t hsa_ven_amd_aqlprofile_profile_t::type
```

Definition at line 186 of file [hsa\\_ven\\_amd\\_aqlprofile.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ven\\_amd\\_aqlprofile.h](#)

## 6.118 hsa\_ven\_amd\_loader\_1\_00\_pfn\_s Struct Reference

Extension function table version 1.00.

```
#include <hsa_ven_amd_loader.h>
```

### Public Attributes

- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_query\\_host\\_address](#) )(const void \*device\_address, const void \*\*host\_address)
- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_query\\_segment\\_descriptors](#) )(hsa\_ven\_amd\_loader\_segment\_descriptor\_t \*segment\_descriptors, size\_t \*num\_segment\_descriptors)
- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_query\\_executable](#) )(const void \*device\_address, [hsa\\_executable\\_t](#) \*executable)

### 6.118.1 Detailed Description

Extension function table version 1.00.

Definition at line 538 of file [hsa\\_ven\\_amd\\_loader.h](#).

## 6.118.2 Member Data Documentation

### 6.118.2.1 hsa\_ven\_amd\_loader\_query\_executable

```
hsa_status_t(* hsa_ven_amd_loader_1_00_pfn_s::hsa_ven_amd_loader_query_executable) (const void
*device_address, hsa_executable_t *executable)
```

Definition at line 547 of file [hsa\\_ven\\_amd\\_loader.h](#).

### 6.118.2.2 hsa\_ven\_amd\_loader\_query\_host\_address

```
hsa_status_t(* hsa_ven_amd_loader_1_00_pfn_s::hsa_ven_amd_loader_query_host_address) (const
void *device_address, const void **host_address)
```

Definition at line 539 of file [hsa\\_ven\\_amd\\_loader.h](#).

### 6.118.2.3 hsa\_ven\_amd\_loader\_query\_segment\_descriptors

```
hsa_status_t(* hsa_ven_amd_loader_1_00_pfn_s::hsa_ven_amd_loader_query_segment_descriptors)
(hsa_ven_amd_loader_segment_descriptor_t *segment_descriptors, size_t *num_segment_descriptors)
```

Definition at line 543 of file [hsa\\_ven\\_amd\\_loader.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ven\\_amd\\_loader.h](#)

## 6.119 hsa\_ven\_amd\_loader\_1\_01\_pfn\_s Struct Reference

Extension function table version 1.01.

```
#include <hsa_ven_amd_loader.h>
```

### Public Attributes

- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_query\\_host\\_address](#) )(const void \*device\_address, const void \*\*host\_address)
- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_query\\_segment\\_descriptors](#) )(hsa\_ven\_amd\_loader\_segment\_descriptor\_t \*segment\_descriptors, size\_t \*num\_segment\_descriptors)
- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_query\\_executable](#) )(const void \*device\_address, [hsa\\_executable\\_t](#) \*executable)
- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_executable\\_iterate\\_loaded\\_code\\_objects](#) )(hsa\_executable\_t executable, [hsa\\_status\\_t](#)(\*callback)(hsa\_executable\_t executable, [hsa\\_loaded\\_code\\_object\\_t](#) loaded\_code\_↵ object, void \*data), void \*data)
- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_loaded\\_code\\_object\\_get\\_info](#) )(hsa\_loaded\_code\_object\_t loaded\_↵ code\_object, hsa\_ven\_amd\_loader\_loaded\_code\_object\_info\_t attribute, void \*value)

### 6.119.1 Detailed Description

Extension function table version 1.01.

Definition at line 555 of file [hsa\\_ven\\_amd\\_loader.h](#).

### 6.119.2 Member Data Documentation

#### 6.119.2.1 hsa\_ven\_amd\_loader\_executable\_iterate\_loaded\_code\_objects

```
hsa_status_t(* hsa_ven_amd_loader_1_01_pfn_s::hsa_ven_amd_loader_executable_iterate_loaded_code_objects) (hsa_executable_t executable, hsa_status_t(*callback)(hsa_executable_t executable, hsa_loaded_code_object_t loaded_code_object, void *data), void *data)
```

Definition at line 568 of file [hsa\\_ven\\_amd\\_loader.h](#).

#### 6.119.2.2 hsa\_ven\_amd\_loader\_loaded\_code\_object\_get\_info

```
hsa_status_t(* hsa_ven_amd_loader_1_01_pfn_s::hsa_ven_amd_loader_loaded_code_object_get_info) (hsa_loaded_code_object_t loaded_code_object, hsa_ven_amd_loader_loaded_code_object_info_t attribute, void *value)
```

Definition at line 576 of file [hsa\\_ven\\_amd\\_loader.h](#).

#### 6.119.2.3 hsa\_ven\_amd\_loader\_query\_executable

```
hsa_status_t(* hsa_ven_amd_loader_1_01_pfn_s::hsa_ven_amd_loader_query_executable) (const void *device_address, hsa_executable_t *executable)
```

Definition at line 564 of file [hsa\\_ven\\_amd\\_loader.h](#).

#### 6.119.2.4 hsa\_ven\_amd\_loader\_query\_host\_address

```
hsa_status_t(* hsa_ven_amd_loader_1_01_pfn_s::hsa_ven_amd_loader_query_host_address) (const void *device_address, const void **host_address)
```

Definition at line 556 of file [hsa\\_ven\\_amd\\_loader.h](#).

### 6.119.2.5 hsa\_ven\_amd\_loader\_query\_segment\_descriptors

```
hsa_status_t(* hsa_ven_amd_loader_1_01_pfn_s::hsa_ven_amd_loader_query_segment_descriptors)
(hsa_ven_amd_loader_segment_descriptor_t *segment_descriptors, size_t *num_segment_descriptors)
```

Definition at line 560 of file [hsa\\_ven\\_amd\\_loader.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ven\\_amd\\_loader.h](#)

## 6.120 hsa\_ven\_amd\_loader\_1\_02\_pfn\_s Struct Reference

Extension function table version 1.02.

```
#include <hsa_ven_amd_loader.h>
```

### Public Attributes

- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_query\\_host\\_address](#) )(const void \*device\_address, const void \*\*host\_address)
- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_query\\_segment\\_descriptors](#) )(hsa\_ven\_amd\_loader\_segment\_descriptor\_t \*segment\_descriptors, size\_t \*num\_segment\_descriptors)
- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_query\\_executable](#) )(const void \*device\_address, [hsa\\_executable\\_t](#) \*executable)
- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_executable\\_iterate\\_loaded\\_code\\_objects](#) )(hsa\_executable\_t executable, [hsa\\_status\\_t](#)(\*callback)(hsa\_executable\_t executable, [hsa\\_loaded\\_code\\_object\\_t](#) loaded\_code\_object, void \*data), void \*data)
- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_loaded\\_code\\_object\\_get\\_info](#) )(hsa\_loaded\_code\_object\_t loaded\_code\_object, hsa\_ven\_amd\_loader\_loaded\_code\_object\_info\_t attribute, void \*value)
- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_code\\_object\\_reader\\_create\\_from\\_file\\_with\\_offset\\_size](#) )(hsa\_file\_t file, size\_t offset, size\_t size, [hsa\\_code\\_object\\_reader\\_t](#) \*code\_object\_reader)

### 6.120.1 Detailed Description

Extension function table version 1.02.

Definition at line 585 of file [hsa\\_ven\\_amd\\_loader.h](#).

### 6.120.2 Member Data Documentation

#### 6.120.2.1 hsa\_ven\_amd\_loader\_code\_object\_reader\_create\_from\_file\_with\_offset\_size

```
hsa_status_t(* hsa_ven_amd_loader_1_02_pfn_s::hsa_ven_amd_loader_code_object_reader_create↵
from_file_with_offset_size) (hsa_file_t file, size_t offset, size_t size, hsa_code_object_reader_t
*code_object_reader)
```

Definition at line 611 of file [hsa\\_ven\\_amd\\_loader.h](#).

#### 6.120.2.2 hsa\_ven\_amd\_loader\_executable\_iterate\_loaded\_code\_objects

```
hsa_status_t(* hsa_ven_amd_loader_1_02_pfn_s::hsa_ven_amd_loader_executable_iterate_loaded↵
code_objects) (hsa_executable_t executable, hsa_status_t(*callback) (hsa_executable_t executable,
hsa_loaded_code_object_t loaded_code_object, void *data), void *data)
```

Definition at line 598 of file [hsa\\_ven\\_amd\\_loader.h](#).

#### 6.120.2.3 hsa\_ven\_amd\_loader\_loaded\_code\_object\_get\_info

```
hsa_status_t(* hsa_ven_amd_loader_1_02_pfn_s::hsa_ven_amd_loader_loaded_code_object_get_info)
(hsa_loaded_code_object_t loaded_code_object, hsa_ven_amd_loader_loaded_code_object_info_t↵
attribute, void *value)
```

Definition at line 606 of file [hsa\\_ven\\_amd\\_loader.h](#).

#### 6.120.2.4 hsa\_ven\_amd\_loader\_query\_executable

```
hsa_status_t(* hsa_ven_amd_loader_1_02_pfn_s::hsa_ven_amd_loader_query_executable) (const void
*device_address, hsa_executable_t *executable)
```

Definition at line 594 of file [hsa\\_ven\\_amd\\_loader.h](#).

#### 6.120.2.5 hsa\_ven\_amd\_loader\_query\_host\_address

```
hsa_status_t(* hsa_ven_amd_loader_1_02_pfn_s::hsa_ven_amd_loader_query_host_address) (const
void *device_address, const void **host_address)
```

Definition at line 586 of file [hsa\\_ven\\_amd\\_loader.h](#).

### 6.120.2.6 hsa\_ven\_amd\_loader\_query\_segment\_descriptors

```
hsa_status_t(* hsa_ven_amd_loader_1_02_pfn_s::hsa_ven_amd_loader_query_segment_descriptors)
(hsa_ven_amd_loader_segment_descriptor_t *segment_descriptors, size_t *num_segment_descriptors)
```

Definition at line 590 of file [hsa\\_ven\\_amd\\_loader.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ven\\_amd\\_loader.h](#)

## 6.121 hsa\_ven\_amd\_loader\_1\_03\_pfn\_s Struct Reference

Extension function table version 1.03.

```
#include <hsa_ven_amd_loader.h>
```

### Public Attributes

- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_query\\_host\\_address](#) )(const void \*device\_address, const void \*\*host\_address)
- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_query\\_segment\\_descriptors](#) )(hsa\_ven\_amd\_loader\_segment\_descriptor\_t \*segment\_descriptors, size\_t \*num\_segment\_descriptors)
- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_query\\_executable](#) )(const void \*device\_address, [hsa\\_executable\\_t](#) \*executable)
- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_executable\\_iterate\\_loaded\\_code\\_objects](#) )(hsa\_executable\_t executable, [hsa\\_status\\_t](#)(\*callback)(hsa\_executable\_t executable, [hsa\\_loaded\\_code\\_object\\_t](#) loaded\_code\_object, void \*data), void \*data)
- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_loaded\\_code\\_object\\_get\\_info](#) )(hsa\_loaded\_code\_object\_t loaded\_code\_object, hsa\_ven\_amd\_loader\_loaded\_code\_object\_info\_t attribute, void \*value)
- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_code\\_object\\_reader\\_create\\_from\\_file\\_with\\_offset\\_size](#) )(hsa\_file\_t file, size\_t offset, size\_t size, [hsa\\_code\\_object\\_reader\\_t](#) \*code\_object\_reader)
- [hsa\\_status\\_t](#)(\* [hsa\\_ven\\_amd\\_loader\\_iterate\\_executables](#) )(hsa\_status\_t(\*callback)(hsa\_executable\_t executable, void \*data), void \*data)

### 6.121.1 Detailed Description

Extension function table version 1.03.

Definition at line 622 of file [hsa\\_ven\\_amd\\_loader.h](#).

### 6.121.2 Member Data Documentation

#### 6.121.2.1 hsa\_ven\_amd\_loader\_code\_object\_reader\_create\_from\_file\_with\_offset\_size

```
hsa_status_t(* hsa_ven_amd_loader_1_03_pfn_s::hsa_ven_amd_loader_code_object_reader_create_↵
from_file_with_offset_size) (hsa_file_t file, size_t offset, size_t size, hsa_code_object_reader_t
*code_object_reader)
```

Definition at line 648 of file [hsa\\_ven\\_amd\\_loader.h](#).

#### 6.121.2.2 hsa\_ven\_amd\_loader\_executable\_iterate\_loaded\_code\_objects

```
hsa_status_t(* hsa_ven_amd_loader_1_03_pfn_s::hsa_ven_amd_loader_executable_iterate_loaded_↵
code_objects) (hsa_executable_t executable, hsa_status_t(*callback) (hsa_executable_t executable,
hsa_loaded_code_object_t loaded_code_object, void *data), void *data)
```

Definition at line 635 of file [hsa\\_ven\\_amd\\_loader.h](#).

#### 6.121.2.3 hsa\_ven\_amd\_loader\_iterate\_executables

```
hsa_status_t(* hsa_ven_amd_loader_1_03_pfn_s::hsa_ven_amd_loader_iterate_executables) (hsa_status_t(*callback)
hsa_executable_t executable, void *data), void *data)
```

Definition at line 655 of file [hsa\\_ven\\_amd\\_loader.h](#).

#### 6.121.2.4 hsa\_ven\_amd\_loader\_loaded\_code\_object\_get\_info

```
hsa_status_t(* hsa_ven_amd_loader_1_03_pfn_s::hsa_ven_amd_loader_loaded_code_object_get_info)
(hsa_loaded_code_object_t loaded_code_object, hsa_ven_amd_loader_loaded_code_object_info_↵
t attribute, void *value)
```

Definition at line 643 of file [hsa\\_ven\\_amd\\_loader.h](#).

#### 6.121.2.5 hsa\_ven\_amd\_loader\_query\_executable

```
hsa_status_t(* hsa_ven_amd_loader_1_03_pfn_s::hsa_ven_amd_loader_query_executable) (const void
*device_address, hsa_executable_t *executable)
```

Definition at line 631 of file [hsa\\_ven\\_amd\\_loader.h](#).



### 6.121.2.6 hsa\_ven\_amd\_loader\_query\_host\_address

```
hsa_status_t(* hsa_ven_amd_loader_1_03_pfn_s::hsa_ven_amd_loader_query_host_address) (const
void *device_address, const void **host_address)
```

Definition at line 623 of file [hsa\\_ven\\_amd\\_loader.h](#).

### 6.121.2.7 hsa\_ven\_amd\_loader\_query\_segment\_descriptors

```
hsa_status_t(* hsa_ven_amd_loader_1_03_pfn_s::hsa_ven_amd_loader_query_segment_descriptors)
(hsa_ven_amd_loader_segment_descriptor_t *segment_descriptors, size_t *num_segment_descriptors)
```

Definition at line 627 of file [hsa\\_ven\\_amd\\_loader.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ven\\_amd\\_loader.h](#)

## 6.122 hsa\_ven\_amd\_loader\_segment\_descriptor\_s Struct Reference

Loaded memory segment descriptor.

```
#include <hsa_ven_amd_loader.h>
```

Collaboration diagram for `hsa_ven_amd_loader_segment_descriptor_s`:

### Public Attributes

- [hsa\\_agent\\_t](#) agent
- [hsa\\_executable\\_t](#) executable
- [hsa\\_ven\\_amd\\_loader\\_code\\_object\\_storage\\_type\\_t](#) [code\\_object\\_storage\\_type](#)
- const void \* [code\\_object\\_storage\\_base](#)
- size\_t [code\\_object\\_storage\\_size](#)
- size\_t [code\\_object\\_storage\\_offset](#)
- const void \* [segment\\_base](#)
- size\_t [segment\\_size](#)

### 6.122.1 Detailed Description

Loaded memory segment descriptor.

Loaded memory segment descriptor describes underlying loaded memory segment. Loaded memory segment is created/allocated by the executable during the loading of the code object that is backing underlying memory segment.

The lifetime of underlying memory segment is limited by the lifetime of the executable that is managing underlying memory segment.

Definition at line 123 of file [hsa\\_ven\\_amd\\_loader.h](#).

## 6.122.2 Member Data Documentation

### 6.122.2.1 agent

```
hsa_agent_t hsa_ven_amd_loader_segment_descriptor_s::agent
```

Agent underlying memory segment is allocated on. If the code object that is backing underlying memory segment is program code object, then 0.

Definition at line 128 of file [hsa\\_ven\\_amd\\_loader.h](#).

### 6.122.2.2 code\_object\_storage\_base

```
const void* hsa_ven_amd_loader_segment_descriptor_s::code_object_storage_base
```

If the storage type of the code object that is backing underlying memory segment is:

- HSA\_VEN\_AMD\_LOADER\_CODE\_OBJECT\_STORAGE\_TYPE\_NONE, then null;
- HSA\_VEN\_AMD\_LOADER\_CODE\_OBJECT\_STORAGE\_TYPE\_FILE, then null-terminated filepath to the code object;
- HSA\_VEN\_AMD\_LOADER\_CODE\_OBJECT\_STORAGE\_TYPE\_MEMORY, then host accessible pointer to the first byte of the code object.

Definition at line 146 of file [hsa\\_ven\\_amd\\_loader.h](#).

### 6.122.2.3 code\_object\_storage\_offset

```
size_t hsa_ven_amd_loader_segment_descriptor_s::code_object_storage_offset
```

If the storage type of the code object that is backing underlying memory segment is:

- HSA\_VEN\_AMD\_LOADER\_CODE\_OBJECT\_STORAGE\_TYPE\_NONE, then 0;
- other, then offset, in bytes, from the beginning of the code object to the first byte in the code object data is copied from.

Definition at line 164 of file [hsa\\_ven\\_amd\\_loader.h](#).

#### 6.122.2.4 code\_object\_storage\_size

```
size_t hsa_ven_amd_loader_segment_descriptor_s::code_object_storage_size
```

If the storage type of the code object that is backing underlying memory segment is:

- HSA\_VEN\_AMD\_LOADER\_CODE\_OBJECT\_STORAGE\_TYPE\_NONE, then 0;
- HSA\_VEN\_AMD\_LOADER\_CODE\_OBJECT\_STORAGE\_TYPE\_FILE, then the length of the filepath to the code object (including null-terminating character);
- HSA\_VEN\_AMD\_LOADER\_CODE\_OBJECT\_STORAGE\_TYPE\_MEMORY, then the size, in bytes, of the memory occupied by the code object.

Definition at line 156 of file [hsa\\_ven\\_amd\\_loader.h](#).

#### 6.122.2.5 code\_object\_storage\_type

```
hsa_ven_amd_loader_code_object_storage_type_t hsa_ven_amd_loader_segment_descriptor_s::code_↵
object_storage_type
```

Storage type of the code object that is backing underlying memory segment.

Definition at line 136 of file [hsa\\_ven\\_amd\\_loader.h](#).

#### 6.122.2.6 executable

```
hsa_executable_t hsa_ven_amd_loader_segment_descriptor_s::executable
```

Executable that is managing this underlying memory segment.

Definition at line 132 of file [hsa\\_ven\\_amd\\_loader.h](#).

#### 6.122.2.7 segment\_base

```
const void* hsa_ven_amd_loader_segment_descriptor_s::segment_base
```

Starting address of the underlying memory segment.

Definition at line 168 of file [hsa\\_ven\\_amd\\_loader.h](#).

### 6.122.2.8 segment\_size

```
size_t hsa_ven_amd_loader_segment_descriptor_s::segment_size
```

Size, in bytes, of the underlying memory segment.

Definition at line 172 of file [hsa\\_ven\\_amd\\_loader.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_ven\\_amd\\_loader.h](#)

## 6.123 hsa\_wavefront\_s Struct Reference

Wavefront handle.

```
#include <hsa.h>
```

### Public Attributes

- `uint64_t` [handle](#)

### 6.123.1 Detailed Description

Wavefront handle.

Definition at line 3900 of file [hsa.h](#).

### 6.123.2 Member Data Documentation

#### 6.123.2.1 handle

```
uint64_t hsa_wavefront_s::handle
```

Opaque handle. Two handles reference the same object of the enclosing type if and only if they are equal.

Definition at line 3905 of file [hsa.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa.h](#)

## 6.124 HsaApiTable Struct Reference

Collaboration diagram for HsaApiTable:

### Public Attributes

- [ApiTableVersion](#) version
- [CoreApiTable](#) \* core\_
- [AmdExtTable](#) \* amd\_ext\_
- [FinalizerExtTable](#) \* finalizer\_ext\_
- [ImageExtTable](#) \* image\_ext\_

### 6.124.1 Detailed Description

Definition at line 372 of file [hsa\\_api\\_trace.h](#).

### 6.124.2 Member Data Documentation

#### 6.124.2.1 amd\_ext\_

[AmdExtTable](#)\* HsaApiTable::amd\_ext\_

Definition at line 381 of file [hsa\\_api\\_trace.h](#).

#### 6.124.2.2 core\_

[CoreApiTable](#)\* HsaApiTable::core\_

Definition at line 378 of file [hsa\\_api\\_trace.h](#).

#### 6.124.2.3 finalizer\_ext\_

[FinalizerExtTable](#)\* HsaApiTable::finalizer\_ext\_

Definition at line 384 of file [hsa\\_api\\_trace.h](#).

#### 6.124.2.4 image\_ext\_

`ImageExtTable*` `HsaApiTable::image_ext_`

Definition at line 387 of file [hsa\\_api\\_trace.h](#).

#### 6.124.2.5 version

`ApiTableVersion` `HsaApiTable::version`

Definition at line 375 of file [hsa\\_api\\_trace.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_api\\_trace.h](#)

## 6.125 HsaApiTableContainer Struct Reference

Collaboration diagram for HsaApiTableContainer:

### Public Attributes

- [HsaApiTable](#) root
- [CoreApiTable](#) core
- [AmdExtTable](#) amd\_ext
- [FinalizerExtTable](#) finalizer\_ext
- [ImageExtTable](#) image\_ext

### 6.125.1 Detailed Description

Definition at line 391 of file [hsa\\_api\\_trace.h](#).

### 6.125.2 Constructor & Destructor Documentation

#### 6.125.2.1 HsaApiTableContainer()

`HsaApiTableContainer::HsaApiTableContainer ( )` `[inline]`

Definition at line 399 of file [hsa\\_api\\_trace.h](#).

## 6.125.3 Member Data Documentation

### 6.125.3.1 amd\_ext

`AmdExtTable` HsaApiTableContainer::amd\_ext

Definition at line 394 of file [hsa\\_api\\_trace.h](#).

### 6.125.3.2 core

`CoreApiTable` HsaApiTableContainer::core

Definition at line 393 of file [hsa\\_api\\_trace.h](#).

### 6.125.3.3 finalizer\_ext

`FinalizerExtTable` HsaApiTableContainer::finalizer\_ext

Definition at line 395 of file [hsa\\_api\\_trace.h](#).

### 6.125.3.4 image\_ext

`ImageExtTable` HsaApiTableContainer::image\_ext

Definition at line 396 of file [hsa\\_api\\_trace.h](#).

### 6.125.3.5 root

`HsaApiTable` HsaApiTableContainer::root

Definition at line 392 of file [hsa\\_api\\_trace.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_api\\_trace.h](#)

## 6.126 ImageExtTable Struct Reference

Collaboration diagram for ImageExtTable:

### Public Attributes

- [ApiTableVersion](#) version
- `decltype(hsa_ext_image_get_capability) * hsa_ext_image_get_capability_fn`
- `decltype(hsa_ext_image_data_get_info) * hsa_ext_image_data_get_info_fn`
- `decltype(hsa_ext_image_create) * hsa_ext_image_create_fn`
- `decltype(hsa_ext_image_import) * hsa_ext_image_import_fn`
- `decltype(hsa_ext_image_export) * hsa_ext_image_export_fn`
- `decltype(hsa_ext_image_copy) * hsa_ext_image_copy_fn`
- `decltype(hsa_ext_image_clear) * hsa_ext_image_clear_fn`
- `decltype(hsa_ext_image_destroy) * hsa_ext_image_destroy_fn`
- `decltype(hsa_ext_sampler_create) * hsa_ext_sampler_create_fn`
- `decltype(hsa_ext_sampler_destroy) * hsa_ext_sampler_destroy_fn`
- `decltype(hsa_ext_image_get_capability_with_layout) * hsa_ext_image_get_capability_with_layout_fn`
- `decltype(hsa_ext_image_data_get_info_with_layout) * hsa_ext_image_data_get_info_with_layout_fn`
- `decltype(hsa_ext_image_create_with_layout) * hsa_ext_image_create_with_layout_fn`

### 6.126.1 Detailed Description

Definition at line 122 of file [hsa\\_api\\_trace.h](#).

### 6.126.2 Member Data Documentation

#### 6.126.2.1 hsa\_ext\_image\_clear\_fn

```
decltype(hsa_ext_image_clear) * ImageExtTable::hsa_ext_image_clear_fn
```

Definition at line 130 of file [hsa\\_api\\_trace.h](#).

#### 6.126.2.2 hsa\_ext\_image\_copy\_fn

```
decltype(hsa_ext_image_copy) * ImageExtTable::hsa_ext_image_copy_fn
```

Definition at line 129 of file [hsa\\_api\\_trace.h](#).



### 6.126.2.3 hsa\_ext\_image\_create\_fn

```
decltype(hsa_ext_image_create) * ImageExtTable::hsa_ext_image_create_fn
```

Definition at line 126 of file [hsa\\_api\\_trace.h](#).

### 6.126.2.4 hsa\_ext\_image\_create\_with\_layout\_fn

```
decltype(hsa_ext_image_create_with_layout) * ImageExtTable::hsa_ext_image_create_with_layout_↵
fn
```

Definition at line 136 of file [hsa\\_api\\_trace.h](#).

### 6.126.2.5 hsa\_ext\_image\_data\_get\_info\_fn

```
decltype(hsa_ext_image_data_get_info) * ImageExtTable::hsa_ext_image_data_get_info_fn
```

Definition at line 125 of file [hsa\\_api\\_trace.h](#).

### 6.126.2.6 hsa\_ext\_image\_data\_get\_info\_with\_layout\_fn

```
decltype(hsa_ext_image_data_get_info_with_layout) * ImageExtTable::hsa_ext_image_data_get_↵
info_with_layout_fn
```

Definition at line 135 of file [hsa\\_api\\_trace.h](#).

### 6.126.2.7 hsa\_ext\_image\_destroy\_fn

```
decltype(hsa_ext_image_destroy) * ImageExtTable::hsa_ext_image_destroy_fn
```

Definition at line 131 of file [hsa\\_api\\_trace.h](#).

### 6.126.2.8 hsa\_ext\_image\_export\_fn

```
decltype(hsa_ext_image_export) * ImageExtTable::hsa_ext_image_export_fn
```

Definition at line 128 of file [hsa\\_api\\_trace.h](#).

### 6.126.2.9 hsa\_ext\_image\_get\_capability\_fn

```
decltype(hsa_ext_image_get_capability) * ImageExtTable::hsa_ext_image_get_capability_fn
```

Definition at line 124 of file [hsa\\_api\\_trace.h](#).

### 6.126.2.10 hsa\_ext\_image\_get\_capability\_with\_layout\_fn

```
decltype(hsa_ext_image_get_capability_with_layout) * ImageExtTable::hsa_ext_image_get_capability↵_with_layout_fn
```

Definition at line 134 of file [hsa\\_api\\_trace.h](#).

### 6.126.2.11 hsa\_ext\_image\_import\_fn

```
decltype(hsa_ext_image_import) * ImageExtTable::hsa_ext_image_import_fn
```

Definition at line 127 of file [hsa\\_api\\_trace.h](#).

### 6.126.2.12 hsa\_ext\_sampler\_create\_fn

```
decltype(hsa_ext_sampler_create) * ImageExtTable::hsa_ext_sampler_create_fn
```

Definition at line 132 of file [hsa\\_api\\_trace.h](#).

### 6.126.2.13 hsa\_ext\_sampler\_destroy\_fn

```
decltype(hsa_ext_sampler_destroy) * ImageExtTable::hsa_ext_sampler_destroy_fn
```

Definition at line 133 of file [hsa\\_api\\_trace.h](#).

### 6.126.2.14 version

```
ApiTableVersion ImageExtTable::version
```

Definition at line 123 of file [hsa\\_api\\_trace.h](#).

The documentation for this struct was generated from the following file:

- [/home/alexv/Programming/ROCR-Runtime/include/hsa\\_api\\_trace.h](#)

## Chapter 7

# File Documentation

### 7.1 amd\_hsa\_common.h

```
00001
00002 //
00003 // The University of Illinois/NCSA
00004 // Open Source License (NCSA)
00005 //
00006 // Copyright (c) 2014-2020, Advanced Micro Devices, Inc. All rights reserved.
00007 //
00008 // Developed by:
00009 //
00010 // AMD Research and AMD HSA Software Development
00011 //
00012 // Advanced Micro Devices, Inc.
00013 //
00014 // www.amd.com
00015 //
00016 // Permission is hereby granted, free of charge, to any person obtaining a copy
00017 // of this software and associated documentation files (the "Software"), to
00018 // deal with the Software without restriction, including without limitation
00019 // the rights to use, copy, modify, merge, publish, distribute, sublicense,
00020 // and/or sell copies of the Software, and to permit persons to whom the
00021 // Software is furnished to do so, subject to the following conditions:
00022 //
00023 // - Redistributions of source code must retain the above copyright notice,
00024 // this list of conditions and the following disclaimers.
00025 // - Redistributions in binary form must reproduce the above copyright
00026 // notice, this list of conditions and the following disclaimers in
00027 // the documentation and/or other materials provided with the distribution.
00028 // - Neither the names of Advanced Micro Devices, Inc,
00029 // nor the names of its contributors may be used to endorse or promote
00030 // products derived from this Software without specific prior written
00031 // permission.
00032 //
00033 // THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR
00034 // IMPLIED, INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY,
00035 // FITNESS FOR A PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL
00036 // THE CONTRIBUTORS OR COPYRIGHT HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR
00037 // OTHER LIABILITY, WHETHER IN AN ACTION OF CONTRACT, TORT OR OTHERWISE,
00038 // ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER
00039 // DEALINGS WITH THE SOFTWARE.
00040 //
00042
00043 // The following set of header files provides definitions for AMD GPU
00044 // Architecture:
00045 // - amd_hsa_common.h
00046 // - amd_hsa_elf.h
00047 // - amd_hsa_kernel_code.h
00048 // - amd_hsa_queue.h
00049 // - amd_hsa_signal.h
00050 //
00051 // Refer to "HSA Application Binary Interface: AMD GPU Architecture" for more
00052 // information.
00053
00054 #ifndef AMD_HSA_COMMON_H
00055 #define AMD_HSA_COMMON_H
00056
00057 #include <stddef.h>
00058 #include <stdint.h>
00059
```

```

00060 // Descriptive version of the HSA Application Binary Interface.
00061 #define AMD_HSA_ABI_VERSION "AMD GPU Architecture v0.35 (June 25, 2015)"
00062
00063 // Alignment attribute that specifies a minimum alignment (in bytes) for
00064 // variables of the specified type.
00065 #if defined(__GNUC__)
00066 # define __ALIGNED__(x) __attribute__((aligned(x)))
00067 #elif defined(_MSC_VER)
00068 # define __ALIGNED__(x) __declspec(aligned(x))
00069 #elif defined(RC_INVOKED)
00070 # define __ALIGNED__(x)
00071 #else
00072 # error
00073 #endif
00074
00075 // Creates enumeration entries for packed types. Enumeration entries include
00076 // bit shift amount, bit width, and bit mask.
00077 #define AMD_HSA_BITS_CREATE_ENUM_ENTRIES(name, shift, width) \
00078 name##_SHIFT = (shift), \
00079 name##_WIDTH = (width), \
00080 name = (((1 < (width)) - 1) << (shift)) \
00081
00082 // Gets bits for specified mask from specified src packed instance.
00083 #define AMD_HSA_BITS_GET(src, mask) \
00084 ((src & mask) >> mask ## _SHIFT) \
00085
00086 // Sets val bits for specified mask in specified dst packed instance.
00087 #define AMD_HSA_BITS_SET(dst, mask, val) \
00088 dst &= (~(1 << mask##_SHIFT) & ~mask); \
00089 dst |= (((val) << mask##_SHIFT) & mask) \
00090
00091 #endif // AMD_HSA_COMMON_H

```

## 7.2 amd\_hsa\_elf.h

```

00001
00002 //
00003 // The University of Illinois/NCSA
00004 // Open Source License (NCSA)
00005 //
00006 // Copyright (c) 2014-2020, Advanced Micro Devices, Inc. All rights reserved.
00007 //
00008 // Developed by:
00009 //
00010 // AMD Research and AMD HSA Software Development
00011 //
00012 // Advanced Micro Devices, Inc.
00013 //
00014 // www.amd.com
00015 //
00016 // Permission is hereby granted, free of charge, to any person obtaining a copy
00017 // of this software and associated documentation files (the "Software"), to
00018 // deal with the Software without restriction, including without limitation
00019 // the rights to use, copy, modify, merge, publish, distribute, sublicense,
00020 // and/or sell copies of the Software, and to permit persons to whom the
00021 // Software is furnished to do so, subject to the following conditions:
00022 //
00023 // - Redistributions of source code must retain the above copyright notice,
00024 // this list of conditions and the following disclaimers.
00025 // - Redistributions in binary form must reproduce the above copyright
00026 // notice, this list of conditions and the following disclaimers in
00027 // the documentation and/or other materials provided with the distribution.
00028 // - Neither the names of Advanced Micro Devices, Inc,
00029 // nor the names of its contributors may be used to endorse or promote
00030 // products derived from this Software without specific prior written
00031 // permission.
00032 //
00033 // THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR
00034 // IMPLIED, INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY,
00035 // FITNESS FOR A PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL
00036 // THE CONTRIBUTORS OR COPYRIGHT HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR
00037 // OTHER LIABILITY, WHETHER IN AN ACTION OF CONTRACT, TORT OR OTHERWISE,
00038 // ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER
00039 // DEALINGS WITH THE SOFTWARE.
00040 //
00042
00043 // Undefine the macro in case it is defined in the system elf.h.
00044 #undef EM_AMDGPU
00045
00046 #ifndef AMD_HSA_ELF_H
00047 #define AMD_HSA_ELF_H
00048
00049 // AMD GPU Specific ELF Header Enumeration Values.

```

```

00050 //
00051 // Values are copied from LLVM BinaryFormat/ELF.h . This file also contains
00052 // code object V1 definitions which are not part of the LLVM header. Code object
00053 // V1 was only supported by the Finalizer which is now deprecated and removed.
00054 //
00055 // TODO: Deprecate and remove V1 support and replace this header with using the
00056 // LLVM header.
00057 namespace ELF {
00058
00059 // Machine architectures
00060 // See current registered ELF machine architectures at:
00061 // http://www.uxsglobal.com/developers/gabi/latest/ch4.eheader.html
00062 enum {
00063 EM_AMDGPU = 224, // AMD GPU architecture
00064 };
00065
00066 // OS ABI identification.
00067 enum {
00068 ELFOSABI_AMDGPU_HSA = 64, // AMD HSA runtime
00069 };
00070
00071 // AMDGPU OS ABI Version identification.
00072 enum {
00073 // ELFBIVERSION_AMDGPU_HSA_V1 does not exist because OS ABI identification
00074 // was never defined for V1.
00075 ELFBIVERSION_AMDGPU_HSA_V2 = 0,
00076 ELFBIVERSION_AMDGPU_HSA_V3 = 1,
00077 ELFBIVERSION_AMDGPU_HSA_V4 = 2,
00078 ELFBIVERSION_AMDGPU_HSA_V5 = 3
00079 };
00080
00081 // AMDGPU specific e_flags.
00082 enum : unsigned {
00083 // Processor selection mask for EF_AMDGPU_MACH_* values.
00084 EF_AMDGPU_MACH = 0x0ff,
00085
00086 // Not specified processor.
00087 EF_AMDGPU_MACH_NONE = 0x000,
00088
00089 // AMDGCN-based processors.
00090 EF_AMDGPU_MACH_AMDGCN_GFX600 = 0x020,
00091 EF_AMDGPU_MACH_AMDGCN_GFX601 = 0x021,
00092 EF_AMDGPU_MACH_AMDGCN_GFX700 = 0x022,
00093 EF_AMDGPU_MACH_AMDGCN_GFX701 = 0x023,
00094 EF_AMDGPU_MACH_AMDGCN_GFX702 = 0x024,
00095 EF_AMDGPU_MACH_AMDGCN_GFX703 = 0x025,
00096 EF_AMDGPU_MACH_AMDGCN_GFX704 = 0x026,
00097 EF_AMDGPU_MACH_AMDGCN_RESERVED_0X27 = 0x027,
00098 EF_AMDGPU_MACH_AMDGCN_GFX801 = 0x028,
00099 EF_AMDGPU_MACH_AMDGCN_GFX802 = 0x029,
00100 EF_AMDGPU_MACH_AMDGCN_GFX803 = 0x02a,
00101 EF_AMDGPU_MACH_AMDGCN_GFX810 = 0x02b,
00102 EF_AMDGPU_MACH_AMDGCN_GFX900 = 0x02c,
00103 EF_AMDGPU_MACH_AMDGCN_GFX902 = 0x02d,
00104 EF_AMDGPU_MACH_AMDGCN_GFX904 = 0x02e,
00105 EF_AMDGPU_MACH_AMDGCN_GFX906 = 0x02f,
00106 EF_AMDGPU_MACH_AMDGCN_GFX908 = 0x030,
00107 EF_AMDGPU_MACH_AMDGCN_GFX909 = 0x031,
00108 EF_AMDGPU_MACH_AMDGCN_GFX90C = 0x032,
00109 EF_AMDGPU_MACH_AMDGCN_GFX1010 = 0x033,
00110 EF_AMDGPU_MACH_AMDGCN_GFX1011 = 0x034,
00111 EF_AMDGPU_MACH_AMDGCN_GFX1012 = 0x035,
00112 EF_AMDGPU_MACH_AMDGCN_GFX1030 = 0x036,
00113 EF_AMDGPU_MACH_AMDGCN_GFX1031 = 0x037,
00114 EF_AMDGPU_MACH_AMDGCN_GFX1032 = 0x038,
00115 EF_AMDGPU_MACH_AMDGCN_GFX1033 = 0x039,
00116 EF_AMDGPU_MACH_AMDGCN_GFX602 = 0x03a,
00117 EF_AMDGPU_MACH_AMDGCN_GFX705 = 0x03b,
00118 EF_AMDGPU_MACH_AMDGCN_GFX805 = 0x03c,
00119 EF_AMDGPU_MACH_AMDGCN_GFX1035 = 0x03d,
00120 EF_AMDGPU_MACH_AMDGCN_GFX1034 = 0x03e,
00121 EF_AMDGPU_MACH_AMDGCN_GFX90A = 0x03f,
00122 EF_AMDGPU_MACH_AMDGCN_RESERVED_0X40 = 0x040,
00123 EF_AMDGPU_MACH_AMDGCN_GFX1100 = 0x041,
00124 EF_AMDGPU_MACH_AMDGCN_GFX1013 = 0x042,
00125 EF_AMDGPU_MACH_AMDGCN_GFX1103 = 0x044,
00126 EF_AMDGPU_MACH_AMDGCN_GFX1036 = 0x045,
00127 EF_AMDGPU_MACH_AMDGCN_GFX1102 = 0x047,
00128
00129 // First/last AMDGCN-based processors.
00130 EF_AMDGPU_MACH_AMDGCN_FIRST = EF_AMDGPU_MACH_AMDGCN_GFX600,
00131 EF_AMDGPU_MACH_AMDGCN_LAST = EF_AMDGPU_MACH_AMDGCN_GFX1102,
00132
00133 // Indicates if the "xnack" target feature is enabled for all code contained
00134 // in the object.
00135 //
00136 // Only valid for ELFOSABI_AMDGPU_HSA and ELFBIVERSION_AMDGPU_HSA_V2.

```

```

00137 EF_AMDGPU_FEATURE_XNACK_V2 = 0x01,
00138 // Indicates if the trap handler is enabled for all code contained
00139 // in the object.
00140 //
00141 // Only valid for ELFOSABI_AMDGPU_HSA and ELFABIVERSION_AMDGPU_HSA_V2.
00142 EF_AMDGPU_FEATURE_TRAP_HANDLER_V2 = 0x02,
00143
00144 // Indicates if the "xnack" target feature is enabled for all code contained
00145 // in the object.
00146 //
00147 // Only valid for ELFOSABI_AMDGPU_HSA and ELFABIVERSION_AMDGPU_HSA_V3.
00148 EF_AMDGPU_FEATURE_XNACK_V3 = 0x100,
00149 // Indicates if the "sramecc" target feature is enabled for all code
00150 // contained in the object.
00151 //
00152 // Only valid for ELFOSABI_AMDGPU_HSA and ELFABIVERSION_AMDGPU_HSA_V3.
00153 EF_AMDGPU_FEATURE_SRAECC_V3 = 0x200,
00154
00155 // XNACK selection mask for EF_AMDGPU_FEATURE_XNACK_* values.
00156 //
00157 // Only valid for ELFOSABI_AMDGPU_HSA and ELFABIVERSION_AMDGPU_HSA_V4,
00158 // ELFABIVERSION_AMDGPU_HSA_V5.
00159 EF_AMDGPU_FEATURE_XNACK_V4 = 0x300,
00160 // XNACK is not supported.
00161 EF_AMDGPU_FEATURE_XNACK_UNSUPPORTED_V4 = 0x000,
00162 // XNACK is any/default/unspecified.
00163 EF_AMDGPU_FEATURE_XNACK_ANY_V4 = 0x100,
00164 // XNACK is off.
00165 EF_AMDGPU_FEATURE_XNACK_OFF_V4 = 0x200,
00166 // XNACK is on.
00167 EF_AMDGPU_FEATURE_XNACK_ON_V4 = 0x300,
00168
00169 // SRAECC selection mask for EF_AMDGPU_FEATURE_SRAECC_* values.
00170 //
00171 // Only valid for ELFOSABI_AMDGPU_HSA and ELFABIVERSION_AMDGPU_HSA_V4,
00172 // ELFABIVERSION_AMDGPU_HSA_V5.
00173 EF_AMDGPU_FEATURE_SRAECC_V4 = 0xc00,
00174 // SRAECC is not supported.
00175 EF_AMDGPU_FEATURE_SRAECC_UNSUPPORTED_V4 = 0x000,
00176 // SRAECC is any/default/unspecified.
00177 EF_AMDGPU_FEATURE_SRAECC_ANY_V4 = 0x400,
00178 // SRAECC is off.
00179 EF_AMDGPU_FEATURE_SRAECC_OFF_V4 = 0x800,
00180 // SRAECC is on.
00181 EF_AMDGPU_FEATURE_SRAECC_ON_V4 = 0xc00,
00182 };
00183
00184 } // end namespace ELF
00185
00186 // ELF Section Header Flag Enumeration Values.
00187 #define SHF_AMDGPU_HSA_GLOBAL (0x00100000 & SHF_MASKOS)
00188 #define SHF_AMDGPU_HSA_READONLY (0x00200000 & SHF_MASKOS)
00189 #define SHF_AMDGPU_HSA_CODE (0x00400000 & SHF_MASKOS)
00190 #define SHF_AMDGPU_HSA_AGENT (0x00800000 & SHF_MASKOS)
00191
00192 //
00193 typedef enum {
00194 AMDGPU_HSA_SEGMENT_GLOBAL_PROGRAM = 0,
00195 AMDGPU_HSA_SEGMENT_GLOBAL_AGENT = 1,
00196 AMDGPU_HSA_SEGMENT_READONLY_AGENT = 2,
00197 AMDGPU_HSA_SEGMENT_CODE_AGENT = 3,
00198 AMDGPU_HSA_SEGMENT_LAST,
00199 } amdgpu_hsa_elf_segment_t;
00200
00201 // ELF Program Header Type Enumeration Values.
00202 #define PT_AMDGPU_HSA_LOAD_GLOBAL_PROGRAM (PT_LOOS + AMDGPU_HSA_SEGMENT_GLOBAL_PROGRAM)
00203 #define PT_AMDGPU_HSA_LOAD_GLOBAL_AGENT (PT_LOOS + AMDGPU_HSA_SEGMENT_GLOBAL_AGENT)
00204 #define PT_AMDGPU_HSA_LOAD_READONLY_AGENT (PT_LOOS + AMDGPU_HSA_SEGMENT_READONLY_AGENT)
00205 #define PT_AMDGPU_HSA_LOAD_CODE_AGENT (PT_LOOS + AMDGPU_HSA_SEGMENT_CODE_AGENT)
00206
00207 // ELF Symbol Type Enumeration Values.
00208 #define STT_AMDGPU_HSA_KERNEL (STT_LOOS + 0)
00209 #define STT_AMDGPU_HSA_INDIRECT_FUNCTION (STT_LOOS + 1)
00210 #define STT_AMDGPU_HSA_METADATA (STT_LOOS + 2)
00211
00212 // ELF Symbol Binding Enumeration Values.
00213 #define STB_AMDGPU_HSA_EXTERNAL (STB_LOOS + 0)
00214
00215 // ELF Symbol Other Information Creation/Retrieval.
00216 #define ELF64_ST_AMDGPU_ALLOCATION(o) (((o) > 2) & 0x3)
00217 #define ELF64_ST_AMDGPU_FLAGS(o) ((o) >> 4)
00218 #define ELF64_ST_AMDGPU_OTHER(f, a, v) (((f) < 4) + (((a) & 0x3) << 2) + (((v) & 0x3))
00219
00220 typedef enum {
00221 AMDGPU_HSA_SYMBOL_ALLOCATION_DEFAULT = 0,
00222 AMDGPU_HSA_SYMBOL_ALLOCATION_GLOBAL_PROGRAM = 1,
00223 AMDGPU_HSA_SYMBOL_ALLOCATION_GLOBAL_AGENT = 2,

```

```

00224 AMDGPU_HSA_SYMBOL_ALLOCATION_READONLY_AGENT = 3,
00225 AMDGPU_HSA_SYMBOL_ALLOCATION_LAST,
00226 } amdgpu_hsa_symbol_allocation_t;
00227
00228 // ELF Symbol Allocation Enumeration Values.
00229 #define STA_AMDGPU_HSA_DEFAULT AMDGPU_HSA_SYMBOL_ALLOCATION_DEFAULT
00230 #define STA_AMDGPU_HSA_GLOBAL_PROGRAM AMDGPU_HSA_SYMBOL_ALLOCATION_GLOBAL_PROGRAM
00231 #define STA_AMDGPU_HSA_GLOBAL_AGENT AMDGPU_HSA_SYMBOL_ALLOCATION_GLOBAL_AGENT
00232 #define STA_AMDGPU_HSA_READONLY_AGENT AMDGPU_HSA_SYMBOL_ALLOCATION_READONLY_AGENT
00233
00234 typedef enum {
00235 AMDGPU_HSA_SYMBOL_FLAG_DEFAULT = 0,
00236 AMDGPU_HSA_SYMBOL_FLAG_CONST = 1,
00237 AMDGPU_HSA_SYMBOL_FLAG_LAST,
00238 } amdgpu_hsa_symbol_flag_t;
00239
00240 // ELF Symbol Flag Enumeration Values.
00241 #define STF_AMDGPU_HSA_CONST AMDGPU_HSA_SYMBOL_FLAG_CONST
00242
00243 // AMD GPU Relocation Type Enumeration Values.
00244 #define R_AMDGPU_NONE 0
00245 #define R_AMDGPU_32_LOW 1
00246 #define R_AMDGPU_32_HIGH 2
00247 #define R_AMDGPU_64 3
00248 #define R_AMDGPU_INIT_SAMPLER 4
00249 #define R_AMDGPU_INIT_IMAGE 5
00250 #define R_AMDGPU_RELATIVE64 13
00251
00252 // AMD GPU Note Type Enumeration Values.
00253 #define NT_AMD_HSA_CODE_OBJECT_VERSION 1
00254 #define NT_AMD_HSA_HSAIL 2
00255 #define NT_AMD_HSA_ISA_VERSION 3
00256 #define NT_AMD_HSA_PRODUCER 4
00257 #define NT_AMD_HSA_PRODUCER_OPTIONS 5
00258 #define NT_AMD_HSA_EXTENSION 6
00259 #define NT_AMD_HSA_ISA_NAME 11
00260 #define NT_AMD_HSA_HLDEBUG_DEBUG 101
00261 #define NT_AMD_HSA_HLDEBUG_TARGET 102
00262
00263 // AMD GPU Metadata Kind Enumeration Values.
00264 typedef uint16_t amdgpu_hsa_metadata_kind16_t;
00265 typedef enum {
00266 AMDGPU_HSA_METADATA_KIND_NONE = 0,
00267 AMDGPU_HSA_METADATA_KIND_INIT_SAMP = 1,
00268 AMDGPU_HSA_METADATA_KIND_INIT_ROIMG = 2,
00269 AMDGPU_HSA_METADATA_KIND_INIT_WOIMG = 3,
00270 AMDGPU_HSA_METADATA_KIND_INIT_RWIMG = 4
00271 } amdgpu_hsa_metadata_kind_t;
00272
00273 // AMD GPU Sampler Coordinate Normalization Enumeration Values.
00274 typedef uint8_t amdgpu_hsa_sampler_coord8_t;
00275 typedef enum {
00276 AMDGPU_HSA_SAMPLER_COORD_UNNORMALIZED = 0,
00277 AMDGPU_HSA_SAMPLER_COORD_NORMALIZED = 1
00278 } amdgpu_hsa_sampler_coord_t;
00279
00280 // AMD GPU Sampler Filter Enumeration Values.
00281 typedef uint8_t amdgpu_hsa_sampler_filter8_t;
00282 typedef enum {
00283 AMDGPU_HSA_SAMPLER_FILTER_NEAREST = 0,
00284 AMDGPU_HSA_SAMPLER_FILTER_LINEAR = 1
00285 } amdgpu_hsa_sampler_filter_t;
00286
00287 // AMD GPU Sampler Addressing Enumeration Values.
00288 typedef uint8_t amdgpu_hsa_sampler_addressing8_t;
00289 typedef enum {
00290 AMDGPU_HSA_SAMPLER_ADDRESSING_UNDEFINED = 0,
00291 AMDGPU_HSA_SAMPLER_ADDRESSING_CLAMP_TO_EDGE = 1,
00292 AMDGPU_HSA_SAMPLER_ADDRESSING_CLAMP_TO_BORDER = 2,
00293 AMDGPU_HSA_SAMPLER_ADDRESSING_REPEAT = 3,
00294 AMDGPU_HSA_SAMPLER_ADDRESSING_MIRRORED_REPEAT = 4
00295 } amdgpu_hsa_sampler_addressing_t;
00296
00297 // AMD GPU Sampler Descriptor.
00298 typedef struct amdgpu_hsa_sampler_descriptor_s {
00299 uint16_t size;
00300 amdgpu_hsa_metadata_kind16_t kind;
00301 amdgpu_hsa_sampler_coord8_t coord;
00302 amdgpu_hsa_sampler_filter8_t filter;
00303 amdgpu_hsa_sampler_addressing8_t addressing;
00304 uint8_t reserved1;
00305 } amdgpu_hsa_sampler_descriptor_t;
00306
00307 // AMD GPU Image Geometry Enumeration Values.
00308 typedef uint8_t amdgpu_hsa_image_geometry8_t;
00309 typedef enum {
00310 AMDGPU_HSA_IMAGE_GEOMETRY_1D = 0,

```

```

00311 AMDGPU_HSA_IMAGE_GEOMETRY_2D = 1,
00312 AMDGPU_HSA_IMAGE_GEOMETRY_3D = 2,
00313 AMDGPU_HSA_IMAGE_GEOMETRY_1DA = 3,
00314 AMDGPU_HSA_IMAGE_GEOMETRY_2DA = 4,
00315 AMDGPU_HSA_IMAGE_GEOMETRY_1DB = 5,
00316 AMDGPU_HSA_IMAGE_GEOMETRY_2DDEPTH = 6,
00317 AMDGPU_HSA_IMAGE_GEOMETRY_2DADEPTH = 7
00318 } amdgpu_hsa_image_geometry_t;
00319
00320 // AMD GPU Image Channel Order Enumeration Values.
00321 typedef uint8_t amdgpu_hsa_image_channel_order8_t;
00322 typedef enum {
00323 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_A = 0,
00324 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_R = 1,
00325 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_RX = 2,
00326 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_RG = 3,
00327 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_RGX = 4,
00328 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_RA = 5,
00329 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_RGB = 6,
00330 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_RGBX = 7,
00331 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_RGBA = 8,
00332 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_BGRA = 9,
00333 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_ARGB = 10,
00334 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_ABGR = 11,
00335 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_SRGB = 12,
00336 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_SRGBX = 13,
00337 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_SRGBA = 14,
00338 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_SBGRA = 15,
00339 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_INTENSITY = 16,
00340 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_LUMINANCE = 17,
00341 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_DEPTH = 18,
00342 AMDGPU_HSA_IMAGE_CHANNEL_ORDER_DEPTH_STENCIL = 19
00343 } amdgpu_hsa_image_channel_order_t;
00344
00345 // AMD GPU Image Channel Type Enumeration Values.
00346 typedef uint8_t amdgpu_hsa_image_channel_type8_t;
00347 typedef enum {
00348 AMDGPU_HSA_IMAGE_CHANNEL_TYPE_SNORM_INT8 = 0,
00349 AMDGPU_HSA_IMAGE_CHANNEL_TYPE_SNORM_INT16 = 1,
00350 AMDGPU_HSA_IMAGE_CHANNEL_TYPE_UNORM_INT8 = 2,
00351 AMDGPU_HSA_IMAGE_CHANNEL_TYPE_UNORM_INT16 = 3,
00352 AMDGPU_HSA_IMAGE_CHANNEL_TYPE_UNORM_INT24 = 4,
00353 AMDGPU_HSA_IMAGE_CHANNEL_TYPE_SHORT_555 = 5,
00354 AMDGPU_HSA_IMAGE_CHANNEL_TYPE_SHORT_565 = 6,
00355 AMDGPU_HSA_IMAGE_CHANNEL_TYPE_INT_101010 = 7,
00356 AMDGPU_HSA_IMAGE_CHANNEL_TYPE_SIGNED_INT8 = 8,
00357 AMDGPU_HSA_IMAGE_CHANNEL_TYPE_SIGNED_INT16 = 9,
00358 AMDGPU_HSA_IMAGE_CHANNEL_TYPE_SIGNED_INT32 = 10,
00359 AMDGPU_HSA_IMAGE_CHANNEL_TYPE_UNSIGNED_INT8 = 11,
00360 AMDGPU_HSA_IMAGE_CHANNEL_TYPE_UNSIGNED_INT16 = 12,
00361 AMDGPU_HSA_IMAGE_CHANNEL_TYPE_UNSIGNED_INT32 = 13,
00362 AMDGPU_HSA_IMAGE_CHANNEL_TYPE_HALF_FLOAT = 14,
00363 AMDGPU_HSA_IMAGE_CHANNEL_TYPE_FLOAT = 15
00364 } amdgpu_hsa_image_channel_type_t;
00365
00366 // AMD GPU Image Descriptor.
00367 typedef struct amdgpu_hsa_image_descriptor_s {
00368 uint16_t size;
00369 amdgpu_hsa_metadata_kind16_t kind;
00370 amdgpu_hsa_image_geometry8_t geometry;
00371 amdgpu_hsa_image_channel_order8_t channel_order;
00372 amdgpu_hsa_image_channel_type8_t channel_type;
00373 uint8_t reserved1;
00374 uint64_t width;
00375 uint64_t height;
00376 uint64_t depth;
00377 uint64_t array;
00378 } amdgpu_hsa_image_descriptor_t;
00379
00380 typedef struct amdgpu_hsa_note_code_object_version_s {
00381 uint32_t major_version;
00382 uint32_t minor_version;
00383 } amdgpu_hsa_note_code_object_version_t;
00384
00385 typedef struct amdgpu_hsa_note_hsail_s {
00386 uint32_t hsail_major_version;
00387 uint32_t hsail_minor_version;
00388 uint8_t profile;
00389 uint8_t machine_model;
00390 uint8_t default_float_round;
00391 } amdgpu_hsa_note_hsail_t;
00392
00393 typedef struct amdgpu_hsa_note_isa_s {
00394 uint16_t vendor_name_size;
00395 uint16_t architecture_name_size;
00396 uint32_t major;
00397 uint32_t minor;

```



```

00398 uint32_t stepping;
00399 char vendor_and_architecture_name[1];
00400 } amdgpu_hsa_note_isa_t;
00401
00402 typedef struct amdgpu_hsa_note_producer_s {
00403 uint16_t producer_name_size;
00404 uint16_t reserved;
00405 uint32_t producer_major_version;
00406 uint32_t producer_minor_version;
00407 char producer_name[1];
00408 } amdgpu_hsa_note_producer_t;
00409
00410 typedef struct amdgpu_hsa_note_producer_options_s {
00411 uint16_t producer_options_size;
00412 char producer_options[1];
00413 } amdgpu_hsa_note_producer_options_t;
00414
00415 typedef enum {
00416 AMDGPU_HSA_RODATA_GLOBAL_PROGRAM = 0,
00417 AMDGPU_HSA_RODATA_GLOBAL_AGENT,
00418 AMDGPU_HSA_RODATA_READONLY_AGENT,
00419 AMDGPU_HSA_DATA_GLOBAL_PROGRAM,
00420 AMDGPU_HSA_DATA_GLOBAL_AGENT,
00421 AMDGPU_HSA_DATA_READONLY_AGENT,
00422 AMDGPU_HSA_BSS_GLOBAL_PROGRAM,
00423 AMDGPU_HSA_BSS_GLOBAL_AGENT,
00424 AMDGPU_HSA_BSS_READONLY_AGENT,
00425 AMDGPU_HSA_SECTION_LAST,
00426 } amdgpu_hsa_elf_section_t;
00427
00428 #endif // AMD_HSA_ELF_H

```

## 7.3 amd\_hsa\_kernel\_code.h

```

00001
00002 //
00003 // The University of Illinois/NCSA
00004 // Open Source License (NCSA)
00005 //
00006 // Copyright (c) 2014-2020, Advanced Micro Devices, Inc. All rights reserved.
00007 //
00008 // Developed by:
00009 //
00010 // AMD Research and AMD HSA Software Development
00011 //
00012 // Advanced Micro Devices, Inc.
00013 //
00014 // www.amd.com
00015 //
00016 // Permission is hereby granted, free of charge, to any person obtaining a copy
00017 // of this software and associated documentation files (the "Software"), to
00018 // deal with the Software without restriction, including without limitation
00019 // the rights to use, copy, modify, merge, publish, distribute, sublicense,
00020 // and/or sell copies of the Software, and to permit persons to whom the
00021 // Software is furnished to do so, subject to the following conditions:
00022 //
00023 // - Redistributions of source code must retain the above copyright notice,
00024 // this list of conditions and the following disclaimers.
00025 // - Redistributions in binary form must reproduce the above copyright
00026 // notice, this list of conditions and the following disclaimers in
00027 // the documentation and/or other materials provided with the distribution.
00028 // - Neither the names of Advanced Micro Devices, Inc,
00029 // nor the names of its contributors may be used to endorse or promote
00030 // products derived from this Software without specific prior written
00031 // permission.
00032 //
00033 // THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR
00034 // IMPLIED, INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY,
00035 // FITNESS FOR A PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL
00036 // THE CONTRIBUTORS OR COPYRIGHT HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR
00037 // OTHER LIABILITY, WHETHER IN AN ACTION OF CONTRACT, TORT OR OTHERWISE,
00038 // ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER
00039 // DEALINGS WITH THE SOFTWARE.
00040 //
00041
00042 #ifndef AMD_HSA_KERNEL_CODE_H
00043 #define AMD_HSA_KERNEL_CODE_H
00044
00045 #include "amd_hsa_common.h"
00046 #include "hsa.h"
00047
00048 // AMD Kernel Code Version Enumeration Values.
00049 typedef uint32_t amd_kernel_code_version32_t;

```

```

00051 enum amd_kernel_code_version_t {
00052 AMD_KERNEL_CODE_VERSION_MAJOR = 1,
00053 AMD_KERNEL_CODE_VERSION_MINOR = 1
00054 };
00055
00056 // AMD Machine Kind Enumeration Values.
00057 typedef uint16_t amd_machine_kind16_t;
00058 enum amd_machine_kind_t {
00059 AMD_MACHINE_KIND_UNDEFINED = 0,
00060 AMD_MACHINE_KIND_AMDGPU = 1
00061 };
00062
00063 // AMD Machine Version.
00064 typedef uint16_t amd_machine_version16_t;
00065
00066 // AMD Float Round Mode Enumeration Values.
00067 enum amd_float_round_mode_t {
00068 AMD_FLOAT_ROUND_MODE_NEAREST_EVEN = 0,
00069 AMD_FLOAT_ROUND_MODE_PLUS_INFINITY = 1,
00070 AMD_FLOAT_ROUND_MODE_MINUS_INFINITY = 2,
00071 AMD_FLOAT_ROUND_MODE_ZERO = 3
00072 };
00073
00074 // AMD Float Denorm Mode Enumeration Values.
00075 enum amd_float_denorm_mode_t {
00076 AMD_FLOAT_DENORM_MODE_FLUSH_SOURCE_OUTPUT = 0,
00077 AMD_FLOAT_DENORM_MODE_FLUSH_OUTPUT = 1,
00078 AMD_FLOAT_DENORM_MODE_FLUSH_SOURCE = 2,
00079 AMD_FLOAT_DENORM_MODE_NO_FLUSH = 3
00080 };
00081
00082 // AMD Compute Program Resource Register One.
00083 typedef uint32_t amd_compute_pgm_rsrc_one32_t;
00084 enum amd_compute_pgm_rsrc_one_t {
00085 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_ONE_GRANULATED_WORKITEM_VGPR_COUNT, 0, 6),
00086 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_ONE_GRANULATED_WAVEFRONT_SGPR_COUNT, 6, 4),
00087 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_ONE_PRIORITY, 10, 2),
00088 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_ONE_FLOAT_ROUND_MODE_32, 12, 2),
00089 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_ONE_FLOAT_ROUND_MODE_16_64, 14, 2),
00090 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_ONE_FLOAT_DENORM_MODE_32, 16, 2),
00091 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_ONE_FLOAT_DENORM_MODE_16_64, 18, 2),
00092 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_ONE_PRIV, 20, 1),
00093 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_ONE_ENABLE_DX10_CLAMP, 21, 1),
00094 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_ONE_DEBUG_MODE, 22, 1),
00095 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_ONE_ENABLE_IEEE_MODE, 23, 1),
00096 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_ONE_BULKY, 24, 1),
00097 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_ONE_CDBG_USER, 25, 1),
00098 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_ONE_RESERVED1, 26, 6)
00099 };
00100
00101 // AMD System VGPR Workitem ID Enumeration Values.
00102 enum amd_system_vgpr_workitem_id_t {
00103 AMD_SYSTEM_VGPR_WORKITEM_ID_X = 0,
00104 AMD_SYSTEM_VGPR_WORKITEM_ID_X_Y = 1,
00105 AMD_SYSTEM_VGPR_WORKITEM_ID_X_Y_Z = 2,
00106 AMD_SYSTEM_VGPR_WORKITEM_ID_UNDEFINED = 3
00107 };
00108
00109 // AMD Compute Program Resource Register Two.
00110 typedef uint32_t amd_compute_pgm_rsrc_two32_t;
00111 enum amd_compute_pgm_rsrc_two_t {
00112 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_ENABLE_SGPR_PRIVATE_SEGMENT_WAVE_BYTE_OFFSET,
00113 0, 1),
00114 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_USER_SGPR_COUNT, 1, 5),
00115 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_ENABLE_TRAP_HANDLER, 6, 1),
00116 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_ENABLE_SGPR_WORKGROUP_ID_X, 7, 1),
00117 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_ENABLE_SGPR_WORKGROUP_ID_Y, 8, 1),
00118 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_ENABLE_SGPR_WORKGROUP_ID_Z, 9, 1),
00119 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_ENABLE_SGPR_WORKGROUP_INFO, 10, 1),
00120 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_ENABLE_VGPR_WORKITEM_ID, 11, 2),
00121 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_ENABLE_EXCEPTION_ADDRESS_WATCH, 13, 1),
00122 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_ENABLE_EXCEPTION_MEMORY_VIOLATION, 14, 1),
00123 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_GRANULATED_LDS_SIZE, 15, 9),
00124 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_ENABLE_EXCEPTION_IEEE_754_FP_INVALID_OPERATION,
00125 24, 1),
00126 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_ENABLE_EXCEPTION_FP_DENORMAL_SOURCE, 25,
00127 1),
00128 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_ENABLE_EXCEPTION_IEEE_754_FP_DIVISION_BY_ZERO,
00129 26, 1),
00130 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_ENABLE_EXCEPTION_IEEE_754_FP_OVERFLOW, 27,
00131 1),
00132 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_ENABLE_EXCEPTION_IEEE_754_FP_UNDERFLOW,
00133 28, 1),
00134 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_ENABLE_EXCEPTION_IEEE_754_FP_INEXACT, 29,

```

```

1),
00129 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_ENABLE_EXCEPTION_INT_DIVISION_BY_ZERO, 30,
1),
00130 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_COMPUTE_PGM_RSRC_TWO_RESERVED1, 31, 1)
00131 };
00132
00133 // AMD Element Byte Size Enumeration Values.
00134 enum amd_element_byte_size_t {
00135 AMD_ELEMENT_BYTE_SIZE_2 = 0,
00136 AMD_ELEMENT_BYTE_SIZE_4 = 1,
00137 AMD_ELEMENT_BYTE_SIZE_8 = 2,
00138 AMD_ELEMENT_BYTE_SIZE_16 = 3
00139 };
00140
00141 // AMD Kernel Code Properties.
00142 typedef uint32_t amd_kernel_code_properties32_t;
00143 enum amd_kernel_code_properties_t {
00144 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_KERNEL_CODE_PROPERTIES_ENABLE_SGPR_PRIVATE_SEGMENT_BUFFER, 0,
1),
00145 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_KERNEL_CODE_PROPERTIES_ENABLE_SGPR_DISPATCH_PTR, 1, 1),
00146 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_KERNEL_CODE_PROPERTIES_ENABLE_SGPR_QUEUE_PTR, 2, 1),
00147 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_KERNEL_CODE_PROPERTIES_ENABLE_SGPR_KERNARG_SEGMENT_PTR, 3, 1),
00148 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_KERNEL_CODE_PROPERTIES_ENABLE_SGPR_DISPATCH_ID, 4, 1),
00149 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_KERNEL_CODE_PROPERTIES_ENABLE_SGPR_FLAT_SCRATCH_INIT, 5, 1),
00150 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_KERNEL_CODE_PROPERTIES_ENABLE_SGPR_PRIVATE_SEGMENT_SIZE, 6, 1),
00151 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_KERNEL_CODE_PROPERTIES_ENABLE_SGPR_GRID_WORKGROUP_COUNT_X, 7,
1),
00152 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_KERNEL_CODE_PROPERTIES_ENABLE_SGPR_GRID_WORKGROUP_COUNT_Y, 8,
1),
00153 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_KERNEL_CODE_PROPERTIES_ENABLE_SGPR_GRID_WORKGROUP_COUNT_Z, 9,
1),
00154 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_KERNEL_CODE_PROPERTIES_RESERVED1, 10, 6),
00155 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_KERNEL_CODE_PROPERTIES_ENABLE_ORDERED_APPEND_GDS, 16, 1),
00156 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_KERNEL_CODE_PROPERTIES_PRIVATE_ELEMENT_SIZE, 17, 2),
00157 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_KERNEL_CODE_PROPERTIES_IS_PTR64, 19, 1),
00158 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_KERNEL_CODE_PROPERTIES_IS_DYNAMIC_CALLSTACK, 20, 1),
00159 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_KERNEL_CODE_PROPERTIES_IS_DEBUG_ENABLED, 21, 1),
00160 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_KERNEL_CODE_PROPERTIES_IS_XNACK_ENABLED, 22, 1),
00161 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_KERNEL_CODE_PROPERTIES_RESERVED2, 23, 9)
00162 };
00163
00164 // AMD Power Of Two Enumeration Values.
00165 typedef uint8_t amd_powertwo8_t;
00166 enum amd_powertwo_t {
00167 AMD_POWER TWO_1 = 0,
00168 AMD_POWER TWO_2 = 1,
00169 AMD_POWER TWO_4 = 2,
00170 AMD_POWER TWO_8 = 3,
00171 AMD_POWER TWO_16 = 4,
00172 AMD_POWER TWO_32 = 5,
00173 AMD_POWER TWO_64 = 6,
00174 AMD_POWER TWO_128 = 7,
00175 AMD_POWER TWO_256 = 8
00176 };
00177
00178 // AMD Enabled Control Directive Enumeration Values.
00179 typedef uint64_t amd_enabled_control_directive64_t;
00180 enum amd_enabled_control_directive_t {
00181 AMD_ENABLED_CONTROL_DIRECTIVE_ENABLE_BREAK_EXCEPTIONS = 1,
00182 AMD_ENABLED_CONTROL_DIRECTIVE_ENABLE_DETECT_EXCEPTIONS = 2,
00183 AMD_ENABLED_CONTROL_DIRECTIVE_MAX_DYNAMIC_GROUP_SIZE = 4,
00184 AMD_ENABLED_CONTROL_DIRECTIVE_MAX_FLAT_GRID_SIZE = 8,
00185 AMD_ENABLED_CONTROL_DIRECTIVE_MAX_FLAT_WORKGROUP_SIZE = 16,
00186 AMD_ENABLED_CONTROL_DIRECTIVE_REQUIRED_DIM = 32,
00187 AMD_ENABLED_CONTROL_DIRECTIVE_REQUIRED_GRID_SIZE = 64,
00188 AMD_ENABLED_CONTROL_DIRECTIVE_REQUIRED_WORKGROUP_SIZE = 128,
00189 AMD_ENABLED_CONTROL_DIRECTIVE_REQUIRE_NO_PARTIAL_WORKGROUPS = 256
00190 };
00191
00192 // AMD Exception Kind Enumeration Values.
00193 typedef uint16_t amd_exception_kind16_t;
00194 enum amd_exception_kind_t {
00195 AMD_EXCEPTION_KIND_INVALID_OPERATION = 1,
00196 AMD_EXCEPTION_KIND_DIVISION_BY_ZERO = 2,
00197 AMD_EXCEPTION_KIND_OVERFLOW = 4,
00198 AMD_EXCEPTION_KIND_UNDERFLOW = 8,
00199 AMD_EXCEPTION_KIND_INEXACT = 16
00200 };
00201
00202 // AMD Control Directives.
00203 #define AMD_CONTROL_DIRECTIVES_ALIGN_BYTES 64
00204 #define AMD_CONTROL_DIRECTIVES_ALIGN __ALIGNED__(AMD_CONTROL_DIRECTIVES_ALIGN_BYTES)
00205 typedef AMD_CONTROL_DIRECTIVES_ALIGN struct amd_control_directives_s {
00206 amd_enabled_control_directive64_t enabled_control_directives;
00207 uint16_t enable_break_exceptions;
00208 uint16_t enable_detect_exceptions;
00209 uint32_t max_dynamic_group_size;

```

```

00210 uint64_t max_flat_grid_size;
00211 uint32_t max_flat_workgroup_size;
00212 uint8_t required_dim;
00213 uint8_t reserved1[3];
00214 uint64_t required_grid_size[3];
00215 uint32_t required_workgroup_size[3];
00216 uint8_t reserved2[60];
00217 } amd_control_directives_t;
00218
00219 // AMD Kernel Code.
00220 #define AMD_ISA_ALIGN_BYTES 256
00221 #define AMD_KERNEL_CODE_ALIGN_BYTES 64
00222 #define AMD_KERNEL_CODE_ALIGN __ALIGNED__(AMD_KERNEL_CODE_ALIGN_BYTES)
00223 typedef AMD_KERNEL_CODE_ALIGN struct amd_kernel_code_s {
00224 amd_kernel_code_version32_t amd_kernel_code_version_major;
00225 amd_kernel_code_version32_t amd_kernel_code_version_minor;
00226 amd_machine_kind16_t amd_machine_kind;
00227 amd_machine_version16_t amd_machine_version_major;
00228 amd_machine_version16_t amd_machine_version_minor;
00229 amd_machine_version16_t amd_machine_version_stepping;
00230 int64_t kernel_code_entry_byte_offset;
00231 int64_t kernel_code_prefetch_byte_offset;
00232 uint64_t kernel_code_prefetch_byte_size;
00233 uint64_t max_scratch_backing_memory_byte_size;
00234 amd_compute_pgm_rsrc_one32_t compute_pgm_rsrc1;
00235 amd_compute_pgm_rsrc_two32_t compute_pgm_rsrc2;
00236 amd_kernel_code_properties32_t kernel_code_properties;
00237 uint32_t workitem_private_segment_byte_size;
00238 uint32_t workgroup_group_segment_byte_size;
00239 uint32_t gds_segment_byte_size;
00240 uint64_t kernarg_segment_byte_size;
00241 uint32_t workgroup_fbarrier_count;
00242 uint16_t wavefront_sgpr_count;
00243 uint16_t workitem_vgpr_count;
00244 uint16_t reserved_vgpr_first;
00245 uint16_t reserved_vgpr_count;
00246 uint16_t reserved_sgpr_first;
00247 uint16_t reserved_sgpr_count;
00248 uint16_t debug_wavefront_private_segment_offset_sgpr;
00249 uint16_t debug_private_segment_buffer_sgpr;
00250 amd_powertwo8_t kernarg_segment_alignment;
00251 amd_powertwo8_t group_segment_alignment;
00252 amd_powertwo8_t private_segment_alignment;
00253 amd_powertwo8_t wavefront_size;
00254 int32_t call_convention;
00255 uint8_t reserved1[12];
00256 uint64_t runtime_loader_kernel_symbol;
00257 amd_control_directives_t control_directives;
00258 } amd_kernel_code_t;
00259
00260 // TODO: this struct should be completely gone once debugger designs/implements
00261 // Debugger APIs.
00262 typedef struct amd_runtime_loader_debug_info_s {
00263 const void* elf_raw;
00264 size_t elf_size;
00265 const char *kernel_name;
00266 const void *owning_segment;
00267 } amd_runtime_loader_debug_info_t;
00268
00269 #endif // AMD_HSA_KERNEL_CODE_H

```

## 7.4 amd\_hsa\_queue.h

```

00001
00002 //
00003 // The University of Illinois/NCSA
00004 // Open Source License (NCSA)
00005 //
00006 // Copyright (c) 2014-2020, Advanced Micro Devices, Inc. All rights reserved.
00007 //
00008 // Developed by:
00009 //
00010 // AMD Research and AMD HSA Software Development
00011 //
00012 // Advanced Micro Devices, Inc.
00013 //
00014 // www.amd.com
00015 //
00016 // Permission is hereby granted, free of charge, to any person obtaining a copy
00017 // of this software and associated documentation files (the "Software"), to
00018 // deal with the Software without restriction, including without limitation
00019 // the rights to use, copy, modify, merge, publish, distribute, sublicense,
00020 // and/or sell copies of the Software, and to permit persons to whom the

```

```

00021 // Software is furnished to do so, subject to the following conditions:
00022 //
00023 // - Redistributions of source code must retain the above copyright notice,
00024 // this list of conditions and the following disclaimers.
00025 // - Redistributions in binary form must reproduce the above copyright
00026 // notice, this list of conditions and the following disclaimers in
00027 // the documentation and/or other materials provided with the distribution.
00028 // - Neither the names of Advanced Micro Devices, Inc,
00029 // nor the names of its contributors may be used to endorse or promote
00030 // products derived from this Software without specific prior written
00031 // permission.
00032 //
00033 // THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR
00034 // IMPLIED, INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY,
00035 // FITNESS FOR A PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL
00036 // THE CONTRIBUTORS OR COPYRIGHT HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR
00037 // OTHER LIABILITY, WHETHER IN AN ACTION OF CONTRACT, TORT OR OTHERWISE,
00038 // ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER
00039 // DEALINGS WITH THE SOFTWARE.
00040 //
00041 //
00042 //
00043 #ifndef AMD_HSA_QUEUE_H
00044 #define AMD_HSA_QUEUE_H
00045 //
00046 #include "amd_hsa_common.h"
00047 #include "hsa.h"
00048 //
00049 // AMD Queue Properties.
00050 typedef uint32_t amd_queue_properties32_t;
00051 enum amd_queue_properties_t {
00052 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_QUEUE_PROPERTIES_ENABLE_TRAP_HANDLER, 0, 1),
00053 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_QUEUE_PROPERTIES_IS_PTR64, 1, 1),
00054 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_QUEUE_PROPERTIES_ENABLE_TRAP_HANDLER_DEBUG_SGPRS, 2, 1),
00055 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_QUEUE_PROPERTIES_ENABLE_PROFILING, 3, 1),
00056 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_QUEUE_PROPERTIES_USE_SCRATCH_ONCE, 4, 1),
00057 AMD_HSA_BITS_CREATE_ENUM_ENTRIES(AMD_QUEUE_PROPERTIES_RESERVED1, 5, 27)
00058 };
00059 //
00060 // AMD Queue.
00061 #define AMD_QUEUE_ALIGN_BYTES 64
00062 #define AMD_QUEUE_ALIGN __ALIGNED__(AMD_QUEUE_ALIGN_BYTES)
00063 typedef struct AMD_QUEUE_ALIGN amd_queue_s {
00064 hsa_queue_t hsa_queue;
00065 uint32_t reserved1[4];
00066 volatile uint64_t write_dispatch_id;
00067 uint32_t group_segment_aperture_base_hi;
00068 uint32_t private_segment_aperture_base_hi;
00069 uint32_t max_cu_id;
00070 uint32_t max_wave_id;
00071 volatile uint64_t max_legacy_doorbell_dispatch_id_plus_1;
00072 volatile uint32_t legacy_doorbell_lock;
00073 uint32_t reserved2[9];
00074 volatile uint64_t read_dispatch_id;
00075 uint32_t read_dispatch_id_field_base_byte_offset;
00076 uint32_t compute_tmpring_size;
00077 uint32_t scratch_resource_descriptor[4];
00078 uint64_t scratch_backing_memory_location;
00079 uint64_t scratch_backing_memory_byte_size;
00080 uint32_t scratch_wave64_lane_byte_size;
00081 amd_queue_properties32_t queue_properties;
00082 uint32_t reserved3[2];
00083 hsa_signal_t queue_inactive_signal;
00084 uint32_t reserved4[14];
00085 } amd_queue_t;
00086 //
00087 #endif // AMD_HSA_QUEUE_H

```

## 7.5 amd\_hsa\_signal.h

```

00001
00002 //
00003 // The University of Illinois/NCSA
00004 // Open Source License (NCSA)
00005 //
00006 // Copyright (c) 2014-2020, Advanced Micro Devices, Inc. All rights reserved.
00007 //
00008 // Developed by:
00009 //
00010 // AMD Research and AMD HSA Software Development
00011 //
00012 // Advanced Micro Devices, Inc.
00013 //
00014 // www.amd.com

```

```

00015 //
00016 // Permission is hereby granted, free of charge, to any person obtaining a copy
00017 // of this software and associated documentation files (the "Software"), to
00018 // deal with the Software without restriction, including without limitation
00019 // the rights to use, copy, modify, merge, publish, distribute, sublicense,
00020 // and/or sell copies of the Software, and to permit persons to whom the
00021 // Software is furnished to do so, subject to the following conditions:
00022 //
00023 // - Redistributions of source code must retain the above copyright notice,
00024 // this list of conditions and the following disclaimers.
00025 // - Redistributions in binary form must reproduce the above copyright
00026 // notice, this list of conditions and the following disclaimers in
00027 // the documentation and/or other materials provided with the distribution.
00028 // - Neither the names of Advanced Micro Devices, Inc,
00029 // nor the names of its contributors may be used to endorse or promote
00030 // products derived from this Software without specific prior written
00031 // permission.
00032 //
00033 // THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR
00034 // IMPLIED, INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY,
00035 // FITNESS FOR A PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL
00036 // THE CONTRIBUTORS OR COPYRIGHT HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR
00037 // OTHER LIABILITY, WHETHER IN AN ACTION OF CONTRACT, TORT OR OTHERWISE,
00038 // ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER
00039 // DEALINGS WITH THE SOFTWARE.
00040 //
00042
00043 #ifndef AMD_HSA_SIGNAL_H
00044 #define AMD_HSA_SIGNAL_H
00045
00046 #include "amd_hsa_common.h"
00047 #include "amd_hsa_queue.h"
00048
00049 // AMD Signal Kind Enumeration Values.
00050 typedef int64_t amd_signal_kind64_t;
00051 enum amd_signal_kind_t {
00052 AMD_SIGNAL_KIND_INVALID = 0,
00053 AMD_SIGNAL_KIND_USER = 1,
00054 AMD_SIGNAL_KIND_DOORBELL = -1,
00055 AMD_SIGNAL_KIND_LEGACY_DOORBELL = -2
00056 };
00057
00058 // AMD Signal.
00059 #define AMD_SIGNAL_ALIGN_BYTES 64
00060 #define AMD_SIGNAL_ALIGN __ALIGNED__(AMD_SIGNAL_ALIGN_BYTES)
00061 typedef struct AMD_SIGNAL_ALIGN amd_signal_s {
00062 amd_signal_kind64_t kind;
00063 union {
00064 volatile int64_t value;
00065 volatile uint32_t* legacy_hardware_doorbell_ptr;
00066 volatile uint64_t* hardware_doorbell_ptr;
00067 };
00068 uint64_t event_mailbox_ptr;
00069 uint32_t event_id;
00070 uint32_t reserved1;
00071 uint64_t start_ts;
00072 uint64_t end_ts;
00073 union {
00074 amd_queue_t* queue_ptr;
00075 uint64_t reserved2;
00076 };
00077 uint32_t reserved3[2];
00078 } amd_signal_t;
00079
00080 #endif // AMD_HSA_SIGNAL_H

```

## 7.6 Brig.h

```

00001 // University of Illinois/NCSA
00002 // Open Source License
00003 //
00004 // Copyright (c) 2013-2015, Advanced Micro Devices, Inc.
00005 // All rights reserved.
00006 //
00007 // Developed by:
00008 //
00009 // HSA Team
00010 //
00011 // Advanced Micro Devices, Inc
00012 //
00013 // www.amd.com
00014 //
00015 // Permission is hereby granted, free of charge, to any person obtaining a copy of

```

```

00016 // this software and associated documentation files (the "Software"), to deal with
00017 // the Software without restriction, including without limitation the rights to
00018 // use, copy, modify, merge, publish, sublicense, and/or sell copies
00019 // of the Software, and to permit persons to whom the Software is furnished to do
00020 // so, subject to the following conditions:
00021 //
00022 // * Redistributions of source code must retain the above copyright notice,
00023 // this list of conditions and the following disclaimers.
00024 //
00025 // * Redistributions in binary form must reproduce the above copyright notice,
00026 // this list of conditions and the following disclaimers in the
00027 // documentation and/or other materials provided with the distribution.
00028 //
00029 // * Neither the names of the LLVM Team, University of Illinois at
00030 // Urbana-Champaign, nor the names of its contributors may be used to
00031 // endorse or promote products derived from this Software without specific
00032 // prior written permission.
00033 //
00034 // THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR
00035 // IMPLIED, INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY, FITNESS
00036 // FOR A PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL THE
00037 // CONTRIBUTORS OR COPYRIGHT HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR OTHER
00038 // LIABILITY, WHETHER IN AN ACTION OF CONTRACT, TORT OR OTHERWISE, ARISING FROM,
00039 // OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER DEALINGS WITH THE
00040 // SOFTWARE.
00041
00042 #ifndef INCLUDED_BRIG_H
00043 #define INCLUDED_BRIG_H
00044
00045 #include <stddef.h> /* size_t */
00046 #include <stdint.h> /* uintXX_t */
00047
00048 #ifdef __cplusplus
00049 extern "C" {
00050 #endif /* __cplusplus */
00051
00052 /*=====*/
00053 /* =====*/
00054 /* =====*/
00055 /* =====*/
00056
00057 typedef uint32_t BrigCodeOffset32_t;
00058 typedef uint32_t BrigOperandOffset32_t;
00059 typedef uint32_t BrigDataOffset32_t;
00060
00061 typedef BrigDataOffset32_t BrigDataOffsetCodeList32_t;
00062 typedef BrigDataOffset32_t BrigDataOffsetOperandList32_t;
00063 typedef BrigDataOffset32_t BrigDataOffsetString32_t;
00064
00065 typedef uint32_t BrigVersion32_t;
00066 enum BrigVersion {
00067 BRIG_VERSION_HSAIL_MAJOR = 1,
00068 BRIG_VERSION_HSAIL_MINOR = 0,
00069 BRIG_VERSION_BRIG_MAJOR = 1,
00070 BRIG_VERSION_BRIG_MINOR = 0
00071 };
00072
00073 typedef uint16_t BrigKind16_t;
00074 enum BrigKind {
00075 BRIG_KIND_NONE = 0x0000,
00076
00077 BRIG_KIND_DIRECTIVE_BEGIN = 0x1000,
00078 BRIG_KIND_DIRECTIVE_ARG_BLOCK_END = 0x1000,
00079 BRIG_KIND_DIRECTIVE_ARG_BLOCK_START = 0x1001,
00080 BRIG_KIND_DIRECTIVE_COMMENT = 0x1002,
00081 BRIG_KIND_DIRECTIVE_CONTROL = 0x1003,
00082 BRIG_KIND_DIRECTIVE_EXTENSION = 0x1004,
00083 BRIG_KIND_DIRECTIVE_FBARRIER = 0x1005,
00084 BRIG_KIND_DIRECTIVE_FUNCTION = 0x1006,
00085 BRIG_KIND_DIRECTIVE_INDIRECT_FUNCTION = 0x1007,
00086 BRIG_KIND_DIRECTIVE_KERNEL = 0x1008,
00087 BRIG_KIND_DIRECTIVE_LABEL = 0x1009,
00088 BRIG_KIND_DIRECTIVE_LOC = 0x100a,
00089 BRIG_KIND_DIRECTIVE_MODULE = 0x100b,
00090 BRIG_KIND_DIRECTIVE_PRAGMA = 0x100c,
00091 BRIG_KIND_DIRECTIVE_SIGNATURE = 0x100d,
00092 BRIG_KIND_DIRECTIVE_VARIABLE = 0x100e,
00093 BRIG_KIND_DIRECTIVE_END = 0x100f,
00094
00095 BRIG_KIND_INST_BEGIN = 0x2000,
00096 BRIG_KIND_INST_ADDR = 0x2000,
00097 BRIG_KIND_INST_ATOMIC = 0x2001,
00098 BRIG_KIND_INST_BASIC = 0x2002,
00099 BRIG_KIND_INST_BR = 0x2003,
00100 BRIG_KIND_INST_CMP = 0x2004,
00101 BRIG_KIND_INST_CVT = 0x2005,
00102 BRIG_KIND_INST_IMAGE = 0x2006,

```

```

00103 BRIG_KIND_INST_LANE = 0x2007,
00104 BRIG_KIND_INST_MEM = 0x2008,
00105 BRIG_KIND_INST_MEM_FENCE = 0x2009,
00106 BRIG_KIND_INST_MOD = 0x200a,
00107 BRIG_KIND_INST_QUERY_IMAGE = 0x200b,
00108 BRIG_KIND_INST_QUERY_SAMPLER = 0x200c,
00109 BRIG_KIND_INST_QUEUE = 0x200d,
00110 BRIG_KIND_INST_SEG = 0x200e,
00111 BRIG_KIND_INST_SEG_CVT = 0x200f,
00112 BRIG_KIND_INST_SIGNAL = 0x2010,
00113 BRIG_KIND_INST_SOURCE_TYPE = 0x2011,
00114 BRIG_KIND_INST_END = 0x2012,
00115
00116 BRIG_KIND_OPERAND_BEGIN = 0x3000,
00117 BRIG_KIND_OPERAND_ADDRESS = 0x3000,
00118 BRIG_KIND_OPERAND_ALIGN = 0x3001,
00119 BRIG_KIND_OPERAND_CODE_LIST = 0x3002,
00120 BRIG_KIND_OPERAND_CODE_REF = 0x3003,
00121 BRIG_KIND_OPERAND_CONSTANT_BYTES = 0x3004,
00122 BRIG_KIND_OPERAND_RESERVED = 0x3005,
00123 BRIG_KIND_OPERAND_CONSTANT_IMAGE = 0x3006,
00124 BRIG_KIND_OPERAND_CONSTANT_OPERAND_LIST = 0x3007,
00125 BRIG_KIND_OPERAND_CONSTANT_SAMPLER = 0x3008,
00126 BRIG_KIND_OPERAND_OPERAND_LIST = 0x3009,
00127 BRIG_KIND_OPERAND_REGISTER = 0x300a,
00128 BRIG_KIND_OPERAND_STRING = 0x300b,
00129 BRIG_KIND_OPERAND_WAVESIZE = 0x300c,
00130 BRIG_KIND_OPERAND_END = 0x300d
00131 };
00132
00133 typedef uint8_t BrigAlignment8_t;
00134 enum BrigAlignment {
00135 BRIG_ALIGNMENT_NONE = 0,
00136 BRIG_ALIGNMENT_1 = 1,
00137 BRIG_ALIGNMENT_2 = 2,
00138 BRIG_ALIGNMENT_4 = 3,
00139 BRIG_ALIGNMENT_8 = 4,
00140 BRIG_ALIGNMENT_16 = 5,
00141 BRIG_ALIGNMENT_32 = 6,
00142 BRIG_ALIGNMENT_64 = 7,
00143 BRIG_ALIGNMENT_128 = 8,
00144 BRIG_ALIGNMENT_256 = 9,
00145 BRIG_ALIGNMENT_MAX = BRIG_ALIGNMENT_256
00146 };
00147
00148 typedef uint8_t BrigAllocation8_t;
00149 enum BrigAllocation {
00150 BRIG_ALLOCATION_NONE = 0,
00151 BRIG_ALLOCATION_PROGRAM = 1,
00152 BRIG_ALLOCATION_AGENT = 2,
00153 BRIG_ALLOCATION_AUTOMATIC = 3
00154 };
00155
00156 typedef uint8_t BrigAluModifier8_t;
00157 enum BrigAluModifierMask {
00158 BRIG_ALU_FTZ = 1
00159 };
00160
00161 typedef uint8_t BrigAtomicOperation8_t;
00162 enum BrigAtomicOperation {
00163 BRIG_ATOMIC_ADD = 0,
00164 BRIG_ATOMIC_AND = 1,
00165 BRIG_ATOMIC_CAS = 2,
00166 BRIG_ATOMIC_EXCH = 3,
00167 BRIG_ATOMIC_LD = 4,
00168 BRIG_ATOMIC_MAX = 5,
00169 BRIG_ATOMIC_MIN = 6,
00170 BRIG_ATOMIC_OR = 7,
00171 BRIG_ATOMIC_ST = 8,
00172 BRIG_ATOMIC_SUB = 9,
00173 BRIG_ATOMIC_WRAPDEC = 10,
00174 BRIG_ATOMIC_WRAPINC = 11,
00175 BRIG_ATOMIC_XOR = 12,
00176 BRIG_ATOMIC_WAIT_EQ = 13,
00177 BRIG_ATOMIC_WAIT_NE = 14,
00178 BRIG_ATOMIC_WAIT_LT = 15,
00179 BRIG_ATOMIC_WAIT_GTE = 16,
00180 BRIG_ATOMIC_WAITTIMEOUT_EQ = 17,
00181 BRIG_ATOMIC_WAITTIMEOUT_NE = 18,
00182 BRIG_ATOMIC_WAITTIMEOUT_LT = 19,
00183 BRIG_ATOMIC_WAITTIMEOUT_GTE = 20
00184 };
00185
00186 typedef uint8_t BrigCompareOperation8_t;
00187 enum BrigCompareOperation {
00188 BRIG_COMPARE_EQ = 0,
00189 BRIG_COMPARE_NE = 1,

```



```
00190 BRIG_COMPARE_LT = 2,
00191 BRIG_COMPARE_LE = 3,
00192 BRIG_COMPARE_GT = 4,
00193 BRIG_COMPARE_GE = 5,
00194 BRIG_COMPARE_EQU = 6,
00195 BRIG_COMPARE_NEU = 7,
00196 BRIG_COMPARE_LTU = 8,
00197 BRIG_COMPARE_LEU = 9,
00198 BRIG_COMPARE_GTU = 10,
00199 BRIG_COMPARE_GEU = 11,
00200 BRIG_COMPARE_NUM = 12,
00201 BRIG_COMPARE_NAN = 13,
00202 BRIG_COMPARE_SEQ = 14,
00203 BRIG_COMPARE_SNE = 15,
00204 BRIG_COMPARE_SLT = 16,
00205 BRIG_COMPARE_SLE = 17,
00206 BRIG_COMPARE_SGT = 18,
00207 BRIG_COMPARE_SGE = 19,
00208 BRIG_COMPARE_SGEU = 20,
00209 BRIG_COMPARE_SEQU = 21,
00210 BRIG_COMPARE_SNEU = 22,
00211 BRIG_COMPARE_SLTU = 23,
00212 BRIG_COMPARE_SLEU = 24,
00213 BRIG_COMPARE_SNUM = 25,
00214 BRIG_COMPARE_SNAN = 26,
00215 BRIG_COMPARE_SGTU = 27
00216 };
00217
00218 typedef uint16_t BrigControlDirective16_t;
00219 enum BrigControlDirective {
00220 BRIG_CONTROL_NONE = 0,
00221 BRIG_CONTROL_ENABLEBREAKEXCEPTIONS = 1,
00222 BRIG_CONTROL_ENABLEDETECTEXCEPTIONS = 2,
00223 BRIG_CONTROL_MAXDYNAMICGROUPSIZE = 3,
00224 BRIG_CONTROL_MAXFLATGRIDSIZE = 4,
00225 BRIG_CONTROL_MAXFLATWORKGROUPSIZE = 5,
00226 BRIG_CONTROL_REQUIREDDIM = 6,
00227 BRIG_CONTROL_REQUIREDGRIDSIZE = 7,
00228 BRIG_CONTROL_REQUIREDWORKGROUPSIZE = 8,
00229 BRIG_CONTROL_REQUIRENOPARTIALWORKGROUPS = 9
00230 };
00231
00232 typedef uint8_t BrigExecutableModifier8_t;
00233 enum BrigExecutableModifierMask {
00234 BRIG_EXECUTABLE_DEFINITION = 1
00235 };
00236
00237 typedef uint8_t BrigImageChannelOrder8_t;
00238 enum BrigImageChannelOrder {
00239 BRIG_CHANNEL_ORDER_A = 0,
00240 BRIG_CHANNEL_ORDER_R = 1,
00241 BRIG_CHANNEL_ORDER_RX = 2,
00242 BRIG_CHANNEL_ORDER_RG = 3,
00243 BRIG_CHANNEL_ORDER_RGX = 4,
00244 BRIG_CHANNEL_ORDER_RA = 5,
00245 BRIG_CHANNEL_ORDER_RGB = 6,
00246 BRIG_CHANNEL_ORDER_RGBX = 7,
00247 BRIG_CHANNEL_ORDER_RGBA = 8,
00248 BRIG_CHANNEL_ORDER_BGRA = 9,
00249 BRIG_CHANNEL_ORDER_ARGB = 10,
00250 BRIG_CHANNEL_ORDER_ABGR = 11,
00251 BRIG_CHANNEL_ORDER_SRGB = 12,
00252 BRIG_CHANNEL_ORDER_SRGBX = 13,
00253 BRIG_CHANNEL_ORDER_SRGBA = 14,
00254 BRIG_CHANNEL_ORDER_SBGRA = 15,
00255 BRIG_CHANNEL_ORDER_INTENSITY = 16,
00256 BRIG_CHANNEL_ORDER_LUMINANCE = 17,
00257 BRIG_CHANNEL_ORDER_DEPTH = 18,
00258 BRIG_CHANNEL_ORDER_DEPTH_STENCIL = 19,
00259
00260 BRIG_CHANNEL_ORDER_FIRST_USER_DEFINED = 128
00261 };
00262
00263 typedef uint8_t BrigImageChannelType8_t;
00264 enum BrigImageChannelType {
00265 BRIG_CHANNEL_TYPE_SNORM_INT8 = 0,
00266 BRIG_CHANNEL_TYPE_SNORM_INT16 = 1,
00267 BRIG_CHANNEL_TYPE_UNORM_INT8 = 2,
00268 BRIG_CHANNEL_TYPE_UNORM_INT16 = 3,
00269 BRIG_CHANNEL_TYPE_UNORM_INT24 = 4,
00270 BRIG_CHANNEL_TYPE_UNORM_SHORT_555 = 5,
00271 BRIG_CHANNEL_TYPE_UNORM_SHORT_565 = 6,
00272 BRIG_CHANNEL_TYPE_UNORM_INT_101010 = 7,
00273 BRIG_CHANNEL_TYPE_SIGNED_INT8 = 8,
00274 BRIG_CHANNEL_TYPE_SIGNED_INT16 = 9,
00275 BRIG_CHANNEL_TYPE_SIGNED_INT32 = 10,
00276 BRIG_CHANNEL_TYPE_UNSIGNED_INT8 = 11,
```

```
00277 BRIG_CHANNEL_TYPE_UNSIGNED_INT16 = 12,
00278 BRIG_CHANNEL_TYPE_UNSIGNED_INT32 = 13,
00279 BRIG_CHANNEL_TYPE_HALF_FLOAT = 14,
00280 BRIG_CHANNEL_TYPE_FLOAT = 15,
00281
00282 BRIG_CHANNEL_TYPE_FIRST_USER_DEFINED = 128
00283 };
00284
00285 typedef uint8_t BrigImageGeometry8_t;
00286 enum BrigImageGeometry {
00287 BRIG_GEOMETRY_1D = 0,
00288 BRIG_GEOMETRY_2D = 1,
00289 BRIG_GEOMETRY_3D = 2,
00290 BRIG_GEOMETRY_1DA = 3,
00291 BRIG_GEOMETRY_2DA = 4,
00292 BRIG_GEOMETRY_1DB = 5,
00293 BRIG_GEOMETRY_2DDEPTH = 6,
00294 BRIG_GEOMETRY_2DADEPTH = 7,
00295
00296 BRIG_GEOMETRY_FIRST_USER_DEFINED = 128
00297 };
00298
00299 typedef uint8_t BrigImageQuery8_t;
00300 enum BrigImageQuery {
00301 BRIG_IMAGE_QUERY_WIDTH = 0,
00302 BRIG_IMAGE_QUERY_HEIGHT = 1,
00303 BRIG_IMAGE_QUERY_DEPTH = 2,
00304 BRIG_IMAGE_QUERY_ARRAY = 3,
00305 BRIG_IMAGE_QUERY_CHANNELORDER = 4,
00306 BRIG_IMAGE_QUERY_CHANNELTYPE = 5,
00307
00308 BRIG_IMAGE_QUERY_FIRST_USER_DEFINED = 6
00309 };
00310
00311 typedef uint8_t BrigLinkage8_t;
00312 enum BrigLinkage {
00313 BRIG_LINKAGE_NONE = 0,
00314 BRIG_LINKAGE_PROGRAM = 1,
00315 BRIG_LINKAGE_MODULE = 2,
00316 BRIG_LINKAGE_FUNCTION = 3,
00317 BRIG_LINKAGE_ARG = 4
00318 };
00319
00320 typedef uint8_t BrigMachineModel8_t;
00321 enum BrigMachineModel {
00322 BRIG_MACHINE_SMALL = 0,
00323 BRIG_MACHINE_LARGE = 1,
00324 };
00325
00326 typedef uint8_t BrigMemoryModifier8_t;
00327 enum BrigMemoryModifierMask {
00328 BRIG_MEMORY_CONST = 1
00329 };
00330
00331 typedef uint8_t BrigMemoryOrder8_t;
00332 enum BrigMemoryOrder {
00333 BRIG_MEMORY_ORDER_NONE = 0,
00334 BRIG_MEMORY_ORDER_RELAXED = 1,
00335 BRIG_MEMORY_ORDER_SC_ACQUIRE = 2,
00336 BRIG_MEMORY_ORDER_SC_RELEASE = 3,
00337 BRIG_MEMORY_ORDER_SC_ACQUIRE_RELEASE = 4,
00338 };
00339
00340 typedef uint8_t BrigMemoryScope8_t;
00341 enum BrigMemoryScope {
00342 BRIG_MEMORY_SCOPE_NONE = 0,
00343 BRIG_MEMORY_SCOPE_WORKITEM = 1,
00344 BRIG_MEMORY_SCOPE_WAVEFRONT = 2,
00345 BRIG_MEMORY_SCOPE_WORKGROUP = 3,
00346 BRIG_MEMORY_SCOPE_AGENT = 4,
00347 BRIG_MEMORY_SCOPE_SYSTEM = 5,
00348 };
00349
00350 typedef uint16_t BrigOpcode16_t;
00351 enum BrigOpcode {
00352 BRIG_OPCODE_NOP = 0,
00353 BRIG_OPCODE_ABS = 1,
00354 BRIG_OPCODE_ADD = 2,
00355 BRIG_OPCODE_BORROW = 3,
00356 BRIG_OPCODE_CARRY = 4,
00357 BRIG_OPCODE_CEIL = 5,
00358 BRIG_OPCODE_COPYSIGN = 6,
00359 BRIG_OPCODE_DIV = 7,
00360 BRIG_OPCODE_FLOOR = 8,
00361 BRIG_OPCODE_FMA = 9,
00362 BRIG_OPCODE_FRACT = 10,
00363 BRIG_OPCODE_MAD = 11,
```

```
00364 BRIG_OPCODE_MAX = 12,
00365 BRIG_OPCODE_MIN = 13,
00366 BRIG_OPCODE_MUL = 14,
00367 BRIG_OPCODE_MULHI = 15,
00368 BRIG_OPCODE_NEG = 16,
00369 BRIG_OPCODE_REM = 17,
00370 BRIG_OPCODE_RINT = 18,
00371 BRIG_OPCODE_SQRT = 19,
00372 BRIG_OPCODE_SUB = 20,
00373 BRIG_OPCODE_TRUNC = 21,
00374 BRIG_OPCODE_MAD24 = 22,
00375 BRIG_OPCODE_MAD24HI = 23,
00376 BRIG_OPCODE_MUL24 = 24,
00377 BRIG_OPCODE_MUL24HI = 25,
00378 BRIG_OPCODE_SHL = 26,
00379 BRIG_OPCODE_SHR = 27,
00380 BRIG_OPCODE_AND = 28,
00381 BRIG_OPCODE_NOT = 29,
00382 BRIG_OPCODE_OR = 30,
00383 BRIG_OPCODE_POPCOUNT = 31,
00384 BRIG_OPCODE_XOR = 32,
00385 BRIG_OPCODE_BITEXTRACT = 33,
00386 BRIG_OPCODE_BITINSERT = 34,
00387 BRIG_OPCODE_BITMASK = 35,
00388 BRIG_OPCODE_BITREV = 36,
00389 BRIG_OPCODE_BITSELECT = 37,
00390 BRIG_OPCODE_FIRSTBIT = 38,
00391 BRIG_OPCODE_LASTBIT = 39,
00392 BRIG_OPCODE_COMBINE = 40,
00393 BRIG_OPCODE_EXPAND = 41,
00394 BRIG_OPCODE_LDA = 42,
00395 BRIG_OPCODE_MOV = 43,
00396 BRIG_OPCODE_SHUFFLE = 44,
00397 BRIG_OPCODE_UNPACKHI = 45,
00398 BRIG_OPCODE_UNPACKLO = 46,
00399 BRIG_OPCODE_PACK = 47,
00400 BRIG_OPCODE_UNPACK = 48,
00401 BRIG_OPCODE_CMOV = 49,
00402 BRIG_OPCODE_CLASS = 50,
00403 BRIG_OPCODE_NCOS = 51,
00404 BRIG_OPCODE_NEXP2 = 52,
00405 BRIG_OPCODE_NFMA = 53,
00406 BRIG_OPCODE_NLOG2 = 54,
00407 BRIG_OPCODE_NRCP = 55,
00408 BRIG_OPCODE_NRSQRT = 56,
00409 BRIG_OPCODE_NSIN = 57,
00410 BRIG_OPCODE_NSQRT = 58,
00411 BRIG_OPCODE_BITALIGN = 59,
00412 BRIG_OPCODE_BYTEALIGN = 60,
00413 BRIG_OPCODE_PACKCVT = 61,
00414 BRIG_OPCODE_UNPACKCVT = 62,
00415 BRIG_OPCODE_LERP = 63,
00416 BRIG_OPCODE_SAD = 64,
00417 BRIG_OPCODE_SADHI = 65,
00418 BRIG_OPCODE_SEGMENTP = 66,
00419 BRIG_OPCODE_FTOS = 67,
00420 BRIG_OPCODE_STOF = 68,
00421 BRIG_OPCODE_CMP = 69,
00422 BRIG_OPCODE_CVT = 70,
00423 BRIG_OPCODE_LD = 71,
00424 BRIG_OPCODE_ST = 72,
00425 BRIG_OPCODE_ATOMIC = 73,
00426 BRIG_OPCODE_ATOMICNORET = 74,
00427 BRIG_OPCODE_SIGNAL = 75,
00428 BRIG_OPCODE_SIGNALNORET = 76,
00429 BRIG_OPCODE_MEMFENCE = 77,
00430 BRIG_OPCODE_RDIMAGE = 78,
00431 BRIG_OPCODE_LDIMAGE = 79,
00432 BRIG_OPCODE_STIMAGE = 80,
00433 BRIG_OPCODE_IMAGEFENCE = 81,
00434 BRIG_OPCODE_QUERYIMAGE = 82,
00435 BRIG_OPCODE_QUERYAMPLER = 83,
00436 BRIG_OPCODE_CBR = 84,
00437 BRIG_OPCODE_BR = 85,
00438 BRIG_OPCODE_SBR = 86,
00439 BRIG_OPCODE_BARRIER = 87,
00440 BRIG_OPCODE_WAVEBARRIER = 88,
00441 BRIG_OPCODE_ARRIVEFBAR = 89,
00442 BRIG_OPCODE_INITFBAR = 90,
00443 BRIG_OPCODE_JOINFBAR = 91,
00444 BRIG_OPCODE_LEAVEFBAR = 92,
00445 BRIG_OPCODE_RELEASEFBAR = 93,
00446 BRIG_OPCODE_WAITFBAR = 94,
00447 BRIG_OPCODE_LDF = 95,
00448 BRIG_OPCODE_ACTIVELANECOUNT = 96,
00449 BRIG_OPCODE_ACTIVELANEID = 97,
00450 BRIG_OPCODE_ACTIVELANEMASK = 98,
```

```
00451 BRIG_OPCODE_ACTIVELANEPERMUTE = 99,
00452 BRIG_OPCODE_CALL = 100,
00453 BRIG_OPCODE_SCALL = 101,
00454 BRIG_OPCODE_ICALL = 102,
00455 BRIG_OPCODE_RET = 103,
00456 BRIG_OPCODE_ALLOCA = 104,
00457 BRIG_OPCODE_CURRENTWORKGROUPSIZE = 105,
00458 BRIG_OPCODE_CURRENTWORKITEMFLATID = 106,
00459 BRIG_OPCODE_DIM = 107,
00460 BRIG_OPCODE_GRIDGROUPS = 108,
00461 BRIG_OPCODE_GRIDSIZE = 109,
00462 BRIG_OPCODE_PACKETCOMPLETIONSIG = 110,
00463 BRIG_OPCODE_PACKETID = 111,
00464 BRIG_OPCODE_WORKGROUPID = 112,
00465 BRIG_OPCODE_WORKGROUPSIZE = 113,
00466 BRIG_OPCODE_WORKITEMABSID = 114,
00467 BRIG_OPCODE_WORKITEMFLATABSID = 115,
00468 BRIG_OPCODE_WORKITEMFLATID = 116,
00469 BRIG_OPCODE_WORKITEMID = 117,
00470 BRIG_OPCODE_CLEARDETECTEXCEPT = 118,
00471 BRIG_OPCODE_GETDETECTEXCEPT = 119,
00472 BRIG_OPCODE_SETDETECTEXCEPT = 120,
00473 BRIG_OPCODE_ADDQUEUEWRITEINDEX = 121,
00474 BRIG_OPCODE_CASQUEUEWRITEINDEX = 122,
00475 BRIG_OPCODE_LDQUEUEWRITEINDEX = 123,
00476 BRIG_OPCODE_LDQUEUEWRITEINDEX = 124,
00477 BRIG_OPCODE_STQUEUEWRITEINDEX = 125,
00478 BRIG_OPCODE_STQUEUEWRITEINDEX = 126,
00479 BRIG_OPCODE_CLOCK = 127,
00480 BRIG_OPCODE_CUID = 128,
00481 BRIG_OPCODE_DEBUGTRAP = 129,
00482 BRIG_OPCODE_GROUPBASEPTR = 130,
00483 BRIG_OPCODE_KERNARGBASEPTR = 131,
00484 BRIG_OPCODE_LANEID = 132,
00485 BRIG_OPCODE_MAXCUID = 133,
00486 BRIG_OPCODE_MAXWAVEID = 134,
00487 BRIG_OPCODE_NULLPTR = 135,
00488 BRIG_OPCODE_WAVEID = 136,
00489
00490 BRIG_OPCODE_FIRST_USER_DEFINED = 32768,
00491 };
00492
00493 typedef uint8_t BrigPack8_t;
00494 enum BrigPack {
00495 BRIG_PACK_NONE = 0,
00496 BRIG_PACK_PP = 1,
00497 BRIG_PACK_PS = 2,
00498 BRIG_PACK_SP = 3,
00499 BRIG_PACK_SS = 4,
00500 BRIG_PACK_S = 5,
00501 BRIG_PACK_P = 6,
00502 BRIG_PACK_PPSAT = 7,
00503 BRIG_PACK_PSSAT = 8,
00504 BRIG_PACK_SPSAT = 9,
00505 BRIG_PACK_SSSAT = 10,
00506 BRIG_PACK_SSAT = 11,
00507 BRIG_PACK_PPSAT = 12
00508 };
00509
00510 typedef uint8_t BrigProfile8_t;
00511 enum BrigProfile {
00512 BRIG_PROFILE_BASE = 0,
00513 BRIG_PROFILE_FULL = 1,
00514 };
00515
00516 typedef uint16_t BrigRegisterKind16_t;
00517 enum BrigRegisterKind {
00518 BRIG_REGISTER_KIND_CONTROL = 0,
00519 BRIG_REGISTER_KIND_SINGLE = 1,
00520 BRIG_REGISTER_KIND_DOUBLE = 2,
00521 BRIG_REGISTER_KIND_QUAD = 3
00522 };
00523
00524 typedef uint8_t BrigRound8_t;
00525 enum BrigRound {
00526 BRIG_ROUND_NONE = 0,
00527 BRIG_ROUND_FLOAT_DEFAULT = 1,
00528 BRIG_ROUND_FLOAT_NEAR_EVEN = 2,
00529 BRIG_ROUND_FLOAT_ZERO = 3,
00530 BRIG_ROUND_FLOAT_PLUS_INFINITY = 4,
00531 BRIG_ROUND_FLOAT_MINUS_INFINITY = 5,
00532 BRIG_ROUND_INTEGER_NEAR_EVEN = 6,
00533 BRIG_ROUND_INTEGER_ZERO = 7,
00534 BRIG_ROUND_INTEGER_PLUS_INFINITY = 8,
00535 BRIG_ROUND_INTEGER_MINUS_INFINITY = 9,
00536 BRIG_ROUND_INTEGER_NEAR_EVEN_SAT = 10,
00537 BRIG_ROUND_INTEGER_ZERO_SAT = 11,
```

```

00538 BRIG_ROUND_INTEGER_PLUS_INFINITY_SAT = 12,
00539 BRIG_ROUND_INTEGER_MINUS_INFINITY_SAT = 13,
00540 BRIG_ROUND_INTEGER_SIGNALING_NEAR_EVEN = 14,
00541 BRIG_ROUND_INTEGER_SIGNALING_ZERO = 15,
00542 BRIG_ROUND_INTEGER_SIGNALING_PLUS_INFINITY = 16,
00543 BRIG_ROUND_INTEGER_SIGNALING_MINUS_INFINITY = 17,
00544 BRIG_ROUND_INTEGER_SIGNALING_NEAR_EVEN_SAT = 18,
00545 BRIG_ROUND_INTEGER_SIGNALING_ZERO_SAT = 19,
00546 BRIG_ROUND_INTEGER_SIGNALING_PLUS_INFINITY_SAT = 20,
00547 BRIG_ROUND_INTEGER_SIGNALING_MINUS_INFINITY_SAT = 21
00548 };
00549
00550 typedef uint8_t BrigSamplerAddressing8_t;
00551 enum BrigSamplerAddressing {
00552 BRIG_ADDRESSING_UNDEFINED = 0,
00553 BRIG_ADDRESSING_CLAMP_TO_EDGE = 1,
00554 BRIG_ADDRESSING_CLAMP_TO_BORDER = 2,
00555 BRIG_ADDRESSING_REPEAT = 3,
00556 BRIG_ADDRESSING_MIRRORED_REPEAT = 4,
00557
00558 BRIG_ADDRESSING_FIRST_USER_DEFINED = 128
00559 };
00560
00561 typedef uint8_t BrigSamplerCoordNormalization8_t;
00562 enum BrigSamplerCoordNormalization {
00563 BRIG_COORD_UNNORMALIZED = 0,
00564 BRIG_COORD_NORMALIZED = 1
00565 };
00566
00567 typedef uint8_t BrigSamplerFilter8_t;
00568 enum BrigSamplerFilter {
00569 BRIG_FILTER_NEAREST = 0,
00570 BRIG_FILTER_LINEAR = 1,
00571
00572 BRIG_FILTER_FIRST_USER_DEFINED = 128
00573 };
00574
00575 typedef uint8_t BrigSamplerQuery8_t;
00576 enum BrigSamplerQuery {
00577 BRIG_SAMPLER_QUERY_ADDRESSING = 0,
00578 BRIG_SAMPLER_QUERY_COORD = 1,
00579 BRIG_SAMPLER_QUERY_FILTER = 2
00580 };
00581
00582 typedef uint32_t BrigSectionIndex32_t;
00583 enum BrigSectionIndex {
00584 BRIG_SECTION_INDEX_DATA = 0,
00585 BRIG_SECTION_INDEX_CODE = 1,
00586 BRIG_SECTION_INDEX_OPERAND = 2,
00587
00588 BRIG_SECTION_INDEX_BEGIN_IMPLEMENTATION_DEFINED = 3,
00589 };
00590
00591 typedef uint8_t BrigSegCvtModifier8_t;
00592 enum BrigSegCvtModifierMask {
00593 BRIG_SEG_CVT_NONULL = 1
00594 };
00595
00596 typedef uint8_t BrigSegment8_t;
00597 enum BrigSegment {
00598 BRIG_SEGMENT_NONE = 0,
00599 BRIG_SEGMENT_FLAT = 1,
00600 BRIG_SEGMENT_GLOBAL = 2,
00601 BRIG_SEGMENT_READONLY = 3,
00602 BRIG_SEGMENT_KERNARG = 4,
00603 BRIG_SEGMENT_GROUP = 5,
00604 BRIG_SEGMENT_PRIVATE = 6,
00605 BRIG_SEGMENT_SPILL = 7,
00606 BRIG_SEGMENT_ARG = 8,
00607
00608 BRIG_SEGMENT_FIRST_USER_DEFINED = 128
00609 };
00610
00611 enum {
00612 BRIG_TYPE_BASE_SIZE = 5,
00613 BRIG_TYPE_PACK_SIZE = 2,
00614 BRIG_TYPE_ARRAY_SIZE = 1,
00615
00616 BRIG_TYPE_BASE_SHIFT = 0,
00617 BRIG_TYPE_PACK_SHIFT = BRIG_TYPE_BASE_SHIFT + BRIG_TYPE_BASE_SIZE,
00618 BRIG_TYPE_ARRAY_SHIFT = BRIG_TYPE_PACK_SHIFT + BRIG_TYPE_PACK_SIZE,
00619
00620 BRIG_TYPE_BASE_MASK = ((1 < BRIG_TYPE_BASE_SIZE) - 1) < BRIG_TYPE_BASE_SHIFT,
00621 BRIG_TYPE_PACK_MASK = ((1 < BRIG_TYPE_PACK_SIZE) - 1) < BRIG_TYPE_PACK_SHIFT,
00622 BRIG_TYPE_ARRAY_MASK = ((1 < BRIG_TYPE_ARRAY_SIZE) - 1) < BRIG_TYPE_ARRAY_SHIFT,
00623
00624 BRIG_TYPE_PACK_NONE = 0 < BRIG_TYPE_PACK_SHIFT,

```

```

00625 BRIG_TYPE_PACK_32 = 1 « BRIG_TYPE_PACK_SHIFT,
00626 BRIG_TYPE_PACK_64 = 2 « BRIG_TYPE_PACK_SHIFT,
00627 BRIG_TYPE_PACK_128 = 3 « BRIG_TYPE_PACK_SHIFT,
00628
00629 BRIG_TYPE_ARRAY = 1 « BRIG_TYPE_ARRAY_SHIFT
00630 };
00631
00632 typedef uint16_t BrigType16_t;
00633 enum BrigType {
00634 BRIG_TYPE_NONE = 0,
00635 BRIG_TYPE_U8 = 1,
00636 BRIG_TYPE_U16 = 2,
00637 BRIG_TYPE_U32 = 3,
00638 BRIG_TYPE_U64 = 4,
00639 BRIG_TYPE_S8 = 5,
00640 BRIG_TYPE_S16 = 6,
00641 BRIG_TYPE_S32 = 7,
00642 BRIG_TYPE_S64 = 8,
00643 BRIG_TYPE_F16 = 9,
00644 BRIG_TYPE_F32 = 10,
00645 BRIG_TYPE_F64 = 11,
00646 BRIG_TYPE_B1 = 12,
00647 BRIG_TYPE_B8 = 13,
00648 BRIG_TYPE_B16 = 14,
00649 BRIG_TYPE_B32 = 15,
00650 BRIG_TYPE_B64 = 16,
00651 BRIG_TYPE_B128 = 17,
00652 BRIG_TYPE_SAMP = 18,
00653 BRIG_TYPE_ROIMG = 19,
00654 BRIG_TYPE_WOIMG = 20,
00655 BRIG_TYPE_RWIMG = 21,
00656 BRIG_TYPE_SIG32 = 22,
00657 BRIG_TYPE_SIG64 = 23,
00658
00659 BRIG_TYPE_U8X4 = BRIG_TYPE_U8 | BRIG_TYPE_PACK_32,
00660 BRIG_TYPE_U8X8 = BRIG_TYPE_U8 | BRIG_TYPE_PACK_64,
00661 BRIG_TYPE_U8X16 = BRIG_TYPE_U8 | BRIG_TYPE_PACK_128,
00662 BRIG_TYPE_U16X2 = BRIG_TYPE_U16 | BRIG_TYPE_PACK_32,
00663 BRIG_TYPE_U16X4 = BRIG_TYPE_U16 | BRIG_TYPE_PACK_64,
00664 BRIG_TYPE_U16X8 = BRIG_TYPE_U16 | BRIG_TYPE_PACK_128,
00665 BRIG_TYPE_U32X2 = BRIG_TYPE_U32 | BRIG_TYPE_PACK_64,
00666 BRIG_TYPE_U32X4 = BRIG_TYPE_U32 | BRIG_TYPE_PACK_128,
00667 BRIG_TYPE_U64X2 = BRIG_TYPE_U64 | BRIG_TYPE_PACK_128,
00668 BRIG_TYPE_S8X4 = BRIG_TYPE_S8 | BRIG_TYPE_PACK_32,
00669 BRIG_TYPE_S8X8 = BRIG_TYPE_S8 | BRIG_TYPE_PACK_64,
00670 BRIG_TYPE_S8X16 = BRIG_TYPE_S8 | BRIG_TYPE_PACK_128,
00671 BRIG_TYPE_S16X2 = BRIG_TYPE_S16 | BRIG_TYPE_PACK_32,
00672 BRIG_TYPE_S16X4 = BRIG_TYPE_S16 | BRIG_TYPE_PACK_64,
00673 BRIG_TYPE_S16X8 = BRIG_TYPE_S16 | BRIG_TYPE_PACK_128,
00674 BRIG_TYPE_S32X2 = BRIG_TYPE_S32 | BRIG_TYPE_PACK_64,
00675 BRIG_TYPE_S32X4 = BRIG_TYPE_S32 | BRIG_TYPE_PACK_128,
00676 BRIG_TYPE_S64X2 = BRIG_TYPE_S64 | BRIG_TYPE_PACK_128,
00677 BRIG_TYPE_F16X2 = BRIG_TYPE_F16 | BRIG_TYPE_PACK_32,
00678 BRIG_TYPE_F16X4 = BRIG_TYPE_F16 | BRIG_TYPE_PACK_64,
00679 BRIG_TYPE_F16X8 = BRIG_TYPE_F16 | BRIG_TYPE_PACK_128,
00680 BRIG_TYPE_F32X2 = BRIG_TYPE_F32 | BRIG_TYPE_PACK_64,
00681 BRIG_TYPE_F32X4 = BRIG_TYPE_F32 | BRIG_TYPE_PACK_128,
00682 BRIG_TYPE_F64X2 = BRIG_TYPE_F64 | BRIG_TYPE_PACK_128,
00683
00684 BRIG_TYPE_U8_ARRAY = BRIG_TYPE_U8 | BRIG_TYPE_ARRAY,
00685 BRIG_TYPE_U16_ARRAY = BRIG_TYPE_U16 | BRIG_TYPE_ARRAY,
00686 BRIG_TYPE_U32_ARRAY = BRIG_TYPE_U32 | BRIG_TYPE_ARRAY,
00687 BRIG_TYPE_U64_ARRAY = BRIG_TYPE_U64 | BRIG_TYPE_ARRAY,
00688 BRIG_TYPE_S8_ARRAY = BRIG_TYPE_S8 | BRIG_TYPE_ARRAY,
00689 BRIG_TYPE_S16_ARRAY = BRIG_TYPE_S16 | BRIG_TYPE_ARRAY,
00690 BRIG_TYPE_S32_ARRAY = BRIG_TYPE_S32 | BRIG_TYPE_ARRAY,
00691 BRIG_TYPE_S64_ARRAY = BRIG_TYPE_S64 | BRIG_TYPE_ARRAY,
00692 BRIG_TYPE_F16_ARRAY = BRIG_TYPE_F16 | BRIG_TYPE_ARRAY,
00693 BRIG_TYPE_F32_ARRAY = BRIG_TYPE_F32 | BRIG_TYPE_ARRAY,
00694 BRIG_TYPE_F64_ARRAY = BRIG_TYPE_F64 | BRIG_TYPE_ARRAY,
00695 BRIG_TYPE_B8_ARRAY = BRIG_TYPE_B8 | BRIG_TYPE_ARRAY,
00696 BRIG_TYPE_B16_ARRAY = BRIG_TYPE_B16 | BRIG_TYPE_ARRAY,
00697 BRIG_TYPE_B32_ARRAY = BRIG_TYPE_B32 | BRIG_TYPE_ARRAY,
00698 BRIG_TYPE_B64_ARRAY = BRIG_TYPE_B64 | BRIG_TYPE_ARRAY,
00699 BRIG_TYPE_B128_ARRAY = BRIG_TYPE_B128 | BRIG_TYPE_ARRAY,
00700 BRIG_TYPE_SAMP_ARRAY = BRIG_TYPE_SAMP | BRIG_TYPE_ARRAY,
00701 BRIG_TYPE_ROIMG_ARRAY = BRIG_TYPE_ROIMG | BRIG_TYPE_ARRAY,
00702 BRIG_TYPE_WOIMG_ARRAY = BRIG_TYPE_WOIMG | BRIG_TYPE_ARRAY,
00703 BRIG_TYPE_RWIMG_ARRAY = BRIG_TYPE_RWIMG | BRIG_TYPE_ARRAY,
00704 BRIG_TYPE_SIG32_ARRAY = BRIG_TYPE_SIG32 | BRIG_TYPE_ARRAY,
00705 BRIG_TYPE_SIG64_ARRAY = BRIG_TYPE_SIG64 | BRIG_TYPE_ARRAY,
00706 BRIG_TYPE_U8X4_ARRAY = BRIG_TYPE_U8X4 | BRIG_TYPE_ARRAY,
00707 BRIG_TYPE_U8X8_ARRAY = BRIG_TYPE_U8X8 | BRIG_TYPE_ARRAY,
00708 BRIG_TYPE_U8X16_ARRAY = BRIG_TYPE_U8X16 | BRIG_TYPE_ARRAY,
00709 BRIG_TYPE_U16X2_ARRAY = BRIG_TYPE_U16X2 | BRIG_TYPE_ARRAY,
00710 BRIG_TYPE_U16X4_ARRAY = BRIG_TYPE_U16X4 | BRIG_TYPE_ARRAY,
00711 BRIG_TYPE_U16X8_ARRAY = BRIG_TYPE_U16X8 | BRIG_TYPE_ARRAY,

```

```

00712 BRIG_TYPE_U32X2_ARRAY = BRIG_TYPE_U32X2 | BRIG_TYPE_ARRAY,
00713 BRIG_TYPE_U32X4_ARRAY = BRIG_TYPE_U32X4 | BRIG_TYPE_ARRAY,
00714 BRIG_TYPE_U64X2_ARRAY = BRIG_TYPE_U64X2 | BRIG_TYPE_ARRAY,
00715 BRIG_TYPE_S8X4_ARRAY = BRIG_TYPE_S8X4 | BRIG_TYPE_ARRAY,
00716 BRIG_TYPE_S8X8_ARRAY = BRIG_TYPE_S8X8 | BRIG_TYPE_ARRAY,
00717 BRIG_TYPE_S8X16_ARRAY = BRIG_TYPE_S8X16 | BRIG_TYPE_ARRAY,
00718 BRIG_TYPE_S16X2_ARRAY = BRIG_TYPE_S16X2 | BRIG_TYPE_ARRAY,
00719 BRIG_TYPE_S16X4_ARRAY = BRIG_TYPE_S16X4 | BRIG_TYPE_ARRAY,
00720 BRIG_TYPE_S16X8_ARRAY = BRIG_TYPE_S16X8 | BRIG_TYPE_ARRAY,
00721 BRIG_TYPE_S32X2_ARRAY = BRIG_TYPE_S32X2 | BRIG_TYPE_ARRAY,
00722 BRIG_TYPE_S32X4_ARRAY = BRIG_TYPE_S32X4 | BRIG_TYPE_ARRAY,
00723 BRIG_TYPE_S64X2_ARRAY = BRIG_TYPE_S64X2 | BRIG_TYPE_ARRAY,
00724 BRIG_TYPE_F16X2_ARRAY = BRIG_TYPE_F16X2 | BRIG_TYPE_ARRAY,
00725 BRIG_TYPE_F16X4_ARRAY = BRIG_TYPE_F16X4 | BRIG_TYPE_ARRAY,
00726 BRIG_TYPE_F16X8_ARRAY = BRIG_TYPE_F16X8 | BRIG_TYPE_ARRAY,
00727 BRIG_TYPE_F32X2_ARRAY = BRIG_TYPE_F32X2 | BRIG_TYPE_ARRAY,
00728 BRIG_TYPE_F32X4_ARRAY = BRIG_TYPE_F32X4 | BRIG_TYPE_ARRAY,
00729 BRIG_TYPE_F64X2_ARRAY = BRIG_TYPE_F64X2 | BRIG_TYPE_ARRAY,
00730 };
00731
00732 typedef uint8_t BrigVariableModifier8_t;
00733 enum BrigVariableModifierMask {
00734 BRIG_VARIABLE_DEFINITION = 1,
00735 BRIG_VARIABLE_CONST = 2
00736 };
00737
00738 typedef uint8_t BrigWidth8_t;
00739 enum BrigWidth {
00740 BRIG_WIDTH_NONE = 0,
00741 BRIG_WIDTH_1 = 1,
00742 BRIG_WIDTH_2 = 2,
00743 BRIG_WIDTH_4 = 3,
00744 BRIG_WIDTH_8 = 4,
00745 BRIG_WIDTH_16 = 5,
00746 BRIG_WIDTH_32 = 6,
00747 BRIG_WIDTH_64 = 7,
00748 BRIG_WIDTH_128 = 8,
00749 BRIG_WIDTH_256 = 9,
00750 BRIG_WIDTH_512 = 10,
00751 BRIG_WIDTH_1024 = 11,
00752 BRIG_WIDTH_2048 = 12,
00753 BRIG_WIDTH_4096 = 13,
00754 BRIG_WIDTH_8192 = 14,
00755 BRIG_WIDTH_16384 = 15,
00756 BRIG_WIDTH_32768 = 16,
00757 BRIG_WIDTH_65536 = 17,
00758 BRIG_WIDTH_131072 = 18,
00759 BRIG_WIDTH_262144 = 19,
00760 BRIG_WIDTH_524288 = 20,
00761 BRIG_WIDTH_1048576 = 21,
00762 BRIG_WIDTH_2097152 = 22,
00763 BRIG_WIDTH_4194304 = 23,
00764 BRIG_WIDTH_8388608 = 24,
00765 BRIG_WIDTH_16777216 = 25,
00766 BRIG_WIDTH_33554432 = 26,
00767 BRIG_WIDTH_67108864 = 27,
00768 BRIG_WIDTH_134217728 = 28,
00769 BRIG_WIDTH_268435456 = 29,
00770 BRIG_WIDTH_536870912 = 30,
00771 BRIG_WIDTH_1073741824 = 31,
00772 BRIG_WIDTH_2147483648 = 32,
00773 BRIG_WIDTH_WAVESIZE = 33,
00774 BRIG_WIDTH_ALL = 34,
00775 };
00776
00777 struct BrigUInt64 {
00778 uint32_t lo;
00779 uint32_t hi;
00780 };
00781
00782 struct BrigBase {
00783 uint16_t byteCount;
00784 BrigKind16_t kind;
00785 };
00786
00787 struct BrigData {
00788 uint32_t byteCount;
00789 uint8_t bytes[1];
00790 };
00791
00792 struct BrigDirectiveArgBlock {
00793 BrigBase base;
00794 };
00795
00796 struct BrigDirectiveComment {
00797 BrigBase base;
00798 BrigDataOffsetString32_t name;

```

```
00799 };
00800
00801 struct BrigDirectiveControl {
00802 BrigBase base;
00803 BrigControlDirective16_t control;
00804 uint16_t reserved;
00805 BrigDataOffsetOperandList32_t operands;
00806 };
00807
00808 struct BrigDirectiveExecutable {
00809 BrigBase base;
00810 BrigDataOffsetString32_t name;
00811 uint16_t outArgCount;
00812 uint16_t inArgCount;
00813 BrigCodeOffset32_t firstInArg;
00814 BrigCodeOffset32_t firstCodeBlockEntry;
00815 BrigCodeOffset32_t nextModuleEntry;
00816 BrigExecutableModifier8_t modifier;
00817 BrigLinkage8_t linkage;
00818 uint16_t reserved;
00819 };
00820
00821 struct BrigDirectiveExtension {
00822 BrigBase base;
00823 BrigDataOffsetString32_t name;
00824 };
00825
00826 struct BrigDirectiveFbarrier {
00827 BrigBase base;
00828 BrigDataOffsetString32_t name;
00829 BrigVariableModifier8_t modifier;
00830 BrigLinkage8_t linkage;
00831 uint16_t reserved;
00832 };
00833
00834 struct BrigDirectiveLabel {
00835 BrigBase base;
00836 BrigDataOffsetString32_t name;
00837 };
00838
00839 struct BrigDirectiveLoc {
00840 BrigBase base;
00841 BrigDataOffsetString32_t filename;
00842 uint32_t line;
00843 uint32_t column;
00844 };
00845
00846 struct BrigDirectiveNone {
00847 BrigBase base;
00848 };
00849
00850 struct BrigDirectivePragma {
00851 BrigBase base;
00852 BrigDataOffsetOperandList32_t operands;
00853 };
00854
00855 struct BrigDirectiveVariable {
00856 BrigBase base;
00857 BrigDataOffsetString32_t name;
00858 BrigOperandOffset32_t init;
00859 BrigType16_t type;
00860 BrigSegment8_t segment;
00861 BrigAlignment8_t align;
00862 BrigUInt64 dim;
00863 BrigVariableModifier8_t modifier;
00864 BrigLinkage8_t linkage;
00865 BrigAllocation8_t allocation;
00866 uint8_t reserved;
00867 };
00868
00869 struct BrigDirectiveModule {
00870 BrigBase base;
00871 BrigDataOffsetString32_t name;
00872 BrigVersion32_t hsailMajor;
00873 BrigVersion32_t hsailMinor;
00874 BrigProfile8_t profile;
00875 BrigMachineModel8_t machineModel;
00876 BrigRound8_t defaultFloatRound;
00877 uint8_t reserved;
00878 };
00879
00880 struct BrigInstBase {
00881 BrigBase base;
00882 BrigOpcode16_t opcode;
00883 BrigType16_t type;
00884 BrigDataOffsetOperandList32_t operands;
00885 };
```



```
00886
00887 struct BrigInstAddr {
00888 BrigInstBase base;
00889 BrigSegment8_t segment;
00890 uint8_t reserved[3];
00891 };
00892
00893 struct BrigInstAtomic {
00894 BrigInstBase base;
00895 BrigSegment8_t segment;
00896 BrigMemoryOrder8_t memoryOrder;
00897 BrigMemoryScope8_t memoryScope;
00898 BrigAtomicOperation8_t atomicOperation;
00899 uint8_t equivClass;
00900 uint8_t reserved[3];
00901 };
00902
00903 struct BrigInstBasic {
00904 BrigInstBase base;
00905 };
00906
00907 struct BrigInstBr {
00908 BrigInstBase base;
00909 BrigWidth8_t width;
00910 uint8_t reserved[3];
00911 };
00912
00913 struct BrigInstCmp {
00914 BrigInstBase base;
00915 BrigType16_t sourceType;
00916 BrigAluModifier8_t modifier;
00917 BrigCompareOperation8_t compare;
00918 BrigPack8_t pack;
00919 uint8_t reserved[3];
00920 };
00921
00922 struct BrigInstCvt {
00923 BrigInstBase base;
00924 BrigType16_t sourceType;
00925 BrigAluModifier8_t modifier;
00926 BrigRound8_t round;
00927 };
00928
00929 struct BrigInstImage {
00930 BrigInstBase base;
00931 BrigType16_t imageType;
00932 BrigType16_t coordType;
00933 BrigImageGeometry8_t geometry;
00934 uint8_t equivClass;
00935 uint16_t reserved;
00936 };
00937
00938 struct BrigInstLane {
00939 BrigInstBase base;
00940 BrigType16_t sourceType;
00941 BrigWidth8_t width;
00942 uint8_t reserved;
00943 };
00944
00945 struct BrigInstMem {
00946 BrigInstBase base;
00947 BrigSegment8_t segment;
00948 BrigAlignment8_t align;
00949 uint8_t equivClass;
00950 BrigWidth8_t width;
00951 BrigMemoryModifier8_t modifier;
00952 uint8_t reserved[3];
00953 };
00954
00955 struct BrigInstMemFence {
00956 BrigInstBase base;
00957 BrigMemoryOrder8_t memoryOrder;
00958 BrigMemoryScope8_t globalSegmentMemoryScope;
00959 BrigMemoryScope8_t groupSegmentMemoryScope;
00960 BrigMemoryScope8_t imageSegmentMemoryScope;
00961 };
00962
00963 struct BrigInstMod {
00964 BrigInstBase base;
00965 BrigAluModifier8_t modifier;
00966 BrigRound8_t round;
00967 BrigPack8_t pack;
00968 uint8_t reserved;
00969 };
00970
00971 struct BrigInstQueryImage {
00972 BrigInstBase base;
```

```
00973 BrigType16_t imageType;
00974 BrigImageGeometry8_t geometry;
00975 BrigImageQuery8_t query;
00976 };
00977
00978 struct BrigInstQuerySampler {
00979 BrigInstBase base;
00980 BrigSamplerQuery8_t query;
00981 uint8_t reserved[3];
00982 };
00983
00984 struct BrigInstQueue {
00985 BrigInstBase base;
00986 BrigSegment8_t segment;
00987 BrigMemoryOrder8_t memoryOrder;
00988 uint16_t reserved;
00989 };
00990
00991 struct BrigInstSeg {
00992 BrigInstBase base;
00993 BrigSegment8_t segment;
00994 uint8_t reserved[3];
00995 };
00996
00997 struct BrigInstSegCvt {
00998 BrigInstBase base;
00999 BrigType16_t sourceType;
01000 BrigSegment8_t segment;
01001 BrigSegCvtModifier8_t modifier;
01002 };
01003
01004 struct BrigInstSignal {
01005 BrigInstBase base;
01006 BrigType16_t signalType;
01007 BrigMemoryOrder8_t memoryOrder;
01008 BrigAtomicOperation8_t signalOperation;
01009 };
01010
01011 struct BrigInstSourceType {
01012 BrigInstBase base;
01013 BrigType16_t sourceType;
01014 uint16_t reserved;
01015 };
01016
01017 struct BrigOperandAddress {
01018 BrigBase base;
01019 BrigCodeOffset32_t symbol;
01020 BrigOperandOffset32_t reg;
01021 BrigUInt64 offset;
01022 };
01023
01024 struct BrigOperandAlign {
01025 BrigBase base;
01026 BrigAlignment8_t align;
01027 uint8_t reserved[3];
01028 };
01029
01030 struct BrigOperandCodeList {
01031 BrigBase base;
01032 BrigDataOffsetCodeList32_t elements;
01033 };
01034
01035 struct BrigOperandCodeRef {
01036 BrigBase base;
01037 BrigCodeOffset32_t ref;
01038 };
01039
01040 struct BrigOperandConstantBytes {
01041 BrigBase base;
01042 BrigType16_t type;
01043 uint16_t reserved;
01044 BrigDataOffsetString32_t bytes;
01045 };
01046
01047 struct BrigOperandConstantOperandList {
01048 BrigBase base;
01049 BrigType16_t type;
01050 uint16_t reserved;
01051 BrigDataOffsetOperandList32_t elements;
01052 };
01053
01054 struct BrigOperandConstantImage {
01055 BrigBase base;
01056 BrigType16_t type;
01057 BrigImageGeometry8_t geometry;
01058 BrigImageChannelOrder8_t channelOrder;
01059 BrigImageChannelType8_t channelType;
```

```

01060 uint8_t reserved[3];
01061 BrigUInt64 width;
01062 BrigUInt64 height;
01063 BrigUInt64 depth;
01064 BrigUInt64 array;
01065 };
01066
01067 struct BrigOperandOperandList {
01068 BrigBase base;
01069 BrigDataOffsetOperandList32_t elements;
01070 };
01071
01072 struct BrigOperandRegister {
01073 BrigBase base;
01074 BrigRegisterKind16_t regKind;
01075 uint16_t regNum;
01076 };
01077
01078 struct BrigOperandConstantSampler {
01079 BrigBase base;
01080 BrigType16_t type;
01081 BrigSamplerCoordNormalization8_t coord;
01082 BrigSamplerFilter8_t filter;
01083 BrigSamplerAddressing8_t addressing;
01084 uint8_t reserved[3];
01085 };
01086
01087 struct BrigOperandString {
01088 BrigBase base;
01089 BrigDataOffsetString32_t string;
01090 };
01091
01092 struct BrigOperandWavesize {
01093 BrigBase base;
01094 };
01095
01096 typedef uint32_t BrigExceptions32_t;
01097 enum BrigExceptionsMask {
01098 BRIG_EXCEPTIONS_INVALID_OPERATION = 1 << 0,
01099 BRIG_EXCEPTIONS_DIVIDE_BY_ZERO = 1 << 1,
01100 BRIG_EXCEPTIONS_OVERFLOW = 1 << 2,
01101 BRIG_EXCEPTIONS_UNDERFLOW = 1 << 3,
01102 BRIG_EXCEPTIONS_INEXACT = 1 << 4,
01103
01104 BRIG_EXCEPTIONS_FIRST_USER_DEFINED = 1 << 16
01105 };
01106
01107 struct BrigSectionHeader {
01108 uint64_t byteCount;
01109 uint32_t headerByteCount;
01110 uint32_t nameLength;
01111 uint8_t name[1];
01112 };
01113
01114 struct BrigModuleHeader {
01115 char identification[8];
01116 BrigVersion32_t brigMajor;
01117 BrigVersion32_t brigMinor;
01118 uint64_t byteCount;
01119 uint8_t hash[64];
01120 uint32_t reserved;
01121 uint32_t sectionCount;
01122 uint64_t sectionIndex;
01123 };
01124
01125 typedef BrigModuleHeader* BrigModule_t;
01126
01127 #ifdef __cplusplus
01128 }
01129 #endif /*__cplusplus*/
01130
01131 #endif // defined(INCLUDED_BRIG_H)

```

## 7.7 hsa.h

```

00001
00002 //
00003 // The University of Illinois/NCSA
00004 // Open Source License (NCSA)
00005 //
00006 // Copyright (c) 2014-2020, Advanced Micro Devices, Inc. All rights reserved.
00007 //
00008 // Developed by:

```

```
00009 //
00010 // AMD Research and AMD HSA Software Development
00011 //
00012 // Advanced Micro Devices, Inc.
00013 //
00014 // www.amd.com
00015 //
00016 // Permission is hereby granted, free of charge, to any person obtaining a copy
00017 // of this software and associated documentation files (the "Software"), to
00018 // deal with the Software without restriction, including without limitation
00019 // the rights to use, copy, modify, merge, publish, distribute, sublicense,
00020 // and/or sell copies of the Software, and to permit persons to whom the
00021 // Software is furnished to do so, subject to the following conditions:
00022 //
00023 // - Redistributions of source code must retain the above copyright notice,
00024 // this list of conditions and the following disclaimers.
00025 // - Redistributions in binary form must reproduce the above copyright
00026 // notice, this list of conditions and the following disclaimers in
00027 // the documentation and/or other materials provided with the distribution.
00028 // - Neither the names of Advanced Micro Devices, Inc,
00029 // nor the names of its contributors may be used to endorse or promote
00030 // products derived from this Software without specific prior written
00031 // permission.
00032 //
00033 // THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR
00034 // IMPLIED, INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY,
00035 // FITNESS FOR A PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL
00036 // THE CONTRIBUTORS OR COPYRIGHT HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR
00037 // OTHER LIABILITY, WHETHER IN AN ACTION OF CONTRACT, TORT OR OTHERWISE,
00038 // ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER
00039 // DEALINGS WITH THE SOFTWARE.
00040 //
00042
00043 #ifndef HSA_RUNTIME_INC_HSA_H_
00044 #define HSA_RUNTIME_INC_HSA_H_
00045
00046 #include <stddef.h> /* size_t */
00047 #include <stdint.h> /* uintXX_t */
00048
00049 #ifndef __cplusplus
00050 #include <stdbool.h> /* bool */
00051 #endif /* __cplusplus */
00052
00053 // Placeholder for calling convention and import/export macros
00054 #ifndef HSA_CALL
00055 #define HSA_CALL
00056 #endif
00057
00058 #ifndef HSA_EXPORT_DECORATOR
00059 #ifdef __GNUC__
00060 #define HSA_EXPORT_DECORATOR __attribute__((visibility("default")))
00061 #else
00062 #define HSA_EXPORT_DECORATOR
00063 #endif
00064 #endif
00065 #define HSA_API_EXPORT HSA_EXPORT_DECORATOR HSA_CALL
00066 #define HSA_API_IMPORT HSA_CALL
00067
00068 #if !defined(HSA_API) && defined(HSA_EXPORT)
00069 #define HSA_API HSA_API_EXPORT
00070 #else
00071 #define HSA_API HSA_API_IMPORT
00072 #endif
00073
00074 // Detect and set large model builds.
00075 #undef HSA_LARGE_MODEL
00076 #if defined(__LP64__) || defined(_M_X64)
00077 #define HSA_LARGE_MODEL
00078 #endif
00079
00080 // Try to detect CPU endianness
00081 #if !defined(LITTLEENDIAN_CPU) && !defined(BIGENDIAN_CPU)
00082 #if defined(__i386__) || defined(__x86_64__) || defined(_M_IX86) || \
00083 defined(_M_X64)
00084 #define LITTLEENDIAN_CPU
00085 #endif
00086 #endif
00087
00088 #undef HSA_LITTLE_ENDIAN
00089 #if defined(LITTLEENDIAN_CPU)
00090 #define HSA_LITTLE_ENDIAN
00091 #elif defined(BIGENDIAN_CPU)
00092 #else
00093 #error "BIGENDIAN_CPU or LITTLEENDIAN_CPU must be defined"
00094 #endif
00095
00096 #ifndef HSA_DEPRECATED
```

```

00097 #define HSA_DEPRECATED
00098 // #ifdef __GNUC__
00099 // #define HSA_DEPRECATED __attribute__((deprecated))
00100 // #else
00101 // #define HSA_DEPRECATED __declspec(deprecated)
00102 // #endif
00103 #endif
00104
00105 #define HSA_VERSION_1_0 1
00106
00107 #ifdef __cplusplus
00108 extern "C" {
00109 #endif /* __cplusplus */
00110
00111 typedef enum {
00112 HSA_STATUS_SUCCESS = 0x0,
00113 HSA_STATUS_INFO_BREAK = 0x1,
00114 HSA_STATUS_ERROR = 0x1000,
00115 HSA_STATUS_ERROR_INVALID_ARGUMENT = 0x1001,
00116 HSA_STATUS_ERROR_INVALID_QUEUE_CREATION = 0x1002,
00117 HSA_STATUS_ERROR_INVALID_ALLOCATION = 0x1003,
00118 HSA_STATUS_ERROR_INVALID_AGENT = 0x1004,
00119 HSA_STATUS_ERROR_INVALID_REGION = 0x1005,
00120 HSA_STATUS_ERROR_INVALID_SIGNAL = 0x1006,
00121 HSA_STATUS_ERROR_INVALID_QUEUE = 0x1007,
00122 HSA_STATUS_ERROR_OUT_OF_RESOURCES = 0x1008,
00123 HSA_STATUS_ERROR_INVALID_PACKET_FORMAT = 0x1009,
00124 HSA_STATUS_ERROR_RESOURCE_FREE = 0x100A,
00125 HSA_STATUS_ERROR_NOT_INITIALIZED = 0x100B,
00126 HSA_STATUS_ERROR_REFCOUNT_OVERFLOW = 0x100C,
00127 HSA_STATUS_ERROR_INCOMPATIBLE_ARGUMENTS = 0x100D,
00128 HSA_STATUS_ERROR_INVALID_INDEX = 0x100E,
00129 HSA_STATUS_ERROR_INVALID_ISA = 0x100F,
00130 HSA_STATUS_ERROR_INVALID_ISA_NAME = 0x1017,
00131 HSA_STATUS_ERROR_INVALID_CODE_OBJECT = 0x1010,
00132 HSA_STATUS_ERROR_INVALID_EXECUTABLE = 0x1011,
00133 HSA_STATUS_ERROR_FROZEN_EXECUTABLE = 0x1012,
00134 HSA_STATUS_ERROR_INVALID_SYMBOL_NAME = 0x1013,
00135 HSA_STATUS_ERROR_VARIABLE_ALREADY_DEFINED = 0x1014,
00136 HSA_STATUS_ERROR_VARIABLE_UNDEFINED = 0x1015,
00137 HSA_STATUS_ERROR_EXCEPTION = 0x1016,
00138 HSA_STATUS_ERROR_INVALID_CODE_SYMBOL = 0x1018,
00139 HSA_STATUS_ERROR_INVALID_EXECUTABLE_SYMBOL = 0x1019,
00140 HSA_STATUS_ERROR_INVALID_FILE = 0x1020,
00141 HSA_STATUS_ERROR_INVALID_CODE_OBJECT_READER = 0x1021,
00142 HSA_STATUS_ERROR_INVALID_CACHE = 0x1022,
00143 HSA_STATUS_ERROR_INVALID_WAVEFRONT = 0x1023,
00144 HSA_STATUS_ERROR_INVALID_SIGNAL_GROUP = 0x1024,
00145 HSA_STATUS_ERROR_INVALID_RUNTIME_STATE = 0x1025,
00146 HSA_STATUS_ERROR_FATAL = 0x1026
00147 } hsa_status_t;
00148
00149 hsa_status_t HSA_API hsa_status_string(
00150 hsa_status_t status,
00151 const char ** status_string);
00152
00153 /*
00154 * @}
00155 */
00156
00157 /*
00158 * \defgroup common Common Definitions
00159 * @{
00160 */
00161
00162 typedef struct hsa_dim3_s {
00163 uint32_t x;
00164 uint32_t y;
00165 uint32_t z;
00166 } hsa_dim3_t;
00167
00168 typedef enum {
00169 HSA_ACCESS_PERMISSION_RO = 1,
00170 HSA_ACCESS_PERMISSION_WO = 2,
00171 HSA_ACCESS_PERMISSION_RW = 3
00172 } hsa_access_permission_t;
00173
00174 typedef int hsa_file_t;
00175
00176 /*
00177 * @} */
00178
00179 /*
00180 * \defgroup initshutdown Initialization and Shut Down

```

```

00344 * @{
00345 */
00346
00364 hsa_status_t HSA_API hsa_init();
00365
00386 hsa_status_t HSA_API hsa_shut_down();
00387
00398 typedef enum {
00402 HSA_ENDIANNESNESS_LITTLE = 0,
00406 HSA_ENDIANNESNESS_BIG = 1
00407 } hsa_endianness_t;
00408
00413 typedef enum {
00417 HSA_MACHINE_MODEL_SMALL = 0,
00421 HSA_MACHINE_MODEL_LARGE = 1
00422 } hsa_machine_model_t;
00423
00430 typedef enum {
00434 HSA_PROFILE_BASE = 0,
00438 HSA_PROFILE_FULL = 1
00439 } hsa_profile_t;
00440
00444 typedef enum {
00449 HSA_SYSTEM_INFO_VERSION_MAJOR = 0,
00454 HSA_SYSTEM_INFO_VERSION_MINOR = 1,
00459 HSA_SYSTEM_INFO_TIMESTAMP = 2,
00464 HSA_SYSTEM_INFO_TIMESTAMP_FREQUENCY = 3,
00469 HSA_SYSTEM_INFO_SIGNAL_MAX_WAIT = 4,
00473 HSA_SYSTEM_INFO_ENDIANNESNESS = 5,
00478 HSA_SYSTEM_INFO_MACHINE_MODEL = 6,
00484 HSA_SYSTEM_INFO_EXTENSIONS = 7,
00488 HSA_AMD_SYSTEM_INFO_BUILD_VERSION = 0x200,
00493 HSA_AMD_SYSTEM_INFO_SVM_SUPPORTED = 0x201,
00494 // TODO: Should this be per Agent?
00502 HSA_AMD_SYSTEM_INFO_SVM_ACCESSIBLE_BY_DEFAULT = 0x202
00503 } hsa_system_info_t;
00504
00522 hsa_status_t HSA_API hsa_system_get_info(
00523 hsa_system_info_t attribute,
00524 void* value);
00525
00529 typedef enum {
00533 HSA_EXTENSION_FINALIZER = 0,
00537 HSA_EXTENSION_IMAGES = 1,
00538
00542 HSA_EXTENSION_PERFORMANCE_COUNTERS = 2,
00543
00547 HSA_EXTENSION_PROFILING_EVENTS = 3,
00551 HSA_EXTENSION_STD_LAST = 3,
00555 HSA_AMD_FIRST_EXTENSION = 0x200,
00559 HSA_EXTENSION_AMD_PROFILER = 0x200,
00563 HSA_EXTENSION_AMD_LOADER = 0x201,
00567 HSA_EXTENSION_AMD_AQLPROFILE = 0x202,
00571 HSA_AMD_LAST_EXTENSION = 0x202
00572 } hsa_extension_t;
00573
00592 hsa_status_t HSA_API hsa_extension_get_name(
00593 uint16_t extension,
00594 const char **name);
00595
00620 hsa_status_t HSA_API HSA_DEPRECATED hsa_system_extension_supported(
00621 uint16_t extension,
00622 uint16_t version_major,
00623 uint16_t version_minor,
00624 bool* result);
00625
00649 hsa_status_t HSA_API hsa_system_major_extension_supported(
00650 uint16_t extension,
00651 uint16_t version_major,
00652 uint16_t *version_minor,
00653 bool* result);
00654
00655
00689 hsa_status_t HSA_API HSA_DEPRECATED hsa_system_get_extension_table(
00690 uint16_t extension,
00691 uint16_t version_major,
00692 uint16_t version_minor,
00693 void *table);
00694
00729 hsa_status_t HSA_API hsa_system_get_major_extension_table(
00730 uint16_t extension,
00731 uint16_t version_major,
00732 size_t table_length,
00733 void *table);
00734
00741 typedef struct hsa_agent_s {
00746 uint64_t handle;

```

```

00747 } hsa_agent_t;
00748
00752 typedef enum {
00757 HSA_AGENT_FEATURE_KERNEL_DISPATCH = 1,
00761 HSA_AGENT_FEATURE_AGENT_DISPATCH = 2
00762 } hsa_agent_feature_t;
00763
00767 typedef enum {
00771 HSA_DEVICE_TYPE_CPU = 0,
00775 HSA_DEVICE_TYPE_GPU = 1,
00779 HSA_DEVICE_TYPE_DSP = 2
00780 } hsa_device_type_t;
00781
00785 typedef enum {
00789 HSA_DEFAULT_FLOAT_ROUNDING_MODE_DEFAULT = 0,
00794 HSA_DEFAULT_FLOAT_ROUNDING_MODE_ZERO = 1,
00800 HSA_DEFAULT_FLOAT_ROUNDING_MODE_NEAR = 2
00801 } hsa_default_float_rounding_mode_t;
00802
00806 typedef enum {
00812 HSA_AGENT_INFO_NAME = 0,
00818 HSA_AGENT_INFO_VENDOR_NAME = 1,
00822 HSA_AGENT_INFO_FEATURE = 2,
00832 HSA_AGENT_INFO_MACHINE_MODEL = 3,
00842 HSA_AGENT_INFO_PROFILE = 4,
00853 HSA_AGENT_INFO_DEFAULT_FLOAT_ROUNDING_MODE = 5,
00866 HSA_AGENT_INFO_BASE_PROFILE_DEFAULT_FLOAT_ROUNDING_MODES = 23,
00878 HSA_AGENT_INFO_FAST_F16_OPERATION = 24,
00890 HSA_AGENT_INFO_WAVEFRONT_SIZE = 6,
00903 HSA_AGENT_INFO_WORKGROUP_MAX_DIM = 7,
00914 HSA_AGENT_INFO_WORKGROUP_MAX_SIZE = 8,
00926 HSA_AGENT_INFO_GRID_MAX_DIM = 9,
00937 HSA_AGENT_INFO_GRID_MAX_SIZE = 10,
00948 HSA_AGENT_INFO_FBARRIER_MAX_SIZE = 11,
00955 HSA_AGENT_INFO_QUEUES_MAX = 12,
00962 HSA_AGENT_INFO_QUEUE_MIN_SIZE = 13,
00968 HSA_AGENT_INFO_QUEUE_MAX_SIZE = 14,
00973 HSA_AGENT_INFO_QUEUE_TYPE = 15,
00980 HSA_AGENT_INFO_NODE = 16,
00985 HSA_AGENT_INFO_DEVICE = 17,
00994 HSA_AGENT_INFO_CACHE_SIZE = 18,
01004 HSA_AGENT_INFO_ISA = 19,
01010 HSA_AGENT_INFO_EXTENSIONS = 20,
01015 HSA_AGENT_INFO_VERSION_MAJOR = 21,
01020 HSA_AGENT_INFO_VERSION_MINOR = 22,
01029 HSA_AGENT_INFO_LAST = INT32_MAX
01030 } hsa_agent_info_t;
01031
01053 hsa_status_t HSA_API hsa_agent_get_info(
01054 hsa_agent_t agent,
01055 hsa_agent_info_t attribute,
01056 void* value);
01057
01078 hsa_status_t HSA_API hsa_iterate_agents(
01079 hsa_status_t (*callback)(hsa_agent_t agent, void* data),
01080 void* data);
01081
01082 /*
01083
01084 // If we do not know the size of an attribute, we need to query it first
01085 // Note: this API will not be in the spec unless needed
01086 hsa_status_t HSA_API hsa_agent_get_info_size(
01087 hsa_agent_t agent,
01088 hsa_agent_info_t attribute,
01089 size_t* size);
01090
01091 // Set the value of an agents attribute
01092 // Note: this API will not be in the spec unless needed
01093 hsa_status_t HSA_API hsa_agent_set_info(
01094 hsa_agent_t agent,
01095 hsa_agent_info_t attribute,
01096 void* value);
01097
01098 */
01099
01103 typedef enum {
01107 HSA_EXCEPTION_POLICY_BREAK = 1,
01111 HSA_EXCEPTION_POLICY_DETECT = 2
01112 } hsa_exception_policy_t;
01113
01141 hsa_status_t HSA_API HSA_DEPRECATED hsa_agent_get_exception_policies(
01142 hsa_agent_t agent,
01143 hsa_profile_t profile,
01144 uint16_t *mask);
01145
01149 typedef struct hsa_cache_s {
01154 uint64_t handle;

```

```

01155 } hsa_cache_t;
01156
01160 typedef enum {
01165 HSA_CACHE_INFO_NAME_LENGTH = 0,
01171 HSA_CACHE_INFO_NAME = 1,
01176 HSA_CACHE_INFO_LEVEL = 2,
01181 HSA_CACHE_INFO_SIZE = 3
01182 } hsa_cache_info_t;
01183
01206 hsa_status_t HSA_API hsa_cache_get_info(
01207 hsa_cache_t cache,
01208 hsa_cache_info_t attribute,
01209 void* value);
01210
01238 hsa_status_t HSA_API hsa_agent_iterate_caches(
01239 hsa_agent_t agent,
01240 hsa_status_t (*callback)(hsa_cache_t cache, void* data),
01241 void* data);
01242
01272 hsa_status_t HSA_API HSA_DEPRECATED hsa_agent_extension_supported(
01273 uint16_t extension,
01274 hsa_agent_t agent,
01275 uint16_t version_major,
01276 uint16_t version_minor,
01277 bool* result);
01278
01307 hsa_status_t HSA_API hsa_agent_major_extension_supported(
01308 uint16_t extension,
01309 hsa_agent_t agent,
01310 uint16_t version_major,
01311 uint16_t *version_minor,
01312 bool* result);
01313
01314
01325 typedef struct hsa_signal_s {
01330 uint64_t handle;
01331 } hsa_signal_t;
01332
01337 #ifdef HSA_LARGE_MODEL
01338 typedef int64_t hsa_signal_value_t;
01339 #else
01340 typedef int32_t hsa_signal_value_t;
01341 #endif
01342
01373 hsa_status_t HSA_API hsa_signal_create(
01374 hsa_signal_value_t initial_value,
01375 uint32_t num_consumers,
01376 const hsa_agent_t *consumers,
01377 hsa_signal_t *signal);
01378
01393 hsa_status_t HSA_API hsa_signal_destroy(
01394 hsa_signal_t signal);
01395
01403 hsa_signal_value_t HSA_API hsa_signal_load_scacquire(
01404 hsa_signal_t signal);
01405
01409 hsa_signal_value_t HSA_API hsa_signal_load_relaxed(
01410 hsa_signal_t signal);
01411
01417 hsa_signal_value_t HSA_API HSA_DEPRECATED hsa_signal_load_acquire(
01418 hsa_signal_t signal);
01419
01430 void HSA_API hsa_signal_store_relaxed(
01431 hsa_signal_t signal,
01432 hsa_signal_value_t value);
01433
01437 void HSA_API hsa_signal_store_screlease(
01438 hsa_signal_t signal,
01439 hsa_signal_value_t value);
01440
01446 void HSA_API HSA_DEPRECATED hsa_signal_store_release(
01447 hsa_signal_t signal,
01448 hsa_signal_value_t value);
01449
01463 void HSA_API hsa_signal_silent_store_relaxed(
01464 hsa_signal_t signal,
01465 hsa_signal_value_t value);
01466
01470 void HSA_API hsa_signal_silent_store_screlease(
01471 hsa_signal_t signal,
01472 hsa_signal_value_t value);
01473
01488 hsa_signal_value_t HSA_API hsa_signal_exchange_scacq_screl(
01489 hsa_signal_t signal,
01490 hsa_signal_value_t value);
01491
01497 hsa_signal_value_t HSA_API HSA_DEPRECATED hsa_signal_exchange_acq_rel(

```



```
01498 hsa_signal_t signal,
01499 hsa_signal_value_t value);
01500
01504 hsa_signal_value_t HSA_API hsa_signal_exchange_scacquire(
01505 hsa_signal_t signal,
01506 hsa_signal_value_t value);
01507
01513 hsa_signal_value_t HSA_API HSA_DEPRECATED hsa_signal_exchange_acquire(
01514 hsa_signal_t signal,
01515 hsa_signal_value_t value);
01516
01520 hsa_signal_value_t HSA_API hsa_signal_exchange_relaxed(
01521 hsa_signal_t signal,
01522 hsa_signal_value_t value);
01526 hsa_signal_value_t HSA_API hsa_signal_exchange_screlease(
01527 hsa_signal_t signal,
01528 hsa_signal_value_t value);
01529
01535 hsa_signal_value_t HSA_API HSA_DEPRECATED hsa_signal_exchange_release(
01536 hsa_signal_t signal,
01537 hsa_signal_value_t value);
01538
01557 hsa_signal_value_t HSA_API hsa_signal_cas_scacq_screl(
01558 hsa_signal_t signal,
01559 hsa_signal_value_t expected,
01560 hsa_signal_value_t value);
01561
01562
01568 hsa_signal_value_t HSA_API HSA_DEPRECATED hsa_signal_cas_acq_rel(
01569 hsa_signal_t signal,
01570 hsa_signal_value_t expected,
01571 hsa_signal_value_t value);
01572
01576 hsa_signal_value_t HSA_API hsa_signal_cas_scacquire(
01577 hsa_signal_t signal,
01578 hsa_signal_value_t expected,
01579 hsa_signal_value_t value);
01580
01586 hsa_signal_value_t HSA_API HSA_DEPRECATED hsa_signal_cas_acquire(
01587 hsa_signal_t signal,
01588 hsa_signal_value_t expected,
01589 hsa_signal_value_t value);
01590
01594 hsa_signal_value_t HSA_API hsa_signal_cas_relaxed(
01595 hsa_signal_t signal,
01596 hsa_signal_value_t expected,
01597 hsa_signal_value_t value);
01598
01602 hsa_signal_value_t HSA_API hsa_signal_cas_screlease(
01603 hsa_signal_t signal,
01604 hsa_signal_value_t expected,
01605 hsa_signal_value_t value);
01606
01612 hsa_signal_value_t HSA_API HSA_DEPRECATED hsa_signal_cas_release(
01613 hsa_signal_t signal,
01614 hsa_signal_value_t expected,
01615 hsa_signal_value_t value);
01616
01629 void HSA_API hsa_signal_add_scacq_screl(
01630 hsa_signal_t signal,
01631 hsa_signal_value_t value);
01632
01638 void HSA_API HSA_DEPRECATED hsa_signal_add_acq_rel(
01639 hsa_signal_t signal,
01640 hsa_signal_value_t value);
01641
01645 void HSA_API hsa_signal_add_scacquire(
01646 hsa_signal_t signal,
01647 hsa_signal_value_t value);
01648
01654 void HSA_API HSA_DEPRECATED hsa_signal_add_acquire(
01655 hsa_signal_t signal,
01656 hsa_signal_value_t value);
01657
01661 void HSA_API hsa_signal_add_relaxed(
01662 hsa_signal_t signal,
01663 hsa_signal_value_t value);
01664
01668 void HSA_API hsa_signal_add_screlease(
01669 hsa_signal_t signal,
01670 hsa_signal_value_t value);
01671
01672
01678 void HSA_API HSA_DEPRECATED hsa_signal_add_release(
01679 hsa_signal_t signal,
01680 hsa_signal_value_t value);
01681
```

```
01694 void HSA_API hsa_signal_subtract_scacq_screl(
01695 hsa_signal_t signal,
01696 hsa_signal_value_t value);
01697
01698
01704 void HSA_API HSA_DEPRECATED hsa_signal_subtract_acq_rel(
01705 hsa_signal_t signal,
01706 hsa_signal_value_t value);
01707
01711 void HSA_API hsa_signal_subtract_scacquire(
01712 hsa_signal_t signal,
01713 hsa_signal_value_t value);
01714
01720 void HSA_API HSA_DEPRECATED hsa_signal_subtract_acquire(
01721 hsa_signal_t signal,
01722 hsa_signal_value_t value);
01723
01727 void HSA_API hsa_signal_subtract_relaxed(
01728 hsa_signal_t signal,
01729 hsa_signal_value_t value);
01730
01734 void HSA_API hsa_signal_subtract_screlease(
01735 hsa_signal_t signal,
01736 hsa_signal_value_t value);
01737
01738
01744 void HSA_API HSA_DEPRECATED hsa_signal_subtract_release(
01745 hsa_signal_t signal,
01746 hsa_signal_value_t value);
01747
01761 void HSA_API hsa_signal_and_scacq_screl(
01762 hsa_signal_t signal,
01763 hsa_signal_value_t value);
01764
01770 void HSA_API HSA_DEPRECATED hsa_signal_and_acq_rel(
01771 hsa_signal_t signal,
01772 hsa_signal_value_t value);
01773
01777 void HSA_API hsa_signal_and_scacquire(
01778 hsa_signal_t signal,
01779 hsa_signal_value_t value);
01780
01786 void HSA_API HSA_DEPRECATED hsa_signal_and_acquire(
01787 hsa_signal_t signal,
01788 hsa_signal_value_t value);
01789
01793 void HSA_API hsa_signal_and_relaxed(
01794 hsa_signal_t signal,
01795 hsa_signal_value_t value);
01796
01800 void HSA_API hsa_signal_and_screlease(
01801 hsa_signal_t signal,
01802 hsa_signal_value_t value);
01803
01804
01810 void HSA_API HSA_DEPRECATED hsa_signal_and_release(
01811 hsa_signal_t signal,
01812 hsa_signal_value_t value);
01813
01826 void HSA_API hsa_signal_or_scacq_screl(
01827 hsa_signal_t signal,
01828 hsa_signal_value_t value);
01829
01830
01836 void HSA_API HSA_DEPRECATED hsa_signal_or_acq_rel(
01837 hsa_signal_t signal,
01838 hsa_signal_value_t value);
01839
01843 void HSA_API hsa_signal_or_scacquire(
01844 hsa_signal_t signal,
01845 hsa_signal_value_t value);
01846
01852 void HSA_API HSA_DEPRECATED hsa_signal_or_acquire(
01853 hsa_signal_t signal,
01854 hsa_signal_value_t value);
01855
01859 void HSA_API hsa_signal_or_relaxed(
01860 hsa_signal_t signal,
01861 hsa_signal_value_t value);
01862
01866 void HSA_API hsa_signal_or_screlease(
01867 hsa_signal_t signal,
01868 hsa_signal_value_t value);
01869
01875 void HSA_API HSA_DEPRECATED hsa_signal_or_release(
01876 hsa_signal_t signal,
01877 hsa_signal_value_t value);
```

```

01878
01892 void HSA_API hsa_signal_xor_scacq_screl(
01893 hsa_signal_t signal,
01894 hsa_signal_value_t value);
01895
01896
01902 void HSA_API HSA_DEPRECATED hsa_signal_xor_acq_rel(
01903 hsa_signal_t signal,
01904 hsa_signal_value_t value);
01905
01909 void HSA_API hsa_signal_xor_scacquire(
01910 hsa_signal_t signal,
01911 hsa_signal_value_t value);
01912
01918 void HSA_API HSA_DEPRECATED hsa_signal_xor_acquire(
01919 hsa_signal_t signal,
01920 hsa_signal_value_t value);
01921
01925 void HSA_API hsa_signal_xor_relaxed(
01926 hsa_signal_t signal,
01927 hsa_signal_value_t value);
01928
01932 void HSA_API hsa_signal_xor_screlease(
01933 hsa_signal_t signal,
01934 hsa_signal_value_t value);
01935
01941 void HSA_API HSA_DEPRECATED hsa_signal_xor_release(
01942 hsa_signal_t signal,
01943 hsa_signal_value_t value);
01944
01948 typedef enum {
01952 HSA_SIGNAL_CONDITION_EQ = 0,
01956 HSA_SIGNAL_CONDITION_NE = 1,
01960 HSA_SIGNAL_CONDITION_LT = 2,
01964 HSA_SIGNAL_CONDITION_GTE = 3
01965 } hsa_signal_condition_t;
01966
01970 typedef enum {
01974 HSA_WAIT_STATE_BLOCKED = 0,
01978 HSA_WAIT_STATE_ACTIVE = 1
01979 } hsa_wait_state_t;
01980
01981
02021 hsa_signal_value_t HSA_API hsa_signal_wait_scacquire(
02022 hsa_signal_t signal,
02023 hsa_signal_condition_t condition,
02024 hsa_signal_value_t compare_value,
02025 uint64_t timeout_hint,
02026 hsa_wait_state_t wait_state_hint);
02027
02031 hsa_signal_value_t HSA_API hsa_signal_wait_relaxed(
02032 hsa_signal_t signal,
02033 hsa_signal_condition_t condition,
02034 hsa_signal_value_t compare_value,
02035 uint64_t timeout_hint,
02036 hsa_wait_state_t wait_state_hint);
02037
02043 hsa_signal_value_t HSA_API HSA_DEPRECATED hsa_signal_wait_acquire(
02044 hsa_signal_t signal,
02045 hsa_signal_condition_t condition,
02046 hsa_signal_value_t compare_value,
02047 uint64_t timeout_hint,
02048 hsa_wait_state_t wait_state_hint);
02049
02053 typedef struct hsa_signal_group_s {
02058 uint64_t handle;
02059 } hsa_signal_group_t;
02060
02093 hsa_status_t HSA_API hsa_signal_group_create(
02094 uint32_t num_signals,
02095 const hsa_signal_t *signals,
02096 uint32_t num_consumers,
02097 const hsa_agent_t *consumers,
02098 hsa_signal_group_t *signal_group);
02099
02112 hsa_status_t HSA_API hsa_signal_group_destroy(
02113 hsa_signal_group_t signal_group);
02114
02162 hsa_status_t HSA_API hsa_signal_group_wait_any_scacquire(
02163 hsa_signal_group_t signal_group,
02164 const hsa_signal_condition_t *conditions,
02165 const hsa_signal_value_t *compare_values,
02166 hsa_wait_state_t wait_state_hint,
02167 hsa_signal_t *signal,
02168 hsa_signal_value_t *value);
02169
02173 hsa_status_t HSA_API hsa_signal_group_wait_any_relaxed(

```

```

02174 hsa_signal_group_t signal_group,
02175 const hsa_signal_condition_t *conditions,
02176 const hsa_signal_value_t *compare_values,
02177 hsa_wait_state_t wait_state_hint,
02178 hsa_signal_t *signal,
02179 hsa_signal_value_t *value);
02180
02193 typedef struct hsa_region_s {
02198 uint64_t handle;
02199 } hsa_region_t;
02200
02212 typedef enum {
02217 HSA_QUEUE_TYPE_MULTI = 0,
02224 HSA_QUEUE_TYPE_SINGLE = 1,
02236 HSA_QUEUE_TYPE_COOPERATIVE = 2
02237 } hsa_queue_type_t;
02238
02242 typedef uint32_t hsa_queue_type32_t;
02243
02247 typedef enum {
02251 HSA_QUEUE_FEATURE_KERNEL_DISPATCH = 1,
02252
02256 HSA_QUEUE_FEATURE_AGENT_DISPATCH = 2
02257 } hsa_queue_feature_t;
02258
02267 typedef struct hsa_queue_s {
02271 hsa_queue_type32_t type;
02272
02277 uint32_t features;
02278
02279 #ifdef HSA_LARGE_MODEL
02280 void* base_address;
02281 #elif defined HSA_LITTLE_ENDIAN
02286 void* base_address;
02290 uint32_t reserved0;
02291 #else
02292 uint32_t reserved0;
02293 void* base_address;
02294 #endif
02295
02307 hsa_signal_t doorbell_signal;
02308
02312 uint32_t size;
02316 uint32_t reserved1;
02320 uint64_t id;
02321
02322 } hsa_queue_t;
02323
02394 hsa_status_t HSA_API hsa_queue_create(
02395 hsa_agent_t agent,
02396 uint32_t size,
02397 hsa_queue_type32_t type,
02398 void (*callback)(hsa_status_t status, hsa_queue_t *source, void *data),
02399 void *data,
02400 uint32_t private_segment_size,
02401 uint32_t group_segment_size,
02402 hsa_queue_t **queue);
02403
02458 hsa_status_t HSA_API hsa_soft_queue_create(
02459 hsa_region_t region,
02460 uint32_t size,
02461 hsa_queue_type32_t type,
02462 uint32_t features,
02463 hsa_signal_t doorbell_signal,
02464 hsa_queue_t **queue);
02465
02489 hsa_status_t HSA_API hsa_queue_destroy(
02490 hsa_queue_t *queue);
02491
02510 hsa_status_t HSA_API hsa_queue_inactivate(
02511 hsa_queue_t *queue);
02512
02518 uint64_t HSA_API HSA_DEPRECATED hsa_queue_load_read_index_acquire(
02519 const hsa_queue_t *queue);
02520
02528 uint64_t HSA_API hsa_queue_load_read_index_scacquire(
02529 const hsa_queue_t *queue);
02530
02534 uint64_t HSA_API hsa_queue_load_read_index_relaxed(
02535 const hsa_queue_t *queue);
02536
02542 uint64_t HSA_API HSA_DEPRECATED hsa_queue_load_write_index_acquire(
02543 const hsa_queue_t *queue);
02544
02552 uint64_t HSA_API hsa_queue_load_write_index_scacquire(
02553 const hsa_queue_t *queue);
02554

```

```
02558 uint64_t HSA_API hsa_queue_load_write_index_relaxed(
02559 const hsa_queue_t *queue);
02560
02573 void HSA_API hsa_queue_store_write_index_relaxed(
02574 const hsa_queue_t *queue,
02575 uint64_t value);
02576
02582 void HSA_API HSA_DEPRECATED hsa_queue_store_write_index_release(
02583 const hsa_queue_t *queue,
02584 uint64_t value);
02585
02589 void HSA_API hsa_queue_store_write_index_screlease(
02590 const hsa_queue_t *queue,
02591 uint64_t value);
02592
02598 uint64_t HSA_API HSA_DEPRECATED hsa_queue_cas_write_index_acq_rel(
02599 const hsa_queue_t *queue,
02600 uint64_t expected,
02601 uint64_t value);
02602
02617 uint64_t HSA_API hsa_queue_cas_write_index_scacq_screl(
02618 const hsa_queue_t *queue,
02619 uint64_t expected,
02620 uint64_t value);
02621
02627 uint64_t HSA_API HSA_DEPRECATED hsa_queue_cas_write_index_acquire(
02628 const hsa_queue_t *queue,
02629 uint64_t expected,
02630 uint64_t value);
02631
02635 uint64_t HSA_API hsa_queue_cas_write_index_scacquire(
02636 const hsa_queue_t *queue,
02637 uint64_t expected,
02638 uint64_t value);
02639
02643 uint64_t HSA_API hsa_queue_cas_write_index_relaxed(
02644 const hsa_queue_t *queue,
02645 uint64_t expected,
02646 uint64_t value);
02647
02653 uint64_t HSA_API HSA_DEPRECATED hsa_queue_cas_write_index_release(
02654 const hsa_queue_t *queue,
02655 uint64_t expected,
02656 uint64_t value);
02657
02661 uint64_t HSA_API hsa_queue_cas_write_index_screlease(
02662 const hsa_queue_t *queue,
02663 uint64_t expected,
02664 uint64_t value);
02665
02671 uint64_t HSA_API HSA_DEPRECATED hsa_queue_add_write_index_acq_rel(
02672 const hsa_queue_t *queue,
02673 uint64_t value);
02674
02684 uint64_t HSA_API hsa_queue_add_write_index_scacq_screl(
02685 const hsa_queue_t *queue,
02686 uint64_t value);
02687
02693 uint64_t HSA_API HSA_DEPRECATED hsa_queue_add_write_index_acquire(
02694 const hsa_queue_t *queue,
02695 uint64_t value);
02696
02700 uint64_t HSA_API hsa_queue_add_write_index_scacquire(
02701 const hsa_queue_t *queue,
02702 uint64_t value);
02703
02707 uint64_t HSA_API hsa_queue_add_write_index_relaxed(
02708 const hsa_queue_t *queue,
02709 uint64_t value);
02710
02716 uint64_t HSA_API HSA_DEPRECATED hsa_queue_add_write_index_release(
02717 const hsa_queue_t *queue,
02718 uint64_t value);
02719
02723 uint64_t HSA_API hsa_queue_add_write_index_screlease(
02724 const hsa_queue_t *queue,
02725 uint64_t value);
02726
02740 void HSA_API hsa_queue_store_read_index_relaxed(
02741 const hsa_queue_t *queue,
02742 uint64_t value);
02743
02749 void HSA_API HSA_DEPRECATED hsa_queue_store_read_index_release(
02750 const hsa_queue_t *queue,
02751 uint64_t value);
02752
02756 void HSA_API hsa_queue_store_read_index_screlease(
```

```

02757 const hsa_queue_t *queue,
02758 uint64_t value);
02769 typedef enum {
02773 HSA_PACKET_TYPE_VENDOR_SPECIFIC = 0,
02779 HSA_PACKET_TYPE_INVALID = 1,
02784 HSA_PACKET_TYPE_KERNEL_DISPATCH = 2,
02790 HSA_PACKET_TYPE_BARRIER_AND = 3,
02795 HSA_PACKET_TYPE_AGENT_DISPATCH = 4,
02801 HSA_PACKET_TYPE_BARRIER_OR = 5
02802 } hsa_packet_type_t;
02803
02807 typedef enum {
02812 HSA_FENCE_SCOPE_NONE = 0,
02816 HSA_FENCE_SCOPE_AGENT = 1,
02821 HSA_FENCE_SCOPE_SYSTEM = 2
02822 } hsa_fence_scope_t;
02823
02831 typedef enum {
02837 HSA_PACKET_HEADER_TYPE = 0,
02843 HSA_PACKET_HEADER_BARRIER = 8,
02853 HSA_PACKET_HEADER_SCACQUIRE_FENCE_SCOPE = 9,
02857 HSA_PACKET_HEADER_ACQUIRE_FENCE_SCOPE = 9,
02867 HSA_PACKET_HEADER_SCRELEASE_FENCE_SCOPE = 11,
02871 HSA_PACKET_HEADER_RELEASE_FENCE_SCOPE = 11
02872 } hsa_packet_header_t;
02873
02877 typedef enum {
02878 HSA_PACKET_HEADER_WIDTH_TYPE = 8,
02879 HSA_PACKET_HEADER_WIDTH_BARRIER = 1,
02880 HSA_PACKET_HEADER_WIDTH_SCACQUIRE_FENCE_SCOPE = 2,
02884 HSA_PACKET_HEADER_WIDTH_ACQUIRE_FENCE_SCOPE = 2,
02885 HSA_PACKET_HEADER_WIDTH_SCRELEASE_FENCE_SCOPE = 2,
02889 HSA_PACKET_HEADER_WIDTH_RELEASE_FENCE_SCOPE = 2
02890 } hsa_packet_header_width_t;
02891
02899 typedef enum {
02904 HSA_KERNEL_DISPATCH_PACKET_SETUP_DIMENSIONS = 0
02905 } hsa_kernel_dispatch_packet_setup_t;
02906
02911 typedef enum {
02912 HSA_KERNEL_DISPATCH_PACKET_SETUP_WIDTH_DIMENSIONS = 2
02913 } hsa_kernel_dispatch_packet_setup_width_t;
02914
02918 typedef struct hsa_kernel_dispatch_packet_s {
02923 uint16_t header;
02924
02930 uint16_t setup;
02931
02935 uint16_t workgroup_size_x;
02936
02941 uint16_t workgroup_size_y;
02942
02947 uint16_t workgroup_size_z;
02948
02952 uint16_t reserved0;
02953
02958 uint32_t grid_size_x;
02959
02965 uint32_t grid_size_y;
02966
02972 uint32_t grid_size_z;
02973
02977 uint32_t private_segment_size;
02978
02985 uint32_t group_segment_size;
02986
02991 uint64_t kernel_object;
02992
02993 #ifdef HSA_LARGE_MODEL
02994 void* kernarg_address;
02995 #elif defined HSA_LITTLE_ENDIAN
03003 void* kernarg_address;
03007 uint32_t reserved1;
03008 #else
03009 uint32_t reserved1;
03010 void* kernarg_address;
03011 #endif
03012
03016 uint64_t reserved2;
03017
03022 hsa_signal_t completion_signal;
03023
03024 } hsa_kernel_dispatch_packet_t;
03025
03029 typedef struct hsa_agent_dispatch_packet_s {
03034 uint16_t header;
03035

```

```

03039 uint16_t type;
03040
03044 uint32_t reserved0;
03045
03046 #ifdef HSA_LARGE_MODEL
03047 void* return_address;
03048 #elif defined HSA_LITTLE_ENDIAN
03052 void* return_address;
03056 uint32_t reserved1;
03057 #else
03058 uint32_t reserved1;
03059 void* return_address;
03060 #endif
03061
03065 uint64_t arg[4];
03066
03070 uint64_t reserved2;
03071
03076 hsa_signal_t completion_signal;
03077
03078 } hsa_agent_dispatch_packet_t;
03079
03083 typedef struct hsa_barrier_and_packet_s {
03088 uint16_t header;
03089
03093 uint16_t reserved0;
03094
03098 uint32_t reserved1;
03099
03105 hsa_signal_t dep_signal[5];
03106
03110 uint64_t reserved2;
03111
03116 hsa_signal_t completion_signal;
03117
03118 } hsa_barrier_and_packet_t;
03119
03123 typedef struct hsa_barrier_or_packet_s {
03128 uint16_t header;
03129
03133 uint16_t reserved0;
03134
03138 uint32_t reserved1;
03139
03145 hsa_signal_t dep_signal[5];
03146
03150 uint64_t reserved2;
03151
03156 hsa_signal_t completion_signal;
03157
03158 } hsa_barrier_or_packet_t;
03159
03169 typedef enum {
03173 HSA_REGION_SEGMENT_GLOBAL = 0,
03178 HSA_REGION_SEGMENT_READONLY = 1,
03182 HSA_REGION_SEGMENT_PRIVATE = 2,
03187 HSA_REGION_SEGMENT_GROUP = 3,
03191 HSA_REGION_SEGMENT_KERNARG = 4
03192 } hsa_region_segment_t;
03193
03197 typedef enum {
03203 HSA_REGION_GLOBAL_FLAG_KERNARG = 1,
03209 HSA_REGION_GLOBAL_FLAG_FINE_GRAINED = 2,
03216 HSA_REGION_GLOBAL_FLAG_COARSE_GRAINED = 4
03217 } hsa_region_global_flag_t;
03218
03222 typedef enum {
03227 HSA_REGION_INFO_SEGMENT = 0,
03234 HSA_REGION_INFO_GLOBAL_FLAGS = 1,
03238 HSA_REGION_INFO_SIZE = 2,
03252 HSA_REGION_INFO_ALLOC_MAX_SIZE = 4,
03259 HSA_REGION_INFO_ALLOC_MAX_PRIVATE_WORKGROUP_SIZE = 8,
03267 HSA_REGION_INFO_RUNTIME_ALLOC_ALLOWED = 5,
03275 HSA_REGION_INFO_RUNTIME_ALLOC_GRANULE = 6,
03282 HSA_REGION_INFO_RUNTIME_ALLOC_ALIGNMENT = 7
03283 } hsa_region_info_t;
03284
03306 hsa_status_t HSA_API hsa_region_get_info(
03307 hsa_region_t region,
03308 hsa_region_info_t attribute,
03309 void* value);
03310
03335 hsa_status_t HSA_API hsa_agent_iterate_regions(
03336 hsa_agent_t agent,
03337 hsa_status_t (*callback)(hsa_region_t region, void* data),
03338 void* data);
03339

```

```
03371 hsa_status_t HSA_API hsa_memory_allocate(hsa_region_t region,
03372 size_t size,
03373 void** ptr);
03374
03387 hsa_status_t HSA_API hsa_memory_free(void* ptr);
03388
03415 hsa_status_t HSA_API hsa_memory_copy(
03416 void *dst,
03417 const void *src,
03418 size_t size);
03419
03462 hsa_status_t HSA_API hsa_memory_assign_agent(
03463 void *ptr,
03464 hsa_agent_t agent,
03465 hsa_access_permission_t access);
03466
03503 hsa_status_t HSA_API hsa_memory_register(
03504 void *ptr,
03505 size_t size);
03506
03525 hsa_status_t HSA_API hsa_memory_deregister(
03526 void *ptr,
03527 size_t size);
03528
03539 typedef struct hsa_isa_s {
03544 uint64_t handle;
03545 } hsa_isa_t;
03546
03573 hsa_status_t HSA_API hsa_isa_from_name(
03574 const char *name,
03575 hsa_isa_t *isa);
03576
03603 hsa_status_t HSA_API hsa_agent_iterate_isas(
03604 hsa_agent_t agent,
03605 hsa_status_t (*callback)(hsa_isa_t isa, void *data),
03606 void *data);
03607
03611 typedef enum {
03616 HSA_ISA_INFO_NAME_LENGTH = 0,
03621 HSA_ISA_INFO_NAME = 1,
03628 HSA_ISA_INFO_CALL_CONVENTION_COUNT = 2,
03635 HSA_ISA_INFO_CALL_CONVENTION_INFO_WAVEFRONT_SIZE = 3,
03644 HSA_ISA_INFO_CALL_CONVENTION_INFO_WAVEFRONTS_PER_COMPUTE_UNIT = 4,
03651 HSA_ISA_INFO_MACHINE_MODELS = 5,
03658 HSA_ISA_INFO_PROFILES = 6,
03670 HSA_ISA_INFO_DEFAULT_FLOAT_ROUNDING_MODES = 7,
03680 HSA_ISA_INFO_BASE_PROFILE_DEFAULT_FLOAT_ROUNDING_MODES = 8,
03686 HSA_ISA_INFO_FAST_F16_OPERATION = 9,
03693 HSA_ISA_INFO_WORKGROUP_MAX_DIM = 12,
03698 HSA_ISA_INFO_WORKGROUP_MAX_SIZE = 13,
03706 HSA_ISA_INFO_GRID_MAX_DIM = 14,
03711 HSA_ISA_INFO_GRID_MAX_SIZE = 16,
03716 HSA_ISA_INFO_FBARRIER_MAX_SIZE = 17
03717 } hsa_isa_info_t;
03718
03756 hsa_status_t HSA_API HSA_DEPRECATED hsa_isa_get_info(
03757 hsa_isa_t isa,
03758 hsa_isa_info_t attribute,
03759 uint32_t index,
03760 void *value);
03761
03786 hsa_status_t HSA_API hsa_isa_get_info_alt(
03787 hsa_isa_t isa,
03788 hsa_isa_info_t attribute,
03789 void *value);
03790
03813 hsa_status_t HSA_API hsa_isa_get_exception_policies(
03814 hsa_isa_t isa,
03815 hsa_profile_t profile,
03816 uint16_t *mask);
03817
03821 typedef enum {
03825 HSA_FP_TYPE_16 = 1,
03829 HSA_FP_TYPE_32 = 2,
03833 HSA_FP_TYPE_64 = 4
03834 } hsa_fp_type_t;
03835
03839 typedef enum {
03843 HSA_FLUSH_MODE_FTZ = 1,
03847 HSA_FLUSH_MODE_NON_FTZ = 2
03848 } hsa_flush_mode_t;
03849
03853 typedef enum {
03857 HSA_ROUND_METHOD_SINGLE = 1,
03861 HSA_ROUND_METHOD_DOUBLE = 2
03862 } hsa_round_method_t;
03863
```



```

03891 hsa_status_t HSA_API hsa_isa_get_round_method(
03892 hsa_isa_t isa,
03893 hsa_fp_type_t fp_type,
03894 hsa_flush_mode_t flush_mode,
03895 hsa_round_method_t *round_method);
03896
03900 typedef struct hsa_wavefront_s {
03905 uint64_t handle;
03906 } hsa_wavefront_t;
03907
03911 typedef enum {
03916 HSA_WAVEFRONT_INFO_SIZE = 0
03917 } hsa_wavefront_info_t;
03918
03940 hsa_status_t HSA_API hsa_wavefront_get_info(
03941 hsa_wavefront_t wavefront,
03942 hsa_wavefront_info_t attribute,
03943 void *value);
03944
03970 hsa_status_t HSA_API hsa_isa_iterate_wavefronts(
03971 hsa_isa_t isa,
03972 hsa_status_t (*callback)(hsa_wavefront_t wavefront, void *data),
03973 void *data);
03974
04001 hsa_status_t HSA_API HSA_DEPRECATED hsa_isa_compatible(
04002 hsa_isa_t code_object_isa,
04003 hsa_isa_t agent_isa,
04004 bool *result);
04005
04019 typedef struct hsa_code_object_reader_s {
04024 uint64_t handle;
04025 } hsa_code_object_reader_t;
04026
04052 hsa_status_t HSA_API hsa_code_object_reader_create_from_file(
04053 hsa_file_t file,
04054 hsa_code_object_reader_t *code_object_reader);
04055
04080 hsa_status_t HSA_API hsa_code_object_reader_create_from_memory(
04081 const void *code_object,
04082 size_t size,
04083 hsa_code_object_reader_t *code_object_reader);
04084
04102 hsa_status_t HSA_API hsa_code_object_reader_destroy(
04103 hsa_code_object_reader_t code_object_reader);
04104
04110 typedef struct hsa_executable_s {
04115 uint64_t handle;
04116 } hsa_executable_t;
04117
04121 typedef enum {
04128 HSA_EXECUTABLE_STATE_UNFROZEN = 0,
04135 HSA_EXECUTABLE_STATE_FROZEN = 1
04136 } hsa_executable_state_t;
04137
04171 hsa_status_t HSA_API HSA_DEPRECATED hsa_executable_create(
04172 hsa_profile_t profile,
04173 hsa_executable_state_t executable_state,
04174 const char *options,
04175 hsa_executable_t *executable);
04176
04207 hsa_status_t HSA_API hsa_executable_create_alt(
04208 hsa_profile_t profile,
04209 hsa_default_float_rounding_mode_t default_float_rounding_mode,
04210 const char *options,
04211 hsa_executable_t *executable);
04212
04234 hsa_status_t HSA_API hsa_executable_destroy(
04235 hsa_executable_t executable);
04236
04240 typedef struct hsa_loaded_code_object_s {
04245 uint64_t handle;
04246 } hsa_loaded_code_object_t;
04247
04292 hsa_status_t HSA_API hsa_executable_load_program_code_object(
04293 hsa_executable_t executable,
04294 hsa_code_object_reader_t code_object_reader,
04295 const char *options,
04296 hsa_loaded_code_object_t *loaded_code_object);
04297
04358 hsa_status_t HSA_API hsa_executable_load_agent_code_object(
04359 hsa_executable_t executable,
04360 hsa_agent_t agent,
04361 hsa_code_object_reader_t code_object_reader,
04362 const char *options,
04363 hsa_loaded_code_object_t *loaded_code_object);
04364
04394 hsa_status_t HSA_API hsa_executable_freeze(

```

```

04395 hsa_executable_t executable,
04396 const char *options);
04397
04401 typedef enum {
04406 HSA_EXECUTABLE_INFO_PROFILE = 1,
04410 HSA_EXECUTABLE_INFO_STATE = 2,
04415 HSA_EXECUTABLE_INFO_DEFAULT_FLOAT_ROUNDING_MODE = 3
04416 } hsa_executable_info_t;
04417
04439 hsa_status_t HSA_API hsa_executable_get_info(
04440 hsa_executable_t executable,
04441 hsa_executable_info_t attribute,
04442 void *value);
04443
04482 hsa_status_t HSA_API hsa_executable_global_variable_define(
04483 hsa_executable_t executable,
04484 const char *variable_name,
04485 void *address);
04486
04529 hsa_status_t HSA_API hsa_executable_agent_global_variable_define(
04530 hsa_executable_t executable,
04531 hsa_agent_t agent,
04532 const char *variable_name,
04533 void *address);
04534
04581 hsa_status_t HSA_API hsa_executable_readonly_variable_define(
04582 hsa_executable_t executable,
04583 hsa_agent_t agent,
04584 const char *variable_name,
04585 void *address);
04586
04609 hsa_status_t HSA_API hsa_executable_validate(
04610 hsa_executable_t executable,
04611 uint32_t *result);
04612
04640 hsa_status_t HSA_API hsa_executable_validate_alt(
04641 hsa_executable_t executable,
04642 const char *options,
04643 uint32_t *result);
04644
04652 typedef struct hsa_executable_symbol_s {
04657 uint64_t handle;
04658 } hsa_executable_symbol_t;
04659
04695 hsa_status_t HSA_API HSA_DEPRECATED hsa_executable_get_symbol(
04696 hsa_executable_t executable,
04697 const char *module_name,
04698 const char *symbol_name,
04699 hsa_agent_t agent,
04700 int32_t call_convention,
04701 hsa_executable_symbol_t *symbol);
04702
04732 hsa_status_t HSA_API hsa_executable_get_symbol_by_name(
04733 hsa_executable_t executable,
04734 const char *symbol_name,
04735 const hsa_agent_t *agent,
04736 hsa_executable_symbol_t *symbol);
04737
04741 typedef enum {
04745 HSA_SYMBOL_KIND_VARIABLE = 0,
04749 HSA_SYMBOL_KIND_KERNEL = 1,
04753 HSA_SYMBOL_KIND_INDIRECT_FUNCTION = 2
04754 } hsa_symbol_kind_t;
04755
04759 typedef enum {
04763 HSA_SYMBOL_LINKAGE_MODULE = 0,
04767 HSA_SYMBOL_LINKAGE_PROGRAM = 1
04768 } hsa_symbol_linkage_t;
04769
04773 typedef enum {
04777 HSA_VARIABLE_ALLOCATION_AGENT = 0,
04781 HSA_VARIABLE_ALLOCATION_PROGRAM = 1
04782 } hsa_variable_allocation_t;
04783
04787 typedef enum {
04791 HSA_VARIABLE_SEGMENT_GLOBAL = 0,
04795 HSA_VARIABLE_SEGMENT_READONLY = 1
04796 } hsa_variable_segment_t;
04797
04801 typedef enum {
04805 HSA_EXECUTABLE_SYMBOL_INFO_TYPE = 0,
04810 HSA_EXECUTABLE_SYMBOL_INFO_NAME_LENGTH = 1,
04816 HSA_EXECUTABLE_SYMBOL_INFO_NAME = 2,
04824 HSA_EXECUTABLE_SYMBOL_INFO_MODULE_NAME_LENGTH = 3,
04833 HSA_EXECUTABLE_SYMBOL_INFO_MODULE_NAME = 4,
04842 HSA_EXECUTABLE_SYMBOL_INFO_AGENT = 20,
04850 HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_ADDRESS = 21,

```

```

04855 HSA_EXECUTABLE_SYMBOL_INFO_LINKAGE = 5,
04860 HSA_EXECUTABLE_SYMBOL_INFO_IS_DEFINITION = 17,
04868 HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_ALLOCATION = 6,
04876 HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_SEGMENT = 7,
04886 HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_ALIGNMENT = 8,
04896 HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_SIZE = 9,
04904 HSA_EXECUTABLE_SYMBOL_INFO_VARIABLE_IS_CONST = 10,
04913 HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_OBJECT = 22,
04920 HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_KERNEL_SEGMENT_SIZE = 11,
04927 HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_KERNEL_SEGMENT_ALIGNMENT = 12,
04937 HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_GROUP_SEGMENT_SIZE = 13,
04949 HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_PRIVATE_SEGMENT_SIZE = 14,
04958 HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_DYNAMIC_CALLSTACK = 15,
04965 HSA_EXECUTABLE_SYMBOL_INFO_KERNEL_CALL_CONVENTION = 18,
04976 HSA_EXECUTABLE_SYMBOL_INFO_INDIRECT_FUNCTION_OBJECT = 23,
04985 HSA_EXECUTABLE_SYMBOL_INFO_INDIRECT_FUNCTION_CALL_CONVENTION = 16
04986 } hsa_executable_symbol_info_t;
04987
05010 hsa_status_t HSA_API hsa_executable_symbol_get_info(
05011 hsa_executable_symbol_t executable_symbol,
05012 hsa_executable_symbol_info_t attribute,
05013 void *value);
05014
05041 hsa_status_t HSA_API HSA_DEPRECATED hsa_executable_iterate_symbols(
05042 hsa_executable_t executable,
05043 hsa_status_t (*callback)(hsa_executable_t exec,
05044 hsa_executable_symbol_t symbol,
05045 void *data),
05046 void *data);
05047
05075 hsa_status_t HSA_API hsa_executable_iterate_agent_symbols(
05076 hsa_executable_t executable,
05077 hsa_agent_t agent,
05078 hsa_status_t (*callback)(hsa_executable_t exec,
05079 hsa_agent_t agent,
05080 hsa_executable_symbol_t symbol,
05081 void *data),
05082 void *data);
05083
05108 hsa_status_t HSA_API hsa_executable_iterate_program_symbols(
05109 hsa_executable_t executable,
05110 hsa_status_t (*callback)(hsa_executable_t exec,
05111 hsa_executable_symbol_t symbol,
05112 void *data),
05113 void *data);
05114
05129 typedef struct hsa_code_object_s {
05134 uint64_t handle;
05135 } hsa_code_object_t;
05136
05143 typedef struct hsa_callback_data_s {
05147 uint64_t handle;
05148 } hsa_callback_data_t;
05149
05196 hsa_status_t HSA_API HSA_DEPRECATED hsa_code_object_serialize(
05197 hsa_code_object_t code_object,
05198 hsa_status_t (*alloc_callback)(size_t size,
05199 hsa_callback_data_t data,
05200 void **address),
05201 hsa_callback_data_t callback_data,
05202 const char *options,
05203 void **serialized_code_object,
05204 size_t *serialized_code_object_size);
05205
05236 hsa_status_t HSA_API HSA_DEPRECATED hsa_code_object_deserialize(
05237 void *serialized_code_object,
05238 size_t serialized_code_object_size,
05239 const char *options,
05240 hsa_code_object_t *code_object);
05241
05261 hsa_status_t HSA_API HSA_DEPRECATED hsa_code_object_destroy(
05262 hsa_code_object_t code_object);
05263
05269 typedef enum {
05274 HSA_CODE_OBJECT_TYPE_PROGRAM = 0
05275 } hsa_code_object_type_t;
05276
05282 typedef enum {
05289 HSA_CODE_OBJECT_INFO_VERSION = 0,
05294 HSA_CODE_OBJECT_INFO_TYPE = 1,
05299 HSA_CODE_OBJECT_INFO_ISA = 2,
05304 HSA_CODE_OBJECT_INFO_MACHINE_MODEL = 3,
05309 HSA_CODE_OBJECT_INFO_PROFILE = 4,
05315 HSA_CODE_OBJECT_INFO_DEFAULT_FLOAT_ROUNDING_MODE = 5
05316 } hsa_code_object_info_t;
05317
05341 hsa_status_t HSA_API HSA_DEPRECATED hsa_code_object_get_info(

```

```

05342 hsa_code_object_t code_object,
05343 hsa_code_object_info_t attribute,
05344 void *value);
05345
05397 hsa_status_t HSA_API HSA_DEPRECATED hsa_executable_load_code_object(
05398 hsa_executable_t executable,
05399 hsa_agent_t agent,
05400 hsa_code_object_t code_object,
05401 const char *options);
05402
05412 typedef struct hsa_code_symbol_s {
05417 uint64_t handle;
05418 } hsa_code_symbol_t;
05419
05445 hsa_status_t HSA_API HSA_DEPRECATED hsa_code_object_get_symbol(
05446 hsa_code_object_t code_object,
05447 const char *symbol_name,
05448 hsa_code_symbol_t *symbol);
05449
05478 hsa_status_t HSA_API HSA_DEPRECATED hsa_code_object_get_symbol_from_name(
05479 hsa_code_object_t code_object,
05480 const char *module_name,
05481 const char *symbol_name,
05482 hsa_code_symbol_t *symbol);
05483
05489 typedef enum {
05493 HSA_CODE_SYMBOL_INFO_TYPE = 0,
05498 HSA_CODE_SYMBOL_INFO_NAME_LENGTH = 1,
05504 HSA_CODE_SYMBOL_INFO_NAME = 2,
05510 HSA_CODE_SYMBOL_INFO_MODULE_NAME_LENGTH = 3,
05517 HSA_CODE_SYMBOL_INFO_MODULE_NAME = 4,
05522 HSA_CODE_SYMBOL_INFO_LINKAGE = 5,
05527 HSA_CODE_SYMBOL_INFO_IS_DEFINITION = 17,
05533 HSA_CODE_SYMBOL_INFO_VARIABLE_ALLOCATION = 6,
05539 HSA_CODE_SYMBOL_INFO_VARIABLE_SEGMENT = 7,
05547 HSA_CODE_SYMBOL_INFO_VARIABLE_ALIGNMENT = 8,
05555 HSA_CODE_SYMBOL_INFO_VARIABLE_SIZE = 9,
05561 HSA_CODE_SYMBOL_INFO_VARIABLE_IS_CONST = 10,
05568 HSA_CODE_SYMBOL_INFO_KERNEL_KERNEL_SEGMENT_SIZE = 11,
05575 HSA_CODE_SYMBOL_INFO_KERNEL_KERNEL_SEGMENT_ALIGNMENT = 12,
05585 HSA_CODE_SYMBOL_INFO_KERNEL_GROUP_SEGMENT_SIZE = 13,
05597 HSA_CODE_SYMBOL_INFO_KERNEL_PRIVATE_SEGMENT_SIZE = 14,
05606 HSA_CODE_SYMBOL_INFO_KERNEL_DYNAMIC_CALLSTACK = 15,
05611 HSA_CODE_SYMBOL_INFO_KERNEL_CALL_CONVENTION = 18,
05617 HSA_CODE_SYMBOL_INFO_INDIRECT_FUNCTION_CALL_CONVENTION = 16
05618 } hsa_code_symbol_info_t;
05619
05643 hsa_status_t HSA_API HSA_DEPRECATED hsa_code_symbol_get_info(
05644 hsa_code_symbol_t code_symbol,
05645 hsa_code_symbol_info_t attribute,
05646 void *value);
05647
05674 hsa_status_t HSA_API HSA_DEPRECATED hsa_code_object_iterate_symbols(
05675 hsa_code_object_t code_object,
05676 hsa_status_t (*callback)(hsa_code_object_t code_object,
05677 hsa_code_symbol_t symbol,
05678 void *data),
05679 void *data);
05680
05683 #ifdef __cplusplus
05684 } // end extern "C" block
05685 #endif
05686
05687 #endif // header guard

```

## 7.8 hsa\_api\_trace.h

```

00001
00002 //
00003 // The University of Illinois/NCSA
00004 // Open Source License (NCSA)
00005 //
00006 // Copyright (c) 2014-2020, Advanced Micro Devices, Inc. All rights reserved.
00007 //
00008 // Developed by:
00009 //
00010 // AMD Research and AMD HSA Software Development
00011 //
00012 // Advanced Micro Devices, Inc.
00013 //
00014 // www.amd.com
00015 //
00016 // Permission is hereby granted, free of charge, to any person obtaining a copy

```

```

00017 // of this software and associated documentation files (the "Software"), to
00018 // deal with the Software without restriction, including without limitation
00019 // the rights to use, copy, modify, merge, publish, distribute, sublicense,
00020 // and/or sell copies of the Software, and to permit persons to whom the
00021 // Software is furnished to do so, subject to the following conditions:
00022 //
00023 // - Redistributions of source code must retain the above copyright notice,
00024 // this list of conditions and the following disclaimers.
00025 // - Redistributions in binary form must reproduce the above copyright
00026 // notice, this list of conditions and the following disclaimers in
00027 // the documentation and/or other materials provided with the distribution.
00028 // - Neither the names of Advanced Micro Devices, Inc,
00029 // nor the names of its contributors may be used to endorse or promote
00030 // products derived from this Software without specific prior written
00031 // permission.
00032 //
00033 // THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR
00034 // IMPLIED, INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY,
00035 // FITNESS FOR A PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL
00036 // THE CONTRIBUTORS OR COPYRIGHT HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR
00037 // OTHER LIABILITY, WHETHER IN AN ACTION OF CONTRACT, TORT OR OTHERWISE,
00038 // ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER
00039 // DEALINGS WITH THE SOFTWARE.
00040 //
00042
00043 #ifndef HSA_RUNTIME_INC_HSA_API_TRACE_H
00044 #define HSA_RUNTIME_INC_HSA_API_TRACE_H
00045
00046 #include "hsa.h"
00047 #ifdef AMD_INTERNAL_BUILD
00048 #include "hsa_ext_image.h"
00049 #include "hsa_ext_amd.h"
00050 #include "hsa_ext_finalize.h"
00051 #else
00052 #include "inc/hsa_ext_image.h"
00053 #include "inc/hsa_ext_amd.h"
00054 #include "inc/hsa_ext_finalize.h"
00055 #endif
00056
00057 #include <string.h>
00058 #include <assert.h>
00059 #include <stddef.h>
00060
00061 // Major Ids of the Api tables exported by Hsa Core Runtime
00062 #define HSA_API_TABLE_MAJOR_VERSION 0x01
00063 #define HSA_CORE_API_TABLE_MAJOR_VERSION 0x01
00064 #define HSA_AMD_EXT_API_TABLE_MAJOR_VERSION 0x01
00065 #define HSA_FINALIZER_API_TABLE_MAJOR_VERSION 0x01
00066 #define HSA_IMAGE_API_TABLE_MAJOR_VERSION 0x01
00067 #define HSA_AQLPROFILE_API_TABLE_MAJOR_VERSION 0x01
00068
00069 // Step Ids of the Api tables exported by Hsa Core Runtime
00070 #define HSA_API_TABLE_STEP_VERSION 0x00
00071 #define HSA_CORE_API_TABLE_STEP_VERSION 0x00
00072 #define HSA_AMD_EXT_API_TABLE_STEP_VERSION 0x00
00073 #define HSA_FINALIZER_API_TABLE_STEP_VERSION 0x00
00074 #define HSA_IMAGE_API_TABLE_STEP_VERSION 0x00
00075 #define HSA_AQLPROFILE_API_TABLE_STEP_VERSION 0x00
00076
00077 // Min function used to copy Api Tables
00078 static inline uint32_t Min(const uint32_t a, const uint32_t b) {
00079 return (a > b) ? b : a;
00080 }
00081
00082 // Declarations of APIs intended for use only by tools.
00083 typedef void (*hsa_amd_queue_intercept_packet_writer)(const void* pkts, uint64_t pkt_count);
00084 typedef void (*hsa_amd_queue_intercept_handler)(const void* pkts, uint64_t pkt_count,
00085 uint64_t user_pkt_index, void* data,
00086 hsa_amd_queue_intercept_packet_writer);
00087 hsa_status_t hsa_amd_queue_intercept_register(hsa_queue_t* queue,
00088 hsa_amd_queue_intercept_handler callback,
00089 void* user_data);
00090 hsa_status_t hsa_amd_queue_intercept_create(
00091 hsa_agent_t agent_handle, uint32_t size, hsa_queue_type32_t type,
00092 void (*callback)(hsa_status_t status, hsa_queue_t* source, void* data), void* data,
00093 uint32_t private_segment_size, uint32_t group_segment_size, hsa_queue_t** queue);
00094
00095 typedef void (*hsa_amd_runtime_queue_notifier)(const hsa_queue_t* queue, hsa_agent_t agent,
00096 void* data);
00097 hsa_status_t hsa_amd_runtime_queue_create_register(hsa_amd_runtime_queue_notifier callback,
00098 void* user_data);
00099
00100 // Structure of Version used to identify an instance of Api table
00101 // Must be the first member (offsetof == 0) of all API tables.
00102 // This is the root of the table passing ABI.
00103 struct ApiTableVersion {
00104 uint32_t major_id;

```

```

00105 uint32_t minor_id;
00106 uint32_t step_id;
00107 uint32_t reserved;
00108 };
00109
00110 // Table to export HSA Finalizer Extension Apis
00111 struct FinalizerExtTable {
00112 ApiTableVersion version;
00113 decltype(hsa_ext_program_create) * hsa_ext_program_create_fn;
00114 decltype(hsa_ext_program_destroy) * hsa_ext_program_destroy_fn;
00115 decltype(hsa_ext_program_add_module) * hsa_ext_program_add_module_fn;
00116 decltype(hsa_ext_program_iterate_modules) * hsa_ext_program_iterate_modules_fn;
00117 decltype(hsa_ext_program_get_info) * hsa_ext_program_get_info_fn;
00118 decltype(hsa_ext_program_finalize) * hsa_ext_program_finalize_fn;
00119 };
00120
00121 // Table to export HSA Image Extension Apis
00122 struct ImageExtTable {
00123 ApiTableVersion version;
00124 decltype(hsa_ext_image_get_capability) * hsa_ext_image_get_capability_fn;
00125 decltype(hsa_ext_image_data_get_info) * hsa_ext_image_data_get_info_fn;
00126 decltype(hsa_ext_image_create) * hsa_ext_image_create_fn;
00127 decltype(hsa_ext_image_import) * hsa_ext_image_import_fn;
00128 decltype(hsa_ext_image_export) * hsa_ext_image_export_fn;
00129 decltype(hsa_ext_image_copy) * hsa_ext_image_copy_fn;
00130 decltype(hsa_ext_image_clear) * hsa_ext_image_clear_fn;
00131 decltype(hsa_ext_image_destroy) * hsa_ext_image_destroy_fn;
00132 decltype(hsa_ext_sampler_create) * hsa_ext_sampler_create_fn;
00133 decltype(hsa_ext_sampler_destroy) * hsa_ext_sampler_destroy_fn;
00134 decltype(hsa_ext_image_get_capability_with_layout) * hsa_ext_image_get_capability_with_layout_fn;
00135 decltype(hsa_ext_image_data_get_info_with_layout) * hsa_ext_image_data_get_info_with_layout_fn;
00136 decltype(hsa_ext_image_create_with_layout) * hsa_ext_image_create_with_layout_fn;
00137 };
00138
00139 // Table to export AMD Extension Apis
00140 struct AmdExtTable {
00141 ApiTableVersion version;
00142 decltype(hsa_amd_coherency_get_type) * hsa_amd_coherency_get_type_fn;
00143 decltype(hsa_amd_coherency_set_type) * hsa_amd_coherency_set_type_fn;
00144 decltype(hsa_amd_profiling_set_profiler_enabled) * hsa_amd_profiling_set_profiler_enabled_fn;
00145 decltype(hsa_amd_profiling_async_copy_enable) * hsa_amd_profiling_async_copy_enable_fn;
00146 decltype(hsa_amd_profiling_get_dispatch_time) * hsa_amd_profiling_get_dispatch_time_fn;
00147 decltype(hsa_amd_profiling_get_async_copy_time) * hsa_amd_profiling_get_async_copy_time_fn;
00148 decltype(hsa_amd_profiling_convert_tick_to_system_domain) *
hsa_amd_profiling_convert_tick_to_system_domain_fn;
00149 decltype(hsa_amd_signal_async_handler) * hsa_amd_signal_async_handler_fn;
00150 decltype(hsa_amd_async_function) * hsa_amd_async_function_fn;
00151 decltype(hsa_amd_signal_wait_any) * hsa_amd_signal_wait_any_fn;
00152 decltype(hsa_amd_queue_cu_set_mask) * hsa_amd_queue_cu_set_mask_fn;
00153 decltype(hsa_amd_memory_pool_get_info) * hsa_amd_memory_pool_get_info_fn;
00154 decltype(hsa_amd_agent_iterate_memory_pools) * hsa_amd_agent_iterate_memory_pools_fn;
00155 decltype(hsa_amd_memory_pool_allocate) * hsa_amd_memory_pool_allocate_fn;
00156 decltype(hsa_amd_memory_pool_free) * hsa_amd_memory_pool_free_fn;
00157 decltype(hsa_amd_memory_async_copy) * hsa_amd_memory_async_copy_fn;
00158 decltype(hsa_amd_agent_memory_pool_get_info) * hsa_amd_agent_memory_pool_get_info_fn;
00159 decltype(hsa_amd_agents_allow_access) * hsa_amd_agents_allow_access_fn;
00160 decltype(hsa_amd_memory_pool_can_migrate) * hsa_amd_memory_pool_can_migrate_fn;
00161 decltype(hsa_amd_memory_migrate) * hsa_amd_memory_migrate_fn;
00162 decltype(hsa_amd_memory_lock) * hsa_amd_memory_lock_fn;
00163 decltype(hsa_amd_memory_unlock) * hsa_amd_memory_unlock_fn;
00164 decltype(hsa_amd_memory_fill) * hsa_amd_memory_fill_fn;
00165 decltype(hsa_amd_interop_map_buffer) * hsa_amd_interop_map_buffer_fn;
00166 decltype(hsa_amd_interop_unmap_buffer) * hsa_amd_interop_unmap_buffer_fn;
00167 decltype(hsa_amd_image_create) * hsa_amd_image_create_fn;
00168 decltype(hsa_amd_pointer_info) * hsa_amd_pointer_info_fn;
00169 decltype(hsa_amd_pointer_info_set_userdata) * hsa_amd_pointer_info_set_userdata_fn;
00170 decltype(hsa_amd_ipc_memory_create) * hsa_amd_ipc_memory_create_fn;
00171 decltype(hsa_amd_ipc_memory_attach) * hsa_amd_ipc_memory_attach_fn;
00172 decltype(hsa_amd_ipc_memory_detach) * hsa_amd_ipc_memory_detach_fn;
00173 decltype(hsa_amd_signal_create) * hsa_amd_signal_create_fn;
00174 decltype(hsa_amd_ipc_signal_create) * hsa_amd_ipc_signal_create_fn;
00175 decltype(hsa_amd_ipc_signal_attach) * hsa_amd_ipc_signal_attach_fn;
00176 decltype(hsa_amd_register_system_event_handler) * hsa_amd_register_system_event_handler_fn;
00177 decltype(hsa_amd_queue_intercept_create) * hsa_amd_queue_intercept_create_fn;
00178 decltype(hsa_amd_queue_intercept_register) * hsa_amd_queue_intercept_register_fn;
00179 decltype(hsa_amd_queue_set_priority) * hsa_amd_queue_set_priority_fn;
00180 decltype(hsa_amd_memory_async_copy_rect) * hsa_amd_memory_async_copy_rect_fn;
00181 decltype(hsa_amd_runtime_queue_create_register) * hsa_amd_runtime_queue_create_register_fn;
00182 decltype(hsa_amd_memory_lock_to_pool) * hsa_amd_memory_lock_to_pool_fn;
00183 decltype(hsa_amd_register_deallocation_callback) * hsa_amd_register_deallocation_callback_fn;
00184 decltype(hsa_amd_deregister_deallocation_callback) * hsa_amd_deregister_deallocation_callback_fn;
00185 decltype(hsa_amd_signal_value_pointer) * hsa_amd_signal_value_pointer_fn;
00186 decltype(hsa_amd_svm_attributes_set) * hsa_amd_svm_attributes_set_fn;
00187 decltype(hsa_amd_svm_attributes_get) * hsa_amd_svm_attributes_get_fn;
00188 decltype(hsa_amd_svm_prefetch_async) * hsa_amd_svm_prefetch_async_fn;
00189 decltype(hsa_amd_queue_cu_get_mask) * hsa_amd_queue_cu_get_mask_fn;
00190 };

```

```
00191
00192 // Table to export HSA Core Runtime Apis
00193 struct CoreApiTable {
00194 ApiTableVersion version;
00195 decltype(hsa_init) * hsa_init_fn;
00196 decltype(hsa_shut_down) * hsa_shut_down_fn;
00197 decltype(hsa_system_get_info) * hsa_system_get_info_fn;
00198 decltype(hsa_system_extension_supported) * hsa_system_extension_supported_fn;
00199 decltype(hsa_system_get_extension_table) * hsa_system_get_extension_table_fn;
00200 decltype(hsa_iterate_agents) * hsa_iterate_agents_fn;
00201 decltype(hsa_agent_get_info) * hsa_agent_get_info_fn;
00202 decltype(hsa_queue_create) * hsa_queue_create_fn;
00203 decltype(hsa_soft_queue_create) * hsa_soft_queue_create_fn;
00204 decltype(hsa_queue_destroy) * hsa_queue_destroy_fn;
00205 decltype(hsa_queue_inactivate) * hsa_queue_inactivate_fn;
00206 decltype(hsa_queue_load_read_index_scacquire) * hsa_queue_load_read_index_scacquire_fn;
00207 decltype(hsa_queue_load_read_index_relaxed) * hsa_queue_load_read_index_relaxed_fn;
00208 decltype(hsa_queue_load_write_index_scacquire) * hsa_queue_load_write_index_scacquire_fn;
00209 decltype(hsa_queue_load_write_index_relaxed) * hsa_queue_load_write_index_relaxed_fn;
00210 decltype(hsa_queue_store_write_index_relaxed) * hsa_queue_store_write_index_relaxed_fn;
00211 decltype(hsa_queue_store_write_index_screlease) * hsa_queue_store_write_index_screlease_fn;
00212 decltype(hsa_queue_cas_write_index_scacq_screl) * hsa_queue_cas_write_index_scacq_screl_fn;
00213 decltype(hsa_queue_cas_write_index_scacquire) * hsa_queue_cas_write_index_scacquire_fn;
00214 decltype(hsa_queue_cas_write_index_relaxed) * hsa_queue_cas_write_index_relaxed_fn;
00215 decltype(hsa_queue_cas_write_index_screlease) * hsa_queue_cas_write_index_screlease_fn;
00216 decltype(hsa_queue_add_write_index_scacq_screl) * hsa_queue_add_write_index_scacq_screl_fn;
00217 decltype(hsa_queue_add_write_index_scacquire) * hsa_queue_add_write_index_scacquire_fn;
00218 decltype(hsa_queue_add_write_index_relaxed) * hsa_queue_add_write_index_relaxed_fn;
00219 decltype(hsa_queue_add_write_index_screlease) * hsa_queue_add_write_index_screlease_fn;
00220 decltype(hsa_queue_store_read_index_relaxed) * hsa_queue_store_read_index_relaxed_fn;
00221 decltype(hsa_queue_store_read_index_screlease) * hsa_queue_store_read_index_screlease_fn;
00222 decltype(hsa_agent_iterate_regions) * hsa_agent_iterate_regions_fn;
00223 decltype(hsa_region_get_info) * hsa_region_get_info_fn;
00224 decltype(hsa_agent_get_exception_policies) * hsa_agent_get_exception_policies_fn;
00225 decltype(hsa_agent_extension_supported) * hsa_agent_extension_supported_fn;
00226 decltype(hsa_memory_register) * hsa_memory_register_fn;
00227 decltype(hsa_memory_deregister) * hsa_memory_deregister_fn;
00228 decltype(hsa_memory_allocate) * hsa_memory_allocate_fn;
00229 decltype(hsa_memory_free) * hsa_memory_free_fn;
00230 decltype(hsa_memory_copy) * hsa_memory_copy_fn;
00231 decltype(hsa_memory_assign_agent) * hsa_memory_assign_agent_fn;
00232 decltype(hsa_signal_create) * hsa_signal_create_fn;
00233 decltype(hsa_signal_destroy) * hsa_signal_destroy_fn;
00234 decltype(hsa_signal_load_relaxed) * hsa_signal_load_relaxed_fn;
00235 decltype(hsa_signal_load_scacquire) * hsa_signal_load_scacquire_fn;
00236 decltype(hsa_signal_store_relaxed) * hsa_signal_store_relaxed_fn;
00237 decltype(hsa_signal_store_screlease) * hsa_signal_store_screlease_fn;
00238 decltype(hsa_signal_wait_relaxed) * hsa_signal_wait_relaxed_fn;
00239 decltype(hsa_signal_wait_scacquire) * hsa_signal_wait_scacquire_fn;
00240 decltype(hsa_signal_and_relaxed) * hsa_signal_and_relaxed_fn;
00241 decltype(hsa_signal_and_scacquire) * hsa_signal_and_scacquire_fn;
00242 decltype(hsa_signal_and_screlease) * hsa_signal_and_screlease_fn;
00243 decltype(hsa_signal_and_scacq_screl) * hsa_signal_and_scacq_screl_fn;
00244 decltype(hsa_signal_or_relaxed) * hsa_signal_or_relaxed_fn;
00245 decltype(hsa_signal_or_scacquire) * hsa_signal_or_scacquire_fn;
00246 decltype(hsa_signal_or_screlease) * hsa_signal_or_screlease_fn;
00247 decltype(hsa_signal_or_scacq_screl) * hsa_signal_or_scacq_screl_fn;
00248 decltype(hsa_signal_xor_relaxed) * hsa_signal_xor_relaxed_fn;
00249 decltype(hsa_signal_xor_scacquire) * hsa_signal_xor_scacquire_fn;
00250 decltype(hsa_signal_xor_screlease) * hsa_signal_xor_screlease_fn;
00251 decltype(hsa_signal_xor_scacq_screl) * hsa_signal_xor_scacq_screl_fn;
00252 decltype(hsa_signal_exchange_relaxed) * hsa_signal_exchange_relaxed_fn;
00253 decltype(hsa_signal_exchange_scacquire) * hsa_signal_exchange_scacquire_fn;
00254 decltype(hsa_signal_exchange_screlease) * hsa_signal_exchange_screlease_fn;
00255 decltype(hsa_signal_exchange_scacq_screl) * hsa_signal_exchange_scacq_screl_fn;
00256 decltype(hsa_signal_add_relaxed) * hsa_signal_add_relaxed_fn;
00257 decltype(hsa_signal_add_scacquire) * hsa_signal_add_scacquire_fn;
00258 decltype(hsa_signal_add_screlease) * hsa_signal_add_screlease_fn;
00259 decltype(hsa_signal_add_scacq_screl) * hsa_signal_add_scacq_screl_fn;
00260 decltype(hsa_signal_subtract_relaxed) * hsa_signal_subtract_relaxed_fn;
00261 decltype(hsa_signal_subtract_scacquire) * hsa_signal_subtract_scacquire_fn;
00262 decltype(hsa_signal_subtract_screlease) * hsa_signal_subtract_screlease_fn;
00263 decltype(hsa_signal_subtract_scacq_screl) * hsa_signal_subtract_scacq_screl_fn;
00264 decltype(hsa_signal_cas_relaxed) * hsa_signal_cas_relaxed_fn;
00265 decltype(hsa_signal_cas_scacquire) * hsa_signal_cas_scacquire_fn;
00266 decltype(hsa_signal_cas_screlease) * hsa_signal_cas_screlease_fn;
00267 decltype(hsa_signal_cas_scacq_screl) * hsa_signal_cas_scacq_screl_fn;
00268
00269 //----- Instruction Set Architecture -----//
00270
00271 decltype(hsa_isa_from_name) * hsa_isa_from_name_fn;
00272 // Deprecated since v1.1.
00273 decltype(hsa_isa_get_info) * hsa_isa_get_info_fn;
00274 // Deprecated since v1.1.
00275 decltype(hsa_isa_compatible) * hsa_isa_compatible_fn;
00276
00277 //----- Code Objects (deprecated) -----//
```



```

00278
00279 // Deprecated since v1.1.
00280 decltype(hsa_code_object_serialize) * hsa_code_object_serialize_fn;
00281 // Deprecated since v1.1.
00282 decltype(hsa_code_object_deserialize) * hsa_code_object_deserialize_fn;
00283 // Deprecated since v1.1.
00284 decltype(hsa_code_object_destroy) * hsa_code_object_destroy_fn;
00285 // Deprecated since v1.1.
00286 decltype(hsa_code_object_get_info) * hsa_code_object_get_info_fn;
00287 // Deprecated since v1.1.
00288 decltype(hsa_code_object_get_symbol) * hsa_code_object_get_symbol_fn;
00289 // Deprecated since v1.1.
00290 decltype(hsa_code_symbol_get_info) * hsa_code_symbol_get_info_fn;
00291 // Deprecated since v1.1.
00292 decltype(hsa_code_object_iterate_symbols) * hsa_code_object_iterate_symbols_fn;
00293
00294 //===== Executable =====//
00295
00296 // Deprecated since v1.1.
00297 decltype(hsa_executable_create) * hsa_executable_create_fn;
00298 decltype(hsa_executable_destroy) * hsa_executable_destroy_fn;
00299 // Deprecated since v1.1.
00300 decltype(hsa_executable_load_code_object) * hsa_executable_load_code_object_fn;
00301 decltype(hsa_executable_freeze) * hsa_executable_freeze_fn;
00302 decltype(hsa_executable_get_info) * hsa_executable_get_info_fn;
00303 decltype(hsa_executable_global_variable_define) *
00304 hsa_executable_global_variable_define_fn;
00305 decltype(hsa_executable_agent_global_variable_define) *
00306 hsa_executable_agent_global_variable_define_fn;
00307 decltype(hsa_executable_readonly_variable_define) *
00308 hsa_executable_readonly_variable_define_fn;
00309 decltype(hsa_executable_validate) * hsa_executable_validate_fn;
00310 // Deprecated since v1.1.
00311 decltype(hsa_executable_get_symbol) * hsa_executable_get_symbol_fn;
00312 decltype(hsa_executable_symbol_get_info) * hsa_executable_symbol_get_info_fn;
00313 // Deprecated since v1.1.
00314 decltype(hsa_executable_iterate_symbols) * hsa_executable_iterate_symbols_fn;
00315
00316 //===== Runtime Notifications =====//
00317
00318 decltype(hsa_status_string) * hsa_status_string_fn;
00319
00320 // Start HSA v1.1 additions
00321 decltype(hsa_extension_get_name) * hsa_extension_get_name_fn;
00322 decltype(hsa_system_major_extension_supported) * hsa_system_major_extension_supported_fn;
00323 decltype(hsa_system_get_major_extension_table) * hsa_system_get_major_extension_table_fn;
00324 decltype(hsa_agent_major_extension_supported) * hsa_agent_major_extension_supported_fn;
00325 decltype(hsa_cache_get_info) * hsa_cache_get_info_fn;
00326 decltype(hsa_agent_iterate_caches) * hsa_agent_iterate_caches_fn;
00327 decltype(hsa_signal_silent_store_relaxed) * hsa_signal_silent_store_relaxed_fn;
00328 decltype(hsa_signal_silent_store_screlease) * hsa_signal_silent_store_screlease_fn;
00329 decltype(hsa_signal_group_create) * hsa_signal_group_create_fn;
00330 decltype(hsa_signal_group_destroy) * hsa_signal_group_destroy_fn;
00331 decltype(hsa_signal_group_wait_any_scacquire) * hsa_signal_group_wait_any_scacquire_fn;
00332 decltype(hsa_signal_group_wait_any_relaxed) * hsa_signal_group_wait_any_relaxed_fn;
00333
00334 //===== Instruction Set Architecture - HSA v1.1 additions =====//
00335
00336 decltype(hsa_agent_iterate_isas) * hsa_agent_iterate_isas_fn;
00337 decltype(hsa_isa_get_info_alt) * hsa_isa_get_info_alt_fn;
00338 decltype(hsa_isa_get_exception_policies) * hsa_isa_get_exception_policies_fn;
00339 decltype(hsa_isa_get_round_method) * hsa_isa_get_round_method_fn;
00340 decltype(hsa_wavefront_get_info) * hsa_wavefront_get_info_fn;
00341 decltype(hsa_isa_iterate_wavefronts) * hsa_isa_iterate_wavefronts_fn;
00342
00343 //===== Code Objects (deprecated) - HSA v1.1 additions =====//
00344
00345 // Deprecated since v1.1.
00346 decltype(hsa_code_object_get_symbol_from_name) *
00347 hsa_code_object_get_symbol_from_name_fn;
00348
00349 //===== Executable - HSA v1.1 additions =====//
00350
00351 decltype(hsa_code_object_reader_create_from_file) *
00352 hsa_code_object_reader_create_from_file_fn;
00353 decltype(hsa_code_object_reader_create_from_memory) *
00354 hsa_code_object_reader_create_from_memory_fn;
00355 decltype(hsa_code_object_reader_destroy) * hsa_code_object_reader_destroy_fn;
00356 decltype(hsa_executable_create_alt) * hsa_executable_create_alt_fn;
00357 decltype(hsa_executable_load_program_code_object) *
00358 hsa_executable_load_program_code_object_fn;
00359 decltype(hsa_executable_load_agent_code_object) *
00360 hsa_executable_load_agent_code_object_fn;
00361 decltype(hsa_executable_validate_alt) * hsa_executable_validate_alt_fn;
00362 decltype(hsa_executable_get_symbol_by_name) *
00363 hsa_executable_get_symbol_by_name_fn;
00364 decltype(hsa_executable_iterate_agent_symbols) *

```



```

00365 hsa_executable_iterate_agent_symbols_fn;
00366 decltype(hsa_executable_iterate_program_symbols)*
00367 hsa_executable_iterate_program_symbols_fn;
00368 };
00369
00370 // Table to export HSA Apis from Core Runtime, Amd Extensions
00371 // Finalizer and Images
00372 struct HsaApiTable {
00373
00374 // Version of Hsa Api Table
00375 ApiTableVersion version;
00376
00377 // Table of function pointers to HSA Core Runtime
00378 CoreApiTable* core_;
00379
00380 // Table of function pointers to AMD extensions
00381 AmdExtTable* amd_ext_;
00382
00383 // Table of function pointers to HSA Finalizer Extension
00384 FinalizerExtTable* finalizer_ext_;
00385
00386 // Table of function pointers to HSA Image Extension
00387 ImageExtTable* image_ext_;
00388 };
00389
00390 // Structure containing instances of different api tables
00391 struct HsaApiTableContainer {
00392 HsaApiTable root;
00393 CoreApiTable core;
00394 AmdExtTable amd_ext;
00395 FinalizerExtTable finalizer_ext;
00396 ImageExtTable image_ext;
00397
00398 // Default initialization of a container instance
00399 HsaApiTableContainer() {
00400 root.version.major_id = HSA_API_TABLE_MAJOR_VERSION;
00401 root.version.minor_id = sizeof(HsaApiTable);
00402 root.version.step_id = HSA_API_TABLE_STEP_VERSION;
00403
00404 core.version.major_id = HSA_CORE_API_TABLE_MAJOR_VERSION;
00405 core.version.minor_id = sizeof(CoreApiTable);
00406 core.version.step_id = HSA_CORE_API_TABLE_STEP_VERSION;
00407 root.core_ = &core;
00408
00409 amd_ext.version.major_id = HSA_AMD_EXT_API_TABLE_MAJOR_VERSION;
00410 amd_ext.version.minor_id = sizeof(AmdExtTable);
00411 amd_ext.version.step_id = HSA_AMD_EXT_API_TABLE_STEP_VERSION;
00412 root.amd_ext_ = &amd_ext;
00413
00414 finalizer_ext.version.major_id = HSA_FINALIZER_API_TABLE_MAJOR_VERSION;
00415 finalizer_ext.version.minor_id = sizeof(FinalizerExtTable);
00416 finalizer_ext.version.step_id = HSA_FINALIZER_API_TABLE_STEP_VERSION;
00417 root.finalizer_ext_ = &finalizer_ext;
00418
00419 image_ext.version.major_id = HSA_IMAGE_API_TABLE_MAJOR_VERSION;
00420 image_ext.version.minor_id = sizeof(ImageExtTable);
00421 image_ext.version.step_id = HSA_IMAGE_API_TABLE_STEP_VERSION;
00422 root.image_ext_ = &image_ext;
00423 }
00424 };
00425
00426 // Api to copy function pointers of a table
00427 static
00428 void inline copyApi(void* src, void* dest, size_t size) {
00429 assert(size >= sizeof(ApiTableVersion));
00430 memcpy((char*)src + sizeof(ApiTableVersion),
00431 (char*)dest + sizeof(ApiTableVersion),
00432 (size - sizeof(ApiTableVersion)));
00433 }
00434
00435 // Copy Api child tables if valid.
00436 static void inline copyElement(ApiTableVersion* dest, ApiTableVersion* src) {
00437 if (src->major_id && (dest->major_id == src->major_id)) {
00438 dest->step_id = src->step_id;
00439 dest->minor_id = Min(dest->minor_id, src->minor_id);
00440 copyApi(dest, src, dest->minor_id);
00441 } else {
00442 dest->major_id = 0;
00443 dest->minor_id = 0;
00444 dest->step_id = 0;
00445 }
00446 }
00447
00448 // Copy constructor for all Api tables. The function assumes the
00449 // user has initialized an instance of tables container correctly
00450 // for the Major, Minor and Stepping Ids of Root and Child Api tables.
00451 // The function will overwrite the value of Minor Id by taking the

```

```

00452 // minimum of source and destination parameters. It will also overwrite
00453 // the stepping Id with value from source parameter.
00454 static void inline copyTables(const HsaApiTable* src, HsaApiTable* dest) {
00455 // Verify Major Id of source and destination tables match
00456 if (dest->version.major_id != src->version.major_id) {
00457 dest->version.major_id = 0;
00458 dest->version.minor_id = 0;
00459 dest->version.step_id = 0;
00460 return;
00461 }
00462
00463 // Initialize the stepping id and minor id of root table. For the
00464 // minor id which encodes struct size, take the minimum of source
00465 // and destination parameters
00466 dest->version.step_id = src->version.step_id;
00467 dest->version.minor_id = Min(dest->version.minor_id, src->version.minor_id);
00468
00469 // Copy child tables if present
00470 if ((offsetof(HsaApiTable, core_) < dest->version.minor_id))
00471 copyElement(&dest->core_>version, &src->core_>version);
00472 if ((offsetof(HsaApiTable, amd_ext_) < dest->version.minor_id))
00473 copyElement(&dest->amd_ext_>version, &src->amd_ext_>version);
00474 if ((offsetof(HsaApiTable, finalizer_ext_) < dest->version.minor_id))
00475 copyElement(&dest->finalizer_ext_>version, &src->finalizer_ext_>version);
00476 if ((offsetof(HsaApiTable, image_ext_) < dest->version.minor_id))
00477 copyElement(&dest->image_ext_>version, &src->image_ext_>version);
00478 }
00479 #endif

```

## 7.9 hsa\_ext\_amd.h

```

00001
00002 //
00003 // The University of Illinois/NCSA
00004 // Open Source License (NCSA)
00005 //
00006 // Copyright (c) 2014-2020, Advanced Micro Devices, Inc. All rights reserved.
00007 //
00008 // Developed by:
00009 //
00010 // AMD Research and AMD HSA Software Development
00011 //
00012 // Advanced Micro Devices, Inc.
00013 //
00014 // www.amd.com
00015 //
00016 // Permission is hereby granted, free of charge, to any person obtaining a copy
00017 // of this software and associated documentation files (the "Software"), to
00018 // deal with the Software without restriction, including without limitation
00019 // the rights to use, copy, modify, merge, publish, distribute, sublicense,
00020 // and/or sell copies of the Software, and to permit persons to whom the
00021 // Software is furnished to do so, subject to the following conditions:
00022 //
00023 // - Redistributions of source code must retain the above copyright notice,
00024 // this list of conditions and the following disclaimers.
00025 // - Redistributions in binary form must reproduce the above copyright
00026 // notice, this list of conditions and the following disclaimers in
00027 // the documentation and/or other materials provided with the distribution.
00028 // - Neither the names of Advanced Micro Devices, Inc,
00029 // nor the names of its contributors may be used to endorse or promote
00030 // products derived from this Software without specific prior written
00031 // permission.
00032 //
00033 // THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR
00034 // IMPLIED, INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY,
00035 // FITNESS FOR A PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL
00036 // THE CONTRIBUTORS OR COPYRIGHT HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR
00037 // OTHER LIABILITY, WHETHER IN AN ACTION OF CONTRACT, TORT OR OTHERWISE,
00038 // ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER
00039 // DEALINGS WITH THE SOFTWARE.
00040 //
00042
00043 // HSA AMD extension.
00044
00045 #ifndef HSA_RUNTIME_EXT_AMD_H_
00046 #define HSA_RUNTIME_EXT_AMD_H_
00047
00048 #include "hsa.h"
00049 #include "hsa_ext_image.h"
00050
00051 #define HSA_AMD_INTERFACE_VERSION_MAJOR 1
00052 #define HSA_AMD_INTERFACE_VERSION_MINOR 0
00053

```

```

00054 #ifdef __cplusplus
00055 extern "C" {
00056 #endif
00057
00065 typedef uint32_t hsa_signal_condition32_t;
00066
00070 typedef enum {
00076 HSA_AMD_PACKET_TYPE_BARRIER_VALUE = 2,
00077 } hsa_amd_packet_type_t;
00078
00082 typedef uint8_t hsa_amd_packet_type8_t;
00083
00087 typedef struct hsa_amd_packet_header_s {
00092 uint16_t header;
00093
00097 hsa_amd_packet_type8_t AmdFormat;
00098
00102 uint8_t reserved;
00103 } hsa_amd_vendor_packet_header_t;
00104
00110 typedef struct hsa_amd_barrier_value_packet_s {
00114 hsa_amd_vendor_packet_header_t header;
00115
00119 uint32_t reserved0;
00120
00126 hsa_signal_t signal;
00127
00131 hsa_signal_value_t value;
00132
00136 hsa_signal_value_t mask;
00137
00141 hsa_signal_condition32_t cond;
00142
00146 uint32_t reserved1;
00147
00151 uint64_t reserved2;
00152
00156 uint64_t reserved3;
00157
00162 hsa_signal_t completion_signal;
00163 } hsa_amd_barrier_value_packet_t;
00164
00172 enum {
00176 HSA_STATUS_ERROR_INVALID_MEMORY_POOL = 40,
00177
00181 HSA_STATUS_ERROR_MEMORY_APERTURE_VIOLATION = 41,
00182
00186 HSA_STATUS_ERROR_ILLEGAL_INSTRUCTION = 42,
00187
00193 HSA_STATUS_ERROR_MEMORY_FAULT = 43,
00194
00200 HSA_STATUS_CU_MASK_REDUCE = 44,
00201 };
00202
00206 typedef enum hsa_amd_agent_info_s {
00210 HSA_AMD_AGENT_INFO_CHIP_ID = 0xA000,
00214 HSA_AMD_AGENT_INFO_CACHELINE_SIZE = 0xA001,
00219 HSA_AMD_AGENT_INFO_COMPUTE_UNIT_COUNT = 0xA002,
00224 HSA_AMD_AGENT_INFO_MAX_CLOCK_FREQUENCY = 0xA003,
00228 HSA_AMD_AGENT_INFO_DRIVER_NODE_ID = 0xA004,
00234 HSA_AMD_AGENT_INFO_MAX_ADDRESS_WATCH_POINTS = 0xA005,
00239 HSA_AMD_AGENT_INFO_BDFID = 0xA006,
00244 HSA_AMD_AGENT_INFO_MEMORY_WIDTH = 0xA007,
00248 HSA_AMD_AGENT_INFO_MEMORY_MAX_FREQUENCY = 0xA008,
00253 HSA_AMD_AGENT_INFO_PRODUCT_NAME = 0xA009,
00258 HSA_AMD_AGENT_INFO_MAX_WAVES_PER_CU = 0xA00A,
00263 HSA_AMD_AGENT_INFO_NUM_SIMDS_PER_CU = 0xA00B,
00268 HSA_AMD_AGENT_INFO_NUM_SHADER_ENGINES = 0xA00C,
00273 HSA_AMD_AGENT_INFO_NUM_SHADER_ARRAYS_PER_SE = 0xA00D,
00278 HSA_AMD_AGENT_INFO_HDP_FLUSH = 0xA00E,
00283 HSA_AMD_AGENT_INFO_DOMAIN = 0xA00F,
00288 HSA_AMD_AGENT_INFO_COOPERATIVE_QUEUES = 0xA010,
00293 HSA_AMD_AGENT_INFO_UUID = 0xA011,
00298 HSA_AMD_AGENT_INFO_ASIC_REVISION = 0xA012,
00303 HSA_AMD_AGENT_INFO_SVM_DIRECT_HOST_ACCESS = 0xA013,
00308 HSA_AMD_AGENT_INFO_COOPERATIVE_COMPUTE_UNIT_COUNT = 0xA014,
00313 HSA_AMD_AGENT_INFO_MEMORY_AVAIL = 0xA015,
00318 HSA_AMD_AGENT_INFO_TIMESTAMP_FREQUENCY = 0xA016,
00323 HSA_AMD_AGENT_INFO_ASIC_FAMILY_ID = 0xA017,
00328
00339 } hsa_amd_agent_info_t;
00340
00342 typedef struct hsa_amd_hdp_flush_s {
00343 uint32_t* HDP_MEM_FLUSH_CNTL;
00344 uint32_t* HDP_REG_FLUSH_CNTL;
00345 } hsa_amd_hdp_flush_t;
00346

```

```

00350 typedef enum hsa_amd_region_info_s {
00355 HSA_AMD_REGION_INFO_HOST_ACCESSIBLE = 0xA000,
00359 HSA_AMD_REGION_INFO_BASE = 0xA001,
00364 HSA_AMD_REGION_INFO_BUS_WIDTH = 0xA002,
00369 HSA_AMD_REGION_INFO_MAX_CLOCK_FREQUENCY = 0xA003
00370 } hsa_amd_region_info_t;
00371
00375 typedef enum hsa_amd_coherency_type_s {
00379 HSA_AMD_COHERENCY_TYPE_COHERENT = 0,
00383 HSA_AMD_COHERENCY_TYPE_NONCOHERENT = 1
00384 } hsa_amd_coherency_type_t;
00385
00403 hsa_status_t HSA_API hsa_amd_coherency_get_type(hsa_agent_t agent,
00404 hsa_amd_coherency_type_t* type);
00405
00424 hsa_status_t HSA_API hsa_amd_coherency_set_type(hsa_agent_t agent,
00425 hsa_amd_coherency_type_t type);
00426
00433 typedef struct hsa_amd_profiling_dispatch_time_s {
00437 uint64_t start;
00441 uint64_t end;
00442 } hsa_amd_profiling_dispatch_time_t;
00443
00450 typedef struct hsa_amd_profiling_async_copy_time_s {
00454 uint64_t start;
00458 uint64_t end;
00459 } hsa_amd_profiling_async_copy_time_t;
00460
00477 hsa_status_t HSA_API
00478 hsa_amd_profiling_set_profiler_enabled(hsa_queue_t* queue, int enable);
00479
00500 hsa_status_t HSA_API
00501 hsa_amd_profiling_async_copy_enable(bool enable);
00502
00530 hsa_status_t HSA_API hsa_amd_profiling_get_dispatch_time(
00531 hsa_agent_t agent, hsa_signal_t signal,
00532 hsa_amd_profiling_dispatch_time_t* time);
00533
00555 hsa_status_t HSA_API hsa_amd_profiling_get_async_copy_time(
00556 hsa_signal_t signal, hsa_amd_profiling_async_copy_time_t* time);
00557
00579 hsa_status_t HSA_API
00580 hsa_amd_profiling_convert_tick_to_system_domain(hsa_agent_t agent,
00581 uint64_t agent_tick,
00582 uint64_t* system_tick);
00583
00587 typedef enum {
00592 HSA_AMD_SIGNAL_AMD_GPU_ONLY = 1,
00600 HSA_AMD_SIGNAL_IPC = 2,
00601 } hsa_amd_signal_attribute_t;
00602
00638 hsa_status_t HSA_API hsa_amd_signal_create(hsa_signal_value_t initial_value, uint32_t num_consumers,
00639 const hsa_agent_t* consumers, uint64_t attributes,
00640 hsa_signal_t* signal);
00641
00670 hsa_status_t hsa_amd_signal_value_pointer(hsa_signal_t signal,
00671 volatile hsa_signal_value_t** value_ptr);
00672
00697 typedef bool (*hsa_amd_signal_handler)(hsa_signal_value_t value, void* arg);
00698
00740 hsa_status_t HSA_API
00741 hsa_amd_signal_async_handler(hsa_signal_t signal,
00742 hsa_signal_condition_t cond,
00743 hsa_signal_value_t value,
00744 hsa_amd_signal_handler handler, void* arg);
00745
00770 hsa_status_t HSA_API
00771 hsa_amd_async_function(void (*callback)(void* arg), void* arg);
00772
00782 uint32_t HSA_API
00783 hsa_amd_signal_wait_any(uint32_t signal_count, hsa_signal_t* signals,
00784 hsa_signal_condition_t* conds,
00785 hsa_signal_value_t* values, uint64_t timeout_hint,
00786 hsa_wait_state_t wait_hint,
00787 hsa_signal_value_t* satisfying_value);
00788
00810 hsa_status_t HSA_API hsa_amd_image_get_info_max_dim(hsa_agent_t agent,
00811 hsa_agent_info_t attribute,
00812 void* value);
00813
00847 hsa_status_t HSA_API hsa_amd_queue_cu_set_mask(const hsa_queue_t* queue,
00848 uint32_t num_cu_mask_count,
00849 const uint32_t* cu_mask);
00850
00875 hsa_status_t HSA_API hsa_amd_queue_cu_get_mask(const hsa_queue_t* queue, uint32_t num_cu_mask_count,
00876 uint32_t* cu_mask);
00877

```

```

00881 typedef enum {
00885 HSA_AMD_SEGMENT_GLOBAL = 0,
00890 HSA_AMD_SEGMENT_READONLY = 1,
00894 HSA_AMD_SEGMENT_PRIVATE = 2,
00899 HSA_AMD_SEGMENT_GROUP = 3,
00900 } hsa_amd_segment_t;
00901
00923 typedef struct hsa_amd_memory_pool_s {
00927 uint64_t handle;
00928 } hsa_amd_memory_pool_t;
00929
00930 typedef enum hsa_amd_memory_pool_global_flag_s {
00936 HSA_AMD_MEMORY_POOL_GLOBAL_FLAG_KERNARG_INIT = 1,
00942 HSA_AMD_MEMORY_POOL_GLOBAL_FLAG_FINE_GRAINED = 2,
00946 HSA_AMD_MEMORY_POOL_GLOBAL_FLAG_COARSE_GRAINED = 4
00947 } hsa_amd_memory_pool_global_flag_t;
00948
00952 typedef enum {
00957 HSA_AMD_MEMORY_POOL_INFO_SEGMENT = 0,
00966 HSA_AMD_MEMORY_POOL_INFO_GLOBAL_FLAGS = 1,
00970 HSA_AMD_MEMORY_POOL_INFO_SIZE = 2,
00978 HSA_AMD_MEMORY_POOL_INFO_RUNTIME_ALLOC_ALLOWED = 5,
00987 HSA_AMD_MEMORY_POOL_INFO_RUNTIME_ALLOC_GRANULE = 6,
00994 HSA_AMD_MEMORY_POOL_INFO_RUNTIME_ALLOC_ALIGNMENT = 7,
01001 HSA_AMD_MEMORY_POOL_INFO_ACCESSIBLE_BY_ALL = 15,
01006 HSA_AMD_MEMORY_POOL_INFO_ALLOC_MAX_SIZE = 16,
01007 } hsa_amd_memory_pool_info_t;
01008
01013 typedef enum hsa_amd_memory_pool_flag_s {
01017 HSA_AMD_MEMORY_POOL_STANDARD_FLAG = 0,
01023 HSA_AMD_MEMORY_POOL_PCIE_FLAG = 1,
01024 } hsa_amd_memory_pool_flag_t;
01025
01041 hsa_status_t HSA_API
01042 hsa_amd_memory_pool_get_info(hsa_amd_memory_pool_t memory_pool,
01043 hsa_amd_memory_pool_info_t attribute,
01044 void* value);
01045
01079 hsa_status_t HSA_API hsa_amd_agent_iterate_memory_pools(
01080 hsa_agent_t agent,
01081 hsa_status_t (*callback)(hsa_amd_memory_pool_t memory_pool, void* data),
01082 void* data);
01083
01120 hsa_status_t HSA_API
01121 hsa_amd_memory_pool_allocate(hsa_amd_memory_pool_t memory_pool, size_t size,
01122 uint32_t flags, void** ptr);
01123
01137 hsa_status_t HSA_API hsa_amd_memory_pool_free(void* ptr);
01138
01197 hsa_status_t HSA_API
01198 hsa_amd_memory_async_copy(void* dst, hsa_agent_t dst_agent, const void* src,
01199 hsa_agent_t src_agent, size_t size,
01200 uint32_t num_dep_signals,
01201 const hsa_signal_t* dep_signals,
01202 hsa_signal_t completion_signal);
01203
01204 /*
01205 [Provisional API]
01206 Pitched memory descriptor.
01207 All elements must be 4 byte aligned. Pitch and slice are in bytes.
01208 */
01209 typedef struct hsa_pitched_ptr_s {
01210 void* base;
01211 size_t pitch;
01212 size_t slice;
01213 } hsa_pitched_ptr_t;
01214
01215 /*
01216 [Provisional API]
01217 Copy direction flag.
01218 */
01219 typedef enum {
01220 hsaHostToHost = 0,
01221 hsaHostToDevice = 1,
01222 hsaDeviceToHost = 2,
01223 hsaDeviceToDevice = 3
01224 } hsa_amd_copy_direction_t;
01225
01226 /*
01227 [Provisional API]
01228 SDMA 3D memory copy API. The same requirements must be met by src and dst as in
01229 hsa_amd_memory_async_copy.
01230 Both src and dst must be directly accessible to the copy_agent during the copy, src and dst rects
01231 must not overlap.
01232 CPU agents are not supported. API requires SDMA and will return an error if SDMA is not available.
01233 Offsets and range carry x in bytes, y and z in rows and layers.

```

```

01234 */
01235 hsa_status_t HSA_API hsa_amd_memory_async_copy_rect(
01236 const hsa_pitched_ptr_t* dst, const hsa_dim3_t* dst_offset, const hsa_pitched_ptr_t* src,
01237 const hsa_dim3_t* src_offset, const hsa_dim3_t* range, hsa_agent_t copy_agent,
01238 hsa_amd_copy_direction_t dir, uint32_t num_dep_signals, const hsa_signal_t* dep_signals,
01239 hsa_signal_t completion_signal);
01240
01241 typedef enum {
01242 HSA_AMD_MEMORY_POOL_ACCESS_NEVER_ALLOWED = 0,
01253 HSA_AMD_MEMORY_POOL_ACCESS_ALLOWED_BY_DEFAULT = 1,
01259 HSA_AMD_MEMORY_POOL_ACCESS_DISALLOWED_BY_DEFAULT = 2
01260 } hsa_amd_memory_pool_access_t;
01261
01262 typedef enum {
01269 HSA_AMD_LINK_INFO_TYPE_HYPERTRANSPORT = 0,
01270
01274 HSA_AMD_LINK_INFO_TYPE_QPI = 1,
01275
01279 HSA_AMD_LINK_INFO_TYPE_PCIE = 2,
01280
01284 HSA_AMD_LINK_INFO_TYPE_INFINIBAND = 3,
01285
01289 HSA_AMD_LINK_INFO_TYPE_XGMI = 4
01290 } hsa_amd_link_info_type_t;
01291
01292 typedef struct hsa_amd_memory_pool_link_info_s {
01301 uint32_t min_latency;
01302
01306 uint32_t max_latency;
01307
01311 uint32_t min_bandwidth;
01312
01316 uint32_t max_bandwidth;
01317
01321 bool atomic_support_32bit;
01322
01326 bool atomic_support_64bit;
01327
01331 bool coherent_support;
01332
01336 hsa_amd_link_info_type_t link_type;
01337
01341 uint32_t numa_distance;
01342 } hsa_amd_memory_pool_link_info_t;
01343
01347 typedef enum {
01368 HSA_AMD_AGENT_MEMORY_POOL_INFO_ACCESS = 0,
01369
01377 HSA_AMD_AGENT_MEMORY_POOL_INFO_NUM_LINK_HOPS = 1,
01378
01385 HSA_AMD_AGENT_MEMORY_POOL_INFO_LINK_INFO = 2
01386 } hsa_amd_agent_memory_pool_info_t;
01387
01388 hsa_status_t HSA_API hsa_amd_agent_memory_pool_get_info(
01407 hsa_agent_t agent, hsa_amd_memory_pool_t memory_pool,
01408 hsa_amd_agent_memory_pool_info_t attribute, void* value);
01409
01447 hsa_status_t HSA_API
01448 hsa_amd_agents_allow_access(uint32_t num_agents, const hsa_agent_t* agents,
01449 const uint32_t* flags, const void* ptr);
01450
01478 hsa_status_t HSA_API
01479 hsa_amd_memory_pool_can_migrate(hsa_amd_memory_pool_t src_memory_pool,
01480 hsa_amd_memory_pool_t dst_memory_pool,
01481 bool* result);
01482
01519 hsa_status_t HSA_API hsa_amd_memory_migrate(const void* ptr,
01520 hsa_amd_memory_pool_t memory_pool,
01521 uint32_t flags);
01522
01557 hsa_status_t HSA_API hsa_amd_memory_lock(void* host_ptr, size_t size,
01558 hsa_agent_t* agents, int num_agent,
01559 void** agent_ptr);
01560
01606 hsa_status_t HSA_API hsa_amd_memory_lock_to_pool(void* host_ptr, size_t size, hsa_agent_t* agents,
01607 int num_agent, hsa_amd_memory_pool_t pool,
01608 uint32_t flags, void** agent_ptr);
01609
01626 hsa_status_t HSA_API hsa_amd_memory_unlock(void* host_ptr);
01627
01650 hsa_status_t HSA_API
01651 hsa_amd_memory_fill(void* ptr, uint32_t value, size_t count);
01652
01689 hsa_status_t HSA_API hsa_amd_interop_map_buffer(uint32_t num_agents,
01690 hsa_agent_t* agents,

```

```

01691 int interop_handle,
01692 uint32_t flags,
01693 size_t* size,
01694 void** ptr,
01695 size_t* metadata_size,
01696 const void** metadata);
01697
01702 hsa_status_t HSA_API hsa_amd_interop_unmap_buffer(void* ptr);
01703
01709 typedef struct hsa_amd_image_descriptor_s {
01710 /*
01711 Version number of the descriptor
01712 */
01713 uint32_t version;
01714
01715 /*
01716 Vendor and device PCI IDs for the format as VENDOR_ID<16|DEVICE_ID.
01717 */
01718 uint32_t deviceID;
01719
01720 /*
01721 Start of vendor specific data.
01722 */
01723 uint32_t data[1];
01724 } hsa_amd_image_descriptor_t;
01725
01751 hsa_status_t HSA_API hsa_amd_image_create(
01752 hsa_agent_t agent,
01753 const hsa_ext_image_descriptor_t *image_descriptor,
01754 const hsa_amd_image_descriptor_t *image_layout,
01755 const void *image_data,
01756 hsa_access_permission_t access_permission,
01757 hsa_ext_image_t *image
01758);
01759
01763 typedef enum {
01764 /*
01765 Memory is not known to the HSA driver. Unallocated or unlocked system memory.
01766 */
01767 HSA_EXT_POINTER_TYPE_UNKNOWN = 0,
01768 /*
01769 Memory was allocated with an HSA memory allocator.
01770 */
01771 HSA_EXT_POINTER_TYPE_HSA = 1,
01772 /*
01773 System memory which has been locked for use with an HSA agent.
01774
01775 Memory of this type is normal malloc'd memory and is always accessible to
01776 the CPU. Pointer info queries may not include CPU agents in the accessible
01777 agents list as the CPU has implicit access.
01778 */
01779 HSA_EXT_POINTER_TYPE_LOCKED = 2,
01780 /*
01781 Memory originated in a graphics component and is shared with ROCr.
01782 */
01783 HSA_EXT_POINTER_TYPE_GRAPHICS = 3,
01784 /*
01785 Memory has been shared with the local process via ROCr IPC APIs.
01786 */
01787 HSA_EXT_POINTER_TYPE_IPC = 4
01788 } hsa_amd_pointer_type_t;
01789
01794 typedef struct hsa_amd_pointer_info_s {
01795 /*
01796 Size in bytes of this structure. Used for version control within a major ROCr
01797 revision. Set to sizeof(hsa_amd_pointer_t) prior to calling
01798 hsa_amd_pointer_info. If the runtime supports an older version of pointer
01799 info then size will be smaller on return. Members starting after the return
01800 value of size will not be updated by hsa_amd_pointer_info.
01801 */
01802 uint32_t size;
01803 /*
01804 The type of allocation referenced.
01805 */
01806 hsa_amd_pointer_type_t type;
01807 /*
01808 Base address at which non-host agents may access the allocation.
01809 */
01810 void* agentBaseAddress;
01811 /*
01812 Base address at which the host agent may access the allocation.
01813 */
01814 void* hostBaseAddress;
01815 /*
01816 Size of the allocation
01817 */
01818 size_t sizeInBytes;

```

```

01819 /*
01820 Application provided value.
01821 */
01822 void* userData;
01823 /*
01824 Reports an agent which "owns" (ie has preferred access to) the pool in which the allocation was
01825 made. When multiple agents share equal access to a pool (ex: multiple CPU agents, or multi-die
01826 GPU boards) any such agent may be returned.
01827 */
01828 hsa_agent_t agentOwner;
01829 /*
01830 Contains a bitfield of hsa_amd_memory_pool_global_flag_t values.
01831 Reports the effective global flags bitmask for the allocation. This field is not meaningful if
01832 the type of the allocation is HSA_EXT_POINTER_TYPE_UNKNOWN.
01833 */
01834 uint32_t global_flags;
01835 } hsa_amd_pointer_info_t;
01836
01870 hsa_status_t HSA_API hsa_amd_pointer_info(const void* ptr,
01871 hsa_amd_pointer_info_t* info,
01872 void* (*alloc)(size_t),
01873 uint32_t* num_agents_accessible,
01874 hsa_agent_t** accessible);
01875
01894 hsa_status_t HSA_API hsa_amd_pointer_info_set_userdata(const void* ptr,
01895 void* userdata);
01896
01901 typedef struct hsa_amd_ipc_memory_s {
01902 uint32_t handle[8];
01903 } hsa_amd_ipc_memory_t;
01904
01934 hsa_status_t HSA_API hsa_amd_ipc_memory_create(void* ptr, size_t len,
01935 hsa_amd_ipc_memory_t* handle);
01936
01968 hsa_status_t HSA_API hsa_amd_ipc_memory_attach(
01969 const hsa_amd_ipc_memory_t* handle, size_t len,
01970 uint32_t num_agents,
01971 const hsa_agent_t* mapping_agents,
01972 void** mapped_ptr);
01973
01989 hsa_status_t HSA_API hsa_amd_ipc_memory_detach(void* mapped_ptr);
01990
01994 typedef hsa_amd_ipc_memory_t hsa_amd_ipc_signal_t;
01995
02020 hsa_status_t HSA_API hsa_amd_ipc_signal_create(hsa_signal_t signal, hsa_amd_ipc_signal_t* handle);
02021
02042 hsa_status_t HSA_API hsa_amd_ipc_signal_attach(const hsa_amd_ipc_signal_t* handle,
02043 hsa_signal_t* signal);
02044
02048 typedef enum hsa_amd_event_type_s {
02049 /*
02050 AMD GPU memory fault.
02051 */
02052 HSA_AMD_GPU_MEMORY_FAULT_EVENT = 0,
02053 } hsa_amd_event_type_t;
02054
02058 typedef enum {
02059 // Page not present or supervisor privilege.
02060 HSA_AMD_MEMORY_FAULT_PAGE_NOT_PRESENT = 1 << 0,
02061 // Write access to a read-only page.
02062 HSA_AMD_MEMORY_FAULT_READ_ONLY = 1 << 1,
02063 // Execute access to a page marked NX.
02064 HSA_AMD_MEMORY_FAULT_NX = 1 << 2,
02065 // GPU attempted access to a host only page.
02066 HSA_AMD_MEMORY_FAULT_HOST_ONLY = 1 << 3,
02067 // DRAM ECC failure.
02068 HSA_AMD_MEMORY_FAULT_DRAECC = 1 << 4,
02069 // Can't determine the exact fault address.
02070 HSA_AMD_MEMORY_FAULT_IMPRECISE = 1 << 5,
02071 // SRAM ECC failure (ie registers, no fault address).
02072 HSA_AMD_MEMORY_FAULT_SRAECC = 1 << 6,
02073 // GPU reset following unspecified hang.
02074 HSA_AMD_MEMORY_FAULT_HANG = 1 << 31
02075 } hsa_amd_memory_fault_reason_t;
02076
02080 typedef struct hsa_amd_gpu_memory_fault_info_s {
02081 /*
02082 The agent where the memory fault occurred.
02083 */
02084 hsa_agent_t agent;
02085 /*
02086 Virtual address accessed.
02087 */
02088 uint64_t virtual_address;
02089 /*
02090 Bit field encoding the memory access failure reasons. There could be multiple bits set
02091 for one fault. Bits are defined in hsa_amd_memory_fault_reason_t.

```



```

02092 */
02093 uint32_t fault_reason_mask;
02094 } hsa_amd_gpu_memory_fault_info_t;
02095
02099 typedef struct hsa_amd_event_s {
02100 /*
02101 The event type.
02102 */
02103 hsa_amd_event_type_t event_type;
02104 union {
02105 /*
02106 The memory fault info, only valid when @p event_type is HSA_AMD_GPU_MEMORY_FAULT_EVENT.
02107 */
02108 hsa_amd_gpu_memory_fault_info_t memory_fault;
02109 };
02110 } hsa_amd_event_t;
02111
02112 typedef hsa_status_t (*hsa_amd_system_event_callback_t)(const hsa_amd_event_t* event, void* data);
02113
02129 hsa_status_t HSA_API hsa_amd_register_system_event_handler(hsa_amd_system_event_callback_t callback,
02130 void* data);
02131
02135 typedef enum hsa_amd_queue_priority_s {
02136 /*
02137 Below normal/high priority compute and all graphics
02138 */
02139 HSA_AMD_QUEUE_PRIORITY_LOW = 0,
02140 /*
02141 Above low priority compute, below high priority compute and all graphics
02142 */
02143 HSA_AMD_QUEUE_PRIORITY_NORMAL = 1,
02144 /*
02145 Above low/normal priority compute and all graphics
02146 */
02147 HSA_AMD_QUEUE_PRIORITY_HIGH = 2,
02148 } hsa_amd_queue_priority_t;
02149
02166 hsa_status_t HSA_API hsa_amd_queue_set_priority(hsa_queue_t* queue,
02167 hsa_amd_queue_priority_t priority);
02168
02172 typedef void (*hsa_amd_deallocation_callback_t)(void* ptr, void* user_data);
02173
02206 hsa_status_t HSA_API hsa_amd_register_deallocation_callback(void* ptr,
02207 hsa_amd_deallocation_callback_t callback,
02208 void* user_data);
02209
02226 hsa_status_t HSA_API hsa_amd_deregister_deallocation_callback(void* ptr,
02227 hsa_amd_deallocation_callback_t callback);
02228
02229 typedef enum hsa_amd_svm_model_s {
02234 HSA_AMD_SVM_GLOBAL_FLAG_FINE_GRAINED = 0,
02239 HSA_AMD_SVM_GLOBAL_FLAG_COARSE_GRAINED = 1,
02249 HSA_AMD_SVM_GLOBAL_FLAG_INDETERMINATE = 2
02250 } hsa_amd_svm_model_t;
02251
02252 typedef enum hsa_amd_svm_attribute_s {
02253 // Memory model attribute.
02254 // Type of this attribute is hsa_amd_svm_model_t.
02255 HSA_AMD_SVM_ATTRIB_GLOBAL_FLAG = 0,
02256 // Marks the range read only. This allows multiple physical copies to be
02257 // placed local to each accessing device.
02258 // Type of this attribute is bool.
02259 HSA_AMD_SVM_ATTRIB_READ_ONLY = 1,
02260 // Automatic migrations should attempt to keep the memory within the xgmi hive
02261 // containing accessible agents.
02262 // Type of this attribute is bool.
02263 HSA_AMD_SVM_ATTRIB_HIVE_LOCAL = 2,
02264 // Page granularity to migrate at once. Page granularity is specified as
02265 // log2(page_count).
02266 // Type of this attribute is uint64_t.
02267 HSA_AMD_SVM_ATTRIB_MIGRATION_GRANULARITY = 3,
02268 // Physical location to prefer when automatic migration occurs.
02269 // Set to the null agent handle (handle == 0) to indicate there
02270 // is no preferred location.
02271 // Type of this attribute is hsa_agent_t.
02272 HSA_AMD_SVM_ATTRIB_PREFERRED_LOCATION = 4,
02273 // This attribute can not be used in ::hsa_amd_svm_attributes_set (see
02274 // ::hsa_amd_svm_prefetch_async).
02275 // Queries the physical location of most recent prefetch command.
02276 // If the prefetch location has not been set or is not uniform across the
02277 // address range then returned hsa_agent_t::handle will be 0.
02278 // Querying this attribute will return the destination agent of the most
02279 // recent ::hsa_amd_svm_prefetch_async targeting the address range. If
02280 // multiple async prefetches have been issued targeting the region and the
02281 // most recently issued prefetch has completed then the query will return
02282 // the location of the most recently completed prefetch.
02283 // Type of this attribute is hsa_agent_t.

```

```

02284 HSA_AMD_SVM_ATTRIB_PREFETCH_LOCATION = 5,
02285 // Optimizes with the anticipation that the majority of operations to the
02286 // range will be read operations.
02287 // Type of this attribute is bool.
02288 HSA_AMD_SVM_ATTRIB_READ_MOSTLY = 6,
02289 // Allows the execution on GPU.
02290 // Type of this attribute is bool.
02291 HSA_AMD_SVM_ATTRIB_GPU_EXEC = 7,
02292 // This attribute can not be used in ::hsa_amd_svm_attributes_get.
02293 // Enables an agent for access to the range. Access may incur a page fault
02294 // and associated memory migration. Either this or
02295 // HSA_AMD_SVM_ATTRIB_AGENT_ACCESSIBLE_IN_PLACE is required prior to SVM
02296 // access if HSA_AMD_SYSTEM_INFO_SVM_ACCESSIBLE_BY_DEFAULT is false.
02297 // Type of this attribute is hsa_agent_t.
02298 HSA_AMD_SVM_ATTRIB_AGENT_ACCESSIBLE = 0x200,
02299 // This attribute can not be used in ::hsa_amd_svm_attributes_get.
02300 // Enables an agent for access to the range without page faults. Access
02301 // will not incur a page fault and will not cause access based migration.
02302 // and associated memory migration. Either this or
02303 // HSA_AMD_SVM_ATTRIB_AGENT_ACCESSIBLE is required prior to SVM access if
02304 // HSA_AMD_SYSTEM_INFO_SVM_ACCESSIBLE_BY_DEFAULT is false.
02305 // Type of this attribute is hsa_agent_t.
02306 HSA_AMD_SVM_ATTRIB_AGENT_ACCESSIBLE_IN_PLACE = 0x201,
02307 // This attribute can not be used in ::hsa_amd_svm_attributes_get.
02308 // Denies an agent access to the memory range. Access will cause a terminal
02309 // segfault.
02310 // Type of this attribute is hsa_agent_t.
02311 HSA_AMD_SVM_ATTRIB_AGENT_NO_ACCESS = 0x202,
02312 // This attribute can not be used in ::hsa_amd_svm_attributes_set.
02313 // Returns the access attribute associated with the agent.
02314 // The agent to query must be set in the attribute value field.
02315 // The attribute enum will be replaced with the agent's current access
02316 // attribute for the address range.
02317 // TODO: Clarify KFD return value for non-uniform access attribute.
02318 // Type of this attribute is hsa_agent_t.
02319 HSA_AMD_SVM_ATTRIB_ACCESS_QUERY = 0x203,
02320 } hsa_amd_svm_attribute_t;
02321
02322 // List type for hsa_amd_svm_attributes_set/get.
02323 typedef struct hsa_amd_svm_attribute_pair_s {
02324 // hsa_amd_svm_attribute_t value.
02325 uint64_t attribute;
02326 // Attribute value. Bit values should be interpreted according to the type
02327 // given in the associated attribute description.
02328 uint64_t value;
02329 } hsa_amd_svm_attribute_pair_t;
02330
02331 hsa_status_t hsa_amd_svm_attributes_set(void* ptr, size_t size,
02332 hsa_amd_svm_attribute_pair_t* attribute_list,
02333 size_t attribute_count);
02334
02335 hsa_status_t hsa_amd_svm_attributes_get(void* ptr, size_t size,
02336 hsa_amd_svm_attribute_pair_t* attribute_list,
02337 size_t attribute_count);
02338
02339 hsa_status_t hsa_amd_svm_prefetch_async(void* ptr, size_t size, hsa_agent_t agent,
02340 uint32_t num_dep_signals, const hsa_signal_t* dep_signals,
02341 hsa_signal_t completion_signal);
02342
02343 #ifdef __cplusplus
02344 } // end extern "C" block
02345 #endif
02346 // header guard

```

## 7.10 hsa\_ext\_finalize.h

```

00001
00002 //
00003 // The University of Illinois/NCSA
00004 // Open Source License (NCSA)
00005 //
00006 // Copyright (c) 2014-2020, Advanced Micro Devices, Inc. All rights reserved.
00007 //
00008 // Developed by:
00009 //
00010 // AMD Research and AMD HSA Software Development
00011 //
00012 // Advanced Micro Devices, Inc.
00013 //
00014 // www.amd.com
00015 //
00016 // Permission is hereby granted, free of charge, to any person obtaining a copy

```

```

00017 // of this software and associated documentation files (the "Software"), to
00018 // deal with the Software without restriction, including without limitation
00019 // the rights to use, copy, modify, merge, publish, distribute, sublicense,
00020 // and/or sell copies of the Software, and to permit persons to whom the
00021 // Software is furnished to do so, subject to the following conditions:
00022 //
00023 // - Redistributions of source code must retain the above copyright notice,
00024 // this list of conditions and the following disclaimers.
00025 // - Redistributions in binary form must reproduce the above copyright
00026 // notice, this list of conditions and the following disclaimers in
00027 // the documentation and/or other materials provided with the distribution.
00028 // - Neither the names of Advanced Micro Devices, Inc,
00029 // nor the names of its contributors may be used to endorse or promote
00030 // products derived from this Software without specific prior written
00031 // permission.
00032 //
00033 // THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR
00034 // IMPLIED, INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY,
00035 // FITNESS FOR A PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL
00036 // THE CONTRIBUTORS OR COPYRIGHT HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR
00037 // OTHER LIABILITY, WHETHER IN AN ACTION OF CONTRACT, TORT OR OTHERWISE,
00038 // ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER
00039 // DEALINGS WITH THE SOFTWARE.
00040 //
00042
00043 #ifndef HSA_RUNTIME_INC_HSA_EXT_FINALIZE_H_
00044 #define HSA_RUNTIME_INC_HSA_EXT_FINALIZE_H_
00045
00046 #include "hsa.h"
00047
00048 #undef HSA_API
00049 #ifdef HSA_EXPORT_FINALIZER
00050 #define HSA_API HSA_API_EXPORT
00051 #else
00052 #define HSA_API HSA_API_IMPORT
00053 #endif
00054
00055 #ifdef __cplusplus
00056 extern "C" {
00057 #endif // __cplusplus
00058
00059 struct BrigModuleHeader;
00060 typedef struct BrigModuleHeader* BrigModule_t;
00061
00062 enum {
00073 HSA_EXT_STATUS_ERROR_INVALID_PROGRAM = 0x2000,
00074 HSA_EXT_STATUS_ERROR_INVALID_MODULE = 0x2001,
00075 HSA_EXT_STATUS_ERROR_INCOMPATIBLE_MODULE = 0x2002,
00076 HSA_EXT_STATUS_ERROR_MODULE_ALREADY_INCLUDED = 0x2003,
00077 HSA_EXT_STATUS_ERROR_SYMBOL_MISMATCH = 0x2004,
00078 HSA_EXT_STATUS_ERROR_FINALIZATION_FAILED = 0x2005,
00079 HSA_EXT_STATUS_ERROR_DIRECTIVE_MISMATCH = 0x2006
00080 };
00081
00082 typedef BrigModule_t hsa_ext_module_t;
00083
00084 typedef struct hsa_ext_program_s {
00085 uint64_t handle;
00086 } hsa_ext_program_t;
00087
00088 hsa_status_t HSA_API hsa_ext_program_create(
00089 hsa_machine_model_t machine_model,
00090 hsa_profile_t profile,
00091 hsa_default_float_rounding_mode_t default_float_rounding_mode,
00092 const char *options,
00093 hsa_ext_program_t *program);
00094
00095 hsa_status_t HSA_API hsa_ext_program_destroy(
00096 hsa_ext_program_t program);
00097
00098 hsa_status_t HSA_API hsa_ext_program_add_module(
00099 hsa_ext_program_t program,
00100 hsa_ext_module_t module);
00101
00102 hsa_status_t HSA_API hsa_ext_program_iterate_modules(
00103 hsa_ext_program_t program,
00104 hsa_status_t (*callback)(hsa_ext_program_t program, hsa_ext_module_t module,
00105 void* data),
00106 void* data);
00107
00108 typedef enum {
00109 HSA_EXT_PROGRAM_INFO_MACHINE_MODEL = 0,
00110 HSA_EXT_PROGRAM_INFO_PROFILE = 1,
00111 HSA_EXT_PROGRAM_INFO_DEFAULT_FLOAT_ROUNDING_MODE = 2
00112 } hsa_ext_program_info_t;
00113
00114 hsa_status_t HSA_API hsa_ext_program_get_info(

```

```

00304 hsa_ext_program_t program,
00305 hsa_ext_program_info_t attribute,
00306 void *value);
00307
00311 typedef enum {
00315 HSA_EXT_FINALIZER_CALL_CONVENTION_AUTO = -1
00316 } hsa_ext_finalizer_call_convention_t;
00317
00322 typedef struct hsa_ext_control_directives_s {
00333 uint64_t control_directives_mask;
00340 uint16_t break_exceptions_mask;
00347 uint16_t detect_exceptions_mask;
00353 uint32_t max_dynamic_group_size;
00366 uint64_t max_flat_grid_size;
00380 uint32_t max_flat_workgroup_size;
00384 uint32_t reserved1;
00398 uint64_t required_grid_size[3];
00412 hsa_dim3_t required_workgroup_size;
00425 uint8_t required_dim;
00429 uint8_t reserved2[75];
00430 } hsa_ext_control_directives_t;
00431
00487 hsa_status_t HSA_API hsa_ext_program_finalize(
00488 hsa_ext_program_t program,
00489 hsa_isa_t isa,
00490 int32_t call_convention,
00491 hsa_ext_control_directives_t control_directives,
00492 const char *options,
00493 hsa_code_object_type_t code_object_type,
00494 hsa_code_object_t *code_object);
00495
00498 #define hsa_ext_finalizer_l_00
00499
00500 typedef struct hsa_ext_finalizer_l_00_pfn_s {
00501 hsa_status_t (*hsa_ext_program_create)(
00502 hsa_machine_model_t machine_model, hsa_profile_t profile,
00503 hsa_default_float_rounding_mode_t default_float_rounding_mode,
00504 const char *options, hsa_ext_program_t *program);
00505
00506 hsa_status_t (*hsa_ext_program_destroy)(hsa_ext_program_t program);
00507
00508 hsa_status_t (*hsa_ext_program_add_module)(hsa_ext_program_t program,
00509 hsa_ext_module_t module);
00510
00511 hsa_status_t (*hsa_ext_program_iterate_modules)(
00512 hsa_ext_program_t program,
00513 hsa_status_t (*callback)(hsa_ext_program_t program,
00514 hsa_ext_module_t module, void *data),
00515 void *data);
00516
00517 hsa_status_t (*hsa_ext_program_get_info)(
00518 hsa_ext_program_t program, hsa_ext_program_info_t attribute,
00519 void *value);
00520
00521 hsa_status_t (*hsa_ext_program_finalize)(
00522 hsa_ext_program_t program, hsa_isa_t isa, int32_t call_convention,
00523 hsa_ext_control_directives_t control_directives, const char *options,
00524 hsa_code_object_type_t code_object_type, hsa_code_object_t *code_object);
00525 } hsa_ext_finalizer_l_00_pfn_t;
00526
00527 #ifdef __cplusplus
00528 } // extern "C" block
00529 #endif // __cplusplus
00530
00531 #endif // HSA_RUNTIME_INC_HSA_EXT_FINALIZE_H_

```

## 7.11 hsa\_ext\_image.h

```

00001
00002 //
00003 // The University of Illinois/NCSA
00004 // Open Source License (NCSA)
00005 //
00006 // Copyright (c) 2014-2020, Advanced Micro Devices, Inc. All rights reserved.
00007 //
00008 // Developed by:
00009 //
00010 // AMD Research and AMD HSA Software Development
00011 //
00012 // Advanced Micro Devices, Inc.
00013 //
00014 // www.amd.com
00015 //

```

```

00016 // Permission is hereby granted, free of charge, to any person obtaining a copy
00017 // of this software and associated documentation files (the "Software"), to
00018 // deal with the Software without restriction, including without limitation
00019 // the rights to use, copy, modify, merge, publish, distribute, sublicense,
00020 // and/or sell copies of the Software, and to permit persons to whom the
00021 // Software is furnished to do so, subject to the following conditions:
00022 //
00023 // - Redistributions of source code must retain the above copyright notice,
00024 // this list of conditions and the following disclaimers.
00025 // - Redistributions in binary form must reproduce the above copyright
00026 // notice, this list of conditions and the following disclaimers in
00027 // the documentation and/or other materials provided with the distribution.
00028 // - Neither the names of Advanced Micro Devices, Inc,
00029 // nor the names of its contributors may be used to endorse or promote
00030 // products derived from this Software without specific prior written
00031 // permission.
00032 //
00033 // THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR
00034 // IMPLIED, INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY,
00035 // FITNESS FOR A PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL
00036 // THE CONTRIBUTORS OR COPYRIGHT HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR
00037 // OTHER LIABILITY, WHETHER IN AN ACTION OF CONTRACT, TORT OR OTHERWISE,
00038 // ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER
00039 // DEALINGS WITH THE SOFTWARE.
00040 //
00042
00043 #ifndef HSA_EXT_IMAGE_H
00044 #define HSA_EXT_IMAGE_H
00045
00046 #include "hsa.h"
00047
00048 #undef HSA_API
00049 #ifdef HSA_EXPORT_IMAGES
00050 #define HSA_API HSA_API_EXPORT
00051 #else
00052 #define HSA_API HSA_API_IMPORT
00053 #endif
00054
00055 #ifdef __cplusplus
00056 extern "C" {
00057 #endif /*__cplusplus*/
00058
00059 enum {
00060 HSA_EXT_STATUS_ERROR_IMAGE_FORMAT_UNSUPPORTED = 0x3000,
00061 HSA_EXT_STATUS_ERROR_IMAGE_SIZE_UNSUPPORTED = 0x3001,
00062 HSA_EXT_STATUS_ERROR_IMAGE_PITCH_UNSUPPORTED = 0x3002,
00063 HSA_EXT_STATUS_ERROR_SAMPLER_DESCRIPTOR_UNSUPPORTED = 0x3003
00064 };
00065
00066 enum {
00067 HSA_EXT_AGENT_INFO_IMAGE_1D_MAX_ELEMENTS = 0x3000,
00068 HSA_EXT_AGENT_INFO_IMAGE_1DA_MAX_ELEMENTS = 0x3001,
00069 HSA_EXT_AGENT_INFO_IMAGE_1DB_MAX_ELEMENTS = 0x3002,
00070 HSA_EXT_AGENT_INFO_IMAGE_2D_MAX_ELEMENTS = 0x3003,
00071 HSA_EXT_AGENT_INFO_IMAGE_2DA_MAX_ELEMENTS = 0x3004,
00072 HSA_EXT_AGENT_INFO_IMAGE_2DDEPTH_MAX_ELEMENTS = 0x3005,
00073 HSA_EXT_AGENT_INFO_IMAGE_2DADEPTH_MAX_ELEMENTS = 0x3006,
00074 HSA_EXT_AGENT_INFO_IMAGE_3D_MAX_ELEMENTS = 0x3007,
00075 HSA_EXT_AGENT_INFO_IMAGE_ARRAY_MAX_LAYERS = 0x3008,
00076 HSA_EXT_AGENT_INFO_MAX_IMAGE_RD_HANDLES = 0x3009,
00077 HSA_EXT_AGENT_INFO_MAX_IMAGE_ROW_HANDLES = 0x300A,
00078 HSA_EXT_AGENT_INFO_MAX_SAMPLER_HANDLERS = 0x300B,
00079 HSA_EXT_AGENT_INFO_IMAGE_LINEAR_ROW_PITCH_ALIGNMENT = 0x300C
00080 };
00081
00082 typedef struct hsa_ext_image_s {
00083 uint64_t handle;
00084 } hsa_ext_image_t;
00085
00086 typedef enum {
00087 HSA_EXT_IMAGE_GEOMETRY_1D = 0,
00088 HSA_EXT_IMAGE_GEOMETRY_2D = 1,
00089 HSA_EXT_IMAGE_GEOMETRY_3D = 2,
00090 HSA_EXT_IMAGE_GEOMETRY_1DA = 3,
00091 HSA_EXT_IMAGE_GEOMETRY_2DA = 4,
00092 HSA_EXT_IMAGE_GEOMETRY_1DB = 5,
00093 HSA_EXT_IMAGE_GEOMETRY_2DDEPTH = 6,
00094 HSA_EXT_IMAGE_GEOMETRY_2DADEPTH = 7
00095 } hsa_ext_image_geometry_t;

```

```

00239
00247 typedef enum {
00248 HSA_EXT_IMAGE_CHANNEL_TYPE_SNORM_INT8 = 0,
00249 HSA_EXT_IMAGE_CHANNEL_TYPE_SNORM_INT16 = 1,
00250 HSA_EXT_IMAGE_CHANNEL_TYPE_UNORM_INT8 = 2,
00251 HSA_EXT_IMAGE_CHANNEL_TYPE_UNORM_INT16 = 3,
00252 HSA_EXT_IMAGE_CHANNEL_TYPE_UNORM_INT24 = 4,
00253 HSA_EXT_IMAGE_CHANNEL_TYPE_UNORM_SHORT_555 = 5,
00254 HSA_EXT_IMAGE_CHANNEL_TYPE_UNORM_SHORT_565 = 6,
00255 HSA_EXT_IMAGE_CHANNEL_TYPE_UNORM_SHORT_101010 = 7,
00256 HSA_EXT_IMAGE_CHANNEL_TYPE_SIGNED_INT8 = 8,
00257 HSA_EXT_IMAGE_CHANNEL_TYPE_SIGNED_INT16 = 9,
00258 HSA_EXT_IMAGE_CHANNEL_TYPE_SIGNED_INT32 = 10,
00259 HSA_EXT_IMAGE_CHANNEL_TYPE_UNSIGNED_INT8 = 11,
00260 HSA_EXT_IMAGE_CHANNEL_TYPE_UNSIGNED_INT16 = 12,
00261 HSA_EXT_IMAGE_CHANNEL_TYPE_UNSIGNED_INT32 = 13,
00262 HSA_EXT_IMAGE_CHANNEL_TYPE_HALF_FLOAT = 14,
00263 HSA_EXT_IMAGE_CHANNEL_TYPE_FLOAT = 15
00264 } hsa_ext_image_channel_type_t;
00265
00269 typedef uint32_t hsa_ext_image_channel_type32_t;
00270
00279 typedef enum {
00280 HSA_EXT_IMAGE_CHANNEL_ORDER_A = 0,
00281 HSA_EXT_IMAGE_CHANNEL_ORDER_R = 1,
00282 HSA_EXT_IMAGE_CHANNEL_ORDER_RX = 2,
00283 HSA_EXT_IMAGE_CHANNEL_ORDER_RG = 3,
00284 HSA_EXT_IMAGE_CHANNEL_ORDER_RGX = 4,
00285 HSA_EXT_IMAGE_CHANNEL_ORDER_RA = 5,
00286 HSA_EXT_IMAGE_CHANNEL_ORDER_RGB = 6,
00287 HSA_EXT_IMAGE_CHANNEL_ORDER_RGBX = 7,
00288 HSA_EXT_IMAGE_CHANNEL_ORDER_RGBA = 8,
00289 HSA_EXT_IMAGE_CHANNEL_ORDER_BGRA = 9,
00290 HSA_EXT_IMAGE_CHANNEL_ORDER_ARGB = 10,
00291 HSA_EXT_IMAGE_CHANNEL_ORDER_ABGR = 11,
00292 HSA_EXT_IMAGE_CHANNEL_ORDER_SRGB = 12,
00293 HSA_EXT_IMAGE_CHANNEL_ORDER_SRGBX = 13,
00294 HSA_EXT_IMAGE_CHANNEL_ORDER_SRGBA = 14,
00295 HSA_EXT_IMAGE_CHANNEL_ORDER_SBGRA = 15,
00296 HSA_EXT_IMAGE_CHANNEL_ORDER_INTENSITY = 16,
00297 HSA_EXT_IMAGE_CHANNEL_ORDER_LUMINANCE = 17,
00298 HSA_EXT_IMAGE_CHANNEL_ORDER_DEPTH = 18,
00299 HSA_EXT_IMAGE_CHANNEL_ORDER_DEPTH_STENCIL = 19
00300 } hsa_ext_image_channel_order_t;
00301
00305 typedef uint32_t hsa_ext_image_channel_order32_t;
00306
00307
00311 typedef struct hsa_ext_image_format_s {
00315 hsa_ext_image_channel_type32_t channel_type;
00316
00320 hsa_ext_image_channel_order32_t channel_order;
00321 } hsa_ext_image_format_t;
00322
00326 typedef struct hsa_ext_image_descriptor_s {
00330 hsa_ext_image_geometry_t geometry;
00334 size_t width;
00341 size_t height;
00346 size_t depth;
00352 size_t array_size;
00356 hsa_ext_image_format_t format;
00357 } hsa_ext_image_descriptor_t;
00358
00362 typedef enum {
00367 HSA_EXT_IMAGE_CAPABILITY_NOT_SUPPORTED = 0x0,
00372 HSA_EXT_IMAGE_CAPABILITY_READ_ONLY = 0x1,
00377 HSA_EXT_IMAGE_CAPABILITY_WRITE_ONLY = 0x2,
00382 HSA_EXT_IMAGE_CAPABILITY_READ_WRITE = 0x4,
00387 HSA_EXT_IMAGE_CAPABILITY_READ_MODIFY_WRITE = 0x8,
00393 HSA_EXT_IMAGE_CAPABILITY_ACCESS_INVARIANT_DATA_LAYOUT = 0x10
00394 } hsa_ext_image_capability_t;
00395
00411 typedef enum {
00417 HSA_EXT_IMAGE_DATA_LAYOUT_OPAQUE = 0x0,
00434 HSA_EXT_IMAGE_DATA_LAYOUT_LINEAR = 0x1
00435 } hsa_ext_image_data_layout_t;
00436
00462 hsa_status_t HSA_API hsa_ext_image_get_capability(
00463 hsa_agent_t agent,
00464 hsa_ext_image_geometry_t geometry,
00465 const hsa_ext_image_format_t *image_format,
00466 uint32_t *capability_mask);
00467
00498 hsa_status_t HSA_API hsa_ext_image_get_capability_with_layout(
00499 hsa_agent_t agent,
00500 hsa_ext_image_geometry_t geometry,
00501 const hsa_ext_image_format_t *image_format,

```

```

00502 hsa_ext_image_data_layout_t image_data_layout,
00503 uint32_t *capability_mask);
00504
00509 typedef struct hsa_ext_image_data_info_s {
00513 size_t size;
00514
00518 size_t alignment;
00519
00520 } hsa_ext_image_data_info_t;
00521
00574 hsa_status_t HSA_API hsa_ext_image_data_get_info(
00575 hsa_agent_t agent,
00576 const hsa_ext_image_descriptor_t *image_descriptor,
00577 hsa_access_permission_t access_permission,
00578 hsa_ext_image_data_info_t *image_data_info);
00579
00657 hsa_status_t HSA_API hsa_ext_image_data_get_info_with_layout(
00658 hsa_agent_t agent,
00659 const hsa_ext_image_descriptor_t *image_descriptor,
00660 hsa_access_permission_t access_permission,
00661 hsa_ext_image_data_layout_t image_data_layout,
00662 size_t image_data_row_pitch,
00663 size_t image_data_slice_pitch,
00664 hsa_ext_image_data_info_t *image_data_info);
00665
00730 hsa_status_t HSA_API hsa_ext_image_create(
00731 hsa_agent_t agent,
00732 const hsa_ext_image_descriptor_t *image_descriptor,
00733 const void *image_data,
00734 hsa_access_permission_t access_permission,
00735 hsa_ext_image_t *image);
00736
00833 hsa_status_t HSA_API hsa_ext_image_create_with_layout(
00834 hsa_agent_t agent,
00835 const hsa_ext_image_descriptor_t *image_descriptor,
00836 const void *image_data,
00837 hsa_access_permission_t access_permission,
00838 hsa_ext_image_data_layout_t image_data_layout,
00839 size_t image_data_row_pitch,
00840 size_t image_data_slice_pitch,
00841 hsa_ext_image_t *image);
00842
00863 hsa_status_t HSA_API hsa_ext_image_destroy(
00864 hsa_agent_t agent,
00865 hsa_ext_image_t image);
00866
00916 hsa_status_t HSA_API hsa_ext_image_copy(
00917 hsa_agent_t agent,
00918 hsa_ext_image_t src_image,
00919 const hsa_dim3_t* src_offset,
00920 hsa_ext_image_t dst_image,
00921 const hsa_dim3_t* dst_offset,
00922 const hsa_dim3_t* range);
00923
00927 typedef struct hsa_ext_image_region_s {
00931 hsa_dim3_t offset;
00932
00937 hsa_dim3_t range;
00938 } hsa_ext_image_region_t;
00939
00989 hsa_status_t HSA_API hsa_ext_image_import(
00990 hsa_agent_t agent,
00991 const void *src_memory,
00992 size_t src_row_pitch,
00993 size_t src_slice_pitch,
00994 hsa_ext_image_t dst_image,
00995 const hsa_ext_image_region_t *image_region);
00996
01044 hsa_status_t HSA_API hsa_ext_image_export(
01045 hsa_agent_t agent,
01046 hsa_ext_image_t src_image,
01047 void *dst_memory,
01048 size_t dst_row_pitch,
01049 size_t dst_slice_pitch,
01050 const hsa_ext_image_region_t *image_region);
01051
01085 hsa_status_t HSA_API hsa_ext_image_clear(
01086 hsa_agent_t agent,
01087 hsa_ext_image_t image,
01088 const void* data,
01089 const hsa_ext_image_region_t *image_region);
01090
01096 typedef struct hsa_ext_sampler_s {
01101 uint64_t handle;
01102 } hsa_ext_sampler_t;
01103
01111 typedef enum {

```

```

01115 HSA_EXT_SAMPLER_ADDRESSING_MODE_UNDEFINED = 0,
01116
01120 HSA_EXT_SAMPLER_ADDRESSING_MODE_CLAMP_TO_EDGE = 1,
01121
01125 HSA_EXT_SAMPLER_ADDRESSING_MODE_CLAMP_TO_BORDER = 2,
01126
01131 HSA_EXT_SAMPLER_ADDRESSING_MODE_REPEAT = 3,
01132
01138 HSA_EXT_SAMPLER_ADDRESSING_MODE_MIRRORED_REPEAT = 4
01139 } hsa_ext_sampler_addressing_mode_t;
01141
01145 typedef uint32_t hsa_ext_sampler_addressing_mode32_t;
01146
01154 typedef enum {
01155
01159 HSA_EXT_SAMPLER_COORDINATE_MODE_UNNORMALIZED = 0,
01160
01165 HSA_EXT_SAMPLER_COORDINATE_MODE_NORMALIZED = 1
01166 } hsa_ext_sampler_coordinate_mode_t;
01172 typedef uint32_t hsa_ext_sampler_coordinate_mode32_t;
01173
01174
01181 typedef enum {
01186 HSA_EXT_SAMPLER_FILTER_MODE_NEAREST = 0,
01187
01193 HSA_EXT_SAMPLER_FILTER_MODE_LINEAR = 1
01194 } hsa_ext_sampler_filter_mode_t;
01196
01200 typedef uint32_t hsa_ext_sampler_filter_mode32_t;
01201
01205 typedef struct hsa_ext_sampler_descriptor_s {
01209 hsa_ext_sampler_coordinate_mode32_t coordinate_mode;
01210
01214 hsa_ext_sampler_filter_mode32_t filter_mode;
01215
01220 hsa_ext_sampler_addressing_mode32_t address_mode;
01221 } hsa_ext_sampler_descriptor_t;
01223
01253 hsa_status_t HSA_API hsa_ext_sampler_create(
01254 hsa_agent_t agent,
01255 const hsa_ext_sampler_descriptor_t *sampler_descriptor,
01256 hsa_ext_sampler_t *sampler);
01257
01276 hsa_status_t HSA_API hsa_ext_sampler_destroy(
01277 hsa_agent_t agent,
01278 hsa_ext_sampler_t sampler);
01279
01280
01281 #define hsa_ext_images_1_00
01282
01286 typedef struct hsa_ext_images_1_00_pfn_s {
01287
01288 hsa_status_t (*hsa_ext_image_get_capability)(
01289 hsa_agent_t agent,
01290 hsa_ext_image_geometry_t geometry,
01291 const hsa_ext_image_format_t *image_format,
01292 uint32_t *capability_mask);
01293
01294 hsa_status_t (*hsa_ext_image_data_get_info)(
01295 hsa_agent_t agent,
01296 const hsa_ext_image_descriptor_t *image_descriptor,
01297 hsa_access_permission_t access_permission,
01298 hsa_ext_image_data_info_t *image_data_info);
01299
01300 hsa_status_t (*hsa_ext_image_create)(
01301 hsa_agent_t agent,
01302 const hsa_ext_image_descriptor_t *image_descriptor,
01303 const void *image_data,
01304 hsa_access_permission_t access_permission,
01305 hsa_ext_image_t *image);
01306
01307 hsa_status_t (*hsa_ext_image_destroy)(
01308 hsa_agent_t agent,
01309 hsa_ext_image_t image);
01310
01311 hsa_status_t (*hsa_ext_image_copy)(
01312 hsa_agent_t agent,
01313 hsa_ext_image_t src_image,
01314 const hsa_dim3_t* src_offset,
01315 hsa_ext_image_t dst_image,
01316 const hsa_dim3_t* dst_offset,
01317 const hsa_dim3_t* range);

```



```

01318
01319 hsa_status_t (*hsa_ext_image_import) (
01320 hsa_agent_t agent,
01321 const void *src_memory,
01322 size_t src_row_pitch,
01323 size_t src_slice_pitch,
01324 hsa_ext_image_t dst_image,
01325 const hsa_ext_image_region_t *image_region);
01326
01327 hsa_status_t (*hsa_ext_image_export) (
01328 hsa_agent_t agent,
01329 hsa_ext_image_t src_image,
01330 void *dst_memory,
01331 size_t dst_row_pitch,
01332 size_t dst_slice_pitch,
01333 const hsa_ext_image_region_t *image_region);
01334
01335 hsa_status_t (*hsa_ext_image_clear) (
01336 hsa_agent_t agent,
01337 hsa_ext_image_t image,
01338 const void* data,
01339 const hsa_ext_image_region_t *image_region);
01340
01341 hsa_status_t (*hsa_ext_sampler_create) (
01342 hsa_agent_t agent,
01343 const hsa_ext_sampler_descriptor_t *sampler_descriptor,
01344 hsa_ext_sampler_t *sampler);
01345
01346 hsa_status_t (*hsa_ext_sampler_destroy) (
01347 hsa_agent_t agent,
01348 hsa_ext_sampler_t sampler);
01349
01350 } hsa_ext_images_1_00_pfn_t;
01351
01352 #define hsa_ext_images_1
01353
01357 typedef struct hsa_ext_images_1_pfn_s {
01358
01359 hsa_status_t (*hsa_ext_image_get_capability) (
01360 hsa_agent_t agent,
01361 hsa_ext_image_geometry_t geometry,
01362 const hsa_ext_image_format_t *image_format,
01363 uint32_t *capability_mask);
01364
01365 hsa_status_t (*hsa_ext_image_data_get_info) (
01366 hsa_agent_t agent,
01367 const hsa_ext_image_descriptor_t *image_descriptor,
01368 hsa_access_permission_t access_permission,
01369 hsa_ext_image_data_info_t *image_data_info);
01370
01371 hsa_status_t (*hsa_ext_image_create) (
01372 hsa_agent_t agent,
01373 const hsa_ext_image_descriptor_t *image_descriptor,
01374 const void *image_data,
01375 hsa_access_permission_t access_permission,
01376 hsa_ext_image_t *image);
01377
01378 hsa_status_t (*hsa_ext_image_destroy) (
01379 hsa_agent_t agent,
01380 hsa_ext_image_t image);
01381
01382 hsa_status_t (*hsa_ext_image_copy) (
01383 hsa_agent_t agent,
01384 hsa_ext_image_t src_image,
01385 const hsa_dim3_t* src_offset,
01386 hsa_ext_image_t dst_image,
01387 const hsa_dim3_t* dst_offset,
01388 const hsa_dim3_t* range);
01389
01390 hsa_status_t (*hsa_ext_image_import) (
01391 hsa_agent_t agent,
01392 const void *src_memory,
01393 size_t src_row_pitch,
01394 size_t src_slice_pitch,
01395 hsa_ext_image_t dst_image,
01396 const hsa_ext_image_region_t *image_region);
01397
01398 hsa_status_t (*hsa_ext_image_export) (
01399 hsa_agent_t agent,
01400 hsa_ext_image_t src_image,
01401 void *dst_memory,
01402 size_t dst_row_pitch,
01403 size_t dst_slice_pitch,
01404 const hsa_ext_image_region_t *image_region);
01405
01406 hsa_status_t (*hsa_ext_image_clear) (
01407 hsa_agent_t agent,

```

```

01408 hsa_ext_image_t image,
01409 const void* data,
01410 const hsa_ext_image_region_t *image_region);
01411
01412 hsa_status_t (*hsa_ext_sampler_create)(
01413 hsa_agent_t agent,
01414 const hsa_ext_sampler_descriptor_t *sampler_descriptor,
01415 hsa_ext_sampler_t *sampler);
01416
01417 hsa_status_t (*hsa_ext_sampler_destroy)(
01418 hsa_agent_t agent,
01419 hsa_ext_sampler_t sampler);
01420
01421 hsa_status_t (*hsa_ext_image_get_capability_with_layout)(
01422 hsa_agent_t agent,
01423 hsa_ext_image_geometry_t geometry,
01424 const hsa_ext_image_format_t *image_format,
01425 hsa_ext_image_data_layout_t image_data_layout,
01426 uint32_t *capability_mask);
01427
01428 hsa_status_t (*hsa_ext_image_data_get_info_with_layout)(
01429 hsa_agent_t agent,
01430 const hsa_ext_image_descriptor_t *image_descriptor,
01431 hsa_access_permission_t access_permission,
01432 hsa_ext_image_data_layout_t image_data_layout,
01433 size_t image_data_row_pitch,
01434 size_t image_data_slice_pitch,
01435 hsa_ext_image_data_info_t *image_data_info);
01436
01437 hsa_status_t (*hsa_ext_image_create_with_layout)(
01438 hsa_agent_t agent,
01439 const hsa_ext_image_descriptor_t *image_descriptor,
01440 const void *image_data,
01441 hsa_access_permission_t access_permission,
01442 hsa_ext_image_data_layout_t image_data_layout,
01443 size_t image_data_row_pitch,
01444 size_t image_data_slice_pitch,
01445 hsa_ext_image_t *image);
01446
01447 } hsa_ext_images_1_pfn_t;
01450 #ifdef __cplusplus
01451 } // end extern "C" block
01452 #endif /*__cplusplus*/
01453
01454 #endif

```

## 7.12 hsa\_ven\_amd\_aqlprofile.h

```

00001
00002 //
00003 // The University of Illinois/NCSA
00004 // Open Source License (NCSA)
00005 //
00006 // Copyright (c) 2017-2020, Advanced Micro Devices, Inc. All rights reserved.
00007 //
00008 // Developed by:
00009 //
00010 // AMD Research and AMD HSA Software Development
00011 //
00012 // Advanced Micro Devices, Inc.
00013 //
00014 // www.amd.com
00015 //
00016 // Permission is hereby granted, free of charge, to any person obtaining a copy
00017 // of this software and associated documentation files (the "Software"), to
00018 // deal with the Software without restriction, including without limitation
00019 // the rights to use, copy, modify, merge, publish, distribute, sublicense,
00020 // and/or sell copies of the Software, and to permit persons to whom the
00021 // Software is furnished to do so, subject to the following conditions:
00022 //
00023 // - Redistributions of source code must retain the above copyright notice,
00024 // this list of conditions and the following disclaimers.
00025 // - Redistributions in binary form must reproduce the above copyright
00026 // notice, this list of conditions and the following disclaimers in
00027 // the documentation and/or other materials provided with the distribution.
00028 // - Neither the names of Advanced Micro Devices, Inc,
00029 // nor the names of its contributors may be used to endorse or promote
00030 // products derived from this Software without specific prior written
00031 // permission.
00032 //
00033 // THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR
00034 // IMPLIED, INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY,
00035 // FITNESS FOR A PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL

```

```

00036 // THE CONTRIBUTORS OR COPYRIGHT HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR
00037 // OTHER LIABILITY, WHETHER IN AN ACTION OF CONTRACT, TORT OR OTHERWISE,
00038 // ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER
00039 // DEALINGS WITH THE SOFTWARE.
00040 //
00042
00043 #ifndef OPENSRC_HSA_RUNTIME_INC_HSA_VEN_AMD_AQLPROFILE_H_
00044 #define OPENSRC_HSA_RUNTIME_INC_HSA_VEN_AMD_AQLPROFILE_H_
00045
00046 #include <stdint.h>
00047 #include "hsa.h"
00048
00049 #define HSA_AQLPROFILE_VERSION_MAJOR 2
00050 #define HSA_AQLPROFILE_VERSION_MINOR 0
00051
00052 #ifdef __cplusplus
00053 extern "C" {
00054 #endif // __cplusplus
00055
00057 // Library version
00058 uint32_t hsa_ven_amd_aqlprofile_version_major();
00059 uint32_t hsa_ven_amd_aqlprofile_version_minor();
00060
00062 // Library API:
00063 // The library provides helper methods for instantiation of
00064 // the profile context object and for populating of the start
00065 // and stop AQL packets. The profile object contains a profiling
00066 // events list and needed for profiling buffers descriptors,
00067 // a command buffer and an output data buffer. To check if there
00068 // was an error the library methods return a status code. Also
00069 // the library provides methods for querying required buffers
00070 // attributes, to validate the event attributes and to get profiling
00071 // output data.
00072 //
00073 // Returned status:
00074 // hsa_status_t - HSA status codes are used from hsa.h header
00075 //
00076 // Supported profiling features:
00077 //
00078 // Supported profiling events
00079 typedef enum {
00080 HSA_VEN_AMD_AQLPROFILE_EVENT_TYPE_PMC = 0,
00081 HSA_VEN_AMD_AQLPROFILE_EVENT_TYPE_TRACE = 1,
00082 } hsa_ven_amd_aqlprofile_event_type_t;
00083
00084 // Supported performance counters (PMC) blocks
00085 // The block ID is the same for a block instances set, for example
00086 // each block instance from the TCC block set, TCC0, TCC1, ..., TCCN
00087 // will have the same block ID HSA_VEN_AMD_AQLPROFILE_BLOCKS_TCC.
00088 typedef enum {
00089 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_CPC = 0,
00090 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_CPF = 1,
00091 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_GDS = 2,
00092 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_GRBM = 3,
00093 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_GRBMSE = 4,
00094 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_SPI = 5,
00095 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_SQ = 6,
00096 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_SQCS = 7,
00097 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_SRBM = 8,
00098 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_SX = 9,
00099 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_TA = 10,
00100 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_TCA = 11,
00101 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_TCC = 12,
00102 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_TCP = 13,
00103 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_TD = 14,
00104 // Memory related blocks
00105 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_MCARB = 15,
00106 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_MCHUB = 16,
00107 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_MCMCBVM = 17,
00108 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_MCSEQ = 18,
00109 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_MCVML2 = 19,
00110 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_MCXBAR = 20,
00111 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_ATC = 21,
00112 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_ATCL2 = 22,
00113 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_GCEA = 23,
00114 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_RPB = 24,
00115 // System blocks
00116 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_SDMA = 25,
00117 // GFX10 added blocks
00118 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_GL1A = 26,
00119 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_GL1C = 27,
00120 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_GL2A = 28,
00121 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_GL2C = 29,
00122 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_GCR = 30,
00123 HSA_VEN_AMD_AQLPROFILE_BLOCK_NAME_GUS = 31,
00124
00125 HSA_VEN_AMD_AQLPROFILE_BLOCKS_NUMBER

```

```

00126 } hsa_ven_amd_aqlprofile_block_name_t;
00127
00128 // PMC event object structure
00129 // 'counter_id' value is specified in GFXIPs perfcounter user guides
00130 // which is the counters select value, "Performance Counters Selection"
00131 // chapter.
00132 typedef struct {
00133 hsa_ven_amd_aqlprofile_block_name_t block_name;
00134 uint32_t block_index;
00135 uint32_t counter_id;
00136 } hsa_ven_amd_aqlprofile_event_t;
00137
00138 // Check if event is valid for the specific GPU
00139 hsa_status_t hsa_ven_amd_aqlprofile_validate_event(
00140 hsa_agent_t agent, // HSA handle for the profiling GPU
00141 const hsa_ven_amd_aqlprofile_event_t* event, // [in] Pointer on validated event
00142 bool* result); // [out] True if the event valid, False otherwise
00143
00144 // Profiling parameters
00145 // All parameters are generic and if not applicable for a specific
00146 // profile configuration then error status will be returned.
00147 typedef enum {
00148 // Trace applicable parameters
00149 HSA_VEN_AMD_AQLPROFILE_PARAMETER_NAME_COMPUTE_UNIT_TARGET = 0,
00150 HSA_VEN_AMD_AQLPROFILE_PARAMETER_NAME_VM_ID_MASK = 1,
00151 HSA_VEN_AMD_AQLPROFILE_PARAMETER_NAME_MASK = 2,
00152 HSA_VEN_AMD_AQLPROFILE_PARAMETER_NAME_TOKEN_MASK = 3,
00153 HSA_VEN_AMD_AQLPROFILE_PARAMETER_NAME_TOKEN_MASK2 = 4,
00154 HSA_VEN_AMD_AQLPROFILE_PARAMETER_NAME_SE_MASK = 5,
00155 HSA_VEN_AMD_AQLPROFILE_PARAMETER_NAME_SAMPLE_RATE = 6,
00156 HSA_VEN_AMD_AQLPROFILE_PARAMETER_NAME_K_CONCURRENT = 7,
00157 } hsa_ven_amd_aqlprofile_parameter_name_t;
00158
00159 // Profile parameter object
00160 typedef struct {
00161 hsa_ven_amd_aqlprofile_parameter_name_t parameter_name;
00162 uint32_t value;
00163 } hsa_ven_amd_aqlprofile_parameter_t;
00164
00165 //
00166 // Profile context object:
00167 // The library provides a profile object structure which contains
00168 // the events array, a buffer for the profiling start/stop commands
00169 // and a buffer for the output data.
00170 // The buffers are specified by the buffer descriptors and allocated
00171 // by the application. The buffers allocation attributes, the command
00172 // buffer size, the PMC output buffer size as well as profiling output
00173 // data can be get using the generic get profile info helper _get_info.
00174 //
00175 // Buffer descriptor
00176 typedef struct {
00177 void* ptr;
00178 uint32_t size;
00179 } hsa_ven_amd_aqlprofile_descriptor_t;
00180
00181 // Profile context object structure, contains profiling events list and
00182 // needed for profiling buffers descriptors, a command buffer and
00183 // an output data buffer
00184 typedef struct {
00185 hsa_agent_t agent; // GFXIP handle
00186 hsa_ven_amd_aqlprofile_event_type_t type; // Events type
00187 const hsa_ven_amd_aqlprofile_event_t* events; // Events array
00188 uint32_t event_count; // Events count
00189 const hsa_ven_amd_aqlprofile_parameter_t* parameters; // Parameters array
00190 uint32_t parameter_count; // Parameters count
00191 hsa_ven_amd_aqlprofile_descriptor_t output_buffer; // Output buffer
00192 hsa_ven_amd_aqlprofile_descriptor_t command_buffer; // PM4 commands
00193 } hsa_ven_amd_aqlprofile_profile_t;
00194
00195 //
00196 // AQL packets populating methods:
00197 // The helper methods to populate provided by the application START and
00198 // STOP AQL packets which the application is required to submit before and
00199 // after profiled GPU task packets respectively.
00200 //
00201 // AQL Vendor Specific packet which carries a PM4 command
00202 typedef struct {
00203 uint16_t header;
00204 uint16_t pm4_command[27];
00205 hsa_signal_t completion_signal;
00206 } hsa_ext_amd_aql_pm4_packet_t;
00207
00208 // Method to populate the provided AQL packet with profiling start commands
00209 // Only 'pm4_command' fields of the packet are set and the application
00210 // is responsible to set Vendor Specific header type a completion signal
00211 hsa_status_t hsa_ven_amd_aqlprofile_start(
00212 hsa_ven_amd_aqlprofile_profile_t* profile, // [in/out] profile context object

```

```

00213 hsa_ext_amd_aql_pm4_packet_t* aql_start_packet); // [out] profile start AQL packet
00214
00215 // Method to populate the provided AQL packet with profiling stop commands
00216 // Only 'pm4_command' fields of the packet are set and the application
00217 // is responsible to set Vendor Specific header type and a completion signal
00218 hsa_status_t hsa_ven_amd_aqlprofile_stop(
00219 const hsa_ven_amd_aqlprofile_profile_t* profile, // [in] profile context object
00220 hsa_ext_amd_aql_pm4_packet_t* aql_stop_packet); // [out] profile stop AQL packet
00221
00222 // Method to populate the provided AQL packet with profiling read commands
00223 // Only 'pm4_command' fields of the packet are set and the application
00224 // is responsible to set Vendor Specific header type and a completion signal
00225 hsa_status_t hsa_ven_amd_aqlprofile_read(
00226 const hsa_ven_amd_aqlprofile_profile_t* profile, // [in] profile context object
00227 hsa_ext_amd_aql_pm4_packet_t* aql_read_packet); // [out] profile stop AQL packet
00228
00229 // Legacy devices, PM4 profiling packet size
00230 const unsigned HSA_VEN_AMD_AQLPROFILE_LEGACY_PM4_PACKET_SIZE = 192;
00231 // Legacy devices, converting the profiling AQL packet to PM4 packet blob
00232 hsa_status_t hsa_ven_amd_aqlprofile_legacy_get_pm4(
00233 const hsa_ext_amd_aql_pm4_packet_t* aql_packet, // [in] AQL packet
00234 void* data); // [out] PM4 packet blob
00235
00236 //
00237 // Get profile info:
00238 // Generic method for getting various profile info including profile buffers
00239 // attributes like the command buffer size and the profiling PMC results.
00240 // It's implied that all counters are 64bit values.
00241 //
00242 // Profile generic output data:
00243 typedef struct {
00244 uint32_t sample_id; // PMC sample or trace buffer index
00245 union {
00246 struct {
00247 hsa_ven_amd_aqlprofile_event_t event; // PMC event
00248 uint64_t result; // PMC result
00249 } pmc_data;
00250 hsa_ven_amd_aqlprofile_descriptor_t trace_data; // Trace output data descriptor
00251 };
00252 } hsa_ven_amd_aqlprofile_info_data_t;
00253
00254 // ID query type
00255 typedef struct {
00256 const char* name;
00257 uint32_t id;
00258 uint32_t instance_count;
00259 } hsa_ven_amd_aqlprofile_id_query_t;
00260
00261 // Profile attributes
00262 typedef enum {
00263 HSA_VEN_AMD_AQLPROFILE_INFO_COMMAND_BUFFER_SIZE = 0, // get_info returns uint32_t value
00264 HSA_VEN_AMD_AQLPROFILE_INFO_PMC_DATA_SIZE = 1, // get_info returns uint32_t value
00265 HSA_VEN_AMD_AQLPROFILE_INFO_PMC_DATA = 2, // get_info returns PMC uint64_t value
00266 // in info_data object
00267 HSA_VEN_AMD_AQLPROFILE_INFO_TRACE_DATA = 3, // get_info returns trace buffer ptr/size
00268 // in info_data object
00269 //
00270 HSA_VEN_AMD_AQLPROFILE_INFO_BLOCK_COUNTERS = 4, // get_info returns number of block counter
00271 HSA_VEN_AMD_AQLPROFILE_INFO_BLOCK_ID = 5, // get_info returns block id, instances
00272 // by name string using _id_query_t
00273 //
00274 HSA_VEN_AMD_AQLPROFILE_INFO_ENABLE_CMD = 6, // get_info returns size/pointer for
00275 // counters enable command buffer
00276 HSA_VEN_AMD_AQLPROFILE_INFO_DISABLE_CMD = 7, // get_info returns size/pointer for
00277 // counters disable command buffer
00278 } hsa_ven_amd_aqlprofile_info_type_t;
00279
00280 // Definition of output data iterator callback
00281 typedef hsa_status_t (*hsa_ven_amd_aqlprofile_data_callback_t)(
00282 hsa_ven_amd_aqlprofile_info_type_t info_type, // [in] data type, PMC or trace data
00283 hsa_ven_amd_aqlprofile_info_data_t* info_data, // [in] info_data object
00284 void* callback_data); // [in/out] data passed to the callback
00285
00286 // Method for getting the profile info
00287 hsa_status_t hsa_ven_amd_aqlprofile_get_info(
00288 const hsa_ven_amd_aqlprofile_profile_t* profile, // [in] profile context object
00289 hsa_ven_amd_aqlprofile_info_type_t attribute, // [in] requested profile attribute
00290 void* value); // [in/out] returned value
00291
00292 // Method for iterating the events output data
00293 hsa_status_t hsa_ven_amd_aqlprofile_iterate_data(
00294 const hsa_ven_amd_aqlprofile_profile_t* profile, // [in] profile context object
00295 hsa_ven_amd_aqlprofile_data_callback_t callback, // [in] callback to iterate the output data
00296 void* data); // [in/out] data passed to the callback
00297
00298 // Return error string
00299 hsa_status_t hsa_ven_amd_aqlprofile_error_string(

```

```

00300 const char** str); // [out] pointer on the error string
00301
00305 #define hsa_ven_amd_aqlprofile_VERSION_MAJOR 1
00306 #define hsa_ven_amd_aqlprofile_LIB(suff) "libhsa-amd-aqlprofile" suff ".so"
00307
00308 #ifdef HSA_LARGE_MODEL
00309 static const char kAqlProfileLib[] = hsa_ven_amd_aqlprofile_LIB("64");
00310 #else
00311 static const char kAqlProfileLib[] = hsa_ven_amd_aqlprofile_LIB("");
00312 #endif
00313
00317 typedef struct hsa_ven_amd_aqlprofile_1_00_pfn_s {
00318 uint32_t (*hsa_ven_amd_aqlprofile_version_major)();
00319 uint32_t (*hsa_ven_amd_aqlprofile_version_minor)();
00320
00321 hsa_status_t (*hsa_ven_amd_aqlprofile_error_string)(
00322 const char** str);
00323
00324 hsa_status_t (*hsa_ven_amd_aqlprofile_validate_event)(
00325 hsa_agent_t agent,
00326 const hsa_ven_amd_aqlprofile_event_t* event,
00327 bool* result);
00328
00329 hsa_status_t (*hsa_ven_amd_aqlprofile_start)(
00330 hsa_ven_amd_aqlprofile_profile_t* profile,
00331 hsa_ext_amd_aql_pm4_packet_t* aql_start_packet);
00332
00333 hsa_status_t (*hsa_ven_amd_aqlprofile_stop)(
00334 const hsa_ven_amd_aqlprofile_profile_t* profile,
00335 hsa_ext_amd_aql_pm4_packet_t* aql_stop_packet);
00336
00337 hsa_status_t (*hsa_ven_amd_aqlprofile_read)(
00338 const hsa_ven_amd_aqlprofile_profile_t* profile,
00339 hsa_ext_amd_aql_pm4_packet_t* aql_read_packet);
00340
00341 hsa_status_t (*hsa_ven_amd_aqlprofile_legacy_get_pm4)(
00342 const hsa_ext_amd_aql_pm4_packet_t* aql_packet,
00343 void* data);
00344
00345 hsa_status_t (*hsa_ven_amd_aqlprofile_get_info)(
00346 const hsa_ven_amd_aqlprofile_profile_t* profile,
00347 hsa_ven_amd_aqlprofile_info_type_t attribute,
00348 void* value);
00349
00350 hsa_status_t (*hsa_ven_amd_aqlprofile_iterate_data)(
00351 const hsa_ven_amd_aqlprofile_profile_t* profile,
00352 hsa_ven_amd_aqlprofile_data_callback_t callback,
00353 void* data);
00354 } hsa_ven_amd_aqlprofile_1_00_pfn_t;
00355
00356 typedef hsa_ven_amd_aqlprofile_1_00_pfn_t hsa_ven_amd_aqlprofile_pfn_t;
00357
00358 #ifdef __cplusplus
00359 }
00360 #endif // __cplusplus
00361
00362 #endif // OPENSRC_HSA_RUNTIME_INC_HSA_VEN_AMD_AQLPROFILE_H_

```

## 7.13 hsa\_ven\_amd\_loader.h

```

00001
00002 //
00003 // The University of Illinois/NCSA
00004 // Open Source License (NCSA)
00005 //
00006 // Copyright (c) 2014-2020, Advanced Micro Devices, Inc. All rights reserved.
00007 //
00008 // Developed by:
00009 //
00010 // AMD Research and AMD HSA Software Development
00011 //
00012 // Advanced Micro Devices, Inc.
00013 //
00014 // www.amd.com
00015 //
00016 // Permission is hereby granted, free of charge, to any person obtaining a copy
00017 // of this software and associated documentation files (the "Software"), to
00018 // deal with the Software without restriction, including without limitation
00019 // the rights to use, copy, modify, merge, publish, distribute, sublicense,
00020 // and/or sell copies of the Software, and to permit persons to whom the
00021 // Software is furnished to do so, subject to the following conditions:
00022 //
00023 // - Redistributions of source code must retain the above copyright notice,

```

```

00024 // this list of conditions and the following disclaimers.
00025 // - Redistributions in binary form must reproduce the above copyright
00026 // notice, this list of conditions and the following disclaimers in
00027 // the documentation and/or other materials provided with the distribution.
00028 // - Neither the names of Advanced Micro Devices, Inc,
00029 // nor the names of its contributors may be used to endorse or promote
00030 // products derived from this Software without specific prior written
00031 // permission.
00032 //
00033 // THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR
00034 // IMPLIED, INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY,
00035 // FITNESS FOR A PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL
00036 // THE CONTRIBUTORS OR COPYRIGHT HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR
00037 // OTHER LIABILITY, WHETHER IN AN ACTION OF CONTRACT, TORT OR OTHERWISE,
00038 // ARISING FROM, OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER
00039 // DEALINGS WITH THE SOFTWARE.
00040 //
00042
00043 // HSA AMD extension for additional loader functionality.
00044
00045 #ifndef HSA_VEN_AMD_LOADER_H
00046 #define HSA_VEN_AMD_LOADER_H
00047
00048 #include "hsa.h"
00049
00050 #ifdef __cplusplus
00051 extern "C" {
00052 #endif /* __cplusplus */
00053
00086 hsa_status_t hsa_ven_amd_loader_query_host_address(
00087 const void *device_address,
00088 const void **host_address);
00089
00094 typedef enum {
00099 HSA_VEN_AMD_LOADER_CODE_OBJECT_STORAGE_TYPE_NONE = 0,
00104 HSA_VEN_AMD_LOADER_CODE_OBJECT_STORAGE_TYPE_FILE = 1,
00109 HSA_VEN_AMD_LOADER_CODE_OBJECT_STORAGE_TYPE_MEMORY = 2
00110 } hsa_ven_amd_loader_code_object_storage_type_t;
00111
00123 typedef struct hsa_ven_amd_loader_segment_descriptor_s {
00128 hsa_agent_t agent;
00132 hsa_executable_t executable;
00136 hsa_ven_amd_loader_code_object_storage_type_t code_object_storage_type;
00146 const void *code_object_storage_base;
00156 size_t code_object_storage_size;
00164 size_t code_object_storage_offset;
00168 const void *segment_base;
00172 size_t segment_size;
00173 } hsa_ven_amd_loader_segment_descriptor_t;
00174
00223 hsa_status_t hsa_ven_amd_loader_query_segment_descriptors(
00224 hsa_ven_amd_loader_segment_descriptor_t *segment_descriptors,
00225 size_t *num_segment_descriptors);
00226
00241 hsa_status_t hsa_ven_amd_loader_query_executable(
00242 const void *device_address,
00243 hsa_executable_t *executable);
00244
00245 //=====//
00246
00273 hsa_status_t hsa_ven_amd_loader_executable_iterate_loaded_code_objects(
00274 hsa_executable_t executable,
00275 hsa_status_t (*callback)(
00276 hsa_executable_t executable,
00277 hsa_loaded_code_object_t loaded_code_object,
00278 void *data),
00279 void *data);
00280
00284 typedef enum {
00288 HSA_VEN_AMD_LOADER_LOADED_CODE_OBJECT_KIND_PROGRAM = 1,
00292 HSA_VEN_AMD_LOADER_LOADED_CODE_OBJECT_KIND_AGENT = 2
00293 } hsa_ven_amd_loader_loaded_code_object_kind_t;
00294
00298 typedef enum hsa_ven_amd_loader_loaded_code_object_info_e {
00303 HSA_VEN_AMD_LOADER_LOADED_CODE_OBJECT_INFO_EXECUTABLE = 1,
00308 HSA_VEN_AMD_LOADER_LOADED_CODE_OBJECT_INFO_KIND = 2,
00316 HSA_VEN_AMD_LOADER_LOADED_CODE_OBJECT_INFO_AGENT = 3,
00322 HSA_VEN_AMD_LOADER_LOADED_CODE_OBJECT_INFO_CODE_OBJECT_STORAGE_TYPE = 4,
00330 HSA_VEN_AMD_LOADER_LOADED_CODE_OBJECT_INFO_CODE_OBJECT_STORAGE_MEMORY_BASE = 5,
00338 HSA_VEN_AMD_LOADER_LOADED_CODE_OBJECT_INFO_CODE_OBJECT_STORAGE_MEMORY_SIZE = 6,
00346 HSA_VEN_AMD_LOADER_LOADED_CODE_OBJECT_INFO_CODE_OBJECT_STORAGE_FILE = 7,
00354 HSA_VEN_AMD_LOADER_LOADED_CODE_OBJECT_INFO_LOAD_DELTA = 8,
00363 HSA_VEN_AMD_LOADER_LOADED_CODE_OBJECT_INFO_LOAD_BASE = 9,
00369 HSA_VEN_AMD_LOADER_LOADED_CODE_OBJECT_INFO_LOAD_SIZE = 10,
00374 HSA_VEN_AMD_LOADER_LOADED_CODE_OBJECT_INFO_URI_LENGTH = 11,
00415 HSA_VEN_AMD_LOADER_LOADED_CODE_OBJECT_INFO_URI = 12,
00416 } hsa_ven_amd_loader_loaded_code_object_info_t;

```

```

00417
00441 hsa_status_t hsa_ven_amd_loader_loaded_code_object_get_info(
00442 hsa_loaded_code_object_t loaded_code_object,
00443 hsa_ven_amd_loader_loaded_code_object_info_t attribute,
00444 void *value);
00445
00446 //=====//
00447
00487 hsa_status_t
00488 hsa_ven_amd_loader_code_object_reader_create_from_file_with_offset_size(
00489 hsa_file_t file,
00490 size_t offset,
00491 size_t size,
00492 hsa_code_object_reader_t *code_object_reader);
00493
00494 //=====//
00495
00521 hsa_status_t
00522 hsa_ven_amd_loader_iterate_executables(
00523 hsa_status_t (*callback)(
00524 hsa_executable_t executable,
00525 void *data),
00526 void *data);
00527
00528 //=====//
00529
00533 #define hsa_ven_amd_loader 001003
00534
00538 typedef struct hsa_ven_amd_loader_1_00_pfn_s {
00539 hsa_status_t (*hsa_ven_amd_loader_query_host_address)(
00540 const void *device_address,
00541 const void **host_address);
00542
00543 hsa_status_t (*hsa_ven_amd_loader_query_segment_descriptors)(
00544 hsa_ven_amd_loader_segment_descriptor_t *segment_descriptors,
00545 size_t *num_segment_descriptors);
00546
00547 hsa_status_t (*hsa_ven_amd_loader_query_executable)(
00548 const void *device_address,
00549 hsa_executable_t *executable);
00550 } hsa_ven_amd_loader_1_00_pfn_t;
00551
00555 typedef struct hsa_ven_amd_loader_1_01_pfn_s {
00556 hsa_status_t (*hsa_ven_amd_loader_query_host_address)(
00557 const void *device_address,
00558 const void **host_address);
00559
00560 hsa_status_t (*hsa_ven_amd_loader_query_segment_descriptors)(
00561 hsa_ven_amd_loader_segment_descriptor_t *segment_descriptors,
00562 size_t *num_segment_descriptors);
00563
00564 hsa_status_t (*hsa_ven_amd_loader_query_executable)(
00565 const void *device_address,
00566 hsa_executable_t *executable);
00567
00568 hsa_status_t (*hsa_ven_amd_loader_executable_iterate_loaded_code_objects)(
00569 hsa_executable_t executable,
00570 hsa_status_t (*callback)(
00571 hsa_executable_t executable,
00572 hsa_loaded_code_object_t loaded_code_object,
00573 void *data),
00574 void *data);
00575
00576 hsa_status_t (*hsa_ven_amd_loader_loaded_code_object_get_info)(
00577 hsa_loaded_code_object_t loaded_code_object,
00578 hsa_ven_amd_loader_loaded_code_object_info_t attribute,
00579 void *value);
00580 } hsa_ven_amd_loader_1_01_pfn_t;
00581
00585 typedef struct hsa_ven_amd_loader_1_02_pfn_s {
00586 hsa_status_t (*hsa_ven_amd_loader_query_host_address)(
00587 const void *device_address,
00588 const void **host_address);
00589
00590 hsa_status_t (*hsa_ven_amd_loader_query_segment_descriptors)(
00591 hsa_ven_amd_loader_segment_descriptor_t *segment_descriptors,
00592 size_t *num_segment_descriptors);
00593
00594 hsa_status_t (*hsa_ven_amd_loader_query_executable)(
00595 const void *device_address,
00596 hsa_executable_t *executable);
00597
00598 hsa_status_t (*hsa_ven_amd_loader_executable_iterate_loaded_code_objects)(
00599 hsa_executable_t executable,
00600 hsa_status_t (*callback)(
00601 hsa_executable_t executable,
00602 hsa_loaded_code_object_t loaded_code_object,

```



```

00603 void *data),
00604 void *data);
00605
00606 hsa_status_t (*hsa_ven_amd_loader_loaded_code_object_get_info)(
00607 hsa_loaded_code_object_t loaded_code_object,
00608 hsa_ven_amd_loader_loaded_code_object_info_t attribute,
00609 void *value);
00610
00611 hsa_status_t
00612 (*hsa_ven_amd_loader_code_object_reader_create_from_file_with_offset_size)(
00613 hsa_file_t file,
00614 size_t offset,
00615 size_t size,
00616 hsa_code_object_reader_t *code_object_reader);
00617 } hsa_ven_amd_loader_1_02_pfn_t;
00618
00622 typedef struct hsa_ven_amd_loader_1_03_pfn_s {
00623 hsa_status_t (*hsa_ven_amd_loader_query_host_address)(
00624 const void *device_address,
00625 const void **host_address);
00626
00627 hsa_status_t (*hsa_ven_amd_loader_query_segment_descriptors)(
00628 hsa_ven_amd_loader_segment_descriptor_t *segment_descriptors,
00629 size_t *num_segment_descriptors);
00630
00631 hsa_status_t (*hsa_ven_amd_loader_query_executable)(
00632 const void *device_address,
00633 hsa_executable_t *executable);
00634
00635 hsa_status_t (*hsa_ven_amd_loader_executable_iterate_loaded_code_objects)(
00636 hsa_executable_t executable,
00637 hsa_status_t (*callback)(
00638 hsa_executable_t executable,
00639 hsa_loaded_code_object_t loaded_code_object,
00640 void *data),
00641 void *data);
00642
00643 hsa_status_t (*hsa_ven_amd_loader_loaded_code_object_get_info)(
00644 hsa_loaded_code_object_t loaded_code_object,
00645 hsa_ven_amd_loader_loaded_code_object_info_t attribute,
00646 void *value);
00647
00648 hsa_status_t
00649 (*hsa_ven_amd_loader_code_object_reader_create_from_file_with_offset_size)(
00650 hsa_file_t file,
00651 size_t offset,
00652 size_t size,
00653 hsa_code_object_reader_t *code_object_reader);
00654
00655 hsa_status_t
00656 (*hsa_ven_amd_loader_iterate_executables)(
00657 hsa_status_t (*callback)(
00658 hsa_executable_t executable,
00659 void *data),
00660 void *data);
00661 } hsa_ven_amd_loader_1_03_pfn_t;
00662
00663 #ifdef __cplusplus
00664 }
00665 #endif /* __cplusplus */
00666
00667 #endif /* HSA_VEN_AMD_LOADER_H */

```



# Index

/home/alexv/Programming/ROCR-Runtime/include/Brig.h, 386  
/home/alexv/Programming/ROCR-Runtime/include/amd\_hsa\_commad.h, 375  
/home/alexv/Programming/ROCR-Runtime/include/amd\_hsa\_elf.h, 376  
/home/alexv/Programming/ROCR-Runtime/include/amd\_hsa\_kernel\_code.h, 381  
/home/alexv/Programming/ROCR-Runtime/include/amd\_hsa\_queue.h, 384  
/home/alexv/Programming/ROCR-Runtime/include/amd\_hsa\_signal.h, 385  
/home/alexv/Programming/ROCR-Runtime/include/hsa.h, and\_ext\_ 399  
/home/alexv/Programming/ROCR-Runtime/include/hsa\_api\_table.h, 416  
/home/alexv/Programming/ROCR-Runtime/include/hsa\_ext\_amd.h, 422  
/home/alexv/Programming/ROCR-Runtime/include/hsa\_ext\_finalize.h, 430  
/home/alexv/Programming/ROCR-Runtime/include/hsa\_ext\_image.h, 432  
/home/alexv/Programming/ROCR-Runtime/include/hsa\_ven\_amd\_aqlprofile.h, 438  
/home/alexv/Programming/ROCR-Runtime/include/hsa\_ven\_amd\_loader.h, 442  
address\_mode  
    hsa\_ext\_sampler\_descriptor\_s, 336  
addressing  
    amdgpu\_hsa\_sampler\_descriptor\_s, 204  
    BrigOperandConstantSampler, 256  
agent  
    hsa\_amd\_gpu\_memory\_fault\_info\_s, 295  
    hsa\_ven\_amd\_aqlprofile\_profile\_t, 357  
    hsa\_ven\_amd\_loader\_segment\_descriptor\_s, 366  
agentBaseAddress  
    hsa\_amd\_pointer\_info\_s, 303  
agentOwner  
    hsa\_amd\_pointer\_info\_s, 303  
align  
    BrigDirectiveVariable, 221  
    BrigInstMem, 234  
    BrigOperandAlign, 249  
alignment  
    hsa\_ext\_image\_data\_info\_s, 324  
allocation  
    BrigDirectiveVariable, 221  
amd\_control\_directives\_s, 179  
    enable\_break\_exceptions, 179  
    enable\_detect\_exceptions, 179  
    enabled\_control\_directives, 180  
    max\_dynamic\_group\_size, 180  
    max\_flat\_grid\_size, 180  
    max\_flat\_workgroup\_size, 180  
    required\_dim, 180  
    required\_grid\_size, 180  
    required\_workgroup\_size, 181  
    reserved1, 181  
    reserved2, 181  
amd\_signal.h, HsaApiTableContainer, 371  
amd\_ext\_ HsaApiTable, 369  
amd\_kernel\_code\_s, 181  
    amd\_kernel\_code\_version\_major, 182  
    amd\_kernel\_code\_version\_minor, 182  
    amd\_machine\_kind, 183  
    amd\_machine\_version\_major, 183  
    amd\_machine\_version\_minor, 183  
    amd\_machine\_version\_stepping, 183  
    call\_convention, 183  
    compute\_pgm\_rsrc1, 183  
    compute\_pgm\_rsrc2, 184  
    control\_directives, 184  
    debug\_private\_segment\_buffer\_sgpr, 184  
    debug\_wavefront\_private\_segment\_offset\_sgpr, 184  
    gds\_segment\_byte\_size, 184  
    group\_segment\_alignment, 184  
    kernarg\_segment\_alignment, 185  
    kernarg\_segment\_byte\_size, 185  
    kernel\_code\_entry\_byte\_offset, 185  
    kernel\_code\_prefetch\_byte\_offset, 185  
    kernel\_code\_prefetch\_byte\_size, 185  
    kernel\_code\_properties, 185  
    max\_scratch\_backing\_memory\_byte\_size, 186  
    private\_segment\_alignment, 186  
    reserved1, 186  
    reserved\_sgpr\_count, 186  
    reserved\_sgpr\_first, 186  
    reserved\_vgpr\_count, 186  
    reserved\_vgpr\_first, 187  
    runtime\_loader\_kernel\_symbol, 187  
    wavefront\_sgpr\_count, 187  
    wavefront\_size, 187  
    workgroup\_fbarrier\_count, 187  
    workgroup\_group\_segment\_byte\_size, 187  
    workitem\_private\_segment\_byte\_size, 188

- workitem\_vgpr\_count, 188
- amd\_kernel\_code\_version\_major
  - amd\_kernel\_code\_s, 182
- amd\_kernel\_code\_version\_minor
  - amd\_kernel\_code\_s, 182
- amd\_machine\_kind
  - amd\_kernel\_code\_s, 183
- amd\_machine\_version\_major
  - amd\_kernel\_code\_s, 183
- amd\_machine\_version\_minor
  - amd\_kernel\_code\_s, 183
- amd\_machine\_version\_stepping
  - amd\_kernel\_code\_s, 183
- amd\_queue\_s, 188
  - compute\_tmpring\_size, 189
  - group\_segment\_aperture\_base\_hi, 189
  - hsa\_queue, 189
  - legacy\_doorbell\_lock, 189
  - max\_cu\_id, 189
  - max\_legacy\_doorbell\_dispatch\_id\_plus\_1, 189
  - max\_wave\_id, 190
  - private\_segment\_aperture\_base\_hi, 190
  - queue\_inactive\_signal, 190
  - queue\_properties, 190
  - read\_dispatch\_id, 190
  - read\_dispatch\_id\_field\_base\_byte\_offset, 190
  - reserved1, 191
  - reserved2, 191
  - reserved3, 191
  - reserved4, 191
  - scratch\_backing\_memory\_byte\_size, 191
  - scratch\_backing\_memory\_location, 191
  - scratch\_resource\_descriptor, 192
  - scratch\_wave64\_lane\_byte\_size, 192
  - write\_dispatch\_id, 192
- amd\_runtime\_loader\_debug\_info\_s, 192
  - elf\_raw, 193
  - elf\_size, 193
  - kernel\_name, 193
  - owning\_segment, 193
- amd\_signal\_s, 194
  - end\_ts, 194
  - event\_id, 194
  - event\_mailbox\_ptr, 194
  - hardware\_doorbell\_ptr, 195
  - kind, 195
  - legacy\_hardware\_doorbell\_ptr, 195
  - queue\_ptr, 195
  - reserved1, 195
  - reserved2, 195
  - reserved3, 196
  - start\_ts, 196
  - value, 196
- AmdExtTable, 196
- AmdFormat
  - hsa\_amd\_packet\_header\_s, 301
- amdgpu\_hsa\_image\_descriptor\_s, 196
  - array, 197
  - channel\_order, 197
  - channel\_type, 197
  - depth, 197
  - geometry, 197
  - height, 197
  - kind, 198
  - reserved1, 198
  - size, 198
  - width, 198
- amdgpu\_hsa\_note\_code\_object\_version\_s, 198
  - major\_version, 199
  - minor\_version, 199
- amdgpu\_hsa\_note\_hsail\_s, 199
  - default\_float\_round, 199
  - hsail\_major\_version, 200
  - hsail\_minor\_version, 200
  - machine\_model, 200
  - profile, 200
- amdgpu\_hsa\_note\_isa\_s, 200
  - architecture\_name\_size, 201
  - major, 201
  - minor, 201
  - stepping, 201
  - vendor\_and\_architecture\_name, 201
  - vendor\_name\_size, 201
- amdgpu\_hsa\_note\_producer\_options\_s, 202
  - producer\_options, 202
  - producer\_options\_size, 202
- amdgpu\_hsa\_note\_producer\_s, 203
  - producer\_major\_version, 203
  - producer\_minor\_version, 203
  - producer\_name, 203
  - producer\_name\_size, 203
  - reserved, 203
- amdgpu\_hsa\_sampler\_descriptor\_s, 204
  - addressing, 204
  - coord, 204
  - filter, 204
  - kind, 205
  - reserved1, 205
  - size, 205
- ApiTableVersion, 205
  - major\_id, 206
  - minor\_id, 206
  - reserved, 206
  - step\_id, 206
- Architected Queuing Language, 98
  - hsa\_amd\_packet\_type8\_t, 99
  - HSA\_AMD\_PACKET\_TYPE\_BARRIER\_VALUE, 100
  - hsa\_amd\_packet\_type\_t, 100
  - HSA\_FENCE\_SCOPE\_AGENT, 100
  - HSA\_FENCE\_SCOPE\_NONE, 100
  - HSA\_FENCE\_SCOPE\_SYSTEM, 100
  - hsa\_fence\_scope\_t, 100
  - HSA\_KERNEL\_DISPATCH\_PACKET\_SETUP\_DIMENSIONS, 101
  - hsa\_kernel\_dispatch\_packet\_setup\_t, 100

- hsa\_kernel\_dispatch\_packet\_setup\_width\_t, 101
- HSA\_PACKET\_HEADER\_ACQUIRE\_FENCE\_SCOPE, 102
- HSA\_PACKET\_HEADER\_BARRIER, 101
- HSA\_PACKET\_HEADER\_RELEASE\_FENCE\_SCOPE, 102
- HSA\_PACKET\_HEADER\_SCACQUIRE\_FENCE\_SCOPE, 101
- HSA\_PACKET\_HEADER\_SCRELEASE\_FENCE\_SCOPE, 102
- hsa\_packet\_header\_t, 101
- HSA\_PACKET\_HEADER\_TYPE, 101
- HSA\_PACKET\_HEADER\_WIDTH\_ACQUIRE\_FENCE\_SCOPE, 102
- HSA\_PACKET\_HEADER\_WIDTH\_RELEASE\_FENCE\_SCOPE, 102
- hsa\_packet\_header\_width\_t, 102
- HSA\_PACKET\_TYPE\_AGENT\_DISPATCH, 103
- HSA\_PACKET\_TYPE\_BARRIER\_AND, 103
- HSA\_PACKET\_TYPE\_BARRIER\_OR, 103
- HSA\_PACKET\_TYPE\_INVALID, 103
- HSA\_PACKET\_TYPE\_KERNEL\_DISPATCH, 103
- hsa\_packet\_type\_t, 102
- HSA\_PACKET\_TYPE\_VENDOR\_SPECIFIC, 103
- hsa\_signal\_condition32\_t, 99
- architecture\_name\_size
- amdgpu\_hsa\_note\_isa\_s, 201
- arg
  - hsa\_agent\_dispatch\_packet\_s, 288
- array
  - amdgpu\_hsa\_image\_descriptor\_s, 197
  - BrigOperandConstantImage, 253
- array\_size
  - hsa\_ext\_image\_descriptor\_s, 325
- atomic\_support\_32bit
  - hsa\_amd\_memory\_pool\_link\_info\_s, 298
- atomic\_support\_64bit
  - hsa\_amd\_memory\_pool\_link\_info\_s, 298
- atomicOperation
  - BrigInstAtomic, 224
- attribute
  - hsa\_amd\_svm\_attribute\_pair\_s, 306
- base
  - BrigDirectiveArgBlock, 208
  - BrigDirectiveComment, 209
  - BrigDirectiveControl, 210
  - BrigDirectiveExecutable, 211
  - BrigDirectiveExtension, 213
  - BrigDirectiveFbarrier, 214
  - BrigDirectiveLabel, 215
  - BrigDirectiveLoc, 216
  - BrigDirectiveModule, 217
  - BrigDirectiveNone, 219
  - BrigDirectivePragma, 220
  - BrigDirectiveVariable, 221
  - BrigInstAddr, 223
  - BrigInstAtomic, 224
  - BrigInstBase, 226
  - BrigInstBasic, 227
  - BrigInstBr, 227
  - BrigInstCmp, 228
  - BrigInstCvt, 230
  - BrigInstImage, 231
  - BrigInstLane, 233
  - BrigInstMem, 234
  - BrigInstMemFence, 235
  - BrigInstMod, 237
  - BrigInstQueryImage, 238
  - BrigInstQuerySampler, 239
  - BrigInstQueue, 240
  - BrigInstSeg, 241
  - BrigInstSegCvt, 242
  - BrigInstSignal, 244
  - BrigInstSourceType, 245
  - BrigOperandAddress, 248
  - BrigOperandAlign, 249
  - BrigOperandCodeList, 250
  - BrigOperandCodeRef, 251
  - BrigOperandConstantBytes, 251
  - BrigOperandConstantImage, 253
  - BrigOperandConstantOperandList, 255
  - BrigOperandConstantSampler, 256
  - BrigOperandOperandList, 257
  - BrigOperandRegister, 258
  - BrigOperandString, 259
  - BrigOperandWavesize, 260
  - hsa\_pitched\_ptr\_s, 344
- base\_address
  - hsa\_queue\_s, 345
- block\_index
  - hsa\_ven\_amd\_aqlprofile\_event\_t, 353
- block\_name
  - hsa\_ven\_amd\_aqlprofile\_event\_t, 353
- break\_exceptions\_mask
  - hsa\_ext\_control\_directives\_s, 318
- BrigBase, 206
  - byteCount, 207
  - kind, 207
- BrigData, 207
  - byteCount, 207
  - bytes, 208
- BrigDirectiveArgBlock, 208
  - base, 208
- BrigDirectiveComment, 209
  - base, 209
  - name, 209
- BrigDirectiveControl, 209
  - base, 210
  - control, 210
  - operands, 210
  - reserved, 210
- BrigDirectiveExecutable, 210
  - base, 211
  - firstCodeBlockEntry, 211
  - firstInArg, 211
  - inArgCount, 211

- linkage, 212
- modifier, 212
- name, 212
- nextModuleEntry, 212
- outArgCount, 212
- reserved, 212
- BrigDirectiveExtension, 213
  - base, 213
  - name, 213
- BrigDirectiveFbarrier, 214
  - base, 214
  - linkage, 214
  - modifier, 214
  - name, 214
  - reserved, 215
- BrigDirectiveLabel, 215
  - base, 215
  - name, 215
- BrigDirectiveLoc, 216
  - base, 216
  - column, 216
  - filename, 216
  - line, 217
- BrigDirectiveModule, 217
  - base, 217
  - defaultFloatRound, 217
  - hsailMajor, 218
  - hsailMinor, 218
  - machineModel, 218
  - name, 218
  - profile, 218
  - reserved, 218
- BrigDirectiveNone, 219
  - base, 219
- BrigDirectivePragma, 219
  - base, 220
  - operands, 220
- BrigDirectiveVariable, 220
  - align, 221
  - allocation, 221
  - base, 221
  - dim, 221
  - init, 221
  - linkage, 221
  - modifier, 222
  - name, 222
  - reserved, 222
  - segment, 222
  - type, 222
- BrigInstAddr, 223
  - base, 223
  - reserved, 223
  - segment, 223
- BrigInstAtomic, 224
  - atomicOperation, 224
  - base, 224
  - equivClass, 224
  - memoryOrder, 224
  - memoryScope, 225
  - reserved, 225
  - segment, 225
- BrigInstBase, 225
  - base, 226
  - opcode, 226
  - operands, 226
  - type, 226
- BrigInstBasic, 226
  - base, 227
- BrigInstBr, 227
  - base, 227
  - reserved, 228
  - width, 228
- BrigInstCmp, 228
  - base, 228
  - compare, 229
  - modifier, 229
  - pack, 229
  - reserved, 229
  - sourceType, 229
- BrigInstCvt, 230
  - base, 230
  - modifier, 230
  - round, 230
  - sourceType, 230
- BrigInstImage, 231
  - base, 231
  - coordType, 231
  - equivClass, 231
  - geometry, 232
  - imageType, 232
  - reserved, 232
- BrigInstLane, 232
  - base, 233
  - reserved, 233
  - sourceType, 233
  - width, 233
- BrigInstMem, 233
  - align, 234
  - base, 234
  - equivClass, 234
  - modifier, 234
  - reserved, 234
  - segment, 235
  - width, 235
- BrigInstMemFence, 235
  - base, 235
  - globalSegmentMemoryScope, 236
  - groupSegmentMemoryScope, 236
  - imageSegmentMemoryScope, 236
  - memoryOrder, 236
- BrigInstMod, 236
  - base, 237
  - modifier, 237
  - pack, 237
  - reserved, 237
  - round, 237

- BrigInstQueryImage, 238
  - base, 238
  - geometry, 238
  - imageType, 238
  - query, 239
- BrigInstQuerySampler, 239
  - base, 239
  - query, 239
  - reserved, 240
- BrigInstQueue, 240
  - base, 240
  - memoryOrder, 240
  - reserved, 241
  - segment, 241
- BrigInstSeg, 241
  - base, 241
  - reserved, 242
  - segment, 242
- BrigInstSegCvt, 242
  - base, 242
  - modifier, 243
  - segment, 243
  - sourceType, 243
- BrigInstSignal, 243
  - base, 244
  - memoryOrder, 244
  - signalOperation, 244
  - signalType, 244
- BrigInstSourceType, 244
  - base, 245
  - reserved, 245
  - sourceType, 245
- brigMajor
  - BrigModuleHeader, 246
- brigMinor
  - BrigModuleHeader, 246
- BrigModuleHeader, 246
  - brigMajor, 246
  - brigMinor, 246
  - byteCount, 246
  - hash, 246
  - identification, 247
  - reserved, 247
  - sectionCount, 247
  - sectionIndex, 247
- BrigOperandAddress, 247
  - base, 248
  - offset, 248
  - reg, 248
  - symbol, 248
- BrigOperandAlign, 249
  - align, 249
  - base, 249
  - reserved, 249
- BrigOperandCodeList, 250
  - base, 250
  - elements, 250
- BrigOperandCodeRef, 250
  - base, 251
  - ref, 251
- BrigOperandConstantBytes, 251
  - base, 251
  - bytes, 252
  - reserved, 252
  - type, 252
- BrigOperandConstantImage, 252
  - array, 253
  - base, 253
  - channelOrder, 253
  - channelType, 253
  - depth, 253
  - geometry, 253
  - height, 254
  - reserved, 254
  - type, 254
  - width, 254
- BrigOperandConstantOperandList, 254
  - base, 255
  - elements, 255
  - reserved, 255
  - type, 255
- BrigOperandConstantSampler, 256
  - addressing, 256
  - base, 256
  - coord, 256
  - filter, 256
  - reserved, 257
  - type, 257
- BrigOperandOperandList, 257
  - base, 257
  - elements, 258
- BrigOperandRegister, 258
  - base, 258
  - regKind, 258
  - regNum, 259
- BrigOperandString, 259
  - base, 259
  - string, 259
- BrigOperandWavesize, 260
  - base, 260
- BrigSectionHeader, 260
  - byteCount, 261
  - headerByteCount, 261
  - name, 261
  - nameLength, 261
- BrigUInt64, 261
  - hi, 262
  - lo, 262
- byteCount
  - BrigBase, 207
  - BrigData, 207
  - BrigModuleHeader, 246
  - BrigSectionHeader, 261
- bytes
  - BrigData, 208
  - BrigOperandConstantBytes, 252

- call\_convention
  - amd\_kernel\_code\_s, 183
- channel\_order
  - amdgpu\_hsa\_image\_descriptor\_s, 197
  - hsa\_ext\_image\_format\_s, 326
- channel\_type
  - amdgpu\_hsa\_image\_descriptor\_s, 197
  - hsa\_ext\_image\_format\_s, 327
- channelOrder
  - BrigOperandConstantImage, 253
- channelType
  - BrigOperandConstantImage, 253
- Code Objects (deprecated), 136
  - hsa\_callback\_data\_t, 137
  - hsa\_code\_object\_deserialize, 141
  - hsa\_code\_object\_destroy, 142
  - hsa\_code\_object\_get\_info, 142
  - hsa\_code\_object\_get\_symbol, 143
  - hsa\_code\_object\_get\_symbol\_from\_name, 143
  - HSA\_CODE\_OBJECT\_INFO\_DEFAULT\_FLOAT\_ROUNDING\_MODE, 138
  - HSA\_CODE\_OBJECT\_INFO\_ISA, 138
  - HSA\_CODE\_OBJECT\_INFO\_MACHINE\_MODEL, 138
  - HSA\_CODE\_OBJECT\_INFO\_PROFILE, 138
  - hsa\_code\_object\_info\_t, 138
  - HSA\_CODE\_OBJECT\_INFO\_TYPE, 138
  - HSA\_CODE\_OBJECT\_INFO\_VERSION, 138
  - hsa\_code\_object\_iterate\_symbols, 144
  - hsa\_code\_object\_serialize, 145
  - hsa\_code\_object\_t, 137
  - HSA\_CODE\_OBJECT\_TYPE\_PROGRAM, 139
  - hsa\_code\_object\_type\_t, 139
  - hsa\_code\_symbol\_get\_info, 146
  - HSA\_CODE\_SYMBOL\_INFO\_INDIRECT\_FUNCTION\_CALL\_CONVENTION, 141
  - HSA\_CODE\_SYMBOL\_INFO\_IS\_DEFINITION, 140
  - HSA\_CODE\_SYMBOL\_INFO\_KERNEL\_CALL\_CONVENTION, 141
  - HSA\_CODE\_SYMBOL\_INFO\_KERNEL\_DYNAMIC\_CALL\_STACK\_SIZE, 141
  - HSA\_CODE\_SYMBOL\_INFO\_KERNEL\_GROUP\_SEGMENT\_SIZE, 140
  - HSA\_CODE\_SYMBOL\_INFO\_KERNEL\_KERNARG\_SEGMENT\_ALIGNMENT, 140
  - HSA\_CODE\_SYMBOL\_INFO\_KERNEL\_KERNARG\_SEGMENT\_SIZE, 140
  - HSA\_CODE\_SYMBOL\_INFO\_KERNEL\_PRIVATE\_SEGMENT\_SIZE, 140
  - HSA\_CODE\_SYMBOL\_INFO\_LINKAGE, 140
  - HSA\_CODE\_SYMBOL\_INFO\_MODULE\_NAME, 139
  - HSA\_CODE\_SYMBOL\_INFO\_MODULE\_NAME\_LENGTH, 139
  - HSA\_CODE\_SYMBOL\_INFO\_NAME, 139
  - HSA\_CODE\_SYMBOL\_INFO\_NAME\_LENGTH, 139
  - hsa\_code\_symbol\_info\_t, 139
  - HSA\_CODE\_SYMBOL\_INFO\_TYPE, 139
  - HSA\_CODE\_SYMBOL\_INFO\_VARIABLE\_ALIGNMENT, 140
  - HSA\_CODE\_SYMBOL\_INFO\_VARIABLE\_ALLOCATION, 140
  - HSA\_CODE\_SYMBOL\_INFO\_VARIABLE\_IS\_CONST, 140
  - HSA\_CODE\_SYMBOL\_INFO\_VARIABLE\_SEGMENT, 140
  - HSA\_CODE\_SYMBOL\_INFO\_VARIABLE\_SIZE, 140
  - hsa\_code\_symbol\_t, 138
  - hsa\_executable\_load\_code\_object, 146
- code\_object\_storage\_base
  - hsa\_ven\_amd\_loader\_segment\_descriptor\_s, 366
- code\_object\_storage\_offset
  - hsa\_ven\_amd\_loader\_segment\_descriptor\_s, 366
- code\_object\_storage\_size
  - hsa\_ven\_amd\_loader\_segment\_descriptor\_s, 366
- code\_object\_storage\_type
  - hsa\_ven\_amd\_loader\_segment\_descriptor\_s, 367
- coherent\_support
  - hsa\_amd\_memory\_pool\_link\_info\_s, 298
- column
  - BrigDirectiveLoc, 216
- command\_buffer
  - hsa\_ven\_amd\_aqlprofile\_profile\_t, 357
- compare
  - BrigInstCmp, 229
- completion\_signal
  - hsa\_agent\_dispatch\_packet\_s, 288
  - hsa\_amd\_barrier\_value\_packet\_s, 291
  - hsa\_barrier\_and\_packet\_s, 307
  - hsa\_ext\_amd\_aql\_pm4\_packet\_t, 317
  - hsa\_kernel\_dispatch\_packet\_s, 339
- compute\_pgm\_rsrc1
  - amd\_kernel\_code\_s, 183
- compute\_pgm\_rsrc2
  - amd\_kernel\_code\_s, 184
- compute\_tmpring\_size
  - amd\_size\_s, 189
- cond
  - hsa\_amd\_barrier\_value\_packet\_s, 291
- control
  - BrigInstControl, 210
- control\_directives
  - amd\_size\_s, 184
- control\_directives\_mask
  - hsa\_ext\_control\_directives\_s, 319
- coord
  - amdgpu\_hsa\_sampler\_descriptor\_s, 204
- BrigOperandConstantSampler, 256
- coordinate\_mode
  - hsa\_ext\_sampler\_descriptor\_s, 336
- coordType
  - BrigInstImage, 231



- core
  - HsaApiTableContainer, 371
- core\_
  - HsaApiTable, 369
- CoreApiTable, 262
  - hsa\_agent\_extension\_supported\_fn, 265
  - hsa\_agent\_get\_exception\_policies\_fn, 265
  - hsa\_agent\_get\_info\_fn, 265
  - hsa\_agent\_iterate\_caches\_fn, 265
  - hsa\_agent\_iterate\_isas\_fn, 265
  - hsa\_agent\_iterate\_regions\_fn, 265
  - hsa\_agent\_major\_extension\_supported\_fn, 266
  - hsa\_cache\_get\_info\_fn, 266
  - hsa\_code\_object\_deserialize\_fn, 266
  - hsa\_code\_object\_destroy\_fn, 266
  - hsa\_code\_object\_get\_info\_fn, 266
  - hsa\_code\_object\_get\_symbol\_fn, 266
  - hsa\_code\_object\_get\_symbol\_from\_name\_fn, 267
  - hsa\_code\_object\_iterate\_symbols\_fn, 267
  - hsa\_code\_object\_reader\_create\_from\_file\_fn, 267
  - hsa\_code\_object\_reader\_create\_from\_memory\_fn, 267
  - hsa\_code\_object\_reader\_destroy\_fn, 267
  - hsa\_code\_object\_serialize\_fn, 267
  - hsa\_code\_symbol\_get\_info\_fn, 268
  - hsa\_executable\_agent\_global\_variable\_define\_fn, 268
  - hsa\_executable\_create\_alt\_fn, 268
  - hsa\_executable\_create\_fn, 268
  - hsa\_executable\_destroy\_fn, 268
  - hsa\_executable\_freeze\_fn, 268
  - hsa\_executable\_get\_info\_fn, 269
  - hsa\_executable\_get\_symbol\_by\_name\_fn, 269
  - hsa\_executable\_get\_symbol\_fn, 269
  - hsa\_executable\_global\_variable\_define\_fn, 269
  - hsa\_executable\_iterate\_agent\_symbols\_fn, 269
  - hsa\_executable\_iterate\_program\_symbols\_fn, 269
  - hsa\_executable\_iterate\_symbols\_fn, 270
  - hsa\_executable\_load\_agent\_code\_object\_fn, 270
  - hsa\_executable\_load\_code\_object\_fn, 270
  - hsa\_executable\_load\_program\_code\_object\_fn, 270
  - hsa\_executable\_readonly\_variable\_define\_fn, 270
  - hsa\_executable\_symbol\_get\_info\_fn, 270
  - hsa\_executable\_validate\_alt\_fn, 271
  - hsa\_executable\_validate\_fn, 271
  - hsa\_extension\_get\_name\_fn, 271
  - hsa\_init\_fn, 271
  - hsa\_isa\_compatible\_fn, 271
  - hsa\_isa\_from\_name\_fn, 271
  - hsa\_isa\_get\_exception\_policies\_fn, 272
  - hsa\_isa\_get\_info\_alt\_fn, 272
  - hsa\_isa\_get\_info\_fn, 272
  - hsa\_isa\_get\_round\_method\_fn, 272
  - hsa\_isa\_iterate\_wavefronts\_fn, 272
  - hsa\_iterate\_agents\_fn, 272
  - hsa\_memory\_allocate\_fn, 273
  - hsa\_memory\_assign\_agent\_fn, 273
  - hsa\_memory\_copy\_fn, 273
  - hsa\_memory\_deregister\_fn, 273
  - hsa\_memory\_free\_fn, 273
  - hsa\_memory\_register\_fn, 273
  - hsa\_queue\_add\_write\_index\_relaxed\_fn, 274
  - hsa\_queue\_add\_write\_index\_scacq\_screl\_fn, 274
  - hsa\_queue\_add\_write\_index\_scacquire\_fn, 274
  - hsa\_queue\_add\_write\_index\_screlease\_fn, 274
  - hsa\_queue\_cas\_write\_index\_relaxed\_fn, 274
  - hsa\_queue\_cas\_write\_index\_scacq\_screl\_fn, 274
  - hsa\_queue\_cas\_write\_index\_scacquire\_fn, 275
  - hsa\_queue\_cas\_write\_index\_screlease\_fn, 275
  - hsa\_queue\_create\_fn, 275
  - hsa\_queue\_destroy\_fn, 275
  - hsa\_queue\_inactivate\_fn, 275
  - hsa\_queue\_load\_read\_index\_relaxed\_fn, 275
  - hsa\_queue\_load\_read\_index\_scacquire\_fn, 276
  - hsa\_queue\_load\_write\_index\_relaxed\_fn, 276
  - hsa\_queue\_load\_write\_index\_scacquire\_fn, 276
  - hsa\_queue\_store\_read\_index\_relaxed\_fn, 276
  - hsa\_queue\_store\_read\_index\_screlease\_fn, 276
  - hsa\_queue\_store\_write\_index\_relaxed\_fn, 276
  - hsa\_queue\_store\_write\_index\_screlease\_fn, 277
  - hsa\_region\_get\_info\_fn, 277
  - hsa\_shut\_down\_fn, 277
  - hsa\_signal\_add\_relaxed\_fn, 277
  - hsa\_signal\_add\_scacq\_screl\_fn, 277
  - hsa\_signal\_add\_scacquire\_fn, 277
  - hsa\_signal\_add\_screlease\_fn, 278
  - hsa\_signal\_and\_relaxed\_fn, 278
  - hsa\_signal\_and\_scacq\_screl\_fn, 278
  - hsa\_signal\_and\_scacquire\_fn, 278
  - hsa\_signal\_and\_screlease\_fn, 278
  - hsa\_signal\_cas\_relaxed\_fn, 278
  - hsa\_signal\_cas\_scacq\_screl\_fn, 279
  - hsa\_signal\_cas\_scacquire\_fn, 279
  - hsa\_signal\_cas\_screlease\_fn, 279
  - hsa\_signal\_create\_fn, 279
  - hsa\_signal\_destroy\_fn, 279
  - hsa\_signal\_exchange\_relaxed\_fn, 279
  - hsa\_signal\_exchange\_scacq\_screl\_fn, 280
  - hsa\_signal\_exchange\_scacquire\_fn, 280
  - hsa\_signal\_exchange\_screlease\_fn, 280
  - hsa\_signal\_group\_create\_fn, 280
  - hsa\_signal\_group\_destroy\_fn, 280
  - hsa\_signal\_group\_wait\_any\_relaxed\_fn, 280
  - hsa\_signal\_group\_wait\_any\_scacquire\_fn, 281
  - hsa\_signal\_load\_relaxed\_fn, 281
  - hsa\_signal\_load\_scacquire\_fn, 281
  - hsa\_signal\_or\_relaxed\_fn, 281
  - hsa\_signal\_or\_scacq\_screl\_fn, 281
  - hsa\_signal\_or\_scacquire\_fn, 281
  - hsa\_signal\_or\_screlease\_fn, 282
  - hsa\_signal\_silent\_store\_relaxed\_fn, 282
  - hsa\_signal\_silent\_store\_screlease\_fn, 282
  - hsa\_signal\_store\_relaxed\_fn, 282
  - hsa\_signal\_store\_screlease\_fn, 282
  - hsa\_signal\_subtract\_relaxed\_fn, 282

- hsa\_signal\_subtract\_scacq\_screl\_fn, 283
- hsa\_signal\_subtract\_scacquire\_fn, 283
- hsa\_signal\_subtract\_screlease\_fn, 283
- hsa\_signal\_wait\_relaxed\_fn, 283
- hsa\_signal\_wait\_scacquire\_fn, 283
- hsa\_signal\_xor\_relaxed\_fn, 283
- hsa\_signal\_xor\_scacq\_screl\_fn, 284
- hsa\_signal\_xor\_scacquire\_fn, 284
- hsa\_signal\_xor\_screlease\_fn, 284
- hsa\_soft\_queue\_create\_fn, 284
- hsa\_status\_string\_fn, 284
- hsa\_system\_extension\_supported\_fn, 284
- hsa\_system\_get\_extension\_table\_fn, 285
- hsa\_system\_get\_info\_fn, 285
- hsa\_system\_get\_major\_extension\_table\_fn, 285
- hsa\_system\_major\_extension\_supported\_fn, 285
- hsa\_wavefront\_get\_info\_fn, 285
- version, 285
- counter\_id
  - hsa\_ven\_amd\_aqlprofile\_event\_t, 353
- data
  - hsa\_amd\_image\_descriptor\_s, 296
- debug\_private\_segment\_buffer\_sgpr
  - amd\_kernel\_code\_s, 184
- debug\_wavefront\_private\_segment\_offset\_sgpr
  - amd\_kernel\_code\_s, 184
- default\_float\_round
  - amdgpu\_hsa\_note\_hsail\_s, 199
- defaultFloatRound
  - BrigDirectiveModule, 217
- dep\_signal
  - hsa\_barrier\_and\_packet\_s, 307
  - hsa\_barrier\_or\_packet\_s, 309
- depth
  - amdgpu\_hsa\_image\_descriptor\_s, 197
  - BrigOperandConstantImage, 253
  - hsa\_ext\_image\_descriptor\_s, 325
- detect\_exceptions\_mask
  - hsa\_ext\_control\_directives\_s, 319
- deviceId
  - hsa\_amd\_image\_descriptor\_s, 296
- dim
  - BrigDirectiveVariable, 221
- doorbell\_signal
  - hsa\_queue\_s, 345
- elements
  - BrigOperandCodeList, 250
  - BrigOperandConstantOperandList, 255
  - BrigOperandOperandList, 258
- elf\_raw
  - amd\_runtime\_loader\_debug\_info\_s, 193
- elf\_size
  - amd\_runtime\_loader\_debug\_info\_s, 193
- enable\_break\_exceptions
  - amd\_control\_directives\_s, 179
- enable\_detect\_exceptions
  - amd\_control\_directives\_s, 179
- enabled\_control\_directives
  - amd\_control\_directives\_s, 180
- end
  - hsa\_amd\_profiling\_async\_copy\_time\_s, 305
  - hsa\_amd\_profiling\_dispatch\_time\_s, 306
- end\_ts
  - amd\_signal\_s, 194
- equivClass
  - BrigInstAtomic, 224
  - BrigInstImage, 231
  - BrigInstMem, 234
- event
  - hsa\_ven\_amd\_aqlprofile\_info\_data\_t, 355
- event\_count
  - hsa\_ven\_amd\_aqlprofile\_profile\_t, 357
- event\_id
  - amd\_signal\_s, 194
- event\_mailbox\_ptr
  - amd\_signal\_s, 194
- event\_type
  - hsa\_amd\_event\_s, 294
- events
  - hsa\_ven\_amd\_aqlprofile\_profile\_t, 357
- Executable, 113
  - hsa\_code\_object\_reader\_create\_from\_file, 121
  - hsa\_code\_object\_reader\_create\_from\_memory, 121
  - hsa\_code\_object\_reader\_destroy, 122
  - hsa\_executable\_agent\_global\_variable\_define, 122
  - hsa\_executable\_create, 123
  - hsa\_executable\_create\_alt, 124
  - hsa\_executable\_destroy, 125
  - hsa\_executable\_freeze, 125
  - hsa\_executable\_get\_info, 126
  - hsa\_executable\_get\_symbol, 126
  - hsa\_executable\_get\_symbol\_by\_name, 127
  - hsa\_executable\_global\_variable\_define, 128
  - HSA\_EXECUTABLE\_INFO\_DEFAULT\_FLOAT\_ROUNDING\_MODE, 116
  - HSA\_EXECUTABLE\_INFO\_PROFILE, 116
  - HSA\_EXECUTABLE\_INFO\_STATE, 116
  - hsa\_executable\_info\_t, 116
  - hsa\_executable\_iterate\_agent\_symbols, 128
  - hsa\_executable\_iterate\_program\_symbols, 129
  - hsa\_executable\_iterate\_symbols, 130
  - hsa\_executable\_load\_agent\_code\_object, 130
  - hsa\_executable\_load\_program\_code\_object, 132
  - hsa\_executable\_readonly\_variable\_define, 133
  - HSA\_EXECUTABLE\_STATE\_FROZEN, 116
  - hsa\_executable\_state\_t, 116
  - HSA\_EXECUTABLE\_STATE\_UNFROZEN, 116
  - hsa\_executable\_symbol\_get\_info, 134
  - HSA\_EXECUTABLE\_SYMBOL\_INFO\_AGENT, 117
  - HSA\_EXECUTABLE\_SYMBOL\_INFO\_INDIRECT\_FUNCTION\_CALL, 119

[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_INDIRECT\\_FUNCTION](#),  
[119](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_IS\\_DEFINITION](#),  
[117](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_KERNEL\\_CALL\\_CONVENTION](#),  
[119](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_KERNEL\\_DYNAMIC\\_CALLSTACK](#),  
[119](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_KERNEL\\_GROUP\\_SEGMENT\\_SIZE](#),  
[119](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_KERNEL\\_KERNEL\\_SEGMENT\\_ALIGNMENT](#),  
[118](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_KERNEL\\_KERNEL\\_SEGMENT\\_SIZE](#),  
[118](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_KERNEL\\_OBJECT](#),  
[118](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_KERNEL\\_PRIVATE\\_SEGMENT\\_SIZE](#),  
[119](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_LINKAGE](#),  
[117](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_MODULE\\_NAME](#),  
[117](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_MODULE\\_NAME\\_LENGTH](#),  
[117](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_NAME](#), [117](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_NAME\\_LENGTH](#),  
[117](#)  
[hsa\\_executable\\_symbol\\_info\\_t](#), [117](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_TYPE](#), [117](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_VARIABLE\\_ADDRESS](#),  
[117](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_VARIABLE\\_ALIGNMENT](#),  
[118](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_VARIABLE\\_ALLOCATION](#),  
[118](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_VARIABLE\\_IS\\_CONSTANT](#),  
[118](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_VARIABLE\\_SEGMENT](#),  
[118](#)  
[HSA\\_EXECUTABLE\\_SYMBOL\\_INFO\\_VARIABLE\\_SIZE](#),  
[118](#)  
[hsa\\_executable\\_symbol\\_t](#), [115](#)  
[hsa\\_executable\\_validate](#), [134](#)  
[hsa\\_executable\\_validate\\_alt](#), [135](#)  
[HSA\\_SYMBOL\\_KIND\\_INDIRECT\\_FUNCTION](#),  
[120](#)  
[HSA\\_SYMBOL\\_KIND\\_KERNEL](#), [120](#)  
[hsa\\_symbol\\_kind\\_t](#), [119](#)  
[HSA\\_SYMBOL\\_KIND\\_VARIABLE](#), [120](#)  
[HSA\\_SYMBOL\\_LINKAGE\\_MODULE](#), [120](#)  
[HSA\\_SYMBOL\\_LINKAGE\\_PROGRAM](#), [120](#)  
[hsa\\_symbol\\_linkage\\_t](#), [120](#)  
[HSA\\_VARIABLE\\_ALLOCATION\\_AGENT](#), [120](#)  
[HSA\\_VARIABLE\\_ALLOCATION\\_PROGRAM](#), [120](#)  
[hsa\\_variable\\_allocation\\_t](#), [120](#)  
[HSA\\_VARIABLE\\_SEGMENT\\_GLOBAL](#), [121](#)  
[HSA\\_VARIABLE\\_SEGMENT\\_READONLY](#), [121](#)  
[hsa\\_variable\\_segment\\_t](#), [120](#)  
[hsa\\_ven\\_amd\\_loader\\_segment\\_descriptor\\_s](#), [367](#)  
[fault\\_reason\\_mask](#)  
[hsa\\_andcpu\\_memory\\_fault\\_info\\_s](#), [295](#)  
[features](#)  
[hsa\\_queue\\_s](#), [345](#)  
[filename](#)  
[BrigDirective](#), [216](#)  
[hsa\\_ext\\_hsa\\_sampler\\_descriptor\\_s](#), [204](#)  
[BrigOperandConstantSampler](#), [256](#)  
[hsa\\_ext\\_sampler\\_descriptor\\_s](#), [337](#)  
[Finalization Extensions](#), [147](#)  
[HSA\\_EXT\\_STATUS\\_ERROR\\_DIRECTIVE\\_MISMATCH](#),  
[148](#)  
[HSA\\_EXT\\_STATUS\\_ERROR\\_FINALIZATION\\_FAILED](#),  
[148](#)  
[HSA\\_EXT\\_STATUS\\_ERROR\\_INCOMPATIBLE\\_MODULE](#),  
[148](#)  
[HSA\\_EXT\\_STATUS\\_ERROR\\_INVALID\\_MODULE](#),  
[148](#)  
[HSA\\_EXT\\_STATUS\\_ERROR\\_INVALID\\_PROGRAM](#),  
[148](#)  
[HSA\\_EXT\\_STATUS\\_ERROR\\_MODULE\\_ALREADY\\_INCLUDED](#),  
[148](#)  
[HSA\\_EXT\\_STATUS\\_ERROR\\_SYMBOL\\_MISMATCH](#),  
[148](#)  
[Finalization Program](#), [148](#)  
[HSA\\_EXT\\_FINALIZER\\_CALL\\_CONVENTION\\_AUTO](#),  
[150](#)  
[hsa\\_ext\\_finalizer\\_call\\_convention\\_t](#), [150](#)  
[hsa\\_ext\\_module\\_t](#), [149](#)  
[hsa\\_ext\\_program\\_add\\_module](#), [150](#)  
[hsa\\_ext\\_program\\_create](#), [151](#)  
[hsa\\_ext\\_program\\_destroy](#), [152](#)  
[hsa\\_ext\\_program\\_finalize](#), [152](#)  
[hsa\\_ext\\_program\\_get\\_info](#), [153](#)  
[HSA\\_EXT\\_PROGRAM\\_INFO\\_DEFAULT\\_FLOAT\\_ROUNDING\\_MODE](#),  
[150](#)  
[HSA\\_EXT\\_PROGRAM\\_INFO\\_MACHINE\\_MODEL](#),  
[150](#)  
[HSA\\_EXT\\_PROGRAM\\_INFO\\_PROFILE](#), [150](#)  
[hsa\\_ext\\_program\\_info\\_t](#), [150](#)  
[hsa\\_ext\\_program\\_iterate\\_modules](#), [154](#)  
[finalizer\\_ext](#)  
[HsaApiTableContainer](#), [371](#)  
[finalizer\\_ext\\_](#)  
[HsaApiTable](#), [369](#)  
[FinalizerExtTable](#), [286](#)  
[hsa\\_ext\\_program\\_add\\_module\\_fn](#), [286](#)  
[hsa\\_ext\\_program\\_create\\_fn](#), [286](#)  
[hsa\\_ext\\_program\\_destroy\\_fn](#), [286](#)  
[hsa\\_ext\\_program\\_finalize\\_fn](#), [287](#)  
[hsa\\_ext\\_program\\_get\\_info\\_fn](#), [287](#)  
[hsa\\_ext\\_program\\_iterate\\_modules\\_fn](#), [287](#)  
[version](#), [287](#)  
[firstCodeBlockEntry](#)

- BrigDirectiveExecutable, 211
- firstInArg
  - BrigDirectiveExecutable, 211
- format
  - hsa\_ext\_image\_descriptor\_s, 325
- gds\_segment\_byte\_size
  - amd\_kernel\_code\_s, 184
- geometry
  - amdgpu\_hsa\_image\_descriptor\_s, 197
  - BrigInstImage, 232
  - BrigInstQueryImage, 238
  - BrigOperandConstantImage, 253
  - hsa\_ext\_image\_descriptor\_s, 325
- global\_flags
  - hsa\_amd\_pointer\_info\_s, 303
- globalSegmentMemoryScope
  - BrigInstMemFence, 236
- grid\_size\_x
  - hsa\_kernel\_dispatch\_packet\_s, 339
- grid\_size\_y
  - hsa\_kernel\_dispatch\_packet\_s, 340
- grid\_size\_z
  - hsa\_kernel\_dispatch\_packet\_s, 340
- group\_segment\_alignment
  - amd\_kernel\_code\_s, 184
- group\_segment\_aperture\_base\_hi
  - amd\_queue\_s, 189
- group\_segment\_size
  - hsa\_kernel\_dispatch\_packet\_s, 340
- groupSegmentMemoryScope
  - BrigInstMemFence, 236
- handle
  - hsa\_agent\_s, 290
  - hsa\_amd\_ipc\_memory\_s, 297
  - hsa\_amd\_memory\_pool\_s, 301
  - hsa\_cache\_s, 311
  - hsa\_callback\_data\_s, 312
  - hsa\_code\_object\_reader\_s, 312
  - hsa\_code\_object\_s, 313
  - hsa\_code\_symbol\_s, 314
  - hsa\_executable\_s, 316
  - hsa\_executable\_symbol\_s, 316
  - hsa\_ext\_image\_s, 328
  - hsa\_ext\_program\_s, 336
  - hsa\_ext\_sampler\_s, 337
  - hsa\_isa\_s, 338
  - hsa\_loaded\_code\_object\_s, 343
  - hsa\_region\_s, 347
  - hsa\_signal\_group\_s, 348
  - hsa\_signal\_s, 348
  - hsa\_wavefront\_s, 368
- hardware\_doorbell\_ptr
  - amd\_signal\_s, 195
- hash
  - BrigModuleHeader, 246
- HDP\_MEM\_FLUSH\_CNTL
  - hsa\_amd\_hdp\_flush\_s, 295
- HDP\_REG\_FLUSH\_CNTL
  - hsa\_amd\_hdp\_flush\_s, 296
- header
  - hsa\_agent\_dispatch\_packet\_s, 288
  - hsa\_amd\_barrier\_value\_packet\_s, 291
  - hsa\_amd\_packet\_header\_s, 301
  - hsa\_barrier\_and\_packet\_s, 308
  - hsa\_barrier\_or\_packet\_s, 309
  - hsa\_ext\_amd\_aql\_pm4\_packet\_t, 317
  - hsa\_kernel\_dispatch\_packet\_s, 340
- headerByteCount
  - BrigSectionHeader, 261
- height
  - amdgpu\_hsa\_image\_descriptor\_s, 197
  - BrigOperandConstantImage, 254
  - hsa\_ext\_image\_descriptor\_s, 325
- hi
  - BrigUInt64, 262
- hostBaseAddress
  - hsa\_amd\_pointer\_info\_s, 303
- HSA\_ACCESS\_PERMISSION\_RO
  - Runtime Notifications, 16
- HSA\_ACCESS\_PERMISSION\_RW
  - Runtime Notifications, 16
- hsa\_access\_permission\_t
  - Runtime Notifications, 16
- HSA\_ACCESS\_PERMISSION\_WO
  - Runtime Notifications, 16
- hsa\_agent\_dispatch\_packet\_s, 287
  - arg, 288
  - completion\_signal, 288
  - header, 288
  - reserved0, 288
  - reserved1, 289
  - reserved2, 289
  - return\_address, 289
  - type, 289
- hsa\_agent\_extension\_supported
  - System and Agent Information, 31
- hsa\_agent\_extension\_supported\_fn
  - CoreApiTable, 265
- HSA\_AGENT\_FEATURE\_AGENT\_DISPATCH
  - System and Agent Information, 22
- HSA\_AGENT\_FEATURE\_KERNEL\_DISPATCH
  - System and Agent Information, 22
- hsa\_agent\_feature\_t
  - System and Agent Information, 21
- hsa\_agent\_get\_exception\_policies
  - System and Agent Information, 31
- hsa\_agent\_get\_exception\_policies\_fn
  - CoreApiTable, 265
- hsa\_agent\_get\_info
  - System and Agent Information, 32
- hsa\_agent\_get\_info\_fn
  - CoreApiTable, 265
- HSA\_AGENT\_INFO\_BASE\_PROFILE\_DEFAULT\_FLOAT\_ROUNDING\_M
  - System and Agent Information, 23
- HSA\_AGENT\_INFO\_CACHE\_SIZE

- System and Agent Information, [26](#)
- HSA\_AGENT\_INFO\_DEFAULT\_FLOAT\_ROUNDING\_MODE
  - System and Agent Information, [23](#)
- HSA\_AGENT\_INFO\_DEVICE
  - System and Agent Information, [26](#)
- HSA\_AGENT\_INFO\_EXTENSIONS
  - System and Agent Information, [26](#)
- HSA\_AGENT\_INFO\_FAST\_F16\_OPERATION
  - System and Agent Information, [24](#)
- HSA\_AGENT\_INFO\_FBARRIER\_MAX\_SIZE
  - System and Agent Information, [25](#)
- HSA\_AGENT\_INFO\_FEATURE
  - System and Agent Information, [22](#)
- HSA\_AGENT\_INFO\_GRID\_MAX\_DIM
  - System and Agent Information, [25](#)
- HSA\_AGENT\_INFO\_GRID\_MAX\_SIZE
  - System and Agent Information, [25](#)
- HSA\_AGENT\_INFO\_ISA
  - System and Agent Information, [26](#)
- HSA\_AGENT\_INFO\_LAST
  - System and Agent Information, [27](#)
- HSA\_AGENT\_INFO\_MACHINE\_MODEL
  - System and Agent Information, [22](#)
- HSA\_AGENT\_INFO\_NAME
  - System and Agent Information, [22](#)
- HSA\_AGENT\_INFO\_NODE
  - System and Agent Information, [26](#)
- HSA\_AGENT\_INFO\_PROFILE
  - System and Agent Information, [23](#)
- HSA\_AGENT\_INFO\_QUEUE\_MAX\_SIZE
  - System and Agent Information, [26](#)
- HSA\_AGENT\_INFO\_QUEUE\_MIN\_SIZE
  - System and Agent Information, [26](#)
- HSA\_AGENT\_INFO\_QUEUE\_TYPE
  - System and Agent Information, [26](#)
- HSA\_AGENT\_INFO\_QUEUES\_MAX
  - System and Agent Information, [26](#)
- hsa\_agent\_info\_t
  - System and Agent Information, [22](#)
- HSA\_AGENT\_INFO\_VENDOR\_NAME
  - System and Agent Information, [22](#)
- HSA\_AGENT\_INFO\_VERSION\_MAJOR
  - System and Agent Information, [26](#)
- HSA\_AGENT\_INFO\_VERSION\_MINOR
  - System and Agent Information, [27](#)
- HSA\_AGENT\_INFO\_WAVEFRONT\_SIZE
  - System and Agent Information, [24](#)
- HSA\_AGENT\_INFO\_WORKGROUP\_MAX\_DIM
  - System and Agent Information, [24](#)
- HSA\_AGENT\_INFO\_WORKGROUP\_MAX\_SIZE
  - System and Agent Information, [25](#)
- hsa\_agent\_iterate\_caches
  - System and Agent Information, [32](#)
- hsa\_agent\_iterate\_caches\_fn
  - CoreApiTable, [265](#)
- hsa\_agent\_iterate\_isas
  - Instruction Set Architecture., [107](#)
- hsa\_agent\_iterate\_isas\_fn
  - CoreApiTable, [265](#)
- hsa\_agent\_iterate\_regions
  - Memory, [76](#)
- hsa\_agent\_iterate\_regions\_fn
  - CoreApiTable, [265](#)
- hsa\_agent\_major\_extension\_supported
  - System and Agent Information, [33](#)
- hsa\_agent\_major\_extension\_supported\_fn
  - CoreApiTable, [266](#)
- hsa\_agent\_s, [290](#)
  - handle, [290](#)
- hsa\_amd\_barrier\_value\_packet\_s, [290](#)
  - completion\_signal, [291](#)
  - cond, [291](#)
  - header, [291](#)
  - mask, [292](#)
  - reserved0, [292](#)
  - reserved1, [292](#)
  - reserved2, [292](#)
  - reserved3, [292](#)
  - signal, [293](#)
  - value, [293](#)
- hsa\_amd\_event\_s, [293](#)
  - event\_type, [294](#)
  - memory\_fault, [294](#)
- HSA\_AMD\_FIRST\_EXTENSION
  - System and Agent Information, [29](#)
- hsa\_amd\_gpu\_memory\_fault\_info\_s, [294](#)
  - agent, [295](#)
  - fault\_reason\_mask, [295](#)
  - virtual\_address, [295](#)
- hsa\_amd\_hdp\_flush\_s, [295](#)
  - HDP\_MEM\_FLUSH\_CNTL, [295](#)
  - HDP\_REG\_FLUSH\_CNTL, [296](#)
- hsa\_amd\_image\_descriptor\_s, [296](#)
  - data, [296](#)
  - deviceId, [296](#)
  - version, [297](#)
- hsa\_amd\_ipc\_memory\_s, [297](#)
  - handle, [297](#)
- HSA\_AMD\_LAST\_EXTENSION
  - System and Agent Information, [29](#)
- hsa\_amd\_memory\_pool\_link\_info\_s, [298](#)
  - atomic\_support\_32bit, [298](#)
  - atomic\_support\_64bit, [298](#)
  - coherent\_support, [298](#)
  - link\_type, [299](#)
  - max\_bandwidth, [299](#)
  - max\_latency, [299](#)
  - min\_bandwidth, [299](#)
  - min\_latency, [299](#)
  - numa\_distance, [300](#)
- hsa\_amd\_memory\_pool\_s, [300](#)
  - handle, [301](#)
- hsa\_amd\_packet\_header\_s, [301](#)
  - AmdFormat, [301](#)
  - header, [301](#)
  - reserved, [302](#)

- hsa\_amd\_packet\_type8\_t
  - Architected Queuing Language, 99
- HSA\_AMD\_PACKET\_TYPE\_BARRIER\_VALUE
  - Architected Queuing Language, 100
- hsa\_amd\_packet\_type\_t
  - Architected Queuing Language, 100
- hsa\_amd\_pointer\_info\_s, 302
  - agentBaseAddress, 303
  - agentOwner, 303
  - global\_flags, 303
  - hostBaseAddress, 303
  - size, 303
  - sizeInBytes, 303
  - type, 304
  - userData, 304
- hsa\_amd\_profiling\_async\_copy\_time\_s, 304
  - end, 305
  - start, 305
- hsa\_amd\_profiling\_dispatch\_time\_s, 305
  - end, 306
  - start, 306
- hsa\_amd\_svm\_attribute\_pair\_s, 306
  - attribute, 306
  - value, 306
- HSA\_AMD\_SYSTEM\_INFO\_BUILD\_VERSION
  - System and Agent Information, 30
- HSA\_AMD\_SYSTEM\_INFO\_SVM\_ACCESSIBLE\_BY\_DEFAULT
  - System and Agent Information, 30
- HSA\_AMD\_SYSTEM\_INFO\_SVM\_SUPPORTED
  - System and Agent Information, 30
- hsa\_barrier\_and\_packet\_s, 307
  - completion\_signal, 307
  - dep\_signal, 307
  - header, 308
  - reserved0, 308
  - reserved1, 308
  - reserved2, 308
- hsa\_barrier\_or\_packet\_s, 309
  - completion\_signal, 309
  - dep\_signal, 309
  - header, 309
  - reserved0, 310
  - reserved1, 310
  - reserved2, 310
- hsa\_cache\_get\_info
  - System and Agent Information, 34
- hsa\_cache\_get\_info\_fn
  - CoreApiTable, 266
- HSA\_CACHE\_INFO\_LEVEL
  - System and Agent Information, 27
- HSA\_CACHE\_INFO\_NAME
  - System and Agent Information, 27
- HSA\_CACHE\_INFO\_NAME\_LENGTH
  - System and Agent Information, 27
- HSA\_CACHE\_INFO\_SIZE
  - System and Agent Information, 27
- hsa\_cache\_info\_t
  - System and Agent Information, 27
- hsa\_cache\_s, 310
  - handle, 311
- hsa\_callback\_data\_s, 311
  - handle, 312
- hsa\_callback\_data\_t
  - Code Objects (deprecated), 137
- hsa\_code\_object\_deserialize
  - Code Objects (deprecated), 141
- hsa\_code\_object\_deserialize\_fn
  - CoreApiTable, 266
- hsa\_code\_object\_destroy
  - Code Objects (deprecated), 142
- hsa\_code\_object\_destroy\_fn
  - CoreApiTable, 266
- hsa\_code\_object\_get\_info
  - Code Objects (deprecated), 142
- hsa\_code\_object\_get\_info\_fn
  - CoreApiTable, 266
- hsa\_code\_object\_get\_symbol
  - Code Objects (deprecated), 143
- hsa\_code\_object\_get\_symbol\_fn
  - CoreApiTable, 266
- hsa\_code\_object\_get\_symbol\_from\_name
  - Code Objects (deprecated), 143
- hsa\_code\_object\_get\_symbol\_from\_name\_fn
  - CoreApiTable, 267
- HSA\_CODE\_OBJECT\_INFO\_DEFAULT\_FLOAT\_ROUNDING\_MODE
  - Code Objects (deprecated), 138
- HSA\_CODE\_OBJECT\_INFO\_ISA
  - Code Objects (deprecated), 138
- HSA\_CODE\_OBJECT\_INFO\_MACHINE\_MODEL
  - Code Objects (deprecated), 138
- HSA\_CODE\_OBJECT\_INFO\_PROFILE
  - Code Objects (deprecated), 138
- hsa\_code\_object\_info\_t
  - Code Objects (deprecated), 138
- HSA\_CODE\_OBJECT\_INFO\_TYPE
  - Code Objects (deprecated), 138
- HSA\_CODE\_OBJECT\_INFO\_VERSION
  - Code Objects (deprecated), 138
- hsa\_code\_object\_iterate\_symbols
  - Code Objects (deprecated), 144
- hsa\_code\_object\_iterate\_symbols\_fn
  - CoreApiTable, 267
- hsa\_code\_object\_reader\_create\_from\_file
  - Executable, 121
- hsa\_code\_object\_reader\_create\_from\_file\_fn
  - CoreApiTable, 267
- hsa\_code\_object\_reader\_create\_from\_memory
  - Executable, 121
- hsa\_code\_object\_reader\_create\_from\_memory\_fn
  - CoreApiTable, 267
- hsa\_code\_object\_reader\_destroy
  - Executable, 122
- hsa\_code\_object\_reader\_destroy\_fn
  - CoreApiTable, 267
- hsa\_code\_object\_reader\_s, 312
  - handle, 312



- hsa\_code\_object\_s, [313](#)
  - handle, [313](#)
- hsa\_code\_object\_serialize
  - Code Objects (deprecated), [145](#)
- hsa\_code\_object\_serialize\_fn
  - CoreApiTable, [267](#)
- hsa\_code\_object\_t
  - Code Objects (deprecated), [137](#)
- HSA\_CODE\_OBJECT\_TYPE\_PROGRAM
  - Code Objects (deprecated), [139](#)
- hsa\_code\_object\_type\_t
  - Code Objects (deprecated), [139](#)
- hsa\_code\_symbol\_get\_info
  - Code Objects (deprecated), [146](#)
- hsa\_code\_symbol\_get\_info\_fn
  - CoreApiTable, [268](#)
- HSA\_CODE\_SYMBOL\_INFO\_INDIRECT\_FUNCTION\_CALL\_CONVENTION
  - Code Objects (deprecated), [141](#)
- HSA\_CODE\_SYMBOL\_INFO\_IS\_DEFINITION
  - Code Objects (deprecated), [140](#)
- HSA\_CODE\_SYMBOL\_INFO\_KERNEL\_CALL\_CONVENTION
  - Code Objects (deprecated), [141](#)
- HSA\_CODE\_SYMBOL\_INFO\_KERNEL\_DYNAMIC\_CALL\_CONVENTION
  - Code Objects (deprecated), [141](#)
- HSA\_CODE\_SYMBOL\_INFO\_KERNEL\_GROUP\_SEGMENT
  - Code Objects (deprecated), [140](#)
- HSA\_CODE\_SYMBOL\_INFO\_KERNEL\_KERNEL\_ARGUMENT
  - Code Objects (deprecated), [140](#)
- HSA\_CODE\_SYMBOL\_INFO\_KERNEL\_KERNEL\_ARGUMENT\_SIZE
  - Code Objects (deprecated), [140](#)
- HSA\_CODE\_SYMBOL\_INFO\_KERNEL\_PRIVATE\_SEGMENT
  - Code Objects (deprecated), [140](#)
- HSA\_CODE\_SYMBOL\_INFO\_LINKAGE
  - Code Objects (deprecated), [140](#)
- HSA\_CODE\_SYMBOL\_INFO\_MODULE\_NAME
  - Code Objects (deprecated), [139](#)
- HSA\_CODE\_SYMBOL\_INFO\_MODULE\_NAME\_LENGTH
  - Code Objects (deprecated), [139](#)
- HSA\_CODE\_SYMBOL\_INFO\_NAME
  - Code Objects (deprecated), [139](#)
- HSA\_CODE\_SYMBOL\_INFO\_NAME\_LENGTH
  - Code Objects (deprecated), [139](#)
- hsa\_code\_symbol\_info\_t
  - Code Objects (deprecated), [139](#)
- HSA\_CODE\_SYMBOL\_INFO\_TYPE
  - Code Objects (deprecated), [139](#)
- HSA\_CODE\_SYMBOL\_INFO\_VARIABLE\_ALIGNMENT
  - Code Objects (deprecated), [140](#)
- HSA\_CODE\_SYMBOL\_INFO\_VARIABLE\_ALLOCATION
  - Code Objects (deprecated), [140](#)
- HSA\_CODE\_SYMBOL\_INFO\_VARIABLE\_IS\_CONST
  - Code Objects (deprecated), [140](#)
- HSA\_CODE\_SYMBOL\_INFO\_VARIABLE\_SEGMENT
  - Code Objects (deprecated), [140](#)
- HSA\_CODE\_SYMBOL\_INFO\_VARIABLE\_SIZE
  - Code Objects (deprecated), [140](#)
- hsa\_code\_symbol\_s, [313](#)
  - handle, [314](#)
- hsa\_code\_symbol\_t
  - Code Objects (deprecated), [138](#)
- HSA\_DEFAULT\_FLOAT\_ROUNDING\_MODE\_DEFAULT
  - System and Agent Information, [27](#)
- HSA\_DEFAULT\_FLOAT\_ROUNDING\_MODE\_NEAR
  - System and Agent Information, [27](#)
- hsa\_default\_float\_rounding\_mode\_t
  - System and Agent Information, [27](#)
- HSA\_DEFAULT\_FLOAT\_ROUNDING\_MODE\_ZERO
  - System and Agent Information, [27](#)
- HSA\_DEVICE\_TYPE\_CPU
  - System and Agent Information, [28](#)
- HSA\_DEVICE\_TYPE\_DSP
  - System and Agent Information, [28](#)
- HSA\_DEVICE\_TYPE\_GPU
  - System and Agent Information, [28](#)
- hsa\_dim3\_s, [314](#)
  - x, [315](#)
  - y, [315](#)
  - z, [315](#)
- HSA\_ENDIANNESS\_BIG
  - System and Agent Information, [28](#)
- HSA\_ENDIANNESS\_LITTLE
  - System and Agent Information, [28](#)
- hsa\_exception\_t
  - System and Agent Information, [28](#)
- HSA\_EXCEPTION\_POLICY\_BREAK
  - System and Agent Information, [28](#)
- HSA\_EXCEPTION\_POLICY\_DETECT
  - System and Agent Information, [28](#)
- hsa\_exception\_policy\_t
  - System and Agent Information, [28](#)
- hsa\_executable\_agent\_global\_variable\_define
  - Executable, [122](#)
- hsa\_executable\_agent\_global\_variable\_define\_fn
  - CoreApiTable, [268](#)
- hsa\_executable\_create
  - Executable, [123](#)
- hsa\_executable\_create\_alt
  - Executable, [124](#)
- hsa\_executable\_create\_alt\_fn
  - CoreApiTable, [268](#)
- hsa\_executable\_create\_fn
  - CoreApiTable, [268](#)
- hsa\_executable\_destroy
  - Executable, [125](#)
- hsa\_executable\_destroy\_fn
  - CoreApiTable, [268](#)
- hsa\_executable\_freeze
  - Executable, [125](#)
- hsa\_executable\_freeze\_fn
  - CoreApiTable, [268](#)
- hsa\_executable\_get\_info
  - Executable, [126](#)
- hsa\_executable\_get\_info\_fn
  - CoreApiTable, [269](#)

- hsa\_executable\_get\_symbol
  - Executable, [126](#)
- hsa\_executable\_get\_symbol\_by\_name
  - Executable, [127](#)
- hsa\_executable\_get\_symbol\_by\_name\_fn
  - CoreApiTable, [269](#)
- hsa\_executable\_get\_symbol\_fn
  - CoreApiTable, [269](#)
- hsa\_executable\_global\_variable\_define
  - Executable, [128](#)
- hsa\_executable\_global\_variable\_define\_fn
  - CoreApiTable, [269](#)
- HSA\_EXECUTABLE\_INFO\_DEFAULT\_FLOAT\_ROUNDING\_MODE
  - Executable, [116](#)
- HSA\_EXECUTABLE\_INFO\_PROFILE
  - Executable, [116](#)
- HSA\_EXECUTABLE\_INFO\_STATE
  - Executable, [116](#)
- hsa\_executable\_info\_t
  - Executable, [116](#)
- hsa\_executable\_iterate\_agent\_symbols
  - Executable, [128](#)
- hsa\_executable\_iterate\_agent\_symbols\_fn
  - CoreApiTable, [269](#)
- hsa\_executable\_iterate\_program\_symbols
  - Executable, [129](#)
- hsa\_executable\_iterate\_program\_symbols\_fn
  - CoreApiTable, [269](#)
- hsa\_executable\_iterate\_symbols
  - Executable, [130](#)
- hsa\_executable\_iterate\_symbols\_fn
  - CoreApiTable, [270](#)
- hsa\_executable\_load\_agent\_code\_object
  - Executable, [130](#)
- hsa\_executable\_load\_agent\_code\_object\_fn
  - CoreApiTable, [270](#)
- hsa\_executable\_load\_code\_object
  - Code Objects (deprecated)., [146](#)
- hsa\_executable\_load\_code\_object\_fn
  - CoreApiTable, [270](#)
- hsa\_executable\_load\_program\_code\_object
  - Executable, [132](#)
- hsa\_executable\_load\_program\_code\_object\_fn
  - CoreApiTable, [270](#)
- hsa\_executable\_readonly\_variable\_define
  - Executable, [133](#)
- hsa\_executable\_readonly\_variable\_define\_fn
  - CoreApiTable, [270](#)
- hsa\_executable\_s, [315](#)
  - handle, [316](#)
- HSA\_EXECUTABLE\_STATE\_FROZEN
  - Executable, [116](#)
- hsa\_executable\_state\_t
  - Executable, [116](#)
- HSA\_EXECUTABLE\_STATE\_UNFROZEN
  - Executable, [116](#)
- hsa\_executable\_symbol\_get\_info
  - Executable, [134](#)
- hsa\_executable\_symbol\_get\_info\_fn
  - CoreApiTable, [270](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_AGENT
  - Executable, [117](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_INDIRECT\_FUNCTION\_CALL\_COUNT
  - Executable, [119](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_INDIRECT\_FUNCTION\_OBJECT\_ID
  - Executable, [119](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_IS\_DEFINITION
  - Executable, [117](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_KERNEL\_CALL\_CONVENTION
  - Executable, [119](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_KERNEL\_DYNAMIC\_CALLSTACK\_SIZE
  - Executable, [119](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_KERNEL\_GROUP\_SEGMENT\_SIZE
  - Executable, [119](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_KERNEL\_KERNARG\_SEGMENT\_SIZE
  - Executable, [118](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_KERNEL\_KERNARG\_SEGMENT\_SIZE\_MAX
  - Executable, [118](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_KERNEL\_OBJECT
  - Executable, [118](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_KERNEL\_PRIVATE\_SEGMENT\_SIZE
  - Executable, [119](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_LINKAGE
  - Executable, [117](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_MODULE\_NAME
  - Executable, [117](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_MODULE\_NAME\_LENGTH
  - Executable, [117](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_NAME
  - Executable, [117](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_NAME\_LENGTH
  - Executable, [117](#)
- hsa\_executable\_symbol\_info\_t
  - Executable, [117](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_TYPE
  - Executable, [117](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_VARIABLE\_ADDRESS
  - Executable, [117](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_VARIABLE\_ALIGNMENT
  - Executable, [118](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_VARIABLE\_ALLOCATION
  - Executable, [118](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_VARIABLE\_IS\_CONST
  - Executable, [118](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_VARIABLE\_SEGMENT
  - Executable, [118](#)
- HSA\_EXECUTABLE\_SYMBOL\_INFO\_VARIABLE\_SIZE
  - Executable, [118](#)
- hsa\_executable\_symbol\_s, [316](#)
  - handle, [316](#)
- hsa\_executable\_symbol\_t
  - Executable, [115](#)
- hsa\_executable\_validate
  - Executable, [134](#)
- hsa\_executable\_validate\_alt
  - Executable, [135](#)



- hsa\_executable\_validate\_alt\_fn
  - CoreApiTable, 271
- hsa\_executable\_validate\_fn
  - CoreApiTable, 271
- HSA\_EXT\_AGENT\_INFO\_IMAGE\_1D\_MAX\_ELEMENTS
  - Images and Samplers, 161
- HSA\_EXT\_AGENT\_INFO\_IMAGE\_1DA\_MAX\_ELEMENTS
  - Images and Samplers, 161
- HSA\_EXT\_AGENT\_INFO\_IMAGE\_1DB\_MAX\_ELEMENTS
  - Images and Samplers, 161
- HSA\_EXT\_AGENT\_INFO\_IMAGE\_2D\_MAX\_ELEMENTS
  - Images and Samplers, 161
- HSA\_EXT\_AGENT\_INFO\_IMAGE\_2DA\_MAX\_ELEMENTS
  - Images and Samplers, 161
- HSA\_EXT\_AGENT\_INFO\_IMAGE\_2DADEPTH\_MAX\_ELEMENTS
  - Images and Samplers, 161
- HSA\_EXT\_AGENT\_INFO\_IMAGE\_2DDEPTH\_MAX\_ELEMENTS
  - Images and Samplers, 161
- HSA\_EXT\_AGENT\_INFO\_IMAGE\_3D\_MAX\_ELEMENTS
  - Images and Samplers, 161
- HSA\_EXT\_AGENT\_INFO\_IMAGE\_ARRAY\_MAX\_LAYERS
  - Images and Samplers, 161
- HSA\_EXT\_AGENT\_INFO\_IMAGE\_LINEAR\_ROW\_PITCH\_ALIGNMENT
  - Images and Samplers, 161
- HSA\_EXT\_AGENT\_INFO\_MAX\_IMAGE\_RD\_HANDLES
  - Images and Samplers, 161
- HSA\_EXT\_AGENT\_INFO\_MAX\_IMAGE\_RORW\_HANDLES
  - Images and Samplers, 161
- HSA\_EXT\_AGENT\_INFO\_MAX\_SAMPLER\_HANDLERS
  - Images and Samplers, 161
- hsa\_ext\_amd\_aql\_pm4\_packet\_t, 317
  - completion\_signal, 317
  - header, 317
  - pm4\_command, 317
- hsa\_ext\_control\_directives\_s, 318
  - break\_exceptions\_mask, 318
  - control\_directives\_mask, 319
  - detect\_exceptions\_mask, 319
  - max\_dynamic\_group\_size, 319
  - max\_flat\_grid\_size, 319
  - max\_flat\_workgroup\_size, 320
  - required\_dim, 320
  - required\_grid\_size, 320
  - required\_workgroup\_size, 321
  - reserved1, 321
  - reserved2, 321
- hsa\_ext\_finalizer\_1\_00\_pfn\_s, 322
  - hsa\_ext\_program\_add\_module, 322
  - hsa\_ext\_program\_create, 322
  - hsa\_ext\_program\_destroy, 322
  - hsa\_ext\_program\_finalize, 322
  - hsa\_ext\_program\_get\_info, 323
  - hsa\_ext\_program\_iterate\_modules, 323
- HSA\_EXT\_FINALIZER\_CALL\_CONVENTION\_AUTO
  - Finalization Program, 150
- hsa\_ext\_finalizer\_call\_convention\_t
  - Finalization Program, 150
- HSA\_EXT\_IMAGE\_CAPABILITY\_ACCESS\_INVARIANT\_DATA\_SIZE
  - Images and Samplers, 162
- HSA\_EXT\_IMAGE\_CAPABILITY\_NOT\_SUPPORTED
  - Images and Samplers, 162
- HSA\_EXT\_IMAGE\_CAPABILITY\_READ\_MODIFY\_WRITE
  - Images and Samplers, 162
- HSA\_EXT\_IMAGE\_CAPABILITY\_READ\_ONLY
  - Images and Samplers, 162
- HSA\_EXT\_IMAGE\_CAPABILITY\_READ\_WRITE
  - Images and Samplers, 162
- hsa\_ext\_image\_capability\_t
  - Images and Samplers, 161
- HSA\_EXT\_IMAGE\_CAPABILITY\_WRITE\_ONLY
  - Images and Samplers, 162
- hsa\_ext\_image\_channel\_order32\_t
  - Images and Samplers, 159
- hsa\_ext\_image\_channel\_order\_t
  - Images and Samplers, 162
- hsa\_ext\_image\_channel\_type32\_t
  - Images and Samplers, 159
- hsa\_ext\_image\_channel\_type\_t
  - Images and Samplers, 162
- hsa\_ext\_image\_clear
  - hsa\_ext\_images\_1\_00\_pfn\_s, 330
- hsa\_ext\_images\_1\_pfn\_s, 332
  - Images and Samplers, 165
- hsa\_ext\_image\_clear\_fn
  - ImageExtTable, 372
- hsa\_ext\_image\_copy
  - hsa\_ext\_images\_1\_00\_pfn\_s, 330
  - hsa\_ext\_images\_1\_pfn\_s, 333
  - Images and Samplers, 166
- hsa\_ext\_image\_copy\_fn
  - ImageExtTable, 372
- hsa\_ext\_image\_create
  - hsa\_ext\_images\_1\_00\_pfn\_s, 330
  - hsa\_ext\_images\_1\_pfn\_s, 333
  - Images and Samplers, 167
- hsa\_ext\_image\_create\_fn
  - ImageExtTable, 372
- hsa\_ext\_image\_create\_with\_layout
  - hsa\_ext\_images\_1\_pfn\_s, 333
  - Images and Samplers, 168
- hsa\_ext\_image\_create\_with\_layout\_fn
  - ImageExtTable, 373
- hsa\_ext\_image\_data\_get\_info
  - hsa\_ext\_images\_1\_00\_pfn\_s, 330
  - hsa\_ext\_images\_1\_pfn\_s, 333
  - Images and Samplers, 170
- hsa\_ext\_image\_data\_get\_info\_fn
  - ImageExtTable, 373
- hsa\_ext\_image\_data\_get\_info\_with\_layout
  - hsa\_ext\_images\_1\_pfn\_s, 333
  - Images and Samplers, 171
- hsa\_ext\_image\_data\_get\_info\_with\_layout\_fn
  - ImageExtTable, 373
- hsa\_ext\_image\_data\_info\_s, 323
  - alignment, 324
  - layout, 324

- HSA\_EXT\_IMAGE\_DATA\_LAYOUT\_LINEAR
  - Images and Samplers, [163](#)
- HSA\_EXT\_IMAGE\_DATA\_LAYOUT\_OPAQUE
  - Images and Samplers, [163](#)
- hsa\_ext\_image\_data\_layout\_t
  - Images and Samplers, [162](#)
- hsa\_ext\_image\_descriptor\_s, [324](#)
  - array\_size, [325](#)
  - depth, [325](#)
  - format, [325](#)
  - geometry, [325](#)
  - height, [325](#)
  - width, [326](#)
- hsa\_ext\_image\_destroy
  - hsa\_ext\_images\_1\_00\_pfn\_s, [330](#)
  - hsa\_ext\_images\_1\_pfn\_s, [334](#)
  - Images and Samplers, [172](#)
- hsa\_ext\_image\_destroy\_fn
  - ImageExtTable, [373](#)
- hsa\_ext\_image\_export
  - hsa\_ext\_images\_1\_00\_pfn\_s, [330](#)
  - hsa\_ext\_images\_1\_pfn\_s, [334](#)
  - Images and Samplers, [173](#)
- hsa\_ext\_image\_export\_fn
  - ImageExtTable, [373](#)
- hsa\_ext\_image\_format\_s, [326](#)
  - channel\_order, [326](#)
  - channel\_type, [327](#)
- HSA\_EXT\_IMAGE\_GEOMETRY\_1D
  - Images and Samplers, [163](#)
- HSA\_EXT\_IMAGE\_GEOMETRY\_1DA
  - Images and Samplers, [164](#)
- HSA\_EXT\_IMAGE\_GEOMETRY\_1DB
  - Images and Samplers, [164](#)
- HSA\_EXT\_IMAGE\_GEOMETRY\_2D
  - Images and Samplers, [163](#)
- HSA\_EXT\_IMAGE\_GEOMETRY\_2DA
  - Images and Samplers, [164](#)
- HSA\_EXT\_IMAGE\_GEOMETRY\_2DADEPTH
  - Images and Samplers, [164](#)
- HSA\_EXT\_IMAGE\_GEOMETRY\_2DDEPTH
  - Images and Samplers, [164](#)
- HSA\_EXT\_IMAGE\_GEOMETRY\_3D
  - Images and Samplers, [163](#)
- hsa\_ext\_image\_geometry\_t
  - Images and Samplers, [163](#)
- hsa\_ext\_image\_get\_capability
  - hsa\_ext\_images\_1\_00\_pfn\_s, [331](#)
  - hsa\_ext\_images\_1\_pfn\_s, [334](#)
  - Images and Samplers, [174](#)
- hsa\_ext\_image\_get\_capability\_fn
  - ImageExtTable, [373](#)
- hsa\_ext\_image\_get\_capability\_with\_layout
  - hsa\_ext\_images\_1\_pfn\_s, [334](#)
  - Images and Samplers, [175](#)
- hsa\_ext\_image\_get\_capability\_with\_layout\_fn
  - ImageExtTable, [374](#)
- hsa\_ext\_image\_import
  - hsa\_ext\_images\_1\_00\_pfn\_s, [331](#)
  - hsa\_ext\_images\_1\_pfn\_s, [334](#)
  - Images and Samplers, [175](#)
- hsa\_ext\_image\_import\_fn
  - ImageExtTable, [374](#)
- hsa\_ext\_image\_region\_s, [327](#)
  - offset, [327](#)
  - range, [328](#)
- hsa\_ext\_image\_s, [328](#)
  - handle, [328](#)
- hsa\_ext\_image\_t
  - Images and Samplers, [159](#)
- hsa\_ext\_images\_1
  - Images and Samplers, [158](#)
- hsa\_ext\_images\_1\_00
  - Images and Samplers, [158](#)
- hsa\_ext\_images\_1\_00\_pfn\_s, [329](#)
  - hsa\_ext\_image\_clear, [330](#)
  - hsa\_ext\_image\_copy, [330](#)
  - hsa\_ext\_image\_create, [330](#)
  - hsa\_ext\_image\_data\_get\_info, [330](#)
  - hsa\_ext\_image\_destroy, [330](#)
  - hsa\_ext\_image\_export, [330](#)
  - hsa\_ext\_image\_get\_capability, [331](#)
  - hsa\_ext\_image\_import, [331](#)
  - hsa\_ext\_sampler\_create, [331](#)
  - hsa\_ext\_sampler\_destroy, [331](#)
- hsa\_ext\_images\_1\_pfn\_s, [332](#)
  - hsa\_ext\_image\_clear, [332](#)
  - hsa\_ext\_image\_copy, [333](#)
  - hsa\_ext\_image\_create, [333](#)
  - hsa\_ext\_image\_create\_with\_layout, [333](#)
  - hsa\_ext\_image\_data\_get\_info, [333](#)
  - hsa\_ext\_image\_data\_get\_info\_with\_layout, [333](#)
  - hsa\_ext\_image\_destroy, [334](#)
  - hsa\_ext\_image\_export, [334](#)
  - hsa\_ext\_image\_get\_capability, [334](#)
  - hsa\_ext\_image\_get\_capability\_with\_layout, [334](#)
  - hsa\_ext\_image\_import, [334](#)
  - hsa\_ext\_sampler\_create, [335](#)
  - hsa\_ext\_sampler\_destroy, [335](#)
- hsa\_ext\_module\_t
  - Finalization Program, [149](#)
- hsa\_ext\_program\_add\_module
  - Finalization Program, [150](#)
  - hsa\_ext\_finalizer\_1\_00\_pfn\_s, [322](#)
- hsa\_ext\_program\_add\_module\_fn
  - FinalizerExtTable, [286](#)
- hsa\_ext\_program\_create
  - Finalization Program, [151](#)
  - hsa\_ext\_finalizer\_1\_00\_pfn\_s, [322](#)
- hsa\_ext\_program\_create\_fn
  - FinalizerExtTable, [286](#)
- hsa\_ext\_program\_destroy
  - Finalization Program, [152](#)
  - hsa\_ext\_finalizer\_1\_00\_pfn\_s, [322](#)
- hsa\_ext\_program\_destroy\_fn
  - FinalizerExtTable, [286](#)

- hsa\_ext\_program\_finalize
  - Finalization Program, [152](#)
  - hsa\_ext\_finalizer\_1\_00\_pfn\_s, [322](#)
- hsa\_ext\_program\_finalize\_fn
  - FinalizerExtTable, [287](#)
- hsa\_ext\_program\_get\_info
  - Finalization Program, [153](#)
  - hsa\_ext\_finalizer\_1\_00\_pfn\_s, [323](#)
- hsa\_ext\_program\_get\_info\_fn
  - FinalizerExtTable, [287](#)
- HSA\_EXT\_PROGRAM\_INFO\_DEFAULT\_FLOAT\_ROUNDING\_MODE
  - Finalization Program, [150](#)
- HSA\_EXT\_PROGRAM\_INFO\_MACHINE\_MODEL
  - Finalization Program, [150](#)
- HSA\_EXT\_PROGRAM\_INFO\_PROFILE
  - Finalization Program, [150](#)
- hsa\_ext\_program\_info\_t
  - Finalization Program, [150](#)
- hsa\_ext\_program\_iterate\_modules
  - Finalization Program, [154](#)
  - hsa\_ext\_finalizer\_1\_00\_pfn\_s, [323](#)
- hsa\_ext\_program\_iterate\_modules\_fn
  - FinalizerExtTable, [287](#)
- hsa\_ext\_program\_s, [335](#)
  - handle, [336](#)
- hsa\_ext\_sampler\_addressing\_mode32\_t
  - Images and Samplers, [159](#)
- HSA\_EXT\_SAMPLER\_ADDRESSING\_MODE\_CLAMP\_TO\_BORDER
  - Images and Samplers, [164](#)
- HSA\_EXT\_SAMPLER\_ADDRESSING\_MODE\_CLAMP\_TO\_EDGE
  - Images and Samplers, [164](#)
- HSA\_EXT\_SAMPLER\_ADDRESSING\_MODE\_MIRROR\_REPEAT
  - Images and Samplers, [164](#)
- HSA\_EXT\_SAMPLER\_ADDRESSING\_MODE\_REPEAT
  - Images and Samplers, [164](#)
- hsa\_ext\_sampler\_addressing\_mode\_t
  - Images and Samplers, [164](#)
- HSA\_EXT\_SAMPLER\_ADDRESSING\_MODE\_UNDEFINED
  - Images and Samplers, [164](#)
- hsa\_ext\_sampler\_coordinate\_mode32\_t
  - Images and Samplers, [159](#)
- HSA\_EXT\_SAMPLER\_COORDINATE\_MODE\_NORMALIZED
  - Images and Samplers, [165](#)
- hsa\_ext\_sampler\_coordinate\_mode\_t
  - Images and Samplers, [164](#)
- HSA\_EXT\_SAMPLER\_COORDINATE\_MODE\_UNNORMALIZED
  - Images and Samplers, [165](#)
- hsa\_ext\_sampler\_create
  - hsa\_ext\_images\_1\_00\_pfn\_s, [331](#)
  - hsa\_ext\_images\_1\_pfn\_s, [335](#)
  - Images and Samplers, [176](#)
- hsa\_ext\_sampler\_create\_fn
  - ImageExtTable, [374](#)
- hsa\_ext\_sampler\_descriptor\_s, [336](#)
  - address\_mode, [336](#)
  - coordinate\_mode, [336](#)
  - filter\_mode, [337](#)
- hsa\_ext\_sampler\_destroy
  - hsa\_ext\_images\_1\_00\_pfn\_s, [331](#)
  - hsa\_ext\_images\_1\_pfn\_s, [335](#)
  - Images and Samplers, [177](#)
- hsa\_ext\_sampler\_destroy\_fn
  - ImageExtTable, [374](#)
- hsa\_ext\_sampler\_filter\_mode32\_t
  - Images and Samplers, [160](#)
- HSA\_EXT\_SAMPLER\_FILTER\_MODE\_LINEAR
  - Images and Samplers, [165](#)
- HSA\_EXT\_SAMPLER\_FILTER\_MODE\_NEAREST
  - Images and Samplers, [165](#)
- hsa\_ext\_sampler\_filter\_mode\_t
  - Images and Samplers, [165](#)
- hsa\_ext\_sampler\_s, [337](#)
  - handle, [337](#)
- HSA\_EXT\_STATUS\_ERROR\_DIRECTIVE\_MISMATCH
  - Finalization Extensions, [148](#)
- HSA\_EXT\_STATUS\_ERROR\_FINALIZATION\_FAILED
  - Finalization Extensions, [148](#)
- HSA\_EXT\_STATUS\_ERROR\_IMAGE\_FORMAT\_UNSUPPORTED
  - Images and Samplers, [160](#)
- HSA\_EXT\_STATUS\_ERROR\_IMAGE\_PITCH\_UNSUPPORTED
  - Images and Samplers, [160](#)
- HSA\_EXT\_STATUS\_ERROR\_IMAGE\_SIZE\_UNSUPPORTED
  - Images and Samplers, [160](#)
- HSA\_EXT\_STATUS\_ERROR\_INCOMPATIBLE\_MODULE
  - Finalization Extensions, [148](#)
- HSA\_EXT\_STATUS\_ERROR\_INVALID\_MODULE
  - Finalization Extensions, [148](#)
- HSA\_EXT\_STATUS\_ERROR\_INVALID\_PROGRAM
  - Finalization Extensions, [148](#)
- HSA\_EXT\_STATUS\_ERROR\_MODULE\_ALREADY\_INCLUDED
  - Finalization Extensions, [148](#)
- HSA\_EXT\_STATUS\_ERROR\_SAMPLER\_DESCRIPTOR\_UNSUPPORTED
  - Images and Samplers, [160](#)
- HSA\_EXT\_STATUS\_ERROR\_SYMBOL\_MISMATCH
  - Finalization Extensions, [148](#)
- HSA\_EXTENSION\_AMD\_AQLPROFILE
  - System and Agent Information, [29](#)
- HSA\_EXTENSION\_AMD\_LOADER
  - System and Agent Information, [29](#)
- HSA\_EXTENSION\_AMD\_PROFILER
  - System and Agent Information, [29](#)
- HSA\_EXTENSION\_FINALIZER
  - System and Agent Information, [29](#)
- hsa\_extension\_get\_name
  - System and Agent Information, [34](#)
- hsa\_extension\_get\_name\_fn
  - CoreApiTable, [271](#)
- HSA\_EXTENSION\_IMAGES
  - System and Agent Information, [29](#)
- HSA\_EXTENSION\_PERFORMANCE\_COUNTERS
  - System and Agent Information, [29](#)
- HSA\_EXTENSION\_PROFILING\_EVENTS
  - System and Agent Information, [29](#)
- HSA\_EXTENSION\_STD\_LAST
  - System and Agent Information, [29](#)
- hsa\_extension\_t

- System and Agent Information, [29](#)
- HSA\_FENCE\_SCOPE\_AGENT
  - Architected Queuing Language, [100](#)
- HSA\_FENCE\_SCOPE\_NONE
  - Architected Queuing Language, [100](#)
- HSA\_FENCE\_SCOPE\_SYSTEM
  - Architected Queuing Language, [100](#)
- hsa\_fence\_scope\_t
  - Architected Queuing Language, [100](#)
- hsa\_file\_t
  - Runtime Notifications, [16](#)
- HSA\_FLUSH\_MODE\_FTZ
  - Instruction Set Architecture., [105](#)
- HSA\_FLUSH\_MODE\_NON\_FTZ
  - Instruction Set Architecture., [105](#)
- hsa\_flush\_mode\_t
  - Instruction Set Architecture., [104](#)
- HSA\_FP\_TYPE\_16
  - Instruction Set Architecture., [105](#)
- HSA\_FP\_TYPE\_32
  - Instruction Set Architecture., [105](#)
- HSA\_FP\_TYPE\_64
  - Instruction Set Architecture., [105](#)
- hsa\_fp\_type\_t
  - Instruction Set Architecture., [105](#)
- hsa\_init
  - Runtime Notifications, [18](#)
- hsa\_init\_fn
  - CoreApiTable, [271](#)
- hsa\_isa\_compatible
  - Instruction Set Architecture., [108](#)
- hsa\_isa\_compatible\_fn
  - CoreApiTable, [271](#)
- hsa\_isa\_from\_name
  - Instruction Set Architecture., [109](#)
- hsa\_isa\_from\_name\_fn
  - CoreApiTable, [271](#)
- hsa\_isa\_get\_exception\_policies
  - Instruction Set Architecture., [109](#)
- hsa\_isa\_get\_exception\_policies\_fn
  - CoreApiTable, [272](#)
- hsa\_isa\_get\_info
  - Instruction Set Architecture., [110](#)
- hsa\_isa\_get\_info\_alt
  - Instruction Set Architecture., [110](#)
- hsa\_isa\_get\_info\_alt\_fn
  - CoreApiTable, [272](#)
- hsa\_isa\_get\_info\_fn
  - CoreApiTable, [272](#)
- hsa\_isa\_get\_round\_method
  - Instruction Set Architecture., [111](#)
- hsa\_isa\_get\_round\_method\_fn
  - CoreApiTable, [272](#)
- HSA\_ISA\_INFO\_BASE\_PROFILE\_DEFAULT\_FLOAT\_ROUNDING\_MODES
  - Instruction Set Architecture., [106](#)
- HSA\_ISA\_INFO\_CALL\_CONVENTION\_COUNT
  - Instruction Set Architecture., [105](#)
- HSA\_ISA\_INFO\_CALL\_CONVENTION\_INFO\_WAVEFRONT\_SIZE
  - Instruction Set Architecture., [105](#)
- HSA\_ISA\_INFO\_CALL\_CONVENTION\_INFO\_WAVEFRONTS\_PER\_CORE
  - Instruction Set Architecture., [106](#)
- HSA\_ISA\_INFO\_DEFAULT\_FLOAT\_ROUNDING\_MODES
  - Instruction Set Architecture., [106](#)
- HSA\_ISA\_INFO\_FAST\_F16\_OPERATION
  - Instruction Set Architecture., [106](#)
- HSA\_ISA\_INFO\_FBARRIER\_MAX\_SIZE
  - Instruction Set Architecture., [107](#)
- HSA\_ISA\_INFO\_GRID\_MAX\_DIM
  - Instruction Set Architecture., [107](#)
- HSA\_ISA\_INFO\_GRID\_MAX\_SIZE
  - Instruction Set Architecture., [107](#)
- HSA\_ISA\_INFO\_MACHINE\_MODELS
  - Instruction Set Architecture., [106](#)
- HSA\_ISA\_INFO\_NAME
  - Instruction Set Architecture., [105](#)
- HSA\_ISA\_INFO\_NAME\_LENGTH
  - Instruction Set Architecture., [105](#)
- HSA\_ISA\_INFO\_PROFILES
  - Instruction Set Architecture., [106](#)
- hsa\_isa\_info\_t
  - Instruction Set Architecture., [105](#)
- HSA\_ISA\_INFO\_WORKGROUP\_MAX\_DIM
  - Instruction Set Architecture., [106](#)
- HSA\_ISA\_INFO\_WORKGROUP\_MAX\_SIZE
  - Instruction Set Architecture., [106](#)
- hsa\_isa\_iterate\_wavefronts
  - Instruction Set Architecture., [112](#)
- hsa\_isa\_iterate\_wavefronts\_fn
  - CoreApiTable, [272](#)
- hsa\_isa\_s, [338](#)
  - handle, [338](#)
- hsa\_iterate\_agents
  - System and Agent Information, [35](#)
- hsa\_iterate\_agents\_fn
  - CoreApiTable, [272](#)
- hsa\_kernel\_dispatch\_packet\_s, [339](#)
  - completion\_signal, [339](#)
  - grid\_size\_x, [339](#)
  - grid\_size\_y, [340](#)
  - grid\_size\_z, [340](#)
  - group\_segment\_size, [340](#)
  - header, [340](#)
  - kernarg\_address, [340](#)
  - kernel\_object, [341](#)
  - private\_segment\_size, [341](#)
  - reserved0, [341](#)
  - reserved1, [341](#)
  - reserved2, [341](#)
  - setup, [342](#)
  - workgroup\_size\_x, [342](#)
  - workgroup\_size\_y, [342](#)
  - workgroup\_size\_z, [342](#)
- HSA\_KERNEL\_DISPATCH\_PACKET\_SETUP\_DIMENSIONS
  - Architected Queuing Language, [101](#)
- hsa\_kernel\_dispatch\_packet\_setup\_t
  - Architected Queuing Language, [100](#)

- hsa\_kernel\_dispatch\_packet\_setup\_width\_t
  - Architected Queuing Language, [101](#)
- hsa\_loaded\_code\_object\_s, [343](#)
  - handle, [343](#)
- HSA\_MACHINE\_MODEL\_LARGE
  - System and Agent Information, [29](#)
- HSA\_MACHINE\_MODEL\_SMALL
  - System and Agent Information, [29](#)
- hsa\_machine\_model\_t
  - System and Agent Information, [29](#)
- hsa\_memory\_allocate
  - Memory, [76](#)
- hsa\_memory\_allocate\_fn
  - CoreApiTable, [273](#)
- hsa\_memory\_assign\_agent
  - Memory, [77](#)
- hsa\_memory\_assign\_agent\_fn
  - CoreApiTable, [273](#)
- hsa\_memory\_copy
  - Memory, [77](#)
- hsa\_memory\_copy\_fn
  - CoreApiTable, [273](#)
- hsa\_memory\_deregister
  - Memory, [78](#)
- hsa\_memory\_deregister\_fn
  - CoreApiTable, [273](#)
- hsa\_memory\_free
  - Memory, [79](#)
- hsa\_memory\_free\_fn
  - CoreApiTable, [273](#)
- hsa\_memory\_register
  - Memory, [79](#)
- hsa\_memory\_register\_fn
  - CoreApiTable, [273](#)
- HSA\_PACKET\_HEADER\_ACQUIRE\_FENCE\_SCOPE
  - Architected Queuing Language, [102](#)
- HSA\_PACKET\_HEADER\_BARRIER
  - Architected Queuing Language, [101](#)
- HSA\_PACKET\_HEADER\_RELEASE\_FENCE\_SCOPE
  - Architected Queuing Language, [102](#)
- HSA\_PACKET\_HEADER\_SCACQUIRE\_FENCE\_SCOPE
  - Architected Queuing Language, [101](#)
- HSA\_PACKET\_HEADER\_SCRELEASE\_FENCE\_SCOPE
  - Architected Queuing Language, [102](#)
- hsa\_packet\_header\_t
  - Architected Queuing Language, [101](#)
- HSA\_PACKET\_HEADER\_TYPE
  - Architected Queuing Language, [101](#)
- HSA\_PACKET\_HEADER\_WIDTH\_ACQUIRE\_FENCE\_SCOPE
  - Architected Queuing Language, [102](#)
- HSA\_PACKET\_HEADER\_WIDTH\_RELEASE\_FENCE\_SCOPE
  - Architected Queuing Language, [102](#)
- hsa\_packet\_header\_width\_t
  - Architected Queuing Language, [102](#)
- HSA\_PACKET\_TYPE\_AGENT\_DISPATCH
  - Architected Queuing Language, [103](#)
- HSA\_PACKET\_TYPE\_BARRIER\_AND
  - Architected Queuing Language, [103](#)
- HSA\_PACKET\_TYPE\_BARRIER\_OR
  - Architected Queuing Language, [103](#)
- HSA\_PACKET\_TYPE\_INVALID
  - Architected Queuing Language, [103](#)
- HSA\_PACKET\_TYPE\_KERNEL\_DISPATCH
  - Architected Queuing Language, [103](#)
- hsa\_packet\_type\_t
  - Architected Queuing Language, [102](#)
- HSA\_PACKET\_TYPE\_VENDOR\_SPECIFIC
  - Architected Queuing Language, [103](#)
- hsa\_pitched\_ptr\_s, [343](#)
  - base, [344](#)
  - pitch, [344](#)
  - slice, [344](#)
- HSA\_PROFILE\_BASE
  - System and Agent Information, [30](#)
- HSA\_PROFILE\_FULL
  - System and Agent Information, [30](#)
- hsa\_profile\_t
  - System and Agent Information, [29](#)
- hsa\_queue
  - amd\_queue\_s, [189](#)
- hsa\_queue\_add\_write\_index\_acq\_rel
  - Queues, [83](#)
- hsa\_queue\_add\_write\_index\_acquire
  - Queues, [84](#)
- hsa\_queue\_add\_write\_index\_relaxed
  - Queues, [84](#)
- hsa\_queue\_add\_write\_index\_relaxed\_fn
  - CoreApiTable, [274](#)
- hsa\_queue\_add\_write\_index\_release
  - Queues, [85](#)
- hsa\_queue\_add\_write\_index\_scacq\_screl
  - Queues, [85](#)
- hsa\_queue\_add\_write\_index\_scacq\_screl\_fn
  - CoreApiTable, [274](#)
- hsa\_queue\_add\_write\_index\_scacquire
  - Queues, [86](#)
- hsa\_queue\_add\_write\_index\_scacquire\_fn
  - CoreApiTable, [274](#)
- hsa\_queue\_add\_write\_index\_screlease
  - Queues, [86](#)
- hsa\_queue\_add\_write\_index\_screlease\_fn
  - CoreApiTable, [274](#)
- hsa\_queue\_cas\_write\_index\_acq\_rel
  - Queues, [86](#)
- hsa\_queue\_cas\_write\_index\_acquire
  - Queues, [87](#)
- hsa\_queue\_cas\_write\_index\_relaxed
  - Queues, [87](#)
- hsa\_queue\_cas\_write\_index\_relaxed\_fn
  - CoreApiTable, [274](#)
- hsa\_queue\_cas\_write\_index\_release
  - Queues, [88](#)
- hsa\_queue\_cas\_write\_index\_scacq\_screl
  - Queues, [88](#)
- hsa\_queue\_cas\_write\_index\_scacq\_screl\_fn
  - CoreApiTable, [274](#)

- hsa\_queue\_cas\_write\_index\_scacquire  
Queues, [89](#)
- hsa\_queue\_cas\_write\_index\_scacquire\_fn  
CoreApiTable, [275](#)
- hsa\_queue\_cas\_write\_index\_screlease  
Queues, [89](#)
- hsa\_queue\_cas\_write\_index\_screlease\_fn  
CoreApiTable, [275](#)
- hsa\_queue\_create  
Queues, [90](#)
- hsa\_queue\_create\_fn  
CoreApiTable, [275](#)
- hsa\_queue\_destroy  
Queues, [91](#)
- hsa\_queue\_destroy\_fn  
CoreApiTable, [275](#)
- HSA\_QUEUE\_FEATURE\_AGENT\_DISPATCH  
Queues, [83](#)
- HSA\_QUEUE\_FEATURE\_KERNEL\_DISPATCH  
Queues, [83](#)
- hsa\_queue\_feature\_t  
Queues, [83](#)
- hsa\_queue\_inactivate  
Queues, [92](#)
- hsa\_queue\_inactivate\_fn  
CoreApiTable, [275](#)
- hsa\_queue\_load\_read\_index\_acquire  
Queues, [92](#)
- hsa\_queue\_load\_read\_index\_relaxed  
Queues, [93](#)
- hsa\_queue\_load\_read\_index\_relaxed\_fn  
CoreApiTable, [275](#)
- hsa\_queue\_load\_read\_index\_scacquire  
Queues, [93](#)
- hsa\_queue\_load\_read\_index\_scacquire\_fn  
CoreApiTable, [276](#)
- hsa\_queue\_load\_write\_index\_acquire  
Queues, [93](#)
- hsa\_queue\_load\_write\_index\_relaxed  
Queues, [94](#)
- hsa\_queue\_load\_write\_index\_relaxed\_fn  
CoreApiTable, [276](#)
- hsa\_queue\_load\_write\_index\_scacquire  
Queues, [94](#)
- hsa\_queue\_load\_write\_index\_scacquire\_fn  
CoreApiTable, [276](#)
- hsa\_queue\_s, [344](#)
  - base\_address, [345](#)
  - doorbell\_signal, [345](#)
  - features, [345](#)
  - id, [345](#)
  - reserved0, [346](#)
  - reserved1, [346](#)
  - size, [346](#)
  - type, [346](#)
- hsa\_queue\_store\_read\_index\_relaxed  
Queues, [94](#)
- hsa\_queue\_store\_read\_index\_relaxed\_fn  
CoreApiTable, [276](#)
- hsa\_queue\_store\_read\_index\_release  
Queues, [95](#)
- hsa\_queue\_store\_read\_index\_screlease  
Queues, [95](#)
- hsa\_queue\_store\_read\_index\_screlease\_fn  
CoreApiTable, [276](#)
- hsa\_queue\_store\_write\_index\_relaxed  
Queues, [96](#)
- hsa\_queue\_store\_write\_index\_relaxed\_fn  
CoreApiTable, [276](#)
- hsa\_queue\_store\_write\_index\_release  
Queues, [96](#)
- hsa\_queue\_store\_write\_index\_screlease  
Queues, [96](#)
- hsa\_queue\_store\_write\_index\_screlease\_fn  
CoreApiTable, [277](#)
- hsa\_queue\_t  
Queues, [82](#)
- hsa\_queue\_type32\_t  
Queues, [82](#)
- HSA\_QUEUE\_TYPE\_COOPERATIVE  
Queues, [83](#)
- HSA\_QUEUE\_TYPE\_MULTI  
Queues, [83](#)
- HSA\_QUEUE\_TYPE\_SINGLE  
Queues, [83](#)
- hsa\_queue\_type\_t  
Queues, [83](#)
- hsa\_region\_get\_info  
Memory, [80](#)
- hsa\_region\_get\_info\_fn  
CoreApiTable, [277](#)
- HSA\_REGION\_GLOBAL\_FLAG\_COARSE\_GRAINED  
Memory, [74](#)
- HSA\_REGION\_GLOBAL\_FLAG\_FINE\_GRAINED  
Memory, [74](#)
- HSA\_REGION\_GLOBAL\_FLAG\_KERNARG  
Memory, [74](#)
- hsa\_region\_global\_flag\_t  
Memory, [73](#)
- HSA\_REGION\_INFO\_ALLOC\_MAX\_PRIVATE\_WORKGROUP\_SIZE  
Memory, [75](#)
- HSA\_REGION\_INFO\_ALLOC\_MAX\_SIZE  
Memory, [74](#)
- HSA\_REGION\_INFO\_GLOBAL\_FLAGS  
Memory, [74](#)
- HSA\_REGION\_INFO\_RUNTIME\_ALLOC\_ALIGNMENT  
Memory, [75](#)
- HSA\_REGION\_INFO\_RUNTIME\_ALLOC\_ALLOWED  
Memory, [75](#)
- HSA\_REGION\_INFO\_RUNTIME\_ALLOC\_GRANULE  
Memory, [75](#)
- HSA\_REGION\_INFO\_SEGMENT  
Memory, [74](#)
- HSA\_REGION\_INFO\_SIZE  
Memory, [74](#)
- hsa\_region\_info\_t



- Memory, [74](#)
- hsa\_region\_s, [347](#)
  - handle, [347](#)
- HSA\_REGION\_SEGMENT\_GLOBAL
  - Memory, [75](#)
- HSA\_REGION\_SEGMENT\_GROUP
  - Memory, [75](#)
- HSA\_REGION\_SEGMENT\_KERNARG
  - Memory, [75](#)
- HSA\_REGION\_SEGMENT\_PRIVATE
  - Memory, [75](#)
- HSA\_REGION\_SEGMENT\_READONLY
  - Memory, [75](#)
- hsa\_region\_segment\_t
  - Memory, [75](#)
- HSA\_ROUND\_METHOD\_DOUBLE
  - Instruction Set Architecture., [107](#)
- HSA\_ROUND\_METHOD\_SINGLE
  - Instruction Set Architecture., [107](#)
- hsa\_round\_method\_t
  - Instruction Set Architecture., [107](#)
- hsa\_shut\_down
  - Runtime Notifications, [18](#)
- hsa\_shut\_down\_fn
  - CoreApiTable, [277](#)
- hsa\_signal\_add\_acq\_rel
  - Signals, [42](#)
- hsa\_signal\_add\_acquire
  - Signals, [43](#)
- hsa\_signal\_add\_relaxed
  - Signals, [43](#)
- hsa\_signal\_add\_relaxed\_fn
  - CoreApiTable, [277](#)
- hsa\_signal\_add\_release
  - Signals, [44](#)
- hsa\_signal\_add\_scacq\_screl
  - Signals, [44](#)
- hsa\_signal\_add\_scacq\_screl\_fn
  - CoreApiTable, [277](#)
- hsa\_signal\_add\_scacquire
  - Signals, [44](#)
- hsa\_signal\_add\_scacquire\_fn
  - CoreApiTable, [277](#)
- hsa\_signal\_add\_screlease
  - Signals, [45](#)
- hsa\_signal\_add\_screlease\_fn
  - CoreApiTable, [278](#)
- hsa\_signal\_and\_acq\_rel
  - Signals, [45](#)
- hsa\_signal\_and\_acquire
  - Signals, [46](#)
- hsa\_signal\_and\_relaxed
  - Signals, [46](#)
- hsa\_signal\_and\_relaxed\_fn
  - CoreApiTable, [278](#)
- hsa\_signal\_and\_release
  - Signals, [46](#)
- hsa\_signal\_and\_scacq\_screl
  - Signals, [47](#)
- hsa\_signal\_and\_scacq\_screl\_fn
  - CoreApiTable, [278](#)
- hsa\_signal\_and\_scacquire
  - Signals, [47](#)
- hsa\_signal\_and\_scacquire\_fn
  - CoreApiTable, [278](#)
- hsa\_signal\_and\_screlease
  - Signals, [47](#)
- hsa\_signal\_and\_screlease\_fn
  - CoreApiTable, [278](#)
- hsa\_signal\_cas\_acq\_rel
  - Signals, [48](#)
- hsa\_signal\_cas\_acquire
  - Signals, [48](#)
- hsa\_signal\_cas\_relaxed
  - Signals, [49](#)
- hsa\_signal\_cas\_relaxed\_fn
  - CoreApiTable, [278](#)
- hsa\_signal\_cas\_release
  - Signals, [49](#)
- hsa\_signal\_cas\_scacq\_screl
  - Signals, [50](#)
- hsa\_signal\_cas\_scacq\_screl\_fn
  - CoreApiTable, [279](#)
- hsa\_signal\_cas\_scacquire
  - Signals, [50](#)
- hsa\_signal\_cas\_scacquire\_fn
  - CoreApiTable, [279](#)
- hsa\_signal\_cas\_screlease
  - Signals, [51](#)
- hsa\_signal\_cas\_screlease\_fn
  - CoreApiTable, [279](#)
- hsa\_signal\_condition32\_t
  - Architected Queuing Language, [99](#)
- HSA\_SIGNAL\_CONDITION\_EQ
  - Signals, [42](#)
- HSA\_SIGNAL\_CONDITION\_GTE
  - Signals, [42](#)
- HSA\_SIGNAL\_CONDITION\_LT
  - Signals, [42](#)
- HSA\_SIGNAL\_CONDITION\_NE
  - Signals, [42](#)
- hsa\_signal\_condition\_t
  - Signals, [42](#)
- hsa\_signal\_create
  - Signals, [51](#)
- hsa\_signal\_create\_fn
  - CoreApiTable, [279](#)
- hsa\_signal\_destroy
  - Signals, [52](#)
- hsa\_signal\_destroy\_fn
  - CoreApiTable, [279](#)
- hsa\_signal\_exchange\_acq\_rel
  - Signals, [52](#)
- hsa\_signal\_exchange\_acquire
  - Signals, [53](#)
- hsa\_signal\_exchange\_relaxed

- Signals, [53](#)
- hsa\_signal\_exchange\_relaxed\_fn
  - CoreApiTable, [279](#)
- hsa\_signal\_exchange\_release
  - Signals, [54](#)
- hsa\_signal\_exchange\_scacq\_screl
  - Signals, [54](#)
- hsa\_signal\_exchange\_scacq\_screl\_fn
  - CoreApiTable, [280](#)
- hsa\_signal\_exchange\_scacquire
  - Signals, [55](#)
- hsa\_signal\_exchange\_scacquire\_fn
  - CoreApiTable, [280](#)
- hsa\_signal\_exchange\_screlease
  - Signals, [55](#)
- hsa\_signal\_exchange\_screlease\_fn
  - CoreApiTable, [280](#)
- hsa\_signal\_group\_create
  - Signals, [56](#)
- hsa\_signal\_group\_create\_fn
  - CoreApiTable, [280](#)
- hsa\_signal\_group\_destroy
  - Signals, [56](#)
- hsa\_signal\_group\_destroy\_fn
  - CoreApiTable, [280](#)
- hsa\_signal\_group\_s, [347](#)
  - handle, [348](#)
- hsa\_signal\_group\_wait\_any\_relaxed
  - Signals, [57](#)
- hsa\_signal\_group\_wait\_any\_relaxed\_fn
  - CoreApiTable, [280](#)
- hsa\_signal\_group\_wait\_any\_scacquire
  - Signals, [58](#)
- hsa\_signal\_group\_wait\_any\_scacquire\_fn
  - CoreApiTable, [281](#)
- hsa\_signal\_load\_acquire
  - Signals, [59](#)
- hsa\_signal\_load\_relaxed
  - Signals, [59](#)
- hsa\_signal\_load\_relaxed\_fn
  - CoreApiTable, [281](#)
- hsa\_signal\_load\_scacquire
  - Signals, [59](#)
- hsa\_signal\_load\_scacquire\_fn
  - CoreApiTable, [281](#)
- hsa\_signal\_or\_acq\_rel
  - Signals, [60](#)
- hsa\_signal\_or\_acquire
  - Signals, [60](#)
- hsa\_signal\_or\_relaxed
  - Signals, [61](#)
- hsa\_signal\_or\_relaxed\_fn
  - CoreApiTable, [281](#)
- hsa\_signal\_or\_release
  - Signals, [61](#)
- hsa\_signal\_or\_scacq\_screl
  - Signals, [61](#)
- hsa\_signal\_or\_scacq\_screl\_fn
  - CoreApiTable, [281](#)
- hsa\_signal\_or\_scacquire
  - Signals, [62](#)
- hsa\_signal\_or\_scacquire\_fn
  - CoreApiTable, [281](#)
- hsa\_signal\_or\_screlease
  - Signals, [62](#)
- hsa\_signal\_or\_screlease\_fn
  - CoreApiTable, [282](#)
- hsa\_signal\_s, [348](#)
  - handle, [348](#)
- hsa\_signal\_silent\_store\_relaxed
  - Signals, [62](#)
- hsa\_signal\_silent\_store\_relaxed\_fn
  - CoreApiTable, [282](#)
- hsa\_signal\_silent\_store\_screlease
  - Signals, [63](#)
- hsa\_signal\_silent\_store\_screlease\_fn
  - CoreApiTable, [282](#)
- hsa\_signal\_store\_relaxed
  - Signals, [63](#)
- hsa\_signal\_store\_relaxed\_fn
  - CoreApiTable, [282](#)
- hsa\_signal\_store\_release
  - Signals, [64](#)
- hsa\_signal\_store\_screlease
  - Signals, [64](#)
- hsa\_signal\_store\_screlease\_fn
  - CoreApiTable, [282](#)
- hsa\_signal\_subtract\_acq\_rel
  - Signals, [64](#)
- hsa\_signal\_subtract\_acquire
  - Signals, [65](#)
- hsa\_signal\_subtract\_relaxed
  - Signals, [65](#)
- hsa\_signal\_subtract\_relaxed\_fn
  - CoreApiTable, [282](#)
- hsa\_signal\_subtract\_release
  - Signals, [66](#)
- hsa\_signal\_subtract\_scacq\_screl
  - Signals, [66](#)
- hsa\_signal\_subtract\_scacq\_screl\_fn
  - CoreApiTable, [283](#)
- hsa\_signal\_subtract\_scacquire
  - Signals, [66](#)
- hsa\_signal\_subtract\_scacquire\_fn
  - CoreApiTable, [283](#)
- hsa\_signal\_subtract\_screlease
  - Signals, [67](#)
- hsa\_signal\_subtract\_screlease\_fn
  - CoreApiTable, [283](#)
- hsa\_signal\_value\_t
  - Signals, [41](#)
- hsa\_signal\_wait\_acquire
  - Signals, [67](#)
- hsa\_signal\_wait\_relaxed
  - Signals, [68](#)
- hsa\_signal\_wait\_relaxed\_fn



- CoreApiTable, [283](#)
- hsa\_signal\_wait\_scacquire
  - Signals, [69](#)
- hsa\_signal\_wait\_scacquire\_fn
  - CoreApiTable, [283](#)
- hsa\_signal\_xor\_acq\_rel
  - Signals, [69](#)
- hsa\_signal\_xor\_acquire
  - Signals, [70](#)
- hsa\_signal\_xor\_relaxed
  - Signals, [70](#)
- hsa\_signal\_xor\_relaxed\_fn
  - CoreApiTable, [283](#)
- hsa\_signal\_xor\_release
  - Signals, [71](#)
- hsa\_signal\_xor\_scacq\_screl
  - Signals, [71](#)
- hsa\_signal\_xor\_scacq\_screl\_fn
  - CoreApiTable, [284](#)
- hsa\_signal\_xor\_scacquire
  - Signals, [71](#)
- hsa\_signal\_xor\_scacquire\_fn
  - CoreApiTable, [284](#)
- hsa\_signal\_xor\_screlease
  - Signals, [72](#)
- hsa\_signal\_xor\_screlease\_fn
  - CoreApiTable, [284](#)
- hsa\_soft\_queue\_create
  - Queues, [97](#)
- hsa\_soft\_queue\_create\_fn
  - CoreApiTable, [284](#)
- HSA\_STATUS\_ERROR
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_EXCEPTION
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_FATAL
  - Runtime Notifications, [18](#)
- HSA\_STATUS\_ERROR\_FROZEN\_EXECUTABLE
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_INCOMPATIBLE\_ARGUMENTS
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_INVALID\_AGENT
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_INVALID\_ALLOCATION
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_INVALID\_ARGUMENT
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_INVALID\_CACHE
  - Runtime Notifications, [18](#)
- HSA\_STATUS\_ERROR\_INVALID\_CODE\_OBJECT
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_INVALID\_CODE\_OBJECT\_READ
  - Runtime Notifications, [18](#)
- HSA\_STATUS\_ERROR\_INVALID\_CODE\_SYMBOL
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_INVALID\_EXECUTABLE
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_INVALID\_EXECUTABLE\_SYMBOL
  - Runtime Notifications, [17](#)
- Runtime Notifications, [18](#)
- HSA\_STATUS\_ERROR\_INVALID\_FILE
  - Runtime Notifications, [18](#)
- HSA\_STATUS\_ERROR\_INVALID\_INDEX
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_INVALID\_ISA
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_INVALID\_ISA\_NAME
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_INVALID\_PACKET\_FORMAT
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_INVALID\_QUEUE
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_INVALID\_QUEUE\_CREATION
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_INVALID\_REGION
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_INVALID\_RUNTIME\_STATE
  - Runtime Notifications, [18](#)
- HSA\_STATUS\_ERROR\_INVALID\_SIGNAL
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_INVALID\_SIGNAL\_GROUP
  - Runtime Notifications, [18](#)
- HSA\_STATUS\_ERROR\_INVALID\_SYMBOL\_NAME
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_INVALID\_WAVEFRONT
  - Runtime Notifications, [18](#)
- HSA\_STATUS\_ERROR\_NOT\_INITIALIZED
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_OUT\_OF\_RESOURCES
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_REFCOUNT\_OVERFLOW
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_RESOURCE\_FREE
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_VARIABLE\_ALREADY\_DEFINED
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_ERROR\_VARIABLE\_UNDEFINED
  - Runtime Notifications, [17](#)
- HSA\_STATUS\_INFO\_BREAK
  - Runtime Notifications, [17](#)
- hsa\_status\_string
  - Runtime Notifications, [19](#)
- hsa\_status\_string\_fn
  - CoreApiTable, [284](#)
- HSA\_STATUS\_SUCCESS
  - Runtime Notifications, [17](#)
- hsa\_status\_t
  - Runtime Notifications, [17](#)
- HSA\_SYMBOL\_KIND\_INDIRECT\_FUNCTION
  - Executable, [120](#)
- HSA\_SYMBOL\_KIND\_KERNEL
  - Executable, [120](#)
- hsa\_symbol\_kind\_t
  - Executable, [119](#)
- HSA\_SYMBOL\_KIND\_VARIABLE
  - Executable, [120](#)
- HSA\_SYMBOL\_LINKAGE\_MODULE

- Executable, [120](#)
- HSA\_SYMBOL\_LINKAGE\_PROGRAM
  - Executable, [120](#)
- hsa\_symbol\_linkage\_t
  - Executable, [120](#)
- hsa\_system\_extension\_supported
  - System and Agent Information, [35](#)
- hsa\_system\_extension\_supported\_fn
  - CoreApiTable, [284](#)
- hsa\_system\_get\_extension\_table
  - System and Agent Information, [36](#)
- hsa\_system\_get\_extension\_table\_fn
  - CoreApiTable, [285](#)
- hsa\_system\_get\_info
  - System and Agent Information, [36](#)
- hsa\_system\_get\_info\_fn
  - CoreApiTable, [285](#)
- hsa\_system\_get\_major\_extension\_table
  - System and Agent Information, [37](#)
- hsa\_system\_get\_major\_extension\_table\_fn
  - CoreApiTable, [285](#)
- HSA\_SYSTEM\_INFO\_ENDIANNES
  - System and Agent Information, [30](#)
- HSA\_SYSTEM\_INFO\_EXTENSIONS
  - System and Agent Information, [30](#)
- HSA\_SYSTEM\_INFO\_MACHINE\_MODEL
  - System and Agent Information, [30](#)
- HSA\_SYSTEM\_INFO\_SIGNAL\_MAX\_WAIT
  - System and Agent Information, [30](#)
- hsa\_system\_info\_t
  - System and Agent Information, [30](#)
- HSA\_SYSTEM\_INFO\_TIMESTAMP
  - System and Agent Information, [30](#)
- HSA\_SYSTEM\_INFO\_TIMESTAMP\_FREQUENCY
  - System and Agent Information, [30](#)
- HSA\_SYSTEM\_INFO\_VERSION\_MAJOR
  - System and Agent Information, [30](#)
- HSA\_SYSTEM\_INFO\_VERSION\_MINOR
  - System and Agent Information, [30](#)
- hsa\_system\_major\_extension\_supported
  - System and Agent Information, [37](#)
- hsa\_system\_major\_extension\_supported\_fn
  - CoreApiTable, [285](#)
- HSA\_VARIABLE\_ALLOCATION\_AGENT
  - Executable, [120](#)
- HSA\_VARIABLE\_ALLOCATION\_PROGRAM
  - Executable, [120](#)
- hsa\_variable\_allocation\_t
  - Executable, [120](#)
- HSA\_VARIABLE\_SEGMENT\_GLOBAL
  - Executable, [121](#)
- HSA\_VARIABLE\_SEGMENT\_READONLY
  - Executable, [121](#)
- hsa\_variable\_segment\_t
  - Executable, [120](#)
- hsa\_ven\_amd\_aqlprofile\_1\_00\_pfn\_s, [349](#)
  - hsa\_ven\_amd\_aqlprofile\_error\_string, [349](#)
  - hsa\_ven\_amd\_aqlprofile\_get\_info, [350](#)
  - hsa\_ven\_amd\_aqlprofile\_iterate\_data, [350](#)
  - hsa\_ven\_amd\_aqlprofile\_legacy\_get\_pm4, [350](#)
  - hsa\_ven\_amd\_aqlprofile\_read, [350](#)
  - hsa\_ven\_amd\_aqlprofile\_start, [350](#)
  - hsa\_ven\_amd\_aqlprofile\_stop, [351](#)
  - hsa\_ven\_amd\_aqlprofile\_validate\_event, [351](#)
  - hsa\_ven\_amd\_aqlprofile\_version\_major, [351](#)
  - hsa\_ven\_amd\_aqlprofile\_version\_minor, [351](#)
- hsa\_ven\_amd\_aqlprofile\_descriptor\_t, [352](#)
  - ptr, [352](#)
  - size, [352](#)
- hsa\_ven\_amd\_aqlprofile\_error\_string
  - hsa\_ven\_amd\_aqlprofile\_1\_00\_pfn\_s, [349](#)
- hsa\_ven\_amd\_aqlprofile\_event\_t, [352](#)
  - block\_index, [353](#)
  - block\_name, [353](#)
  - counter\_id, [353](#)
- hsa\_ven\_amd\_aqlprofile\_get\_info
  - hsa\_ven\_amd\_aqlprofile\_1\_00\_pfn\_s, [350](#)
- hsa\_ven\_amd\_aqlprofile\_id\_query\_t, [353](#)
  - id, [353](#)
  - instance\_count, [354](#)
  - name, [354](#)
- hsa\_ven\_amd\_aqlprofile\_info\_data\_t, [354](#)
  - event, [355](#)
  - result, [355](#)
  - sample\_id, [355](#)
  - trace\_data, [355](#)
- hsa\_ven\_amd\_aqlprofile\_iterate\_data
  - hsa\_ven\_amd\_aqlprofile\_1\_00\_pfn\_s, [350](#)
- hsa\_ven\_amd\_aqlprofile\_legacy\_get\_pm4
  - hsa\_ven\_amd\_aqlprofile\_1\_00\_pfn\_s, [350](#)
- hsa\_ven\_amd\_aqlprofile\_parameter\_t, [355](#)
  - parameter\_name, [356](#)
  - value, [356](#)
- hsa\_ven\_amd\_aqlprofile\_profile\_t, [356](#)
  - agent, [357](#)
  - command\_buffer, [357](#)
  - event\_count, [357](#)
  - events, [357](#)
  - output\_buffer, [357](#)
  - parameter\_count, [357](#)
  - parameters, [358](#)
  - type, [358](#)
- hsa\_ven\_amd\_aqlprofile\_read
  - hsa\_ven\_amd\_aqlprofile\_1\_00\_pfn\_s, [350](#)
- hsa\_ven\_amd\_aqlprofile\_start
  - hsa\_ven\_amd\_aqlprofile\_1\_00\_pfn\_s, [350](#)
- hsa\_ven\_amd\_aqlprofile\_stop
  - hsa\_ven\_amd\_aqlprofile\_1\_00\_pfn\_s, [351](#)
- hsa\_ven\_amd\_aqlprofile\_validate\_event
  - hsa\_ven\_amd\_aqlprofile\_1\_00\_pfn\_s, [351](#)
- hsa\_ven\_amd\_aqlprofile\_version\_major
  - hsa\_ven\_amd\_aqlprofile\_1\_00\_pfn\_s, [351](#)
- hsa\_ven\_amd\_aqlprofile\_version\_minor
  - hsa\_ven\_amd\_aqlprofile\_1\_00\_pfn\_s, [351](#)
- hsa\_ven\_amd\_loader\_1\_00\_pfn\_s, [358](#)
  - hsa\_ven\_amd\_loader\_query\_executable, [359](#)

- hsa\_ven\_amd\_loader\_query\_host\_address, [359](#)
- hsa\_ven\_amd\_loader\_query\_segment\_descriptors, [359](#)
- hsa\_ven\_amd\_loader\_1\_01\_pfn\_s, [359](#)
- hsa\_ven\_amd\_loader\_executable\_iterate\_loaded\_code\_objects, [360](#)
- hsa\_ven\_amd\_loader\_loaded\_code\_object\_get\_info, [360](#)
- hsa\_ven\_amd\_loader\_query\_executable, [360](#)
- hsa\_ven\_amd\_loader\_query\_host\_address, [360](#)
- hsa\_ven\_amd\_loader\_query\_segment\_descriptors, [360](#)
- hsa\_ven\_amd\_loader\_1\_02\_pfn\_s, [361](#)
- hsa\_ven\_amd\_loader\_code\_object\_reader\_create\_from\_file\_with\_size, [361](#)
- hsa\_ven\_amd\_loader\_executable\_iterate\_loaded\_code\_objects, [362](#)
- hsa\_ven\_amd\_loader\_loaded\_code\_object\_get\_info, [362](#)
- hsa\_ven\_amd\_loader\_query\_executable, [362](#)
- hsa\_ven\_amd\_loader\_query\_host\_address, [362](#)
- hsa\_ven\_amd\_loader\_query\_segment\_descriptors, [362](#)
- hsa\_ven\_amd\_loader\_1\_03\_pfn\_s, [363](#)
- hsa\_ven\_amd\_loader\_code\_object\_reader\_create\_from\_file\_with\_size, [363](#)
- hsa\_ven\_amd\_loader\_executable\_iterate\_loaded\_code\_objects, [364](#)
- hsa\_ven\_amd\_loader\_iterate\_executables, [364](#)
- hsa\_ven\_amd\_loader\_loaded\_code\_object\_get\_info, [364](#)
- hsa\_ven\_amd\_loader\_query\_executable, [364](#)
- hsa\_ven\_amd\_loader\_query\_host\_address, [364](#)
- hsa\_ven\_amd\_loader\_query\_segment\_descriptors, [365](#)
- hsa\_ven\_amd\_loader\_code\_object\_reader\_create\_from\_file\_with\_size, [365](#)
- hsa\_ven\_amd\_loader\_1\_02\_pfn\_s, [361](#)
- hsa\_ven\_amd\_loader\_1\_03\_pfn\_s, [363](#)
- hsa\_ven\_amd\_loader\_executable\_iterate\_loaded\_code\_objects, [360](#)
- hsa\_ven\_amd\_loader\_1\_01\_pfn\_s, [360](#)
- hsa\_ven\_amd\_loader\_1\_02\_pfn\_s, [362](#)
- hsa\_ven\_amd\_loader\_1\_03\_pfn\_s, [364](#)
- hsa\_ven\_amd\_loader\_iterate\_executables, [364](#)
- hsa\_ven\_amd\_loader\_1\_03\_pfn\_s, [364](#)
- hsa\_ven\_amd\_loader\_loaded\_code\_object\_get\_info, [360](#)
- hsa\_ven\_amd\_loader\_1\_01\_pfn\_s, [360](#)
- hsa\_ven\_amd\_loader\_1\_02\_pfn\_s, [362](#)
- hsa\_ven\_amd\_loader\_1\_03\_pfn\_s, [364](#)
- hsa\_ven\_amd\_loader\_query\_executable, [359](#)
- hsa\_ven\_amd\_loader\_1\_00\_pfn\_s, [359](#)
- hsa\_ven\_amd\_loader\_1\_01\_pfn\_s, [360](#)
- hsa\_ven\_amd\_loader\_1\_02\_pfn\_s, [362](#)
- hsa\_ven\_amd\_loader\_1\_03\_pfn\_s, [364](#)
- hsa\_ven\_amd\_loader\_query\_host\_address, [359](#)
- hsa\_ven\_amd\_loader\_1\_00\_pfn\_s, [359](#)
- hsa\_ven\_amd\_loader\_1\_01\_pfn\_s, [360](#)
- hsa\_ven\_amd\_loader\_1\_02\_pfn\_s, [362](#)
- hsa\_ven\_amd\_loader\_1\_03\_pfn\_s, [364](#)
- hsa\_ven\_amd\_loader\_query\_segment\_descriptors, [365](#)
- agent, [366](#)
- code\_object\_storage\_base, [366](#)
- code\_object\_storage\_offset, [366](#)
- code\_object\_storage\_size, [366](#)
- code\_object\_storage\_type, [367](#)
- executable, [367](#)
- segment\_base, [367](#)
- segment\_offset, [367](#)
- HSA\_WAIT\_STATE\_ACTIVE, [42](#)
- HSA\_WAIT\_STATE\_BLOCKED, [42](#)
- Signals, [42](#)
- hsa\_wait\_state\_t, [42](#)
- Signals, [42](#)
- hsa\_wavefront\_get\_info, [112](#)
- Instruction Set Architecture., [112](#)
- hsa\_wavefront\_get\_info\_fn, [285](#)
- CoreApiTable, [285](#)
- HSA\_WAVEFRONT\_INFO\_SIZE, [107](#)
- Instruction Set Architecture., [107](#)
- hsa\_wavefront\_info\_t, [107](#)
- Instruction Set Architecture., [107](#)
- hsa\_wavefront\_s, [368](#)
- handle, [368](#)
- HsaApiTable, [369](#)
- amd\_ext\_, [369](#)
- core\_, [369](#)
- finalizer\_ext\_, [369](#)
- image\_ext\_, [369](#)
- version, [370](#)
- HsaApiTableContainer, [370](#)
- amd\_ext, [371](#)
- score, [371](#)
- finalizer\_ext, [371](#)
- HsaApiTableContainer, [370](#)
- image\_ext, [371](#)
- root, [371](#)
- hsail\_major\_version, [200](#)
- amdgpu\_hsa\_note\_hsail\_s, [200](#)
- hsail\_minor\_version, [200](#)
- amdgpu\_hsa\_note\_hsail\_s, [200](#)
- hsailMajor, [218](#)
- BrigDirectiveModule, [218](#)
- hsailMinor, [218](#)
- BrigDirectiveModule, [218](#)
- id, [345](#)
- hsa\_queue\_s, [345](#)
- hsa\_ven\_amd\_aqlprofile\_id\_query\_t, [353](#)
- identification, [247](#)
- BrigModuleHeader, [247](#)
- image\_ext, [371](#)
- HsaApiTableContainer, [371](#)

- image\_ext\_
  - HsaApiTable, 369
- ImageExtTable, 372
  - hsa\_ext\_image\_clear\_fn, 372
  - hsa\_ext\_image\_copy\_fn, 372
  - hsa\_ext\_image\_create\_fn, 372
  - hsa\_ext\_image\_create\_with\_layout\_fn, 373
  - hsa\_ext\_image\_data\_get\_info\_fn, 373
  - hsa\_ext\_image\_data\_get\_info\_with\_layout\_fn, 373
  - hsa\_ext\_image\_destroy\_fn, 373
  - hsa\_ext\_image\_export\_fn, 373
  - hsa\_ext\_image\_get\_capability\_fn, 373
  - hsa\_ext\_image\_get\_capability\_with\_layout\_fn, 374
  - hsa\_ext\_image\_import\_fn, 374
  - hsa\_ext\_sampler\_create\_fn, 374
  - hsa\_ext\_sampler\_destroy\_fn, 374
  - version, 374
- Images and Samplers, 155
  - HSA\_EXT\_AGENT\_INFO\_IMAGE\_1D\_MAX\_ELEMENTS, 161
  - HSA\_EXT\_AGENT\_INFO\_IMAGE\_1DA\_MAX\_ELEMENTS, 161
  - HSA\_EXT\_AGENT\_INFO\_IMAGE\_1DB\_MAX\_ELEMENTS, 161
  - HSA\_EXT\_AGENT\_INFO\_IMAGE\_2D\_MAX\_ELEMENTS, 161
  - HSA\_EXT\_AGENT\_INFO\_IMAGE\_2DA\_MAX\_ELEMENTS, 161
  - HSA\_EXT\_AGENT\_INFO\_IMAGE\_2DADEPTH\_MAX\_ELEMENTS, 161
  - HSA\_EXT\_AGENT\_INFO\_IMAGE\_2DDEPTH\_MAX\_ELEMENTS, 161
  - HSA\_EXT\_AGENT\_INFO\_IMAGE\_3D\_MAX\_ELEMENTS, 161
  - HSA\_EXT\_AGENT\_INFO\_IMAGE\_ARRAY\_MAX\_LAYERS, 161
  - HSA\_EXT\_AGENT\_INFO\_IMAGE\_LINEAR\_ROW\_PITCH\_ALIGNMENT, 161
  - HSA\_EXT\_AGENT\_INFO\_MAX\_IMAGE\_RD\_HANDLES, 161
  - HSA\_EXT\_AGENT\_INFO\_MAX\_IMAGE\_RORW\_HANDLES, 161
  - HSA\_EXT\_AGENT\_INFO\_MAX\_SAMPLER\_HANDLERS, 161
  - HSA\_EXT\_IMAGE\_CAPABILITY\_ACCESS\_INVARIANT\_DATA\_LAYOUT, 162
  - HSA\_EXT\_IMAGE\_CAPABILITY\_NOT\_SUPPORTED, 162
  - HSA\_EXT\_IMAGE\_CAPABILITY\_READ\_MODIFY\_WRITE, 162
  - HSA\_EXT\_IMAGE\_CAPABILITY\_READ\_ONLY, 162
  - HSA\_EXT\_IMAGE\_CAPABILITY\_READ\_WRITE, 162
  - hsa\_ext\_image\_capability\_t, 161
  - HSA\_EXT\_IMAGE\_CAPABILITY\_WRITE\_ONLY, 162
  - hsa\_ext\_image\_channel\_order32\_t, 159
  - hsa\_ext\_image\_channel\_order\_t, 162
  - hsa\_ext\_image\_channel\_type32\_t, 159
  - hsa\_ext\_image\_channel\_type\_t, 162
  - hsa\_ext\_image\_clear, 165
  - hsa\_ext\_image\_copy, 166
  - hsa\_ext\_image\_create, 167
  - hsa\_ext\_image\_create\_with\_layout, 168
  - hsa\_ext\_image\_data\_get\_info, 170
  - hsa\_ext\_image\_data\_get\_info\_with\_layout, 171
  - HSA\_EXT\_IMAGE\_DATA\_LAYOUT\_LINEAR, 163
  - HSA\_EXT\_IMAGE\_DATA\_LAYOUT\_OPAQUE, 163
  - hsa\_ext\_image\_data\_layout\_t, 162
  - hsa\_ext\_image\_destroy, 172
  - hsa\_ext\_image\_export, 173
  - HSA\_EXT\_IMAGE\_GEOMETRY\_1D, 163
  - HSA\_EXT\_IMAGE\_GEOMETRY\_1DA, 164
  - HSA\_EXT\_IMAGE\_GEOMETRY\_1DB, 164
  - HSA\_EXT\_IMAGE\_GEOMETRY\_2D, 163
  - HSA\_EXT\_IMAGE\_GEOMETRY\_2DA, 164
  - HSA\_EXT\_IMAGE\_GEOMETRY\_2DADEPTH, 164
  - HSA\_EXT\_IMAGE\_GEOMETRY\_2DDEPTH, 164
  - HSA\_EXT\_IMAGE\_GEOMETRY\_3D, 163
  - hsa\_ext\_image\_geometry\_t, 163
  - hsa\_ext\_image\_get\_capability, 174
  - hsa\_ext\_image\_get\_capability\_with\_layout, 175
  - hsa\_ext\_image\_import, 175
  - hsa\_ext\_image\_t, 159
  - hsa\_ext\_images\_1, 158
  - hsa\_ext\_images\_1\_00, 158
  - hsa\_ext\_sampler\_addressing\_mode32\_t, 159
  - HSA\_EXT\_SAMPLER\_ADDRESSING\_MODE\_CLAMP\_TO\_BORDER, 164
  - HSA\_EXT\_SAMPLER\_ADDRESSING\_MODE\_CLAMP\_TO\_EDGE, 164
  - HSA\_EXT\_SAMPLER\_ADDRESSING\_MODE\_MIRRORED\_REPEAT, 164
  - HSA\_EXT\_SAMPLER\_ADDRESSING\_MODE\_REPEAT, 164
  - hsa\_ext\_sampler\_addressing\_mode\_t, 164
  - HSA\_EXT\_SAMPLER\_ADDRESSING\_MODE\_UNDEFINED, 164
  - hsa\_ext\_sampler\_coordinate\_mode32\_t, 159
  - HSA\_EXT\_SAMPLER\_COORDINATE\_MODE\_NORMALIZED, 165
  - hsa\_ext\_sampler\_coordinate\_mode\_t, 164
  - HSA\_EXT\_SAMPLER\_COORDINATE\_MODE\_UNNORMALIZED, 165
  - hsa\_ext\_sampler\_create, 176
  - hsa\_ext\_sampler\_destroy, 177
  - hsa\_ext\_sampler\_filter\_mode32\_t, 160
  - HSA\_EXT\_SAMPLER\_FILTER\_MODE\_LINEAR, 165
  - HSA\_EXT\_SAMPLER\_FILTER\_MODE\_NEAREST, 165
  - hsa\_ext\_sampler\_filter\_mode\_t, 165
  - HSA\_EXT\_STATUS\_ERROR\_IMAGE\_FORMAT\_UNSUPPORTED, 160

- HSA\_EXT\_STATUS\_ERROR\_IMAGE\_PITCH\_UNSUPPORTED, 160
- HSA\_EXT\_STATUS\_ERROR\_IMAGE\_SIZE\_UNSUPPORTED, 160
- HSA\_EXT\_STATUS\_ERROR\_SAMPLER\_DESCRIPTOR\_UNSUPPORTED, 160
- imageSegmentMemoryScope
  - BrigInstMemFence, 236
- imageType
  - BrigInstImage, 232
  - BrigInstQueryImage, 238
- inArgCount
  - BrigDirectiveExecutable, 211
- init
  - BrigDirectiveVariable, 221
- instance\_count
  - hsa\_ven\_amd\_aqlprofile\_id\_query\_t, 354
- Instruction Set Architecture., 103
  - hsa\_agent\_iterate\_isas, 107
  - HSA\_FLUSH\_MODE\_FTZ, 105
  - HSA\_FLUSH\_MODE\_NON\_FTZ, 105
  - hsa\_flush\_mode\_t, 104
  - HSA\_FP\_TYPE\_16, 105
  - HSA\_FP\_TYPE\_32, 105
  - HSA\_FP\_TYPE\_64, 105
  - hsa\_fp\_type\_t, 105
  - hsa\_isa\_compatible, 108
  - hsa\_isa\_from\_name, 109
  - hsa\_isa\_get\_exception\_policies, 109
  - hsa\_isa\_get\_info, 110
  - hsa\_isa\_get\_info\_alt, 110
  - hsa\_isa\_get\_round\_method, 111
  - HSA\_ISA\_INFO\_BASE\_PROFILE\_DEFAULT\_FLOAT\_ROUNDING\_MODES, 106
  - HSA\_ISA\_INFO\_CALL\_CONVENTION\_COUNT, 105
  - HSA\_ISA\_INFO\_CALL\_CONVENTION\_INFO\_WAVEFRONT\_SIZE, 105
  - HSA\_ISA\_INFO\_CALL\_CONVENTION\_INFO\_WAVEFRONTS\_PER\_COMPUTE\_UNIT, 106
  - HSA\_ISA\_INFO\_DEFAULT\_FLOAT\_ROUNDING\_MODES, 106
  - HSA\_ISA\_INFO\_FAST\_F16\_OPERATION, 106
  - HSA\_ISA\_INFO\_FBARRIER\_MAX\_SIZE, 107
  - HSA\_ISA\_INFO\_GRID\_MAX\_DIM, 107
  - HSA\_ISA\_INFO\_GRID\_MAX\_SIZE, 107
  - HSA\_ISA\_INFO\_MACHINE\_MODELS, 106
  - HSA\_ISA\_INFO\_NAME, 105
  - HSA\_ISA\_INFO\_NAME\_LENGTH, 105
  - HSA\_ISA\_INFO\_PROFILES, 106
  - hsa\_isa\_info\_t, 105
  - HSA\_ISA\_INFO\_WORKGROUP\_MAX\_DIM, 106
  - HSA\_ISA\_INFO\_WORKGROUP\_MAX\_SIZE, 106
  - hsa\_isa\_iterate\_wavefronts, 112
  - HSA\_ROUND\_METHOD\_DOUBLE, 107
  - HSA\_ROUND\_METHOD\_SINGLE, 107
  - hsa\_round\_method\_t, 107
  - hsa\_wavefront\_get\_info, 112
- hsa\_wavefront\_info\_size, 107
  - hsa\_wavefront\_info\_t, 107
- hsa\_kernel\_dispatch\_packet\_s, 340
- kernarg\_address
- kernarg\_segment\_alignment
  - amd\_kernel\_code\_s, 185
- kernarg\_segment\_byte\_size
  - amd\_kernel\_code\_s, 185
- kernel\_code\_entry\_byte\_offset
  - amd\_kernel\_code\_s, 185
- kernel\_code\_prefetch\_byte\_offset
  - amd\_kernel\_code\_s, 185
- kernel\_code\_prefetch\_byte\_size
  - amd\_kernel\_code\_s, 185
- kernel\_code\_properties
  - amd\_kernel\_code\_s, 185
- kernel\_name
  - amd\_runtime\_loader\_debug\_info\_s, 193
- kernel\_object
  - hsa\_kernel\_dispatch\_packet\_s, 341
- kind
  - amd\_signal\_s, 195
  - amdgpu\_hsa\_image\_descriptor\_s, 198
  - amdgpu\_hsa\_sampler\_descriptor\_s, 205
  - BrigBase, 207
- legacy\_doorbell\_lock
  - amd\_queue\_s, 189
- legacy\_hardware\_doorbell\_ptr
  - amd\_signal\_s, 195
- line
  - BrigDirective, 217
- link\_type
  - hsa\_amd\_memory\_pool\_link\_info\_s, 299
- linkage
  - BrigDirectiveExecutable, 212
  - BrigDirectiveFbarrier, 214
  - BrigDirectiveVariable, 221
  - lo
  - BrigUInt64, 262
- machine\_model
  - amdgpu\_hsa\_note\_hsail\_s, 200
- machineModel
  - BrigDirectiveModule, 218
- major
  - amdgpu\_hsa\_note\_isa\_s, 201
- major\_id
  - ApiTableVersion, 206
- major\_version
  - amdgpu\_hsa\_note\_code\_object\_version\_s, 199
- mask
  - hsa\_amd\_barrier\_value\_packet\_s, 292
- max\_bandwidth
  - hsa\_amd\_memory\_pool\_link\_info\_s, 299
- max\_cu\_id
  - amd\_queue\_s, 189
- max\_dynamic\_group\_size

- amd\_control\_directives\_s, 180
- hsa\_ext\_control\_directives\_s, 319
- max\_flat\_grid\_size
  - amd\_control\_directives\_s, 180
  - hsa\_ext\_control\_directives\_s, 319
- max\_flat\_workgroup\_size
  - amd\_control\_directives\_s, 180
  - hsa\_ext\_control\_directives\_s, 320
- max\_latency
  - hsa\_amd\_memory\_pool\_link\_info\_s, 299
- max\_legacy\_doorbell\_dispatch\_id\_plus\_1
  - amd\_queue\_s, 189
- max\_scratch\_backing\_memory\_byte\_size
  - amd\_kernel\_code\_s, 186
- max\_wave\_id
  - amd\_queue\_s, 190
- Memory, 72
  - hsa\_agent\_iterate\_regions, 76
  - hsa\_memory\_allocate, 76
  - hsa\_memory\_assign\_agent, 77
  - hsa\_memory\_copy, 77
  - hsa\_memory\_deregister, 78
  - hsa\_memory\_free, 79
  - hsa\_memory\_register, 79
  - hsa\_region\_get\_info, 80
  - HSA\_REGION\_GLOBAL\_FLAG\_COARSE\_GRAINED, 74
  - HSA\_REGION\_GLOBAL\_FLAG\_FINE\_GRAINED, 74
  - HSA\_REGION\_GLOBAL\_FLAG\_KERNARG, 74
  - hsa\_region\_global\_flag\_t, 73
  - HSA\_REGION\_INFO\_ALLOC\_MAX\_PRIVATE\_WORKGROUP\_SIZE, 75
  - HSA\_REGION\_INFO\_ALLOC\_MAX\_SIZE, 74
  - HSA\_REGION\_INFO\_GLOBAL\_FLAGS, 74
  - HSA\_REGION\_INFO\_RUNTIME\_ALLOC\_ALIGNMENT, 75
  - HSA\_REGION\_INFO\_RUNTIME\_ALLOC\_ALLOWED, 75
  - HSA\_REGION\_INFO\_RUNTIME\_ALLOC\_GRANULE, 75
  - HSA\_REGION\_INFO\_SEGMENT, 74
  - HSA\_REGION\_INFO\_SIZE, 74
  - hsa\_region\_info\_t, 74
  - HSA\_REGION\_SEGMENT\_GLOBAL, 75
  - HSA\_REGION\_SEGMENT\_GROUP, 75
  - HSA\_REGION\_SEGMENT\_KERNARG, 75
  - HSA\_REGION\_SEGMENT\_PRIVATE, 75
  - HSA\_REGION\_SEGMENT\_READONLY, 75
  - hsa\_region\_segment\_t, 75
- memory\_fault
  - hsa\_amd\_event\_s, 294
- memoryOrder
  - BrigInstAtomic, 224
  - BrigInstMemFence, 236
  - BrigInstQueue, 240
  - BrigInstSignal, 244
- memoryScope
  - BrigInstAtomic, 225
- min\_bandwidth
  - hsa\_amd\_memory\_pool\_link\_info\_s, 299
- min\_latency
  - hsa\_amd\_memory\_pool\_link\_info\_s, 299
- minor
  - amdgpu\_hsa\_note\_isa\_s, 201
- minor\_id
  - ApiTableVersion, 206
- minor\_version
  - amdgpu\_hsa\_note\_code\_object\_version\_s, 199
- modifier
  - BrigDirectiveExecutable, 212
  - BrigDirectiveFbarrier, 214
  - BrigDirectiveVariable, 222
  - BrigInstCmp, 229
  - BrigInstCvt, 230
  - BrigInstMem, 234
  - BrigInstMod, 237
  - BrigInstSegCvt, 243
- name
  - BrigDirectiveComment, 209
  - BrigDirectiveExecutable, 212
  - BrigDirectiveExtension, 213
  - BrigDirectiveFbarrier, 214
  - BrigDirectiveLabel, 215
  - BrigDirectiveModule, 218
  - BrigDirectiveVariable, 222
  - BrigSectionHeader, 261
  - hsa\_ven\_amd\_aqlprofile\_id\_query\_t, 354
  - BrigSectionHeader, 261
- nextModuleEntry
  - BrigDirectiveExecutable, 212
- mma\_distance
  - hsa\_amd\_memory\_pool\_link\_info\_s, 300
- offset
  - BrigOperandAddress, 248
  - hsa\_ext\_image\_region\_s, 327
- opcode
  - BrigInstBase, 226
- operands
  - BrigDirectiveControl, 210
  - BrigDirectivePragma, 220
  - BrigInstBase, 226
- outArgCount
  - BrigDirectiveExecutable, 212
- output\_buffer
  - hsa\_ven\_amd\_aqlprofile\_profile\_t, 357
- owning\_segment
  - amd\_runtime\_loader\_debug\_info\_s, 193
- pack
  - BrigInstCmp, 229
  - BrigInstMod, 237
- parameter\_count
  - hsa\_ven\_amd\_aqlprofile\_profile\_t, 357



- parameter\_name
  - hsa\_ven\_amd\_aqlprofile\_parameter\_t, 356
- parameters
  - hsa\_ven\_amd\_aqlprofile\_profile\_t, 358
- pitch
  - hsa\_pitched\_ptr\_s, 344
- pm4\_command
  - hsa\_ext\_amd\_aql\_pm4\_packet\_t, 317
- private\_segment\_alignment
  - amd\_kernel\_code\_s, 186
- private\_segment\_aperture\_base\_hi
  - amd\_queue\_s, 190
- private\_segment\_size
  - hsa\_kernel\_dispatch\_packet\_s, 341
- producer\_major\_version
  - amdgpu\_hsa\_note\_producer\_s, 203
- producer\_minor\_version
  - amdgpu\_hsa\_note\_producer\_s, 203
- producer\_name
  - amdgpu\_hsa\_note\_producer\_s, 203
- producer\_name\_size
  - amdgpu\_hsa\_note\_producer\_s, 203
- producer\_options
  - amdgpu\_hsa\_note\_producer\_options\_s, 202
- producer\_options\_size
  - amdgpu\_hsa\_note\_producer\_options\_s, 202
- profile
  - amdgpu\_hsa\_note\_hsail\_s, 200
  - BrigDirectiveModule, 218
- ptr
  - hsa\_ven\_amd\_aqlprofile\_descriptor\_t, 352
- query
  - BrigInstQueryImage, 239
  - BrigInstQuerySampler, 239
- queue\_inactive\_signal
  - amd\_queue\_s, 190
- queue\_properties
  - amd\_queue\_s, 190
- queue\_ptr
  - amd\_signal\_s, 195
- Queues, 80
  - hsa\_queue\_add\_write\_index\_acq\_rel, 83
  - hsa\_queue\_add\_write\_index\_acquire, 84
  - hsa\_queue\_add\_write\_index\_relaxed, 84
  - hsa\_queue\_add\_write\_index\_release, 85
  - hsa\_queue\_add\_write\_index\_scacq\_screl, 85
  - hsa\_queue\_add\_write\_index\_scacquire, 86
  - hsa\_queue\_add\_write\_index\_screlease, 86
  - hsa\_queue\_cas\_write\_index\_acq\_rel, 86
  - hsa\_queue\_cas\_write\_index\_acquire, 87
  - hsa\_queue\_cas\_write\_index\_relaxed, 87
  - hsa\_queue\_cas\_write\_index\_release, 88
  - hsa\_queue\_cas\_write\_index\_scacq\_screl, 88
  - hsa\_queue\_cas\_write\_index\_scacquire, 89
  - hsa\_queue\_cas\_write\_index\_screlease, 89
  - hsa\_queue\_create, 90
  - hsa\_queue\_destroy, 91
  - HSA\_QUEUE\_FEATURE\_AGENT\_DISPATCH, 83
  - HSA\_QUEUE\_FEATURE\_KERNEL\_DISPATCH, 83
  - hsa\_queue\_feature\_t, 83
  - hsa\_queue\_inactivate, 92
  - hsa\_queue\_load\_read\_index\_acquire, 92
  - hsa\_queue\_load\_read\_index\_relaxed, 93
  - hsa\_queue\_load\_read\_index\_scacquire, 93
  - hsa\_queue\_load\_write\_index\_acquire, 93
  - hsa\_queue\_load\_write\_index\_relaxed, 94
  - hsa\_queue\_load\_write\_index\_scacquire, 94
  - hsa\_queue\_store\_read\_index\_relaxed, 94
  - hsa\_queue\_store\_read\_index\_release, 95
  - hsa\_queue\_store\_read\_index\_screlease, 95
  - hsa\_queue\_store\_write\_index\_relaxed, 96
  - hsa\_queue\_store\_write\_index\_release, 96
  - hsa\_queue\_store\_write\_index\_screlease, 96
  - hsa\_queue\_t, 82
  - hsa\_queue\_type32\_t, 82
  - HSA\_QUEUE\_TYPE\_COOPERATIVE, 83
  - HSA\_QUEUE\_TYPE\_MULTI, 83
  - HSA\_QUEUE\_TYPE\_SINGLE, 83
  - hsa\_queue\_type\_t, 83
  - hsa\_soft\_queue\_create, 97
- range
  - hsa\_ext\_image\_region\_s, 328
- read\_dispatch\_id
  - amd\_queue\_s, 190
- read\_dispatch\_id\_field\_base\_byte\_offset
  - amd\_queue\_s, 190
- ref
  - BrigOperandCodeRef, 251
- reg
  - BrigOperandAddress, 248
- regKind
  - BrigOperandRegister, 258
- regNum
  - BrigOperandRegister, 259
- required\_dim
  - amd\_control\_directives\_s, 180
  - hsa\_ext\_control\_directives\_s, 320
- required\_grid\_size
  - amd\_control\_directives\_s, 180
  - hsa\_ext\_control\_directives\_s, 320
- required\_workgroup\_size
  - amd\_control\_directives\_s, 181
  - hsa\_ext\_control\_directives\_s, 321
- reserved
  - amdgpu\_hsa\_note\_producer\_s, 203
  - ApiTableVersion, 206
  - BrigDirectiveControl, 210
  - BrigDirectiveExecutable, 212
  - BrigDirectiveFbarrier, 215
  - BrigDirectiveModule, 218
  - BrigDirectiveVariable, 222
  - BrigInstAddr, 223
  - BrigInstAtomic, 225
  - BrigInstBr, 228
  - BrigInstCmp, 229

- BrigInstImage, [232](#)
- BrigInstLane, [233](#)
- BrigInstMem, [234](#)
- BrigInstMod, [237](#)
- BrigInstQuerySampler, [240](#)
- BrigInstQueue, [241](#)
- BrigInstSeg, [242](#)
- BrigInstSourceType, [245](#)
- BrigModuleHeader, [247](#)
- BrigOperandAlign, [249](#)
- BrigOperandConstantBytes, [252](#)
- BrigOperandConstantImage, [254](#)
- BrigOperandConstantOperandList, [255](#)
- BrigOperandConstantSampler, [257](#)
- hsa\_amd\_packet\_header\_s, [302](#)
- reserved0
  - hsa\_agent\_dispatch\_packet\_s, [288](#)
  - hsa\_amd\_barrier\_value\_packet\_s, [292](#)
  - hsa\_barrier\_and\_packet\_s, [308](#)
  - hsa\_barrier\_or\_packet\_s, [310](#)
  - hsa\_kernel\_dispatch\_packet\_s, [341](#)
  - hsa\_queue\_s, [346](#)
- reserved1
  - amd\_control\_directives\_s, [181](#)
  - amd\_kernel\_code\_s, [186](#)
  - amd\_queue\_s, [191](#)
  - amd\_signal\_s, [195](#)
  - amdgpu\_hsa\_image\_descriptor\_s, [198](#)
  - amdgpu\_hsa\_sampler\_descriptor\_s, [205](#)
  - hsa\_agent\_dispatch\_packet\_s, [289](#)
  - hsa\_amd\_barrier\_value\_packet\_s, [292](#)
  - hsa\_barrier\_and\_packet\_s, [308](#)
  - hsa\_barrier\_or\_packet\_s, [310](#)
  - hsa\_ext\_control\_directives\_s, [321](#)
  - hsa\_kernel\_dispatch\_packet\_s, [341](#)
  - hsa\_queue\_s, [346](#)
- reserved2
  - amd\_control\_directives\_s, [181](#)
  - amd\_queue\_s, [191](#)
  - amd\_signal\_s, [195](#)
  - hsa\_agent\_dispatch\_packet\_s, [289](#)
  - hsa\_amd\_barrier\_value\_packet\_s, [292](#)
  - hsa\_barrier\_and\_packet\_s, [308](#)
  - hsa\_barrier\_or\_packet\_s, [310](#)
  - hsa\_ext\_control\_directives\_s, [321](#)
  - hsa\_kernel\_dispatch\_packet\_s, [341](#)
- reserved3
  - amd\_queue\_s, [191](#)
  - amd\_signal\_s, [196](#)
  - hsa\_amd\_barrier\_value\_packet\_s, [292](#)
- reserved4
  - amd\_queue\_s, [191](#)
- reserved\_sgpr\_count
  - amd\_kernel\_code\_s, [186](#)
- reserved\_sgpr\_first
  - amd\_kernel\_code\_s, [186](#)
- reserved\_vgpr\_count
  - amd\_kernel\_code\_s, [186](#)
- reserved\_vgpr\_first
  - amd\_kernel\_code\_s, [187](#)
- result
  - hsa\_ven\_amd\_aqlprofile\_info\_data\_t, [355](#)
- return\_address
  - hsa\_agent\_dispatch\_packet\_s, [289](#)
- root
  - HsaApiTableContainer, [371](#)
- round
  - BrigInstCvt, [230](#)
  - BrigInstMod, [237](#)
- Runtime Notifications, [15](#)
  - HSA\_ACCESS\_PERMISSION\_RO, [16](#)
  - HSA\_ACCESS\_PERMISSION\_RW, [16](#)
  - hsa\_access\_permission\_t, [16](#)
  - HSA\_ACCESS\_PERMISSION\_WO, [16](#)
  - hsa\_file\_t, [16](#)
  - hsa\_init, [18](#)
  - hsa\_shut\_down, [18](#)
  - HSA\_STATUS\_ERROR, [17](#)
  - HSA\_STATUS\_ERROR\_EXCEPTION, [17](#)
  - HSA\_STATUS\_ERROR\_FATAL, [18](#)
  - HSA\_STATUS\_ERROR\_FROZEN\_EXECUTABLE, [17](#)
  - HSA\_STATUS\_ERROR\_INCOMPATIBLE\_ARGUMENTS, [17](#)
  - HSA\_STATUS\_ERROR\_INVALID\_AGENT, [17](#)
  - HSA\_STATUS\_ERROR\_INVALID\_ALLOCATION, [17](#)
  - HSA\_STATUS\_ERROR\_INVALID\_ARGUMENT, [17](#)
  - HSA\_STATUS\_ERROR\_INVALID\_CACHE, [18](#)
  - HSA\_STATUS\_ERROR\_INVALID\_CODE\_OBJECT, [17](#)
  - HSA\_STATUS\_ERROR\_INVALID\_CODE\_OBJECT\_READER, [18](#)
  - HSA\_STATUS\_ERROR\_INVALID\_CODE\_SYMBOL, [17](#)
  - HSA\_STATUS\_ERROR\_INVALID\_EXECUTABLE, [17](#)
  - HSA\_STATUS\_ERROR\_INVALID\_EXECUTABLE\_SYMBOL, [18](#)
  - HSA\_STATUS\_ERROR\_INVALID\_FILE, [18](#)
  - HSA\_STATUS\_ERROR\_INVALID\_INDEX, [17](#)
  - HSA\_STATUS\_ERROR\_INVALID\_ISA, [17](#)
  - HSA\_STATUS\_ERROR\_INVALID\_ISA\_NAME, [17](#)
  - HSA\_STATUS\_ERROR\_INVALID\_PACKET\_FORMAT, [17](#)
  - HSA\_STATUS\_ERROR\_INVALID\_QUEUE, [17](#)
  - HSA\_STATUS\_ERROR\_INVALID\_QUEUE\_CREATION, [17](#)
  - HSA\_STATUS\_ERROR\_INVALID\_REGION, [17](#)
  - HSA\_STATUS\_ERROR\_INVALID\_RUNTIME\_STATE, [18](#)
  - HSA\_STATUS\_ERROR\_INVALID\_SIGNAL, [17](#)
  - HSA\_STATUS\_ERROR\_INVALID\_SIGNAL\_GROUP, [18](#)
  - HSA\_STATUS\_ERROR\_INVALID\_SYMBOL\_NAME, [18](#)



- 17
- HSA\_STATUS\_ERROR\_INVALID\_WAVEFRONT, 18
- HSA\_STATUS\_ERROR\_NOT\_INITIALIZED, 17
- HSA\_STATUS\_ERROR\_OUT\_OF\_RESOURCES, 17
- HSA\_STATUS\_ERROR\_REFCOUNT\_OVERFLOW, 17
- HSA\_STATUS\_ERROR\_RESOURCE\_FREE, 17
- HSA\_STATUS\_ERROR\_VARIABLE\_ALREADY\_DEFINED, 17
- HSA\_STATUS\_ERROR\_VARIABLE\_UNDEFINED, 17
- HSA\_STATUS\_INFO\_BREAK, 17
- hsa\_status\_string, 19
- HSA\_STATUS\_SUCCESS, 17
- hsa\_status\_t, 17
- runtime\_loader\_kernel\_symbol
  - amd\_kernel\_code\_s, 187
- sample\_id
  - hsa\_ven\_amd\_aqlprofile\_info\_data\_t, 355
- scratch\_backing\_memory\_byte\_size
  - amd\_queue\_s, 191
- scratch\_backing\_memory\_location
  - amd\_queue\_s, 191
- scratch\_resource\_descriptor
  - amd\_queue\_s, 192
- scratch\_wave64\_lane\_byte\_size
  - amd\_queue\_s, 192
- sectionCount
  - BrigModuleHeader, 247
- sectionIndex
  - BrigModuleHeader, 247
- segment
  - BrigDirectiveVariable, 222
  - BrigInstAddr, 223
  - BrigInstAtomic, 225
  - BrigInstMem, 235
  - BrigInstQueue, 241
  - BrigInstSeg, 242
  - BrigInstSegCvt, 243
- segment\_base
  - hsa\_ven\_amd\_loader\_segment\_descriptor\_s, 367
- segment\_size
  - hsa\_ven\_amd\_loader\_segment\_descriptor\_s, 367
- setup
  - hsa\_kernel\_dispatch\_packet\_s, 342
- signal
  - hsa\_amd\_barrier\_value\_packet\_s, 293
- signalOperation
  - BrigInstSignal, 244
- Signals, 38
  - hsa\_signal\_add\_acq\_rel, 42
  - hsa\_signal\_add\_acquire, 43
  - hsa\_signal\_add\_relaxed, 43
  - hsa\_signal\_add\_release, 44
  - hsa\_signal\_add\_scacq\_screl, 44
  - hsa\_signal\_add\_scacquire, 44
  - hsa\_signal\_add\_screlease, 45
  - hsa\_signal\_and\_acq\_rel, 45
  - hsa\_signal\_and\_acquire, 46
  - hsa\_signal\_and\_relaxed, 46
  - hsa\_signal\_and\_release, 46
  - hsa\_signal\_and\_scacq\_screl, 47
  - hsa\_signal\_and\_scacquire, 47
  - hsa\_signal\_and\_screlease, 47
  - hsa\_signal\_cas\_acq\_rel, 48
  - hsa\_signal\_cas\_acquire, 48
  - hsa\_signal\_cas\_relaxed, 49
  - hsa\_signal\_cas\_release, 49
  - hsa\_signal\_cas\_scacq\_screl, 50
  - hsa\_signal\_cas\_scacquire, 50
  - hsa\_signal\_cas\_screlease, 51
  - HSA\_SIGNAL\_CONDITION\_EQ, 42
  - HSA\_SIGNAL\_CONDITION\_GTE, 42
  - HSA\_SIGNAL\_CONDITION\_LT, 42
  - HSA\_SIGNAL\_CONDITION\_NE, 42
  - hsa\_signal\_condition\_t, 42
  - hsa\_signal\_create, 51
  - hsa\_signal\_destroy, 52
  - hsa\_signal\_exchange\_acq\_rel, 52
  - hsa\_signal\_exchange\_acquire, 53
  - hsa\_signal\_exchange\_relaxed, 53
  - hsa\_signal\_exchange\_release, 54
  - hsa\_signal\_exchange\_scacq\_screl, 54
  - hsa\_signal\_exchange\_scacquire, 55
  - hsa\_signal\_exchange\_screlease, 55
  - hsa\_signal\_group\_create, 56
  - hsa\_signal\_group\_destroy, 56
  - hsa\_signal\_group\_wait\_any\_relaxed, 57
  - hsa\_signal\_group\_wait\_any\_scacquire, 58
  - hsa\_signal\_load\_acquire, 59
  - hsa\_signal\_load\_relaxed, 59
  - hsa\_signal\_load\_scacquire, 59
  - hsa\_signal\_or\_acq\_rel, 60
  - hsa\_signal\_or\_acquire, 60
  - hsa\_signal\_or\_relaxed, 61
  - hsa\_signal\_or\_release, 61
  - hsa\_signal\_or\_scacq\_screl, 61
  - hsa\_signal\_or\_scacquire, 62
  - hsa\_signal\_or\_screlease, 62
  - hsa\_signal\_silent\_store\_relaxed, 62
  - hsa\_signal\_silent\_store\_screlease, 63
  - hsa\_signal\_store\_relaxed, 63
  - hsa\_signal\_store\_release, 64
  - hsa\_signal\_store\_screlease, 64
  - hsa\_signal\_subtract\_acq\_rel, 64
  - hsa\_signal\_subtract\_acquire, 65
  - hsa\_signal\_subtract\_relaxed, 65
  - hsa\_signal\_subtract\_release, 66
  - hsa\_signal\_subtract\_scacq\_screl, 66
  - hsa\_signal\_subtract\_scacquire, 66
  - hsa\_signal\_subtract\_screlease, 67
  - hsa\_signal\_value\_t, 41
  - hsa\_signal\_wait\_acquire, 67
  - hsa\_signal\_wait\_relaxed, 68

- hsa\_signal\_wait\_scacquire, 69
- hsa\_signal\_xor\_acq\_rel, 69
- hsa\_signal\_xor\_acquire, 70
- hsa\_signal\_xor\_relaxed, 70
- hsa\_signal\_xor\_release, 71
- hsa\_signal\_xor\_scacq\_screl, 71
- hsa\_signal\_xor\_scacquire, 71
- hsa\_signal\_xor\_screlease, 72
- HSA\_WAIT\_STATE\_ACTIVE, 42
- HSA\_WAIT\_STATE\_BLOCKED, 42
- hsa\_wait\_state\_t, 42
- signalType
  - BrigInstSignal, 244
- size
  - amdgpu\_hsa\_image\_descriptor\_s, 198
  - amdgpu\_hsa\_sampler\_descriptor\_s, 205
  - hsa\_amd\_pointer\_info\_s, 303
  - hsa\_ext\_image\_data\_info\_s, 324
  - hsa\_queue\_s, 346
  - hsa\_ven\_amd\_aqlprofile\_descriptor\_t, 352
- sizeInBytes
  - hsa\_amd\_pointer\_info\_s, 303
- slice
  - hsa\_pitched\_ptr\_s, 344
- sourceType
  - BrigInstCmp, 229
  - BrigInstCvt, 230
  - BrigInstLane, 233
  - BrigInstSegCvt, 243
  - BrigInstSourceType, 245
- start
  - hsa\_amd\_profiling\_async\_copy\_time\_s, 305
  - hsa\_amd\_profiling\_dispatch\_time\_s, 306
- start\_ts
  - amd\_signal\_s, 196
- step\_id
  - ApiTableVersion, 206
- stepping
  - amdgpu\_hsa\_note\_isa\_s, 201
- string
  - BrigOperandString, 259
- symbol
  - BrigOperandAddress, 248
- System and Agent Information, 19
  - hsa\_agent\_extension\_supported, 31
  - HSA\_AGENT\_FEATURE\_AGENT\_DISPATCH, 22
  - HSA\_AGENT\_FEATURE\_KERNEL\_DISPATCH, 22
  - hsa\_agent\_feature\_t, 21
  - hsa\_agent\_get\_exception\_policies, 31
  - hsa\_agent\_get\_info, 32
  - HSA\_AGENT\_INFO\_BASE\_PROFILE\_DEFAULT\_FLOAT\_ROUNDING\_MODE, 23
  - HSA\_AGENT\_INFO\_CACHE\_SIZE, 26
  - HSA\_AGENT\_INFO\_DEFAULT\_FLOAT\_ROUNDING\_MODE, 23
  - HSA\_AGENT\_INFO\_DEVICE, 26
  - HSA\_AGENT\_INFO\_EXTENSIONS, 26
  - HSA\_AGENT\_INFO\_FAST\_F16\_OPERATION, 24
  - HSA\_AGENT\_INFO\_FBARRIER\_MAX\_SIZE, 25
  - HSA\_AGENT\_INFO\_FEATURE, 22
  - HSA\_AGENT\_INFO\_GRID\_MAX\_DIM, 25
  - HSA\_AGENT\_INFO\_GRID\_MAX\_SIZE, 25
  - HSA\_AGENT\_INFO\_ISA, 26
  - HSA\_AGENT\_INFO\_LAST, 27
  - HSA\_AGENT\_INFO\_MACHINE\_MODEL, 22
  - HSA\_AGENT\_INFO\_NAME, 22
  - HSA\_AGENT\_INFO\_NODE, 26
  - HSA\_AGENT\_INFO\_PROFILE, 23
  - HSA\_AGENT\_INFO\_QUEUE\_MAX\_SIZE, 26
  - HSA\_AGENT\_INFO\_QUEUE\_MIN\_SIZE, 26
  - HSA\_AGENT\_INFO\_QUEUE\_TYPE, 26
  - HSA\_AGENT\_INFO\_QUEUES\_MAX, 26
  - hsa\_agent\_info\_t, 22
  - HSA\_AGENT\_INFO\_VENDOR\_NAME, 22
  - HSA\_AGENT\_INFO\_VERSION\_MAJOR, 26
  - HSA\_AGENT\_INFO\_VERSION\_MINOR, 27
  - HSA\_AGENT\_INFO\_WAVEFRONT\_SIZE, 24
  - HSA\_AGENT\_INFO\_WORKGROUP\_MAX\_DIM, 24
  - HSA\_AGENT\_INFO\_WORKGROUP\_MAX\_SIZE, 25
  - hsa\_agent\_iterate\_caches, 32
  - hsa\_agent\_major\_extension\_supported, 33
  - HSA\_AMD\_FIRST\_EXTENSION, 29
  - HSA\_AMD\_LAST\_EXTENSION, 29
  - HSA\_AMD\_SYSTEM\_INFO\_BUILD\_VERSION, 30
  - HSA\_AMD\_SYSTEM\_INFO\_SVM\_ACCESSIBLE\_BY\_DEFAULT, 30
  - HSA\_AMD\_SYSTEM\_INFO\_SVM\_SUPPORTED, 30
  - hsa\_cache\_get\_info, 34
  - HSA\_CACHE\_INFO\_LEVEL, 27
  - HSA\_CACHE\_INFO\_NAME, 27
  - HSA\_CACHE\_INFO\_NAME\_LENGTH, 27
  - HSA\_CACHE\_INFO\_SIZE, 27
  - hsa\_cache\_info\_t, 27
  - HSA\_DEFAULT\_FLOAT\_ROUNDING\_MODE\_DEFAULT, 27
  - HSA\_DEFAULT\_FLOAT\_ROUNDING\_MODE\_NEAR, 27
  - hsa\_default\_float\_rounding\_mode\_t, 27
  - HSA\_DEFAULT\_FLOAT\_ROUNDING\_MODE\_ZERO, 27
  - HSA\_DEVICE\_TYPE\_CPU, 28
  - HSA\_DEVICE\_TYPE\_DSP, 28
  - HSA\_DEVICE\_TYPE\_GPU, 28
  - hsa\_device\_type\_t, 28
  - HSA\_ENDIANNESSE\_BIG, 28
  - HSA\_ENDIANNESSE\_LITTLE, 28
  - hsa\_endianness\_t, 28
  - HSA\_EXCEPTION\_POLICY\_BREAK, 28
  - HSA\_EXCEPTION\_POLICY\_DETECT, 28
  - hsa\_exception\_policy\_t, 28
  - HSA\_EXTENSION\_AMD\_AQLPROFILE, 29

- HSA\_EXTENSION\_AMD\_LOADER, [29](#)
- HSA\_EXTENSION\_AMD\_PROFILER, [29](#)
- HSA\_EXTENSION\_FINALIZER, [29](#)
- hsa\_extension\_get\_name, [34](#)
- HSA\_EXTENSION\_IMAGES, [29](#)
- HSA\_EXTENSION\_PERFORMANCE\_COUNTERS, [29](#)
- HSA\_EXTENSION\_PROFILING\_EVENTS, [29](#)
- HSA\_EXTENSION\_STD\_LAST, [29](#)
- hsa\_extension\_t, [29](#)
- hsa\_iterate\_agents, [35](#)
- HSA\_MACHINE\_MODEL\_LARGE, [29](#)
- HSA\_MACHINE\_MODEL\_SMALL, [29](#)
- hsa\_machine\_model\_t, [29](#)
- HSA\_PROFILE\_BASE, [30](#)
- HSA\_PROFILE\_FULL, [30](#)
- hsa\_profile\_t, [29](#)
- hsa\_system\_extension\_supported, [35](#)
- hsa\_system\_get\_extension\_table, [36](#)
- hsa\_system\_get\_info, [36](#)
- hsa\_system\_get\_major\_extension\_table, [37](#)
- HSA\_SYSTEM\_INFO\_ENDIANNES, [30](#)
- HSA\_SYSTEM\_INFO\_EXTENSIONS, [30](#)
- HSA\_SYSTEM\_INFO\_MACHINE\_MODEL, [30](#)
- HSA\_SYSTEM\_INFO\_SIGNAL\_MAX\_WAIT, [30](#)
- hsa\_system\_info\_t, [30](#)
- HSA\_SYSTEM\_INFO\_TIMESTAMP, [30](#)
- HSA\_SYSTEM\_INFO\_TIMESTAMP\_FREQUENCY, [30](#)
- HSA\_SYSTEM\_INFO\_VERSION\_MAJOR, [30](#)
- HSA\_SYSTEM\_INFO\_VERSION\_MINOR, [30](#)
- hsa\_system\_major\_extension\_supported, [37](#)
- trace\_data
  - hsa\_ven\_amd\_aqlprofile\_info\_data\_t, [355](#)
- type
  - BrigDirectiveVariable, [222](#)
  - BrigInstBase, [226](#)
  - BrigOperandConstantBytes, [252](#)
  - BrigOperandConstantImage, [254](#)
  - BrigOperandConstantOperandList, [255](#)
  - BrigOperandConstantSampler, [257](#)
  - hsa\_agent\_dispatch\_packet\_s, [289](#)
  - hsa\_amd\_pointer\_info\_s, [304](#)
  - hsa\_queue\_s, [346](#)
  - hsa\_ven\_amd\_aqlprofile\_profile\_t, [358](#)
- userData
  - hsa\_amd\_pointer\_info\_s, [304](#)
- value
  - amd\_signal\_s, [196](#)
  - hsa\_amd\_barrier\_value\_packet\_s, [293](#)
  - hsa\_amd\_svm\_attribute\_pair\_s, [306](#)
  - hsa\_ven\_amd\_aqlprofile\_parameter\_t, [356](#)
- vendor\_and\_architecture\_name
  - amdgpu\_hsa\_note\_isa\_s, [201](#)
- vendor\_name\_size
  - amdgpu\_hsa\_note\_isa\_s, [201](#)
- version
  - CoreApiTable, [285](#)
  - FinalizerExtTable, [287](#)
  - hsa\_amd\_image\_descriptor\_s, [297](#)
  - HsaApiTable, [370](#)
  - ImageExtTable, [374](#)
- virtual\_address
  - hsa\_amd\_gpu\_memory\_fault\_info\_s, [295](#)
- wavefront\_sgpr\_count
  - amd\_kernel\_code\_s, [187](#)
- wavefront\_size
  - amd\_kernel\_code\_s, [187](#)
- width
  - amdgpu\_hsa\_image\_descriptor\_s, [198](#)
  - BrigInstBr, [228](#)
  - BrigInstLane, [233](#)
  - BrigInstMem, [235](#)
  - BrigOperandConstantImage, [254](#)
  - hsa\_ext\_image\_descriptor\_s, [326](#)
- workgroup\_fbarrier\_count
  - amd\_kernel\_code\_s, [187](#)
- workgroup\_group\_segment\_byte\_size
  - amd\_kernel\_code\_s, [187](#)
- workgroup\_size\_x
  - hsa\_kernel\_dispatch\_packet\_s, [342](#)
- workgroup\_size\_y
  - hsa\_kernel\_dispatch\_packet\_s, [342](#)
- workgroup\_size\_z
  - hsa\_kernel\_dispatch\_packet\_s, [342](#)
- workitem\_private\_segment\_byte\_size
  - amd\_kernel\_code\_s, [188](#)
- workitem\_vgpr\_count
  - amd\_kernel\_code\_s, [188](#)
- write\_dispatch\_id
  - amd\_queue\_s, [192](#)
- x
  - hsa\_dim3\_s, [315](#)
- y
  - hsa\_dim3\_s, [315](#)
- z
  - hsa\_dim3\_s, [315](#)