

BYTE NYBBLES

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A TMS-9900 Monitor

F000 MONITOR
F002 BOOT

Everyone has their own idea of what a good monitor should and should not do. The TMS-9900 monitor described here is aimed at a small system (without disks) with a terminal (64 by 32 screen size) for I/O (input/output). It has been designed such that programs (which may be cross-assembled elsewhere) can be debugged efficiently. To this end, the monitor contains an instant assembler, a disassembler and comprehensive user program tracing facilities. The instant assembler allows modifications in code to be made quickly, since calculating op codes is difficult because the op code fields are not aligned on nybble boundaries.

The monitor occupies slightly less than 256 bytes of memory and has been assembled to occupy hexadecimal locations F400 thru FFFE. Three workspaces are required in programmable memory and these are placed just below the monitor at F3E0, F3C0, and F3A0, respectively. A stack is required, and this starts just below the three workspaces. The stack is used for subroutine linkage and may be used by all user programs. The monitor also makes use of interrupt level 1 (for tracing) and the XOP vectors 0 to 5 inclusive (See figure 1). The description of the monitor is divided into two parts: how to use the monitor, and a general description of how the monitor has been implemented.

How to Use the Monitor

When the monitor is started (by reset or load signals), it outputs << TMS9900 MONITOR >> to the terminal followed by the prompt >. The monitor is now ready to accept commands, all of which have a similar format:

COMMAND :: = {<HEXNO>} <COMMAND LETTER>

The optional hexadecimal number (only the last four digits count), unless otherwise stated, is taken to be a memory address. If no hexadecimal number has been input, then a default action takes place. The monitor keeps and

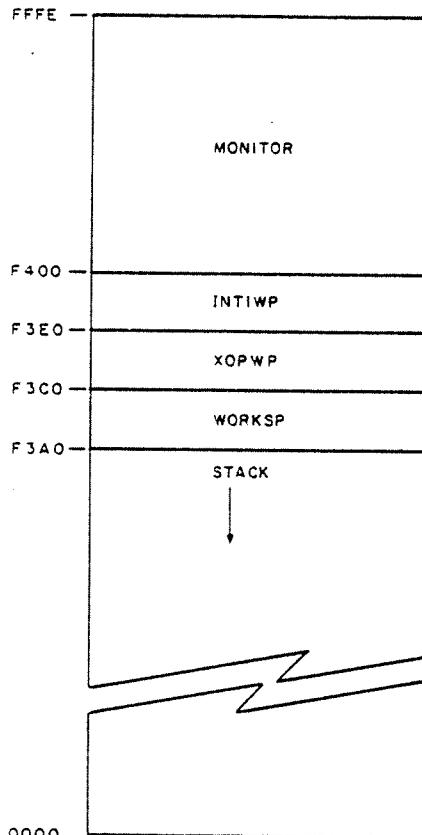


Figure 1: Memory usage in the Texas Instruments TMS-9900 system that uses the authors' monitor. The monitor occupies slightly less than 256 bytes of memory. Three workspaces are required, and they are placed just below the monitor. The stack, used for subroutine linkage, starts immediately below the three workspaces. The monitor also makes use of interrupt level 1 (for tracing) and the XOP vectors 0 to 5, inclusive.

updates two variables, low and last opened cell. The default action takes its address (hexadecimal number) from one of these. In all the examples that follow, the characters that the user inputs are underlined.

Command L

This command allows the monitor variable low to be set. The default action is to output the current value of low, else it is set to the hexadecimal number input. No other command changes the value of low, eg:

```
>2000L
>L
LOW = 2000
>
```

Command L

This command (space) allows memory cells (locations) to be examined and changed. The default action is to open the last opened cell. It outputs the address of the opened cell and its current contents. To change the contents, input a hexadecimal number followed by a delimiter; otherwise, input a delimiter only. If a space is used as a delimiter, then the next cell is opened, a minus sign opens the previous cell, and any other character will return control to the monitor. This command updates the monitor variable last opened cell, eg:

```
>2000L
2000 1234L
2002 6789 05678L
2004 9ABC-
2002 5678 <CR>
```

The symbol <CR> indicates a carriage return and is used throughout this article.

>C6P			
00C6	2CEO	XOP	@ #011P,3
00CA	2CEO	XOP	@ #0108,3
00CE	2CEO	XOP	@ #011D,3
00D2	06A0	BL	@ #022E
00D6	0204	LI	R4, #0013
00DA	9064	CB	@ #0182(R4),R1
00DE	1305	JEQ	#00EA
00EO	0604	DEC	R4
00E2	18FB	JOC	=00DA
00E4	2CEO	XOP	@ #0126,3
00E8	10F2	JMP	=00CE
00EA	0A14	SLA	R4,1
00EC	C124	MOV	@ #0196(R4),R4
00F0	C0C3	MOV	R3,R3
00F2	0454	B	*R4
00F4	7FA0	SB	@ #0080,*R14+ <CR>

Listing 1: An example of the use of command P, a command in the authors' TMS-9900 monitor that lists the memory contents in disassembled form starting at the input address (in this case, hexadecimal C6). 16 disassembled instructions are listed. Inputting a space causes one more disassembled instruction to be output each time.

Command /

This command allows ASCII characters to be written into consecutive memory locations starting at the input address. The default action is to start at the last opened cell. Only characters greater than ASCII hexadecimal 20 (space) and also ASCII hexadecimal 00 may be input. Any other character (except ASCII hexadecimal 08 (backspace)) returns control to the monitor. The use of backspace allows a limited editing facility on the current line. This command updates the monitor variable last opened cell. Note that if ASCII 00 (control shift @) is input, then underscore is output.

```
>5010/
5010 4142 /123456789ABCDEF0/
5020 0000 /12 <CR>
>5010L
5010 3031 <CR>
>
```

Command P

This command lists memory contents in disassembled form starting at the input address. The default action is to start at address low. It lists 16 disassembled instructions, after which inputting further spaces will list one more disassembled instruction; any other character passes control back to the monitor (See listing 1).

Command .

This command enters the instant assembler mode. The default action is to open the last opened cell. The address of the opened cell, its current contents and a . are output. The assembly mnemonic is input followed by a space. The mnemonic is checked to see if it is legal; if not, then ?? is output. The operands must then be input using the same format as the disassembler output, using R to denote registers, etc. The instant assembler will prompt if it can, and control is passed back to the monitor if any character less than ASCII hexadecimal 20 is input. Any other illegal characters simply cause ?? to be output and the user is allowed to try again. If no address is input for a jump address, a displacement of 00 is used. This command updates the monitor variable last opened cell, eg:

```
>7000.
7000 4CA0 MOVA L ???
7000 4CA0 MOVB L @ =1234(R10),*R15+ L
7004 7FE0 LI R1 =3 <CR>
>7000L
7000 DFEA L (Check code produced)
7002 1234 L
7004 0201 L
7006 0003 <CR>
>
```

Command O

This command outputs the contents of memory in block form starting at the input address. The default action is to start at address low. It will output 64 locations after which inputting further spaces will output further 32 word

>C6L

>0

```
00C6 2CE0 011D 2CE0 0108 2CE0  
00D0 011D 06A0 022E 0204 0013 9064 0182 1305  
00E0 0604 18FB 2CE0 0126 10F2 0A14 C124 0196  
00F0 C0C3 0454 7FA0 0080 7FE0 070E 01C6 01E8  
0100 01EE 0200 0210 0222 3E20 544D 5339 3930  
0110 3020 4D4F 4E49 544F 5220 3C3C 000D 0A0A  
0120 2020 2020 3E00 203F 3F00 0A0D 0A20 2020  
0130 2020 0020 5354 4143 4B20 4F56 4552 464C<CR>
```

>

Listing 2: An example of the use of command O, which outputs the contents of memory in block form starting at the input address. 64 locations are output.

blocks. Inputting any other character passes control back to the monitor (See listing 2).

Command Z

This command searches memory sequentially for occurrences of the hexadecimal number input. The search begins at address low. If the hexadecimal number is found, then the address and contents are output. To change the hexadecimal contents input a hexadecimal number followed by a delimiter; otherwise input a delimiter only. If a space is used as a delimiter, then searching continues; other characters pass control back to the monitor. The Z routine will search all of memory (64 K bytes) and if the search is unsuccessful, then NOT FOUND is output.

```
>1234Z  
7000 1234 0 [ ] (Change Contents)  
7003 1234 [ ]  
7F9A 1234 [ ]  
NOT FOUND
```

>

Command M

This command allows a block of data to be moved from one part of memory to another. The instruction syntax is as follows:

<START ADDRESS> : <FINISH ADDRESS> <DESTINATION ADDRESS>

The software checks that the start address is less than the finish address to stop wrap around. After the three parameters have been input, the question YES or NO? is output. It must be answered with the letter Y if the move is to take place, eg:

```
>MOVE BYTES 100:200 [ ] TO A000 [ ]  
YES OR NO? Y
```

>

Command ↑Z (Control Z)

This command clears the terminal screen (in our implementation) and branches to the start of the monitor.

Command G

This command causes execution to pass to the input address. The default action is to branch to the start of the monitor.

Command W

This command allows the user workspace to be set. It is intended to set a user program's workspace when the tracing facilities are used. The default action is to output the value of the current user workspace.

>1000W

>W

WP = 1000

>

Instruction R

This instruction prints the contents of the registers taking the input address as the workspace pointer. The default action uses the user workspace pointer.

```
>1000 R  
WP = 1000  
R0 = 0400 R1 = FFFF  
R2 = 8608 R3 = 870A  
R4 = 0000 R5 = 0012  
R6 = 0080 R7 = 865E  
R8 = 0012 R9 = 0002  
R10 = 0002 R11 = 830C  
R12 = 0000 R13 = 0001  
R14 = 4006 R15 = 0001  
>
```

Command H

This command allows up to four breakpoints to be set in user programs. If during the T (Trace) or X (execute) instructions a breakpoint instruction is executed, control is passed to the monitor. The registers and environment of the user program may be examined and altered using any of the other monitor commands. The user program may be restarted from the breakpoint with further X, T, or S (Step) commands.

The H command allows the four breakpoints to be set in a user program. On receipt of the H command, BP1 SET AT XXXX is output. Inputting a hexadecimal number will change the address of the breakpointed instruction. Using a space as a delimiter will proceed to the next breakpoint; any other character will pass control to the monitor.

>H

BP 1 SET AT 124C []

BP 2 SET AT 124E 1230 [] (Change Breakpoint 2)

BP 3 SET AT 1260 []

>

Command T

This command allows a user program to be traced from the start address supplied. The default action is to continue the user program from where it was before. The instruction outputs the addresses of the next 32 instructions that the computer executes, followed by a ?. Inputting further spaces will trace further sets of 32 instructions; any other character passes control to the monitor. If a breakpointed instruction is executed, then control passes back to the monitor. Note: If the traced program outputs to the terminal, these characters will be mixed with the instruction addresses.

Command X

This command is similar to the T command, except that the instruction addresses are not output. Control is passed back to the monitor only if a breakpointed instruction is executed. It is possible to execute the monitor itself using the X command, except of course that the monitor program runs much more slowly.

Command S

This command allows a user program to be single-stepped from the input address. The default action is to continue where it was before. The software outputs a disassembly of the instruction that has just been executed and the contents of the status register.

Monitor Implementation

Firmly believing in structured programming, we felt it necessary to be able to nest subroutine calls to an unlimited depth. This is not possible with the BL (branch and link) instruction since it always puts the return address in R11 of the current workspace. This means that R11 must be saved elsewhere before further branch and link instructions are used.

The BLWP (branch and link workspace pointer) instruction allows unlimited nesting if a predefined workspace is used for each subroutine (gives nonreentrant code). Obtaining a completely new workspace at each subroutine call can be very wasteful of memory space and furthermore, many variables usually have to be accessed via the old workspace. Clearly this results in a situation where R15, R14 and R13 cannot be used (since they contain linkage data), and variables have to be accessed using indirect addressing, yielding too many unnecessary multiple-word instructions.

To overcome these limitations, we used the TMS-9900 XOP (extended operation) instruction to augment the

instruction set. XOP0, XOP1, XOP4 and XOP5 have been coded to allow a good clean subroutine linkage that greatly outweighs the added execution time to perform an XOP instruction. All the XOP routines share the same workspace (XOPWP) and, consequently, interrupts are not allowed during their execution. The four routines use a stack that is pointed to by R10 of the XOPWP.

For subroutine calls, XOP0 is used. For example, to jump to subroutine REGISTER, the instruction XOP @ REGISTER, 0 would be used. The return address (address of the next instruction) is placed on the stack and the stack pointer is decremented. The next instruction executed is at address REGISTER. No register of the current workspace or the status is altered.

For subroutine return, XOP1 is used. For example, to return from the subroutine REGISTER, the instruction XOP R0, 1 would be used. R0 is a dummy parameter. The return address is obtained from the top of the stack and the stack pointer is incremented.

XOP4 and XOP5 are used to save and restore the workspace registers, respectively. For example, XOP RX, 4 pushes copies of registers 0 thru RX onto the stack while XOP RX,5 restores registers 0 thru RX by popping them off the stack and putting them in the current workspace. Register 9 of the XOPWP is used as a stack limit, as a precaution against the stack growing too far.

XOP2 and XOP3 are used for input and output to the terminal, respectively. XOP2 inputs a character from the terminal keyboard and places it at the specified location. For example, XOP RX,2 inputs one character and stores it in RX. XOP @ BUFFER,2 inputs one character and stores it at memory address BUFFER. XOP3 is really a print string operation in that it outputs characters from the specified address until a 00 byte has been output (the terminal is assumed to ignore the NUL code), eg:

LI	R0,	'AD'
LI	R1,	'D'
XOP	R1,	3
XOP	R0,	3

Prints "D"
Prints "ADD"

The monitor itself is quite straightforward, and a brief description should suffice. The first routine is an initialization routine, which clears the interrupt mask and initializes the interrupts and XOP vectors, low, last opened cell, stack pointer and the stack limit.

The next section is the monitor kernel. It recognizes the input command and jumps to the appropriate routine. The instructions command letters are in table INTAB, and the routine addresses are in SUBTAB. There is space for four more commands which we envisage will be used for cassette or disk drivers.

In this section, an assembler command WREN is used. This stands for write enable. The first 2 K bytes of memory is write protected. This prevents programs which are under development from running amok and overwriting themselves. This protection is implemented by external hardware, which simply inhibits the write enable signal to the memory. After a write enable instruction, only the next instruction is able to write into the protected memory.

An Example of the Use of the Tracing Facility in the Authors' Monitor.

1. The program is entered into memory using the instant assembler.
2. The user workspace is assigned.
3. A breakpoint is set on the last instruction.
4. The program is traced from the beginning.
5. Single step (from where trace ended).
6. Registers are examined (only R0 and R1 are in use).
7. The program is single stepped again.
8. The program is executed until the breakpointed instruction has been executed.
9. The final value of R0 is displayed.

```
>A000
A000  FFFF  .CLR R0
A002  FFFF  .LI R1, =20
A006  FFFF  .A R1, R0
A008  FFFF  .DEC R1
A00A  FFFF  .JOC =A006
A00C  FFFF  .SRL R0, 1
A00E  FFFF  .AI R0, =200<CR>
>1020W
>H
BP 1 SET AT 0000 A00E<CR>
>A000T
A000 =>A002 =>A006 =>A008 =>A00A =>A006 =>A008 =>A00A =>
A006 =>A008 =>A00A =>A006 =>A008 =>A00A =>A006 =>A008 =>
A00A =>A006 =>A008 =>A00A =>A006 =>A008 =>A00A =>A006 =>
A008 =>A00A =>A006 =>A008 =>A00A =>A006 =>A008 =>A00A => ? <CR>
>S
A006  A001  A  R1, R0          ST = C001
>R
WP = 1020
R0 = 0129    R1 = 0016
R2 = 9000    R3 = 0200
R4 = 3000    R5 = 0200
R6 = 910E    R7 = 9106
R8 = 000D    R9 = 0006
R10 = 9000   R11 = 82E2
R12 = 0000   R13 = 1040
R14 = 85EA   R15 = C001
>S
A008  0601  DEC   R1
>X
BP AT A00E    WP = 1020    ST = C001
>1020
1020  0308 <CR>
```

This simple feature has proved to be very useful and it is a rare occasion indeed when a program is completely destroyed (See figure 2).

Perhaps a brief description of the instant assembler and disassembler would be of interest. The starting point is to understand the op code structure (See table 2). It can be seen that the instruction set can be divided into nine basic types and a subroutine can be written to deal with each type.

Number	Type	+5V	GND
IC1	7474	14	7
IC2	7416	14	7
IC3	7474	14	7
IC4	7474	14	7
IC5	7416	14	7

Table 1: Power wiring table for figures 2 and 3.

Instant Assembler

The operation sequence is as follows

1. Obtain op code mnemonic from terminal. The mnemonic must be terminated with a space and cannot be more than four characters long.
2. The mnemonic is searched for in the mnemonic table to see if it is valid.
3. The mnemonic type and basic op code are found. This causes a branch to an appropriate routine to obtain the correct parameters.
4. Having obtained the parameters, the complete op code plus additional words are calculated.
5. Finally, the completely assembled instruction is written into memory at the address given by last opened cell, and this variable is incremented by 2, 4 or 6, appropriately.

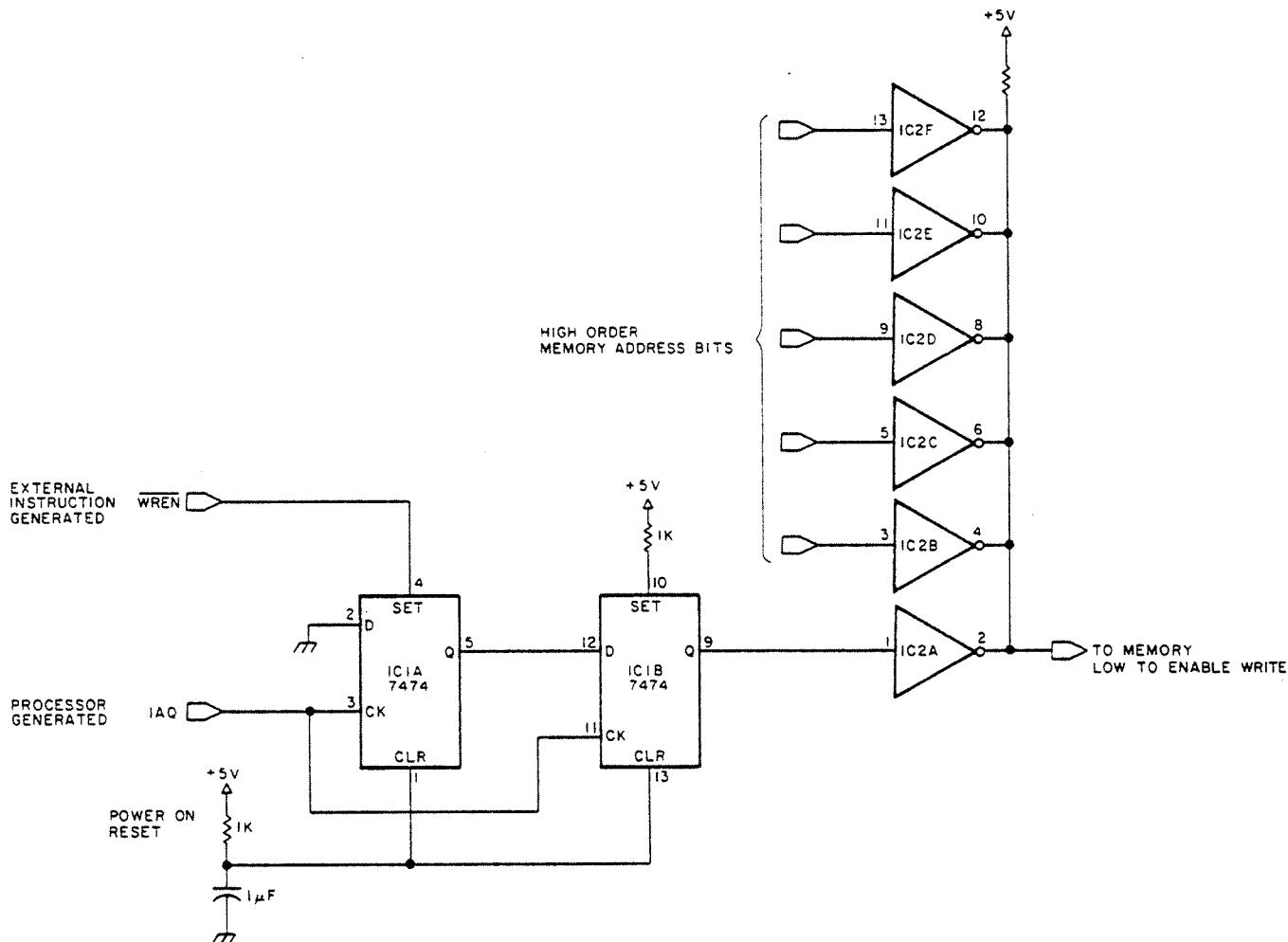


Figure 2: Hardware modification to write-protect the first 2 K bytes of memory in the TMS-9900 system. This prevents programs that are under development from overwriting themselves. After a write enable instruction, only the next instruction is able to write into the protected memory.

Disassembler

The disassembler is organized as a subroutine and is called by the instruction XOP @ DISASM, O. This will disassemble the instruction pointed to by R12, after which R12 will have been incremented by 2, 4 or 6, appropriately. The sequence of operation is as follows

1. Determine the type of the instruction by systematically checking the op code.
2. Branch to the appropriate instruction type handler. Decode the parameters and output the disassembled instruction to the terminal.
3. Increment R12 appropriately and return.

The TRACE, EXECUTE AND STEP Instructions

These instructions require a small amount of additional hardware to cause an interrupt after the execution of an

instruction (See figure 3). The circuitry uses the processor-generated signal IAQ (instruction acquisition), which goes high every time the processor obtains an instruction from memory. Consider the return from interrupt mechanism used by the trace routines:

"

"

TRON (turns trace hardware on)
RTWP (returns to user program).

Note that TRON and TROFF are alternate names for the instructions LREX and CKOF, respectively, and they are used to turn the trace interrupt logic on and off. On returning from an interrupt, the circuitry must generate an interrupt request on the second IAQ pulse, the first pulse being generated by the RTWP instruction and the second by the actual user instruction. The advantage of this mechanism is that the user program does not have to be altered in any way whatsoever for it to be traced.

Op Code	Op Code	Op Code	Instruction Type	
0XXX -	00XX : ILL			
	01XX : ILL			
	02XX -	020X : LI		
		022X : AI		
		024X : ANDI	TYPE 6	
		026X : ORI		
		028X : CI		
		02AX : STWP		
		02CX : STST	TYPE 7	
		02EX : LWPI		
	03XX -	030X : LIMI	TYPE 8	
		032X : ILL		
		034X : IDLE		
		036X : RSET		
		038X : RTWP		
		03AX : CKON	TYPE 9	
		03CX : CKOF		
		03EX : LREX		
	04XX -	040X : BLWP		
		044X : B		
		048X : X		
		04CX : CLR		
	05XX -	050X : NEG		
		054X : INV		
		058X : INC		
		05CX : INCT	TYPE 3	
	06XX -	060X : DEC		
		064X : DECT		
		068X : BL		
		06CX : SWPB		
	07XX -	070X : SETO		
		074X : ABS		
		078X : ILL		
		07CX : ILL		
	08XX : SRA			
	09XX : SRL			
	0AXX : SLA		TYPE 5	
	OBXX : SRC			
	OCXX : ILL			
	ODXX : ILL			
	OEXX : ILL			
	OFXX : ILL			

Note: ILL = ILLEGAL OP CODE

Table 2: TMS-9900 op code structure. For the purposes of the authors' instant assembler and disassembler, the instruction set can be divided into nine basic types, and a subroutine can be written to deal with each type.

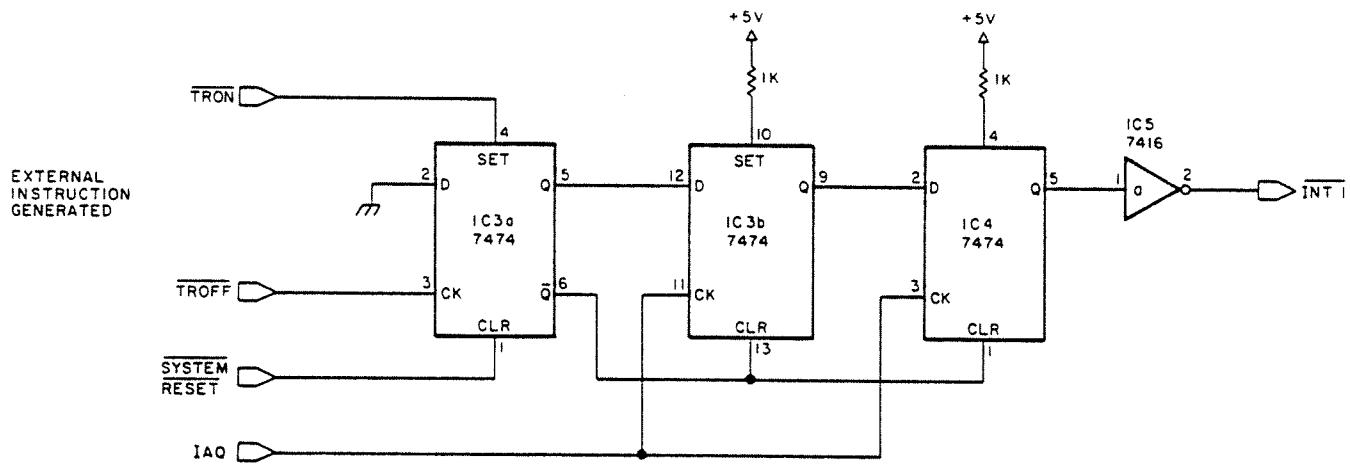


Figure 3: Hardware modification to the TMS-9900 system to enable the use of the authors' Trace, Execute, and Step instructions. The circuitry uses the processor-generated IAQ signal (instruction acquisition) which goes high every time the processor obtains an instruction from memory. TRON and TROFF are alternate names for the LREX and CKOF instructions, respectively. On returning from an interrupt, the circuitry must generate an interrupt request on the second IAQ pulse. With this modification, user programs can be traced without having to be altered.

The interrupt handler uses a separate workspace (INTIWP). Registers 2 thru 5 of this workspace contain the addresses of the breakpointed instructions, and it is the contents of these four registers that are changed by the H instruction. On acknowledgement of a trace interrupt, the appropriate handler, as determined by a pointer contained in R10, is executed. This pointer has been previously set up by the T, X and S routines. If the mode is Trace or Execute, then the breakpoint table is searched, if a match is found, the breakpoint message is output and control is passed back to the monitor. It is important to note that R15, R14 and R13 contain the environment of the traced program. In the step mode (S), a call to the disassembler is made. This displays in mnemonic form the last instruction executed together with the status register, and passes control back to the monitor. The user program may be examined or even changed before resuming execution.

The Move Instruction

The routine for this instruction is not quite as simple as it first seems. Basically, there are two ways of moving data from one part of memory to another to ensure that the new contents do not overwrite the old contents before the old contents have been moved (See figure 4).

Conclusion

This monitor has been in constant use for a period of several months debugging on-going projects (BASIC and Pascal interpreters) and has proved to be an invaluable working tool. ■

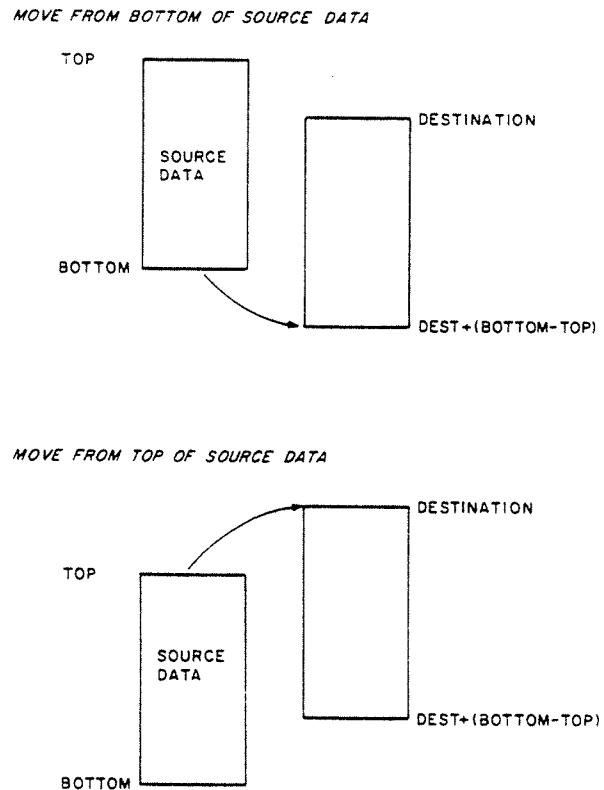


Figure 4: The two cases of the move instruction. Shown are the two ways to move data from one part of the memory to another to ensure that the new contents do not overwrite the old contents before the latter have been moved.

Listing 3: An example of the actual disassembler output.

0D40	02E0	LWPI	#E020
0D44	020C	LJ	R12, # 0060
0D48	1F01	TB	#0001
0D4A	13FE	JEQ	#0D48
0D4C	1D08	SBO	#0008
0D4E	04C4	CLR	R4
0D50	D064	MOV B	@ # C000 (R4), R1
0D54	9060	C8	@ #0CEA, R1
0D58	141A	JHE	#0D8E
0D5A	06C1	SWPB	R1
0D5C	0241	AND I	R1, #003F
0D60	C081	MOV	R1, R2
0D62	0A22	SLA	R2, 2
0D64	A042	A	R2, R1
0D66	04C3	CLR	R3
0D68	1F00	TB	#0000
0D6A	16FE	JNE	#0D68
0D6C	D161	MOV B	@ # 0880 (R1), R5
0D70	0583	INC	R3
0D72	0581	INC	R1
0D74	1F04	TB	#0004
0D76	16FE	JNE	# 0D74
0D78	3205	LDCR	R5, 8
0D7A	1D08	SBO	#0008
0D7C	1F04	TB	#0004
0D7E	13FE	JEQ	#0D7C
0D80	0283	CI	R3, #0005
0D84	1AF3	JL	#0D6C
0D86	0584	INC	R4
0D88	8804	C	R4, @ #7FC8
0D8C	1AE1	JL	#0D50
0D8E	1E08	SBZ	#0008
0D90	1D0A	SBO	#000A
0D92	04C2	CLR	R2
0D94	0403	CLR	R3
0D96	1020	SBO	#0009
0D98	0582	INC	R2
0D9A	0583	INC	R3
0D9C	0283	CI	R3, #0200
0DA0	1AFC	JL	#0D9A
0DA2	1E09	SBZ	#0009
0DA4	04C3	CLR	R3
0DA6	0583	INC	R3
0DA8	0283	CI	R3, #0600
0DAC	1AFC	JL	#0DA6
0DAE	0282	CI	R2, #0009
0DB2	1AF0	JL	#0D94
0DB4	1F01	TB	#0001
0DB6	13FE	JEQ	#0DB4
0DB8	1E0A	SBZ	#000A
0DBA	2C40	XOP	R0, 1

Listing 4: The Texas Instruments' TMS-9908 monitor

*** TMS9900 CROSS ASSEMBLER ***

>> TMS9900 MONITOR V2.0 <<

ADDR OP OP1 OP2 ST. NO.

10 PRINT
20 CREF
30 NOUBJ
40 *****
50 *
60 * TMS9900 MONITOR, INSTANT ASSEMBLER AND DISASSEMBLER *
70 *
80 *****
90 *
100 TITLE=>> TMS9900 MONITOR V2.0 <<
110 *
120 R0 EQU 0
130 R1 EQU 1
140 R2 EQU 2
150 R3 EQU 3
160 R4 EQU 4
170 R5 EQU 5
180 R6 EQU 6
190 R7 EQU 7
200 R8 EQU 8
210 R9 EQU 9
220 R10 EQU 10
230 R11 EQU 11
240 R12 EQU 12
250 R13 EQU 13
260 R14 EQU 14
270 R15 EQU 15
280 *
290 CR EQU 000
300 LF EQU 00A
310 *
320 INT1WP / EQU #F3F0
CARRIAGE RETURN
LINE FEED

```

*F3C0* EFC0
*F3A0* EFA0
0000
F400 F400 F400 F400
F402 02E0 F3A0
F406 04C0
F408 0201 F474
F40C 03A0
F40E CC31
F410 0280 0008
F414 16FB
F416 0200 0040
F41A 0201 F47C
F41E 0202 F3C0
F422 03A0
F424 CC02
F426 03A0
F428 CC41
F42A 02B0 0058
F42F 16F9
F430 04CF
F432 04CE
F434 04E0 F3FF
F438 0200 EC00
F43C C800 F3D2
F440 C820 F474 F3D4
F446 2CE0 F49D
F44A 2CE0 F48H
F44E 2CE0 F49D
F452 06A0 F5AE
F456 0204 0013
F45A 9064 F502
F45E 1305
F460 0604
F462 1HFH
F464 2CF0 F4AB
F468 10F2
F46A 0A14
F46C C124 F516
F470 C0C3
F472 0454
F474 F3A0 F440
F476 FAHE

330 XOPWP EOII #F3C0
340 WORKSP FOII #F3A0
350 * INIT #F400
360 * MONITOR KERNEL
380 * INITIALISATION
390 * CLEAR INTERRUPT MASK
400 * SET UP RESET AND INTERRUPT 1 VECTORS
410 * WRITE ENABLE
420 INITIAL RSET
430 WORKSP
440 CLR R0
450 INIT0 L1
460 INIT0 WREN
470 *R1^,*R0^
480 CI R0,8
490 JNE INIT0
500 LI R0,*40
510 LI R1,XOPTAB
520 LI R2,XOPWP
530 INIT1 WHEN
540 MOV R2,*R0^
550 WHEN
560 MOV *R1^,*R0^
570 CI R0,*58
580 JBE INIT1
590 CLR R15
600 CLR R14
610 E2*R15+INT1WP
620 LI R0,WORKSP-*R00+*60
630 MOV R0,*2*R9+XOPWP
640 INIT2
650 *RESTAB.*R2*R10+XOPWP
660 MONITOR XIP
670 XIP
680 MON00 XIP
690 BL,0HFXIN
700 LI R4,19
710 MON01 CB,INITAH(RA),R1
720 JFO MON03
730 DEC R4
740 JIC MON01
750 MON02 XOP
760 JEP MON00
770 MON03 SLA
780 MOV R4,1
790 MOV R3,R3
800 H *R4
810 * TABLES AND MESSAGES
820 * PRINT " ??"?
830 * BRANCH TO APPROPRIATE ROUTINE
840 RESTAB WORD WORKSP,INITIAL,INT1WP,INT1

```

Fo10

F47C	F546	F568	F56E	850	XOPTAB	WORD	XOP0,XOP1,XOP2,XOP3,XOP4,XOP5
F482	F580	F590	F5A2	860	MESS00	TEXT	♦> TMS9900 MONITOR <<*-
F488	3F20	544D	5319				
F48F	3930	3020	4D4F				
F494	4E49	544F	5220				
F49A	3C3C	00					
F49D	0D	0A0A		870	MESS01	BYTE	CR,LF,LF
F4A0	2020	2020	3F00	880	TEXT	♦>-	
F4A6	203F	3F00		890	MESS02	TEXT	♦?*-
F4AA	0AQD	0A		900	MESS03	BYTE	LF,CR,LF
F4AD	20	2020	2020	910	TEXT	♦*-	
F4B2	00						
F4B3	20	5354	4143	920	MESS04	TEXT	♦ STACK OVERFLOW AT'*-
F4B8	4B20	4F56	4552				
F4BF	464C	4F57	2041				
F4C4	5400						
F4C6	40			930	ATHASH	BYTE	'@'
F4C7	23	00		940	HASH	BYTE	'1111,0
F4C9	2C	00		950	COMM	BYTE	'1111,0
F4CB	20	2020	2020	960	R	TEXT	'1111,
F4D0	5200			970	RR	BYTE	'R',0
F4D2	3031	3233	3435	980	EVEN		
F4D8	3637	3839	4142	990	HEXTAB	TEXT	'0123456789ARCDEF'
F4DF	4344	4546					
F4F2	3020	3120	3220	1000	NUMTAB	TEXT	♦ 0 1 2 3 4 5 6 7 8 9 101112131415!
F4FB	3320	3420	3520				
F4FF	3620	3720	3820				
F4F4	3920	3130	3131				
F4FA	3132	3133	3134				
F500	3135						
F502	1A			1010	INTAB	BYTE	#1A
F503	2F	2F2F	2F47	1020	TEXT	////////GMZ0WRPX1HST/. ,	CONTROL Z(CLEAR SCREEN)
F508	4D5A	4F57	5250				
F50F	584C	4853	542F				
F514	2F20						
F516	F446	0000	0000				
F51C	0000	0000	FF76				
F522	FDRA	FF28	FFB0				
F528	FE96	FB16	FB64				
F52E	FA4C	FE80	FA02				
F534	FA52	FA40	F638				
F53A	F70E	F5F4					
F53F	001F			1050	*		
F540	000F			1060	MASK32	WORD	#001F
F542	0007			1070	MASK16	WORD	#000F
F544	0003			1080	MASK8	WORD	#0007
				1090	MASK3	WORD	#0003
				1100	*		
				1110	*	XOPO	
				1120	*	SUBROUTINE CALLING MECHANISM	
				1130	*		
				1140	*		

F546	0460	XOP0	RSFT		DECREMENT STACK POINTER
F548	064A	1160	DECX	R10	CHECK FOR STACK OVERFLOW
F54A	824A	1170	C	R10,R9	
F54C	1H0A	1180	JH	XOP0AA	STACK OVERFLOW HANDLER
F54F	02E0	E3A0	XOP0XXX	L.WPI	WORKSP
F552	COAO	F3DC	MOV	A2,*14+XOPW.P.R2	
F554	2CE0	F4B3	XOP	MESS04,*3	
F556	06A0	F5D8	BL	HEXOUTX	
F55A	0460	F440	1220	H	INIT2
F55F	C68E	F5D2	1230	MOV	R14,*R10
F562	C3B4	1240	XOP0AA	MOV	R11,R14
F564	0380	1250	RTWP		
F566		1260			
		1270	*	XOP1	
		1280	*	XOP1	
		1290	*		
		1300	*	SUBROUTINE RETURN MECHANISM	
		1310	*		
		1320	XOP1	RSET	
		1330	MOV	*R10^,R14	
		1340	RTWP		
		1350	*		
		1360	*	XUP2	
		1370	*		
		1380	*	TERMINAL INPUT EXTENDED OPERATION	
		1390	*		
		1400	XOP2	RSFT	
		1410	LI	R12,*40	
		1420	XOP2AA	TR	W8
		1430	JNE	XOP2AA	
		1440	STCR	R0,7	
		1450	SB2	#8	
		1460	MOVH	R0,*R11	
		1470	RTWP		
		1480	*		
		1490	*	XOP3	
		1500	*		
		1510	*	TERMINAL OUTPUT EXTENDDFD OPERATION	
		1520	*		
		1530	XOP3	RSET	
		1540	CLR	R12	
		1550	XOP3AA	TR	W8
		1560	JNF	XOP3AA	
		1570	LDCR	*R11^,8	
		1580	SHZ	#8	
		1590	JNF	XOP3AA	
		1600	RTWP		
		1610	*		
		1620	*	XOP4	
		1630	*		
		1640	*	PUSHES R0-RX ONTO STACK	
		1650	*		
		1660	XOP4	RSFT	
		1670	MOV	R13,R1	
F580	0360				
F582	04CC				
F584	1FOH				
F586	16FE				
F588	323B				
F58A	1E08				
F58C	16FB				
F58E	0380				
F590	0460				
F592	C04D				

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F594 064A          XOP4AA    DECT    R10
F596 824A          C        R10,R9
F598 12DA          JLE     XOP0XX
F59A C6B1          MOV     *R1*,*R10
F59C 82C1          C        R1,R11
F59F 12FA          JLE     XOP4AA
F5A0 0380          RTWP

1680 XOP4AA    DECT    R10
1690 C        R10,R9
1700 JLE     XOP0XX
1710 MOV     *R1*,*R10
1720 C        R1,R11
1730 JLE     XOP4AA
1740 RTWP

1750 * XOP5
1760 * XOP5
1770 * XOP5
1780 * POPS RX-R0 OFF STACK
1790 *
1800 XOP5    RSET    *R10*,*R11
1810 XOP5AA  MOV     R11
1820 DECT    R11
1830 C        R11,R13
1840 JHE     XOP5AA
1850 RTWP

1860 *
1870 * SUBROUTINE HEXIN
1880 * USES R1,R2,R3,R4
1890 *
1900 HEXIN   CLR     R1
1910 CLR     R2
1920 SETO    R3
1930 HEXINO  XOP     R1,2
1940 XOP     R1,3
1950 L1      R4,15
1960 HEXIN1 CR      @HEXTAB(R4),R1
1970 JNE     HEXIN2
1980 CLR     R3
1990 SLA     R2,4
2000 SOC     R4,R2
2010 JMP     HEXIN0
2020 HEXIN2 DEC     R4
2030 JOC     HEXIN1
2040 MOV     R3,R3
2050 RT

2060 *
2070 * SUBROUTINE HEXOUT
2080 * USES R0,R1,R2,R3
2090 *
2100 HEXOUT  XOP     @MESS03+7,3
2110 HEXOUT0 XOP     R3,4
2120 HEXOUTX CLR     R0
2130 I,I      R3,#04
2140 HEXOUT1 SRC     R2,12
2150 MOV     R2,R1
2160 ANDI   R1,#000F
2170 MOVI   @HEXTAB(R1),R0
2180 XOP     R0,3
2190 DEC     R3
2200 JNE     HEXOUT1

PRINTS "
SAVE R0-R3

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	R3.5	XOP	RT		
2043	2210	JNE	ON HEXIN FLAG	RESTORE R3=R0	
045B	2220	OPEN	R2,R14		
7542	2230	OPEN00	R14,#FFFF	MAKE ADDRESS EVEN	
	2240	* INSTRUCTION * *	OPEN01	PRINT ADDRESS AND CONTENTS	
75F4	1601	JNE	XOP	JNE ON HEXIN FLAG	
75F6	C3B2	MOV	BL		
75F8	024E	AND1	BL		
75FA	FFFE	OPEN01	BL		
75FC	2C20	OPEN00	BL		
7600	F622	OPEN01	BL		
7600	06A0	XOP	BL		
7604	F5AF	OPEN01	BL		
7604	1602	JNE	OPEN02	JNE ON HEXIN FLAG	
7606	03A0	WREN			
7608	60B	MOV			
760A	C7B2	OPEN02			
760A	0281	CJ	R2,*R14	LAST CHARACTER=? - ?	
760C	2000	JNE	R1,*		
760F	1602	OPEN03	OPEN03		
6110	05CE	JNE			
6110	10F4	INCT			
6112	10F4	JMP			
6114	0281	OPEN03	CJ	LAST CHARACTER=? - ?	
6114	2D00	JNE	R1,*		
6118	1602	2380	CI		
611A	064E	2390	MON00A		
611C	10EF	2400	DECT		
611E	0460	2410	R14		
611E	F44E	2420	JMP		
		MON00A	OPEN01		
		B	MON00		
		*			
6272	2CE0	F4AB	2430	*	
6276	C08E	PADDRC	XOP	MESS03+1.3	PRINT INDENTATION
6288	06A0	2450	MOV	R14,R2	PRINT ADDRESS
629C	C09E	2460	BL		
62A0	06A0	2470	MOV	#HEXOUT0	
62A0	F5D2	2480	BL	*R14,R2	
62A0	2CE0	2490	MOV	#HEXOUT	
6332	F4B0	2500	BL		
6336	2C40	2510	XOP	#MESS03+6.3	
			RO,1		
		*			
6338	1601	2530	* INSTRUCTION /		
633A	C3B2	CHAR	JNE	CHAR00	JNE ON HEXIN FLAG
633C	0203	MOV	BL	R2,R14	
633E	2F00	L1	R3,*	/	
6400	C10E	CHAR00	MOV	R14,R4	SET START ADDRESS
6402	2C20	CHAR01	MOV	@PADDRC,0	PRINT ADDRESS AND CONTENTS
6402	F622	XOP	XOP		PRINT "/"
6406	2CC3	XOP	XOP		GET CHARACTER
6408	2C81	CHAR02	XOP	R3.3	BACKSPACE?
640A	0281	0800	CJ	R1,*0800	
640E	1605	2630	JNE	CHAR03	
6410	H3B4	2640	C	R4,R14	
6452	13FA	2650	JEQ	CHAR02	
6454	2CC1	XOP	XOP	R1.3	PRINT BACKSPACE
6456	060F	2660	DEC	R14	
6458	10F7	2670	JMP	CHAR02	
645A	C041	2680	MOV	R1,R1	IS CHARACTER CONTROL SHIFT
645C	1303	2690	JEQ	CHAR04	
645E	0281	2700	CJ	R1,*	
6462	2000	2710	CI	MON00A	RETURN TO MONITOR
6462	1AD0	2720	JL		
6464	03A0	2730	CHAR04	WREN	

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F666 DF81 2740      MOVB    R1.*R14*
F668 1602 2750      JNE     CHAR05
F66A 0201 5F00 2760      L1      R1._.
F66E 2CC1  CHAH05 2770      XOP     R1.3
F670 27A0  FS4E 2780      CZC     @MASK32,R14
F674 16E9  CHAR02 2790      JNE     CHAR02
F676 2CC3  2800      XOP     R3,3
F678 10E3  2810      JMP     CHAR01
                                PRINT "/"

                                2820 *
                                2830 *
                                2840 * INSTANT ASSEMBLER
                                2850 *
                                2860 * TABLES
                                2870 *
                                2880 TYPE1   TEXT    'SVC SZCRS  SR C  CB A  AB '
                                2890      TEXT    'MOV MOVRSC SOC#'
                                2900      TYPE2   TEXT    'COC CZC XOR XOP LDCRSTCRMPY DIV '
                                2910      TYPE3   TEXT    'BLWPB  X CLR NEG INV INC INCT'
                                2920      TEXT    'DEC DECTBL SWPSETOARS ILL?ILL?'
                                2930      TYPE4   TEXT    'JMP JLT JLE JEO JHE JGE JNE JNC '
                                2940      TEXT    'JNC JNO JLJ JH JOP SRO SBZ TB '

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DOS

F74A	5352	4120	5352	2950	TYPES	TEXT	'SRA SRL SLA SRC '
F750	4C20	534C	4120				
F756	5352	4320					
F75A	4C49	2020	4149	2960	TYPE6	TEXT	*LI AI ANDIORI CI *
F760	2020	414E	4449				
F766	4F52	4920	4349				
F76C	2020						
F76E	5354	5750	5354	2970	TYPE7	TEXT	*STWPSTST *
F774	5354						
F776	4C57	5049	4C49	2980	TYPE8	TEXT	*LWPILIMI *
F77C	4D49						
F77E	494C	4C3F	4944	2990	TYPE9	TEXT	*ILL?IDLERSETRTWPWRENCKOFLREX *
F784	4C45	5253	4554				
F78A	5254	5750	5752				
F790	454E	434H	4F46				
F796	4C52	4558					
F79A	F67A	F6AA	F6CA	3000	TYTAB	WORD	TYPE1.TYPE2.TYPE3.TYPE4.TYPES
F7A0	F70A	F74A					
F7A4	F75A	F76E	F776	3010	WORD		TYPE6.TYPE7.TYPE8.TYPE9.TYTAB
F7AA	F77E	F79A					
F7AE	0A08	0406	0603	3020	TYSHIFT	BYTE	10.8,4,6,6,3,3,3,3,0
F7B4	0303	0300					
F7B8	4000	2000	0400	3030	TYBASE	WORD	#4000,#2000,#0400,#1000,#0800,#0200,#02A0,#02E0,
F7BE	1000	0800	0200				#0320
F7C4	02A0	02E0	0320				
F7CA	F898	F8AC	F8CC	3040	TYSUB	WORD	T1,T2,T3,T4,T5,T6,T7,T8,T9
F7D0	F8D4	F904	F91A				
F7D6	F92A	F934	F960				
F7DC	2E00			3050	DOT	WORD	..*
				3060	*		
				3070	*	GET MNEMONIC AND PLACE IN R8,R9	
				3080	*		
				3090	INSTANT	JNE	JNE ON HEXIN FLAG
				3100	MOV	R2,R14	
				3110	STWP	R0	
				3120	A1	R0,2*8	R0->R8
				3130	LI	R8,1	INITIALISE R8,R9
				3140	MOV	R8,R9	
				3150	XOP	EPADDRC,0	
				3160	XOP	EDOT,3	
				3170	CLR	R2	
				3180	INST01	XOP	GET CHARACTER
				3190	XOP	R1,2	PRINT ADDRESS AND CONTENTS
				3200	C1	R1,'A'	PRINT *"
				3210	JL	INST02	CHARACTER COUNT=0
				3220	C1	R1,'Z'	
				3230	JH	INST02	
				3240	MOVW	R1,*R0-	
				3250	INC	R2	
				3260	C1	R2,45	
				3270	JNE	INST01	
				3280	INST02	XOP	MESS03+3(R2),3
				3290	C1		R1,*

```

F81A 1H3B          JH   ERROR
F81C 1A2E          JL   INST10
3300
3310
3320 *
3330 * SEARCH FOR MNEMONIC IN TABLE
3340 *
3350 L,I   R7,TYPE1
3360 INST03 C   *R7,R8
3370 JNE   INST04
3380 C   @#0002(R7).R9
3390 JEQ   INST05
3400 INST04 AI  R7,4
3410 CI  R7,TYTAB
3420 JNE   INST03
3430 JMP  ERROR
3440 *
3450 * FIND MNEMONIC TYPE AND CALCULATE BASIC OPCODE
3460 *
3470 INST05 C,I,R  R4
3480 L,I   R3,TYTAB+2
3490 INST06 C   R7,*R3*
3500 JL   INST07
3510 INC  R4
3520 JMP  INST06
3530 INST07 S   @~4(R3).R7
3540 MOVB R7SHIFT(R4).R0
3550 SRL  R0,8
3560 SLA  R7,0
3570 SLA  R4,1
3580 A   @TYBASE(R4).R7
3590 C,I,R  R10
3600 MOV  @TYSUB(R4).R0
3610 B   *R0
3620 *
3630 T9   XOP  R1,2
3640 INST08 XOP  R1,3
3650 STWP R0
3660 AI   R0,2*7
3670 INST09 WREN
3680 MOV  DEC R10
3690 DEC  JNC INST09
3700 JNC  CJ  R1,1
3710 JH   ERROR
3720 JEQ  INST00
3730 JEQ  @MON00
3740 INST10 B
3750 *
3760 DELIM C,I   R1,1
3770 JEQ  C,I   DE1,IM00
3780 C,I   R1,1
3790 JH   ERROR
3800 INST10
3810 DELIM00 XOP  @COMMA,3
3820 RT

```

RETURN TO MONITOR

CHECK CH IN (" . . . ")

PRINT " , "

F892	2CE0 F4A6	3830 *	XOP	PRINT " ??"
F896	10A5	3840 ERROR	JMP	MESS02, 3
F898	06A0 F97C	3850 * INST00		
F89C	06A0 F87F	3860 *		
F8A0	A1C0	3870 T1	BL	INSTRUCTION OF TYPE1
F8A2	06A0 F97C	3880	BL	
F8A6	0A60	3890	A	
F8A8	A1C0	3900	BL	
F8AA	10DB	3910 T100	SLA	
		3920 T101	A	
		3930	RO.R7	
		*	JMP	INST08
F8AC	06A0 F97C	3940 *		INSTRUCTION OF TYPE2
F8B0	06A0 F87F	3950 T2	BL	
F8B4	A1C0	3960	BL	
F8B6	0287 2C00	3970	A	
F8B8	1A03	3980	C1	
F8BC	0287 3400	3990	JL	CHECK IF XOP,LDCR OR STCR
F8C0	1A02	4000	C1	
F8C2	2CE0 F4D0	4010	JL	
F8C6	06A0 F942	4020 T200	XOP	
F8CA	10ED	4030 T201	BL	
		4040	GETRC	
		*	JMP	T100
F8CC	06A0 F97C	4050 *		INSTRUCTION OF TYPE3
F8D0	A1C0	4060 T3	BL	
F8D2	10C7	4070	A	
		4080	RO.R7	
		*	JMP	INST08
F8D4	2CE0 F4C7	4090 *		INSTRUCTION OF TYPE4
F8DB	06A0 F5AE	4100 T4	XOP	
F8DC	16C2	4110	BL	
F8DE	0287 1D00	4120	JME	
F8E2	140B	4130	CI	DEFAULT FOR JUMP INSTRUCTIONS
F8E4	608E	4140	JHE	IS INSTRUCTION SBO, SHZ, TB
F8E6	0642	4150	S	
F8EB	0H12	4160	R14.R2	
F8EA	0282 FF80	4170	DEFCT	
F8FF	1178	4180	SR	
F8F0	0282 007F	4190	R2	
F8F4	1575	4200	SR	
F8FB	0242 00FF	4210	R2.1	
F8FA	0282 00FF	4220	JGT	
F8FF	1H70	4230	ANDI	
F900	A1C2	4240	CI	CALCULATE JUMP OFFSET
F902	10AF	4250	R2.128	
		4260	R2.0FF	
		*	JMP	
F904	2CE0 F4D0	4270 *		INSTRUCTION OF TYPE5
F908	06A0 F942	4280 T5	XOP	
F90C	06A0 F87F	4290	BL	
F910	A1C0	4300	BL	
F912	06A0 F942	4310	A	
F916	0A40	4320	RO.R7	
F918	10C7	4330	BL	
		4340	GETRC	
		*	JMP	R0.4
		4350	JMP	T101

INSTRUCTION OF TYPE 6			
F91A	2CE0 F4D0	4360	T6 XOP
F91E	06A0 F942	4370	BL
F922	A1C0	4380	A
F924	06A0 F87F	4390	BL
F92A	1005	4400	JMP
F92A	2CE0 F4D0	4410	*
F92E	06A0 F942	4420	T7 XOP
F932	10RA	4430	BL
F934	2CE0 F4C7	4440	JMP
F938	06A0 F5AF	4450	*
F93C	C202	4460	T8 XOP
F93E	058A	4470	BL
F940	1090	4480	MOV
		4490	INC
		4500	JMP
		4510	*
		4520	*
		4530	*
		4540	GETRC
		4550	STWP
		4560	AI
		4570	LI
		4580	GETRC00
		4590	XOP
		4600	CI
		4610	JL
		4620	CI
		4630	JH
		4640	XOP
		4650	MOV
		4660	DEC
		4670	GETRC01
		4680	LI
		4690	GETRC02
		4700	C
		4710	JEQ
		4720	DEC
		4730	GETRC03
		4740	SRL
		4750	MOV
		4760	RT
		4770	*
		4780	SIX
		4790	MV
		4800	XOP
		4810	XOP
		4820	CI
		4830	JNE
		4840	BL
		4850	R
		4860	CJ
		4870	JNF
		4880	RR,3
		4890	2C81
		4900	2CC1
		4910	0135
		4920	02A1
		4930	5200
		4940	1603
		4950	F942
		4960	045C
		4970	0281
		4980	2A00
		4990	160E
		5000	2CE0 F4D0
INSTRUCTION OF TYPE 7		INSTRUCTION OF TYPE 8	
			AT MOST 3 CHARACTERS
			GET CHARACTER
			IN {0...9} ?
			TEST IF VALID NUMBER
			RO:=HEX EQUIVALENT OF DECIMAL NUMBER
			OBTAINS SIXBIT SOURCE/DESTINATION FIELD
			SAVE RETURN ADDRESS
			GET CHARACTER
			PRINT CHARACTER
			REGISTER ADDRESSING MODE
			RETURN
			INDIRECT ADDRESSING MODE
			PRINT "R"

```

F996 06A0 F942          BL      R0. #10
      0220 0010          AI      R1. ++
      0281 2B00          CI      SIX02
      1604          JNE    R1. 3
      2CC1          XOP    R0. #20
      0020 0020          AI      R0. #20
      2CB1          XOP    R1. 2
      045C          AMODE:=3
      0281 4000          XOP    R1. 2
      1615          JNE    R1. @
      2CE0 F4C7          JNE    ERR1
      06A0 F5AE          XOP    @HASH.3
      C28A          HL    @HEXIN
      1602          MOV    R10.R10
      C202          JNE    SIX04
      04C0          MOV    R2.R8
      1001          JMP    SJX05
      C242          MOV    R2.R9
      058A          INC    R10
      04CA          CLR    RO
      02B1 2B00          CLR    R1.'.
      F9C4          5040          SJX04
      F9C6          5050          MOV    SJX07
      C2C2          5060          INC    R10
      F9C8          5070          CLR    RO
      F9CA          5080          CLR    R1.'.
      F9CC          5090          JNE    SJX07
      F9D0 160B          5100          XOP    @R.3
      F9D2 2CE0 F4D0          5110          BL    @GETRC
      F9D6 06A0 F942          5120          CI    R1.'.
      F9DA 0281 2900          5130          JEO    SJX06
      F9DE 1302          5140          ERR1  B    @ERROR
      F9E0 0460 F892          5150          SJX06
      F9E4 2CC1          5160          XOP    R1. 3
      F9E6 2CB1          5170          XOP    R1. 2
      F9E8 0220 0020          5180          A1    R0. #20
      F9EC 045C          5190          B    *R12
      5200          *
      5210          * INSTRUCTION H
      5220          * SET AT'-
      5230          SMESS0 BYTE    CR.LF,LF
      5240          SMESS1 TEXT    , BP -
      5250          SMESS1 TEXT    , SET AT'-
      FA02          5260          FVEN
      FA02          5270          SETBP  LI    R5.INT1WP
      FA06 0205 F3F0          5280          A1    R5..2*4
      FA0A 0225 0008          5290          L1    R8..1
      FA0A 0208 3100          5300          SETBP00 XOP  @MESS0.3
      FA0E 2CE0 F9EF          5310          XOP  R8..3
      FA12 2CC8          5320          XOP  @MESS1.3
      FA14 2CE0 F9FA          5330          MOV  *R5..R2
      FA18 C0B5          5340          BL    @HEXOUT
      FA1A 06A0 F5D2          5350          XOP  @MESS03+6..3
      FA1F 2CE0 F4H0          5360          HL    @HEXIN
      FA22 06A0 F5AF          5370          JNE    SETBP01
      FA26 1602          5380          MOV  R2. @-2(R5)
      FA2H C942 FFFF          5390          SKTBP01 CT    R1.. '

```

```

FA30 1605 .INE SETBP02
FA32 0228 0100 5400 .AT R8.*#100
FA36 0288 3500 5410 C1 R8.*5*
FA3A 16E9 5420 JNF SETBP00
FA3C 0460 F44F 5430 SETBP02 R @MON00
      * INSTRUCTION T
      * INSTRUCTION X
      * INSTRUCTION S
      * STEP LI R4.*4
      * SETUP RSET R4.*#2*10+INT1WP
      * MOV MOV R4.*#2*9+INT1WP
      * CLR CLR R4
      * JMP JMP SETUP
      * TEST HEXIN FLAG
      * SET UP NEW START ADDRESS
      * CHANGE WORKSPACF
      * ADDRESS OF NEXT INSTRUCTION
      * R4.*14+INT1WP
      * INT1WP
      * R14.R12
      * @MASK16.R15
      * SETUP01 INC R15
      * TRON RTWP
      * INTERRUPT 1 HANDLER
      * INT1AA.INT1CC.INT1GG TRACE,XCUTE,STEP
      * WORD IMESS0 TEXT !=>!--
      * TEXT IMESS1 TEXT + WP=!--
      * BYTE IMESS2 TEXT #18,*#2C
      * TEXT IMESS3 TEXT , STZ!--
      * EVEN TROFF
      * MOV B @RITAB(R10).R0
      * R0
      * C2C JNF @MASKR.R9
      * INT1BB JNF @MASKS03+1.3
      * XOP R12.R2
      * MOV INT1BH PRINT INDENTATION
      * PRINT ADDRESS OF LAST INST.

```

```

F0A2 06A0 F5D6          BL,      @HEXOUT0
F0A6 2CE0 FA7C          XOP,    @MESS0.3
F0AA 0609                R9
F0AC 1609                INT1CC
F0AF 04C1                R1
F0B0 2CE0 F4A8          XOP,    @MESS02+2.3
F0B4 2CB1 2000          XOP,    R1-2
F0B6 0281                C1
F0B8 161F                R1-1
F0B9 0209 0020          JNE    R1
F0C0 0202 0004          L1
F0C4 02A0                R9->BP 1
F0C6 0220 0008          STWP   IS INSTRUCTION BREAKPOINTED?
F0CA 8330                INT1DD
F0CC 1305                AI
F0CE 0602                C*RO^,R12
F0D0 16FC                INT1EE
F0D2 C30E                DEC
F0D4 03C0                JEQ
F0D6 0380                R2
F0D8 2CE0 F9EE          INT1DD
F0DC 2CE0 F9FF          INT1EE
F0E0 C0BC                XOP
F0E2 06A0 F5D2          XOP
F0E6 2CE0 FA7F          XOP
F0FA C0BD                MOV
F0FC 06A0 F5D6          BL,      @HEXOUT
F0F0 2CE0 FA87          XOP
F0F4 C08F                MOV
F0F6 06A0 F5D6          R12,R2
F0FA 02E0 F3A0          R13,R2
F0FE 0460 F44E          @HEXOUT0
F0F02 200B                INT1FF
F0F04 2C20 FBDA          IWP1
F0F0B 2D4B                WORKSP
F0F0A 2CE0 FA85          MON00
F0F0F C08F                R15,R2
F0F10 06A0 F5D6          @HEXOUT0
F0F14 10F2                JMP
F0F16 0204 F3FA          INT1FF
F0F1A C114                REGIST
F0F1C C0C3                L1
F0F1E 1601                R4,2*13+INT1WP
F0F20 C102                R4,R4
F0F22 0244 FFFF          R3,R3
F0F24 6370 * INSTRUCTION R
F0F26 6380 * REGIST
F0F28 6390 * MOV
F0F2A 6400 * MOV
F0F2C 6410 * JNE
F0F2D 6420 * MOV
F0F2E 6430 * REGIST ANDI
F0F2F 6440 * R4,#FFFF
F0F30 6450 * PRINT "ST="*
F0F32 6460 * PRINT STATUS
F0F34 6470 * RETURN TO MONITOR
F0F36 6480 * PRINT "MESS1.3"
F0F38 6490 * PRINT USER WORKSPACE
F0F3A 64A0 * PRINT "MESS3.3"
F0F3C 64B0 * PRINT USER STATUS
F0F3E 64C0 * PRINT "ST="*
F0F40 64D0 * RESTORE R0-R11
F0F42 64E0 * PRINT "ST="*
F0F44 64F0 * TEST HEXIN FLAG
F0F46 6500 * MAKE ADDRESS EVEN

```


	WORD	
FAC0	FCF6 FCF6 FD0C	
FAC6	FD0C FD00	
FBCA	FC3A FC3A FC3A	
FBD0	FC3A FC3A FC3A	
FHD6	FC3A FC3A	
		6910
FHDA	2CE0 F4AB	6920 *
FHDE	C08C	6930 DISASM
FHE0	06A0 F5D6	6940 MOV
FHF4	C17C	6950 BL
FHF6	C0B5	6960 MOV
FHF8	06A0 F5D2	6970 MOV
FHF.C	C1H5	6980 BL
FHF.E	09C6	6990 MOV
FHF0	1605	7000 SRL
FHF2	C1B5	7010 JNE
FHF4	0976	7020 MOV
FHF6	C026 FHB8	7030 SRL
FHF.A	0450	7040 MOV
FHF.C	0286 0003	7050 B
FC00	1B2A	7060 DISASM1
FC02	0286 0002	7070 CI
FC06	141A	7080 JH
		7090 CI
		7100 JHE
FC08	C005	7110 *
FC0A	0240 0F00	MOV
FC0E	0960	R5,R0
FC10	0220 F70A	ANDI
FC14	06A0 FD8C	R0, #0F00
FC18	C0B5	SRL
FC1A	0242 00FF	R0,6
FC1E	0285 1D00	AI
FC22	1407	R0,TYPE4
FC24	0282 0080	BL
FC28	1A02	PPNNEH
FC2A	0262 FF00	MOV
FC2E	0A12	R5,R2
FC30	A08C	AND1
FC32	2CE0 F4C7	R2, #0FFF
FC36	06A0 F5D6	AND1
FC3A	2C40	CI
		R5, #1D00
		JHE
		CODE100
		C1
		R2, #0080
		JL
		CODE101
		ORI
		R2, #FF00
		CODE101 SLA
		R2,1
		A
		R12,R2
		CODE100 XOP
		RHASH,3
		#HEXOUT0
		RETURN
FC3C	C1H5	7280 *
FC3E	0246 1C00	7290 CODE2
FC42	0986	MOV
FC44	C0B6	R5,R6
FC46	0220 F6AA	AND1
FC4A	06A0 FD8C	R6, #1C00
FC4E	0916	SRL
FC50	C026 FHAA	R6,8
FC54	0450	MOV
FC56	0A26	R6, R0
		*R0
		EC2ATAB(R6),R0
		B
		R6,2
		SLA

```

FC58 C006          R6.R0
FC5A 0220 F66A      MOV    AI
FC5F: 06A0 FDAC      BL
FC62 2C20 FD30      XOP
FC66 2CE0 F4C9      XOP
FC6A 2C20 FD34      XOP
FC6E: 2C40          XOP
                  R0,1
7400 *              R0.TYPE1-16
7410             AT
7420             BL
7430             XOP
7440             XOP
7450             XOP
7460             XOP
7470             *              PRINT "."
7480 CODE00        I,I
7490             BL
7500             XOP
7510             *              RETURN
7520 CODE02        ANDI
7530             SRL
7540             R6.101E0
7550             MOV
7560             AJ
7570             BL
7580             SRL
7590             MOV
7600             R
7610             *              R6.R6
7620 CODE04        MOV
7630             ANDI
7640             SRL
7650             AI
7660             BL
7670             CI
7680             JHE
7690             XOP
7700 CODE040       ANDI
7710             XOP
7720 CODE08        MOV
7730             ANDI
7740             SRL
7750             AI
7760             BL
7770             MOV
7780             XOP
7790             XOP
7800             SRL
7810 CFIELD        ANDI
7820             MOV
7830             XOP
7840             XOP
7850             *              R5.R0
7860 DSIX           XOP
7870             XOP
7880             MOV
7890             SRL
7900             XOP
7910             XOP
7920             *
                  PRINTS SIXBIT FIELD + REGISTER
PRINT "."
R5.R6
R6.6
R6.R6
R0.1
RETURN

```

FCEA	2C20 FD30	7930	KSI X	XOP	@SIXBITS.0
FCEE	2CE0 F4C9	7940		XOP	@COMM A.3
FCF2	0955	7950		SRL	R5.5
FCF4	10EB	7960	*	JMP	CFIELD
		7970	*		
		7980	*		
		7990	*		
FCF6	C1B5	8000	IMMR	MOV	R5,R6
FCF8	2C20 FD14	8010		XOP	EREG.0
FCFC	2CE0 F4C9	8020		XOP	@COMM A.3
FD00	2CE0 F4C7	8030	IMM	XOP	@HASH.3
FD04	C0BC	8040		MOV	*R12*,R2
FD06	06A0 FD06	8050		BL	@HEXOUT0
FD0A	2C40	8060		XOP	R0,1
		8070	*		
FDO C	C1B5	8080	ONER	MOV	R5,R6
FDO E	2C20 FD14	8090		XOP	EREG.0
FD12	2C40	9100	*	XOP	R0,1
		9110	*		
FD14	0246 000F	8120	REG	ANDI	R6,#000F
FD18	0A16	8130		SIA	R6,1
FD1A	2CE0 F4D0	8140		XOP	ER.R.3
FD1E	C266 F4E2	8150		MOV	@NUMTAB(R6),R9
FD22	0286 0014	8160		CI	R6,#14
FD26	1402	8170		JHE	REG00
FD28	0249 FF00	8180		ANDI	R9,#FF00
FD2C	2CC9	8190	REG00	XOP	R9,3
FD2E	2C40	8200		XOP	R0,1
		8210	*		
FD30	C1B5	8220	SIXBITS	MOV	R5,R6
FD32	1002	8230		JMP	SIXBIT
FD34	C1B5	8240	SIXBITS	MOV	R5,R6
FD36	0966	8250		SRL	R6,6
FD38	0246 003F	8260	SIXBIT	ANDI	R6,#003F
FD3C	C1C6	8270		MOV	R6,R7
FD3F	0246 000F	8280		ANDI	R6,#000F
FD42	0947	8290		SRL	R7,4
FD44	1603	8300		JNE	SIXBIT0
FD46	2C20 FD14	8310		XOP	EREG.0
FD4A	101F	8320		JMP	SIXBIT3
FD4C	0287 0002	8330	SIXBIT0	CI	R7,#2
FD50	1611	8340		JNE	SIXBIT2
FD52	2CE0 F4C6	8350		XOP	@ATHASH.3
FD56	C0BC	8360		MOV	*R12*,R2
FD58	06A0 FD06	8370		BL	@HEXOUT0
FD5C	0286 0000	8380		CT	R6,#0000
FD60	1314	8390		JEQ	SIXBIT3
FD62	0201 2B00	8400		LI	R1,1
FD66	2CC1	8410		XOP	R1,3
FD68	2C20 FD14	8420		XOP	EREG.0
FD6C	0201 2900	8430		LI	R1,1
FD70	2CC1	8440		XOP	R1,3
FD72	100B	8450		JMP	SIXBIT3

				INDIRECT REGISTER ADDRESSING MODE
FD74	0204 2A00	8460 SIXBIT2 LI	R1.**	
FD78	2CC1	8470 XOP	R1.3	
FD7A	2C20 FD14	8480 XOP	AREG.0	
FD7E	0287 0003	8490 C1	R7.0003	
FD82	1603	8500 JNE	SIXBIT3	
FD84	0204 2R00	8510 R1.4*		AUTO INCREMENT ADDRESSING MODEBT
FD8A	2CC1	8520 XOP	R1.3	
FD8A	2C40	8530 SIXBIT3 XOP	R0.1	
FD8C	2CE0 F4H0	8540 *		RETURN
FD90	04CA	8550 PMNEM XOP	@MESS03+6.3	SUBROUTINE TO PRINT OPCODE MNEMONIC
FD92	C230	8560 CLR R10		
FD94	C250	8570 MOV *R0*,R8		
FD96	2CC8	8580 MOV *R0,R9		
FD98	2CE0 F4H1	8590 R8.3		
FD9C	045B	8600 XOP @MESS03+7.3		PRINT " "
FD9E	2CE0 F4H0	8610 RT		
FD9F	00	8620 *		
FDA4	4F56 4520 4259	8630 * INSTRUCTION M		
FDA4	5445 5320 00	8640 *		
FDA9	54 4F20 00	8650 MMESSO TEXT	'OVE BYTES '-'	
FDA0	59 4553 204F	8660 MMESS1 TEXT	'TO ' -	
FDB2	5220 4E4F 3F20	8670 MMESS2 TEXT	'YES OR NO? ' -	
FDB8	00	8680 EVEN	@MESS03.3	PRINT "OVE BYTES "
FDHA	2CE0 FD9E	8690 MOVE XOP	@HEXIN	JNF ON HEXIN FLAG
FDFE	06A0 F5AE	8700 BL	MOVE04	R8:=LOW ADDRESS
FDC2	162B	8710 JNE R2,R8		
FDC4	C202	8720 MOV C1	R1.**	ERROR
FDC6	0281 3A00	8730 JNE R1.	MOVE04	
FDC8	1627	8740 RT	@HEXIN	JNE ON HEXIN FLAG
FDC9	06A0 F5AF	8750 JNE R8,R2	MOVE04	R7:=HIGH ADDRESS
FDD0	1624	8760 C	MOVE04	PRINT " TO"
FDD2	8088	8770 JH	R2,R7	JNE ON HEXIN FLAG
FDD4	1B22	8780 MOV XUP @MESS1.3		PRINT INDENTATION
FDD6	C1C2	8790 JNE R8,R6	@MESS03.3	PRINT "YES OR NO?"
FDD8	2CE0 FDA9	8800 XOP R1.2	@MESS2.3	GET CHARACTER
FDDC	06A0 F5AE	8810 BL XOP R1.3	MOVE01	PRINT CHARACTER
FDE0	161C	8820 JNE R6,XOP R1.Y*		
FDF2	C182	8830 MOV JH MON00C		RETURN TO MONITOR
FDF4	2CE0 F4AA	8840 XOP R6.R8		
FDF8	2CE0 FDAD	8850 XOP C		
FDFC	2C81	8860 JEQ MON00C		
FDFE	2CC1	8870 XOP R1.Y*		
FDF0	0281 5900	8880 C JH MON00C		
FDF4	1610	8890 JNE R6.R8		
FDF6	8206	8900 C JEQ MON00C		
FDF8	130E	8910 R910 JH MOVE01		LOW=DESTINATION
FDFA	1B04	8920 XOP R1.Y*		
FDFC	DD88	8930 MOVE00 C *R8*,*R6*		
FDFE	81C8	8940 C JH R8.R7		
FF00	1R0A	8950 JH MON00C		RETURN TO MONITOR


```

9480 * * INSTRUCTION L
9490 * * INSTRUCTION L
9500 * * INSTRUCTION L
9510 LMESS0 TEXT *LOW=-*
9520 EVEN JNE
9530 LOW JNE
9540 MOV R2,R15
9550 MON00C
9560 LOW00 XOP @MESS03.3
9570 XOP @MESS00.3
9580 MOV R15,R2
9590 BL @HEXOUT0
9600 JMP MON00C
9610 * * INSTRUCTION W
9620 * * INSTRUCTION W
9630 * * INSTRUCTION W
9640 WP JNE WPO0
9650 MOV R2,@R13+INT1WP
9660 MON00C
9670 WP00 XOP @MESS03.3
9680 XOP @MESS1+2.3
9690 MOV @2*R13+INT1WP.R2
9700 RL @HEXOUT0
9710 JMP MON00C
9720 * * INSTRUCTION O
9730 * * INSTRUCTION O
9740 * * INSTRUCTION O
9750 OUTPUT MOV R15,R4
9760 MOV R3,R3
9770 JNE OUTPUT0
9780 MOV R2,R4
9790 OUTPUT0 AND1 R4.#FFFF
9800 MOV R4,R5
9810 AND1 R5.#FFF0
9820 AI R5.#80
9830 OUTPUT1 XOP @MESS03+1.3
9840 MOV R4,R2
9850 BL @HEXOUT0
9860 XOP @MESS03+7.3
9870 OUTPUT2 MOV *R4*,R2
9880 RL @HEXOUT
9890 C R4,R5
9900 JEQ OUTPUT3
9910 C2C @MASK16.R4
9920 JEQ OUTPUT1
9930 JMP OUTPUT2
9940 OUTPUT3 XOP R1.2
9950 CI R1.* *
9960 JNE MON00C
9970 AI R5.#40
9980 JMP OUTPUT1
9990 * * * *
10000 END

```

** CROSS REFERENCE LISTING **

LABEL, NAME	DEFN	VALUE	STATEMENT NUMBERS
ATHASH	930	F4C6	8350
C02ATAB	6900	FBBA	7590
C0ATAB	6870	FBBA	7040
C2ATAB	6B90	FBAA	7360
CFIELD	7810	FCCC	7960
CHAR	2550	F638	1040
CHAR00	2570	F63C	2550
CHAR01	2580	F640	2810
CHAR02	2610	F648	2790
CHAR03	2690	F65A	2630
CHAR04	2730	F664	2700
CHAR05	2770	F66E	2750
CODE00	7480	FC70	6880
CODE02	7520	FC7A	6870
CODE04	7620	FC94	6870
CODE040	7700	FCAE	7680
CODE08	7720	FCB0	6880
CODE100	7250	FC32	7190
CODE101	7230	FC2E	7210
CODE2	7290	FC3C	7090
CODE4	7390	FC56	7070
COMMA	950	F4C9	8020
CR	290	000D	5230
DELIM00	3H10	F87E	4390
DISASM	6930	F8AC	3770
DISASM1	7060	FHD4	6740
DISASM2	7270	FHF4	7010
DOT	3050	F7DC	3160
DSIX	7860	FC08	6890
ERR1	5140	F9E0	4980
ERROR	3840	F992	5140
FIND	9130	FE28	1030
FIND00	9150	FE2C	9220
FIND01	9210	FE3B	9410
FIND02	9260	FE46	9200
FIND03	9400	FE6E	9340
FMESS00	9110	FE1E	9240
GETRC	4540	F942	5110
GETRC00	4580	F950	4660
GETRC01	4670	F966	4620
GETRC02	4680	F96A	4710
GETRC03	4730	F976	4690
GO	9460	FE76	1030
HASH	940	F4C7	8030
HEXIN	1900	F5AE	9330
HEXINO	1930	F5B4	2010
HEXIN1	1960	F5HC	2030
HEXIN2	2020	F5CA	1970

HEXOUT	2100	F5D2	9880	9310	6980	6580	6180	5340	2490
HEXOUT0	2110	F5D6	9850	9700	9590	9290	8370	8050	7260
HEXOUT1	2140	F5DE	2200						
HEXOUTX	2120	F5D8	1220						
HEXTAB	990	F4D2	2170	1960					
I1TAB	5770	FA76	5840						
IMESS0	5780	FA7C	5920						
IMESS1	5790	FA7F	9680	6450	6190				
IMESS2	5800	FA85	6310						
IMESS3	5810	FA87	6220						
IMM	8030	FB00	6910	6900					
IMMR	8000	FCF6	6900						
INIT0	460	F40C	490						
INIT1	530	F422	580						
INIT2	640	F440	1230						
INITIAL	420	F400	840						
INST00	3110	F7E2	3850	3730	3090				
INST01	3180	F7F8	3270						
INST02	3280	F912	3230	3210					
INST03	3360	FB22	3420						
INST04	3400	FB2C	3370						
INST05	3470	FB38	3190						
INST06	3490	FB3E	3520						
INST07	3530	FR46	3500						
INST08	3640	FB62	4500	4260	4120	4080	3930		
INST09	3670	FB6A	3700						
INST10	3740	FB7A	3800	3310					
INSTANT	3090	F7DF	1040						
INT1	5830	FA8E	840						
INT1AA	5870	FA96	5770						
INT1BB	5900	FAA0	5880						
INT1CC	6040	FAC0	5940	5770					
INT1DD	6070	FACA	6100						
INT1EE	6150	FAD8	6080						
INT1FF	6250	FAFA	6340	5990					
INT1GG	6280	FB02	5770						
INT1WP	320	F3E0	9690	9650	6380	5670	5660	5630	5490
INTAR	1010	F502	710						
KSIX	7930	FCEA	6890						
L.F	300	000A	5230	900	870				
LMES0	9510	FE7A	9570						
LOW	9530	FE80	1040						
LOW0	9560	FE86	9530						
MASK16	1070	F540	9910	5690					
MASK3	1090	F544	6620						
MASK32	1060	F53F	2780						
MASK8	1080	F542	5870						
MESS00	860	F488	670						
MESS01	870	F490	680	660					
MESS02	890	F4A6	5960	3840	750				
MESS03	900	F4AA	9860	9830	9670	9560	9320	9260	9230
MESS04	920	F4B3	2450	2100					

MMESSO	8650	FD9E	8690
MMMESSI	8660	FDA9	8800
MMMESS2	8670	FDAD	8850
MIN00A	2420	F61F	2390
MON00B	6810	FB86	6610
MON00C	9060	FE16	9960
MON00	680	F44E	9060
MON01	710	F45A	740
MON02	750	F464	9070
MON03	770	F46A	720
MONITOR	660	F446	1030
MOVE:	8690	FDBA	1030
MOVE00	8930	FDFF	8960
MOVEF01	8970	FF04	8920
MOVE02	9010	FE0C	9050
MOVE04	9070	FE1A	8920
NUMTAB	1000	F4E2	8150
ONER	8080	FD0C	6900
OPEN	2260	F5F4	1040
OPEN00	2280	F5F8	2260
OPEN01	2290	F5FC	2410
OPEN02	2340	F60A	2310
OPEN03	2380	F614	2350
OUTPUT	9750	FEH0	1030
OUTPUT0	9790	FEH8	9770
OUTPUT1	9830	FEC6	9980
OUTPUT2	9870	FED4	9930
OUTPUT3	9940	FEE6	9900
PADDRC	2450	F622	3150
PMNEM	8550	FD8C	7760
PRINT	6680	FB64	1030
PRINT00	6720	FR6C	6700
PRINT01	6740	FB74	6800
R	960	F4CB	6520
R0	120	0000	8570
R1	130	0001	9950
R11	230	000B	6300
R12	240	000C	8360
R13	250	000D	9690
R14	260	000E	6110

R15	270	000F	9750	9540	9210	9130	6680	6320	6230	5710	5690	610	590	9280	9270	9140	8830		
R2	140	0002	9870	9840	9780	9690	9650	9580	9540	9470	9390	9360	9300	9300	9280	9270	9140	8830	
			8790	8770	8720	8360	8040	7240	7230	7220	7200	7170	7160	6970	6940	6710	6570	6460	
			6420	6320	6230	6200	6170	6090	6040	5900	5660	5380	5330	5050	5030	4740	4730	4700	
R3	150	0003	9760	6690	6400	5640	3530	3490	3480	2800	2600	2570	2210	2140	2150	2000	1990	1910	1980
R4	160	0004	9910	9890	9870	9840	9800	9790	9780	9750	6570	6460	6430	6420	6390	6380	6360	6360	6360
			5550	5500	5490	5480	3600	3580	3570	3540	3510	3470	2640	2580	2020	2000	1960	1950	
R5	170	0005	9970	9890	9820	9810	9800	8240	8220	8080	8000	7950	7880	7820	7810	7800	7770	7720	
			7670	7620	7520	7290	7180	7160	7110	7020	6990	6970	6960	6620	6600	6590	6530	6480	
R6	180	0006	9030	9010	9000	8990	8970	8930	8900	8830	8800	8830	8800	8280	8270	8250	8240	8220	8160
			8150	8130	8120	8080	8000	7890	7880	7770	7590	7580	7550	7540	7530	7520	7400	7390	
			7360	7350	7320	7310	7300	7290	7080	7060	7040	7030	7020	7000	6990	6460	4550	4540	
R7	190	0007	9300	9190	9180	9170	9150	9140	9040	9020	9010	9000	8970	8940	8790	8490	8330	8290	
			8270	4380	4310	4250	4130	4070	4000	3980	3970	3920	3890	3580	3560	3530	3490	3410	
R8	200	0008	9040	8990	8940	8930	8900	8770	8720	8590	8570	5420	5410	5310	5290	5030	4480	3360	
R9	210	0009	9390	9360	9270	9210	9180	9150	9130	8580	8190	8180	8150	7830	7820	6560	6550	6540	
REG	8120	FD14	8480	8420	8310	8090	8010	7900	7780										
REG0	8190	FD2C	8170																
REG1ST	6380	FR16	1030																
REG1ST0	6430	FR22	6410																
REG1ST1	6500	FR38	6630																
REG1ST2	6520	FB3E	6640																
REFSTAB	840	F474	640	450															
RR	970	FA00	8140	5100	4880	4420	4360	4280	4070										
SFTTRP	5270	FA02	1040																
SFTHP00	5300	FA0E	5430																
SFTRP01	5390	FA2C	5370																
SFTRP02	5440	FA3C	5400																
SETUP	5620	FA56	5560	5510															
SETUP00	5670	FA64	5650																
SFTOP01	5720	FA72	5700																
SIX	4790	F97C	4060	3950	3900	3870													
SIX00	4860	F98F	4830																
SIX02	4960	F9AE	4920																
SIX03	4970	F9B0	4870																
SIX04	5050	F9C6	5020																
SIX05	5060	F9C8	5040																
SIX06	5150	F9E4	5130																
SIX07	5170	F9E8	5090																
SIXBIT	8260	FD38	8230																
SIXBIT0	8330	FD4C	8300																
SIXBIT2	8460	FD74	8340																
SIXBIT3	8530	FD8A	8500	8450	8390	8320													
SIXBITD	8240	FD34	7450																

SIXBITS	8220	FD30	7930	7860	7690	7430
SMFSS0	5240	F9EE	6150	5300		
SMFSS1	5250	F9FA	6160	5320		
STFP	5600	FA52	1040			
SUBTAH	1030	FS16	780			
T1	3870	F898	3040			
T100	3910	F8A6	4040			
T101	3920	F8A8	4440	4340		
T2	3950	F8AC	3040			
T200	4020	F8C2	3990			
T201	4030	F8C6	4010			
T3	4060	F8CC	3040			
T4	4100	F8D4	3040			
T400	4230	F8FA	4140			
T5	4280	F904	3040			
T6	4360	F91A	3040			
T7	4420	F92A	3040			
T8	4460	F934	4400	3040		
T9	3630	F860	3040			
TRACE	5480	FA40	1040			
TYHASE	3030	F768	3580			
TYPE1	2880	F67A	7410	3350	3000	
TYPE2	2900	F6AA	7330	3000		
TYPE3	2910	F6CA	7650	3000		
TYPE4	2930	F70A	7140	3000		
TYPE5	2950	F74A	7750	3000		
TYPE6	2960	F75A	7560	3010		
TYPE7	2970	F76E	3010			
TYPE8	2980	F776	3010			
TYPE9	2990	F77E	7480	3010		
TYSHIFT	3020	F7AE	3540			
TYSUB	3040	F7CA	3600			
TYTAB	3000	F79A	3480	3410	3010	
WORKSP	340	F3A0	6250	1030	430	
WP	9640	FF96	9640			
WP00	9670	FE9F	9640			
XCTE	5550	FA4C	1040			
XDP0	1150	F546	850			
XDP0AA	1240	F562	1180			
XDP0XX	1190	F54E	1700			
XDP1	1320	F568	850			
XDP2	1400	F56E	850			
XDP2AA	1420	F574	1430			
XDP3	1530	F580	850			
XDP3AA	1550	F584	1590	1560		
XDP4	1660	F590	850			
XDP4AA	1680	F594	1730			
XDP5	1800	F5A2	850			
XDP5AA	1810	F5A4	1840			
XOPTAB	850	F47C	510			
XOPWP	330	F3C0	1200	640	630	520
NOLABELS=	240	NOPINMES=	390	ALPHAS=	1.69	
SYMBOL_TABLE	57-35	* FULL				