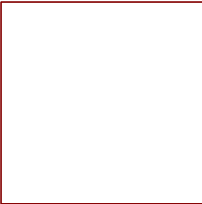


Power



File: Power.kicad_sch

Noise Module

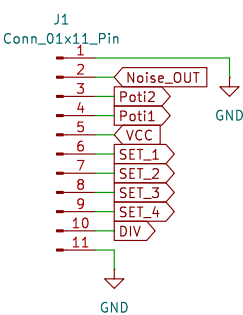


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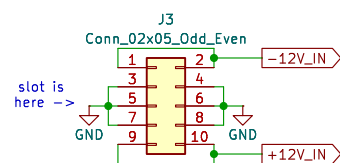
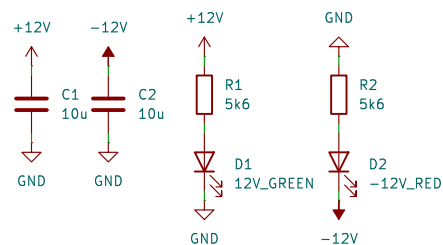
Mounting Holes

- H1 MountingHole
- H2 MountingHole

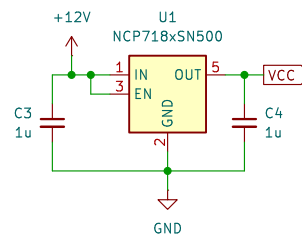
Front-Connector



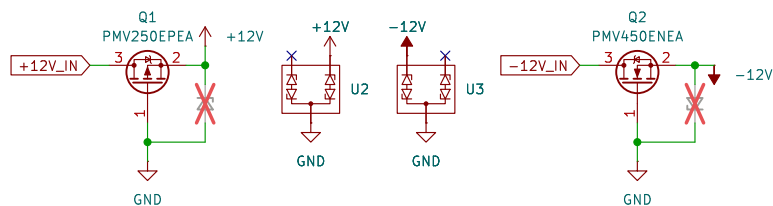
Power IN



VCC Power (+5V)



Reverse Polarity Protection and Transient Protection



Handles input power connection, power limiting, and filtering

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Sheet: /Power/

File: Power.kicad_sch

Title: Noise source and frequency filter

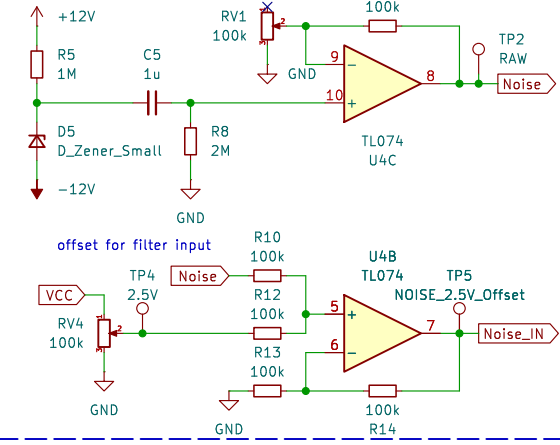
Size: A4 Date: 11/12/2024

KiCad E.D.A. 8.0.8

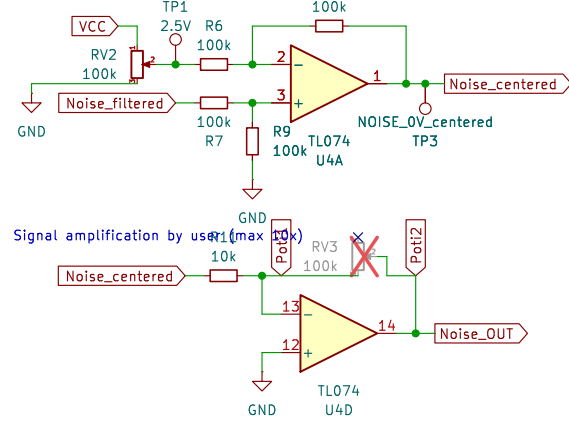
Rev: V1

Id: 2/3

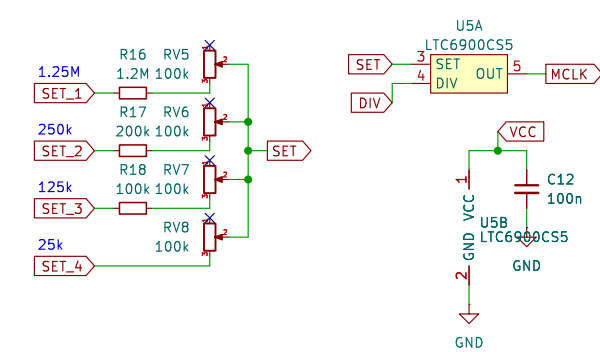
Noise Generator



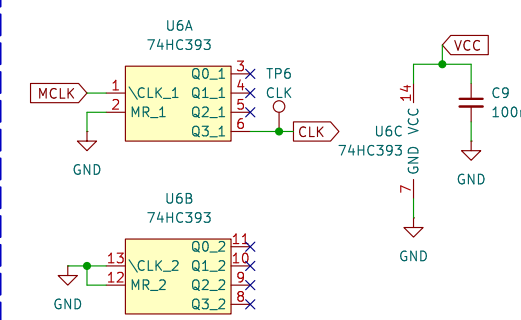
Offset Correction after filtering



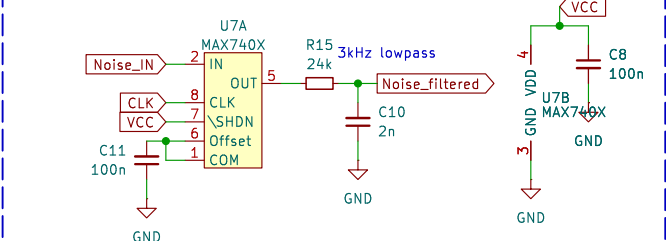
Master Clock Generator



Clock Divider by 16



Frequency Filter



Noise source and frequency filter

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Sheet: /Noise Module/

File: Noise_Module.kicad_sch

Title: Noise source and frequency filter

Size: A4

Date: 11/12/2024

Rev: V1

KiCad E.D.A. 8.0.8

Id: 3/3