# SEMICONDUCTOR TECHNICAL DATA

JC357

#### Features

- 1. Current transfer ratio: 50-600% (at condition IF=5mA, VCE=5V)
- 2. High isolation voltage between input and output  $(V_{\text{ISO}}=3750 \text{Vrms})$
- 3. Compact 4 pin SSOP
- 4. Pb free and RoHS compliant
- 5. CQC approval
  - UL approval
  - VDE approval
  - CE approval

### Description

The JC357 device consist of an infrared emitting diode, optically coupled to a phototransistor detector encapsulated with green compound.

Length of lead pin: 2.54mm

# Applications:

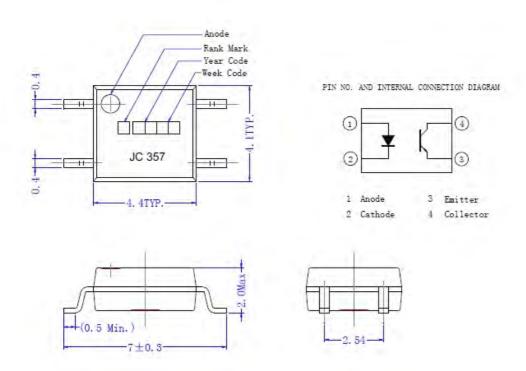
DC-DC converters

Programmable controllers

Telecommunication equipments

Single transmission between circuits of different potentials and impedances

### Dimension:





# • Electrical Characteristics (Ta=25°C unless specified other wise)

Parameter		Symbol	Condition	Min	Тур.	Max	Unit
Input	Forward Voltage	V <sub>F</sub>	I <sub>F</sub> =20mA		1.2	1.4	V
	Reverse Current	IR	V <sub>R</sub> =4V			10	uА
	Input Capacitance	$C_{\mathrm{t}}$	V=0, f=1KHz		30	250	рF
	Collector-Emitter dark	Iceo	$V_{CE}=20V$ , $I_{F}=0$			100	nA
	current						
	Collector-Emitter	BVceo	Ic=0.1mA	80			V
	breakdown voltage		$I_F=0$				
	Emitter-Collector	BVECO	I <sub>E</sub> =10 μ A	7			V
Output	breakdown voltage		$I_F=0$				
	Collector current	Ic	IF=5mA	4		30	mA
	*2 Current transfer ratio	CTR	Vce=5V	80		600	%
	Saturation voltage	VCE(sat)	$I_F=20$ mA		0.1	0.2	V
			Ic= 1mA				
	Isolation resistor	$R_{iso}$	DC 500V	$5\times10_{10}$			Ω
			40 <sup>~</sup> 60%R. H.				
	Capacitance	$C_{\mathrm{f}}$	V=0, f=1MHz		0.6	1	рF
	cut-off frequency	$ m f_{c}$	Vce=5V, Ic=2mA		80		kHz
			$R_L=100\Omega$ , $-3dB$				
	Rise time	tr	Vce=2V, Ic=2mA		4	18	us
	Fall time	tf	RL=100 Ω		3	18	us

# $*1: CTR=I_C/I_F \times 100\%$

# • CTR range

BIN range	Min(%)	Max(%)	
A	80	160	
В	130	260	
С	200	400	
D	300	600	
A or B or C or D	80	600	

Test condition at:IF=5mA, VCE=5V, Ta=25 °C.

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# lacktriangle Absolute Maximum Ratings(Ta=25°C)

Parameter		Symbol	Rating	Unit
	Forward current	IF	50	mA
Input	Reverse voltage	$V_R$	6	V
	Power dissipation	PD	70	mW
	Collector-Emitter breakdown	Vceo	80	
	voltage			
Output	Emitter-Collector breakdown	VECO	7	V
	voltage			
	Collector current	Ic	50	mA
	Collector power dissipation	Pc	150	mW
Total power dissipation		P <sub>tot</sub>	200	mW
*1 Isolation voltage		Viso	3750	Vrms
Operating temperature		Topr	-55 to + 110	
Storage temperature		Tstg	-55 to + 125	$^{\circ}$ C
*2 Soldering temperature		Tsol	260	

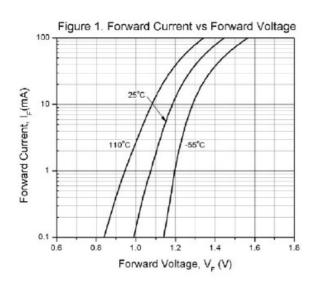
### Notes:

1. AC for one minute, R. H. = $40^{\circ}60\%$ .

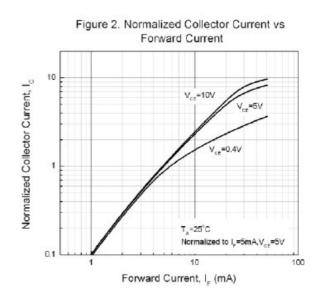
### Test methods:

- 1); In this test, pins 1&2 are shorted together, and pins 3&4 are shorted together.
- 2); For 10 seconds.

# Typicalperformancecurves:



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### Typical performance curves:

Figure 3. Normalized Current Transfer Ratio vs
Forward Current

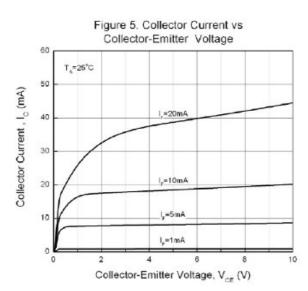
T<sub>A</sub>=25\*C

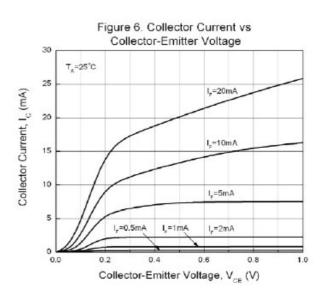
Normalized to I<sub>p</sub>=5mA,V<sub>ce</sub>=5V

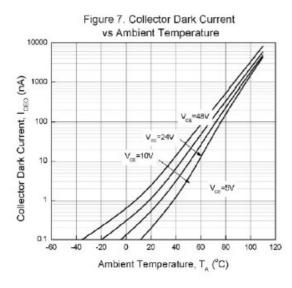
V<sub>ce</sub>=5V

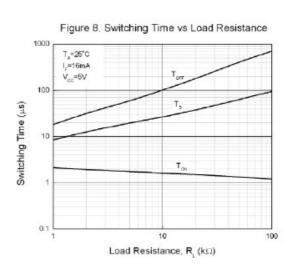
V<sub>ce</sub>=0.4V

Forward Current, IF (mA)











# Typical performance curves:

Figure 9. Collector-Emitter Saturation Voltage
vs Ambient Temperature

0.24

0.22

0.20

0.18

0.16

0.12

0.10

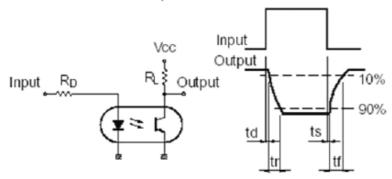
0.08

0.00

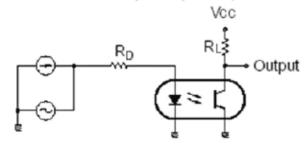
0.00

Ambient Temperature (°C)

Test Circuit for Response Time



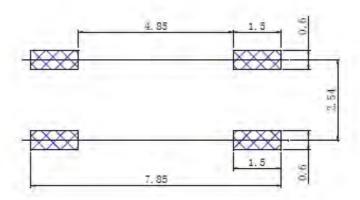
# Test Circuit for Frequency Response



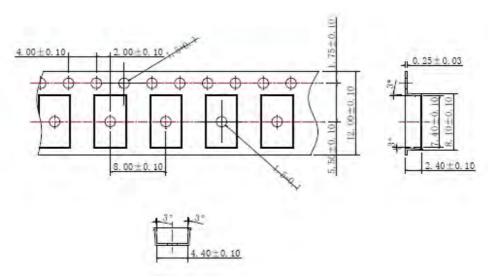
2013. 09. 02 Revision No : 0 **First Silicon** 5/6



• Installation and dimension recommendation:



- Packaging
  - A. Tape & Reel Packing Specifications:



B. Tape dimensions: 1KP/Reel

