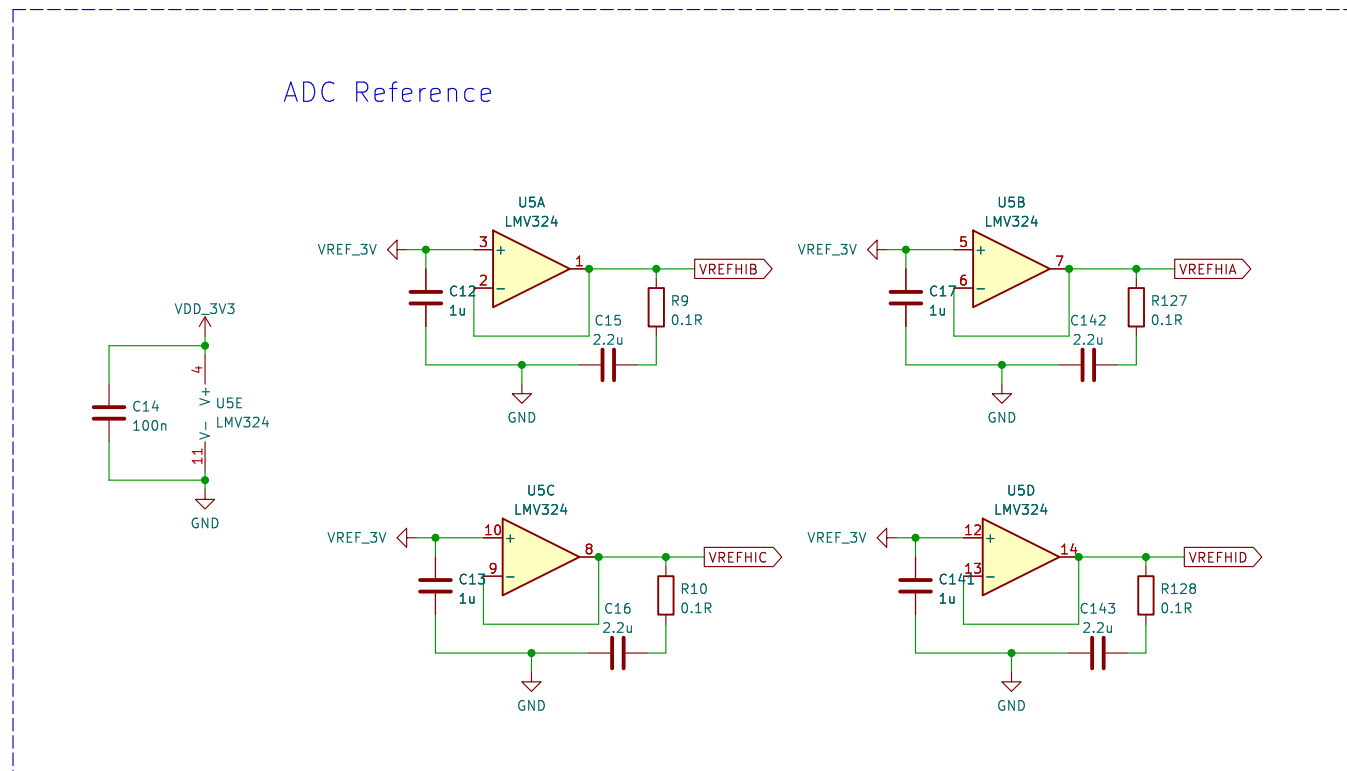
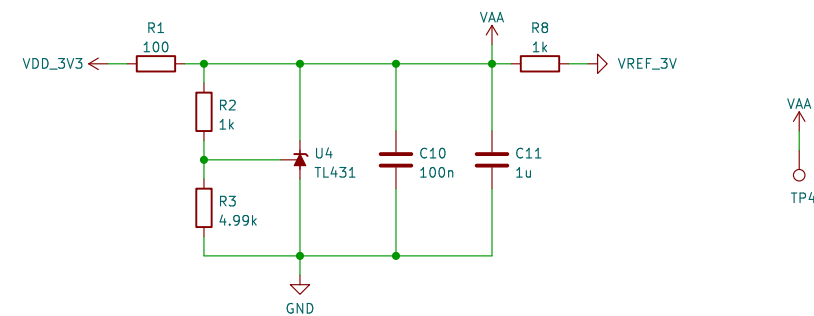
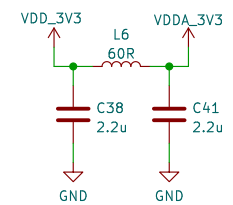
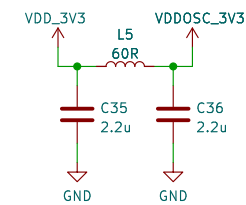
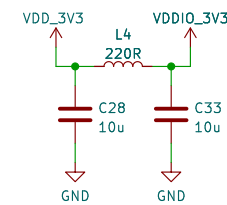
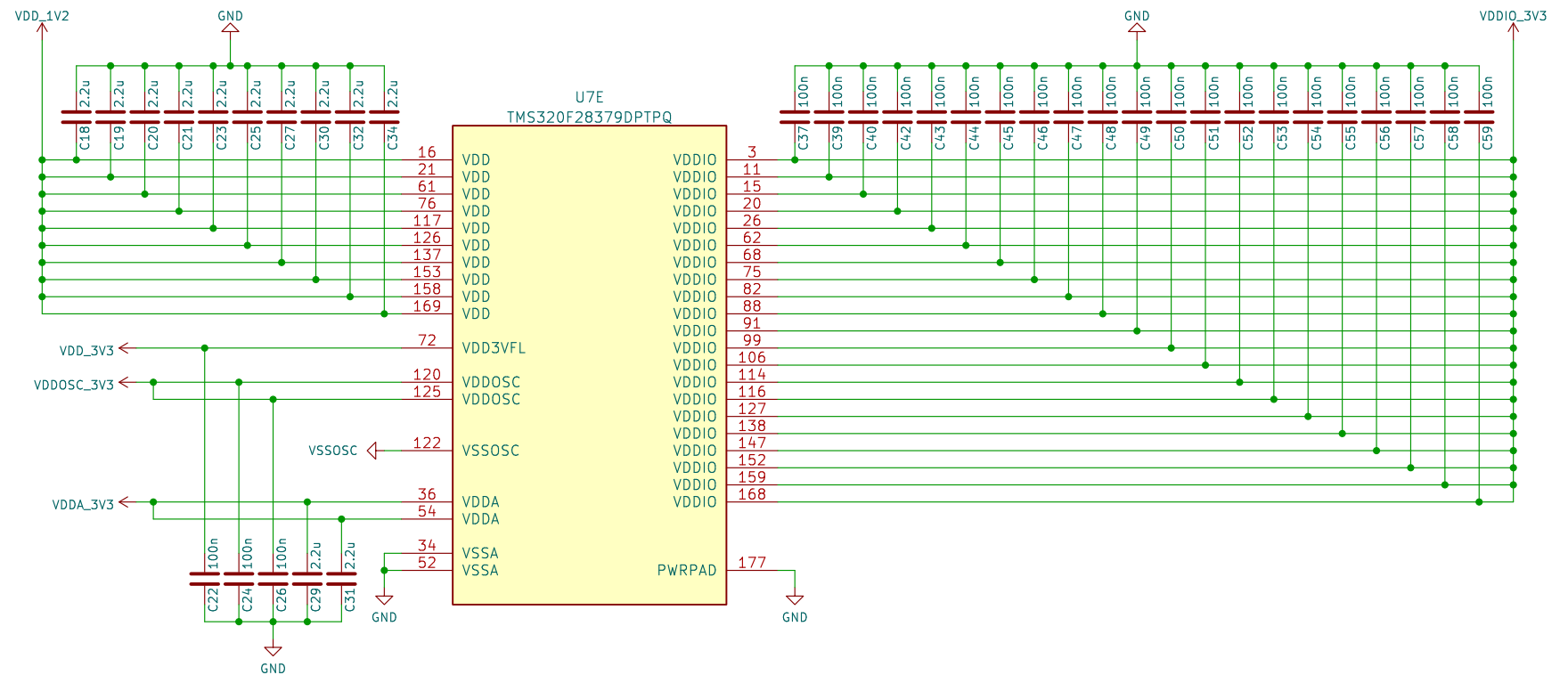
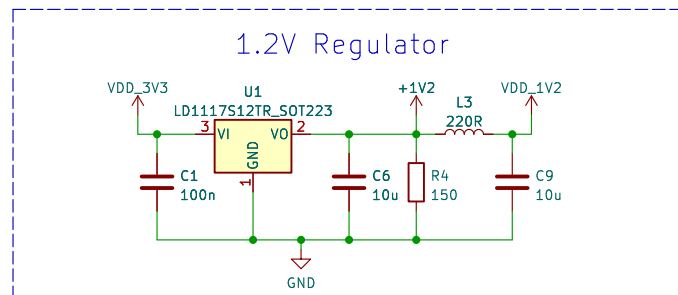
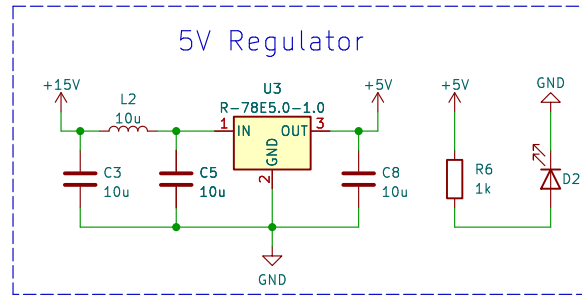
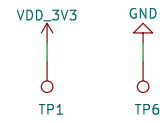
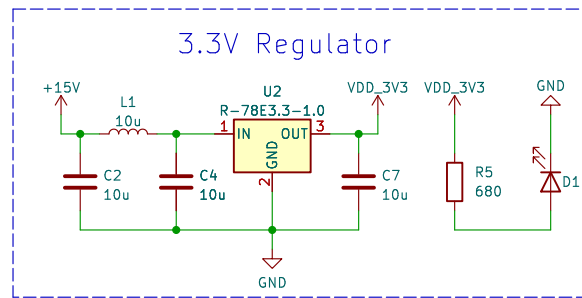
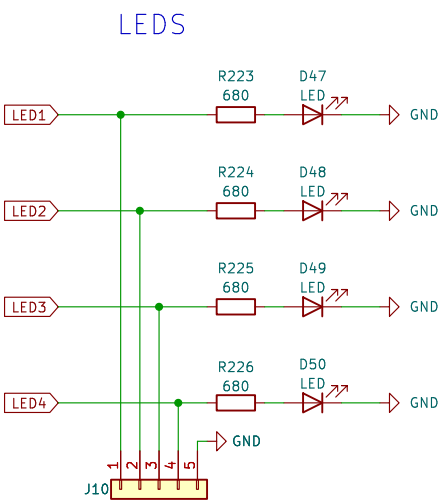
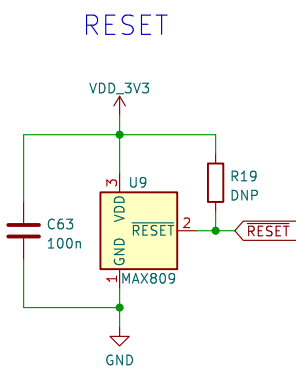
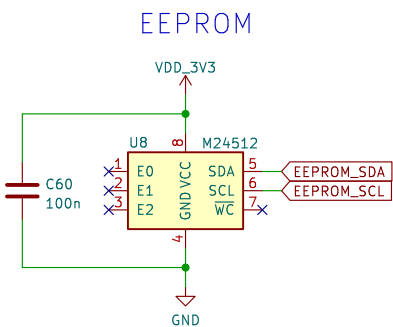
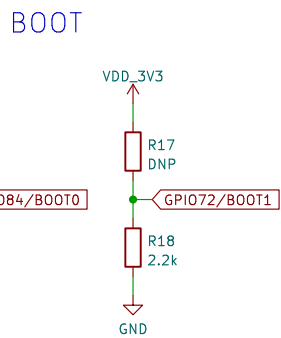
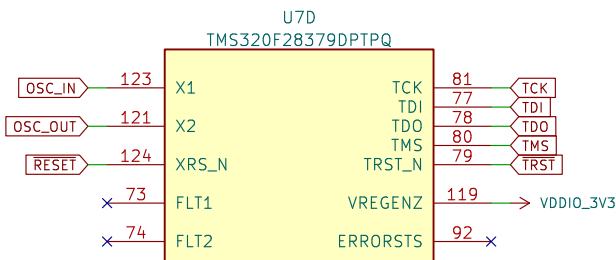
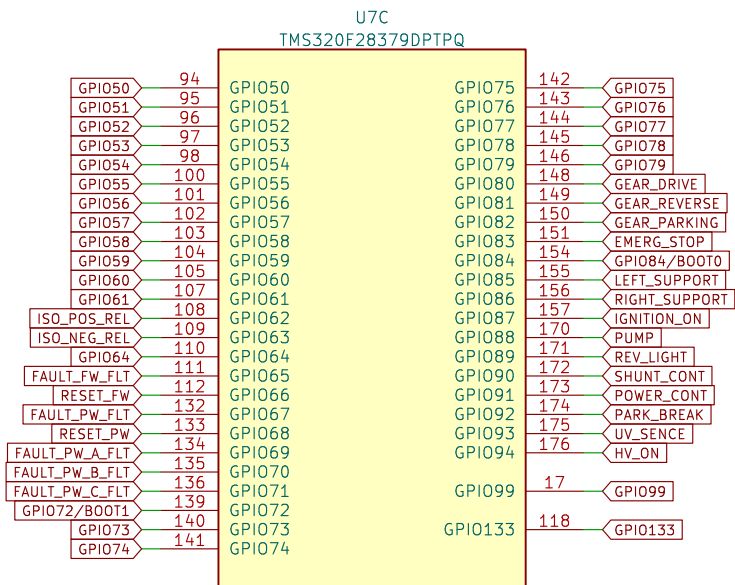
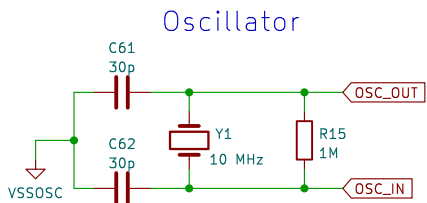
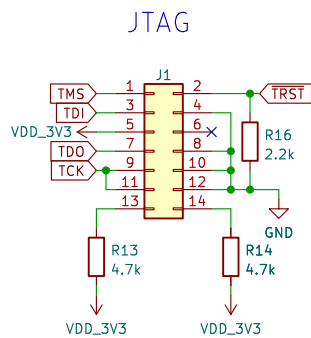
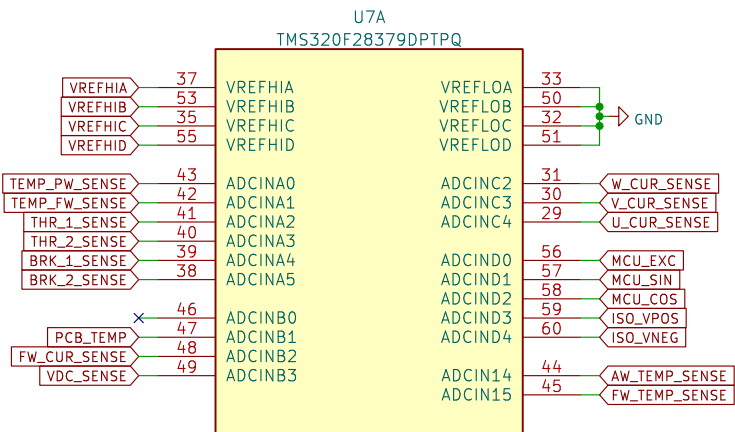
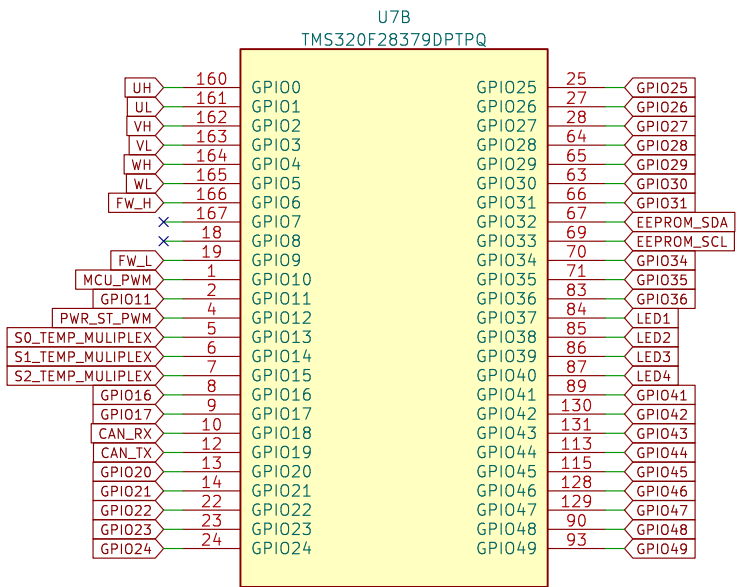


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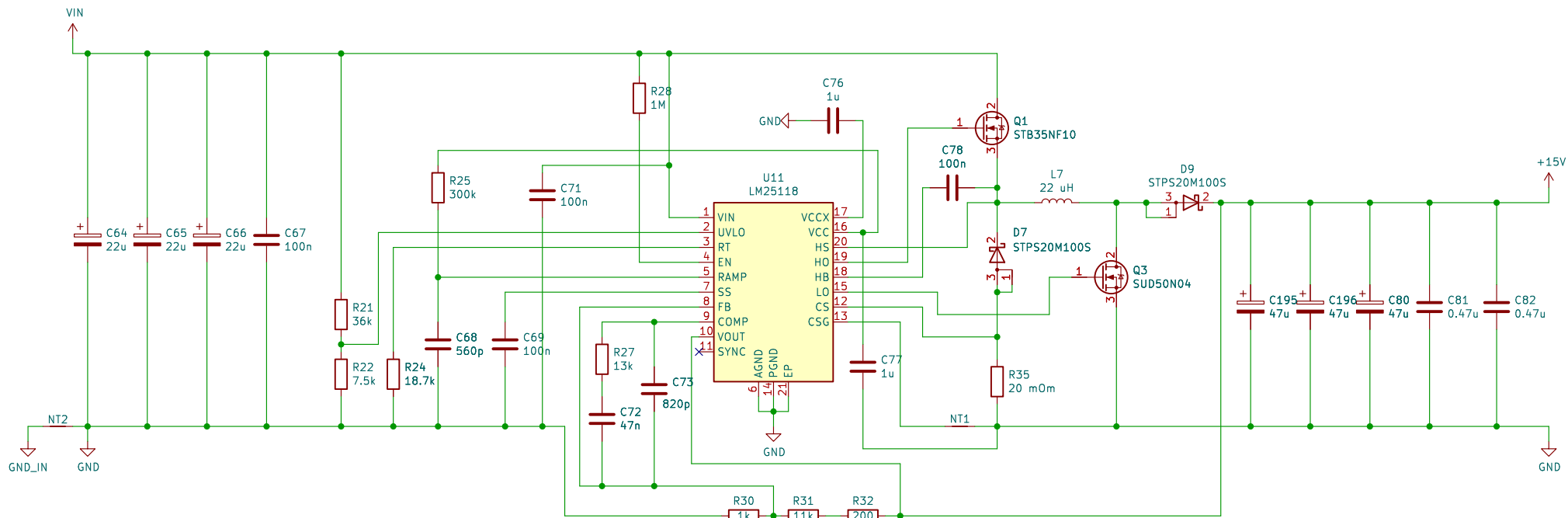
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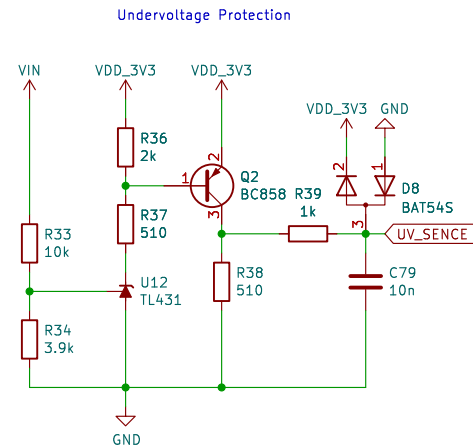
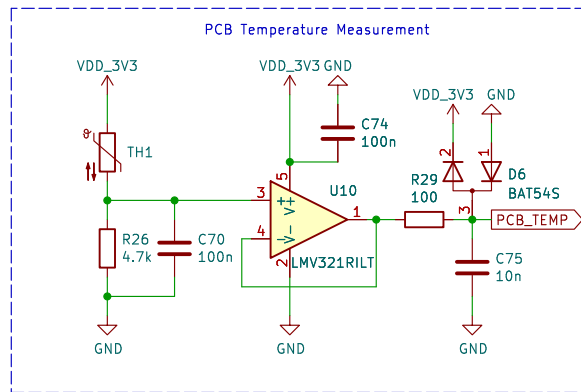
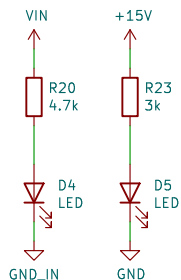


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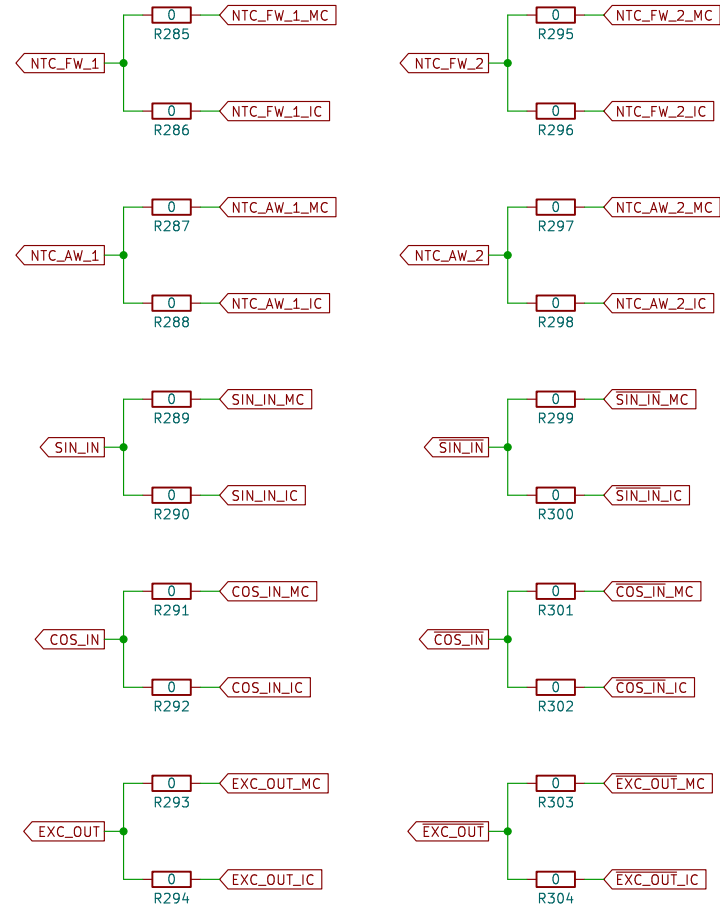
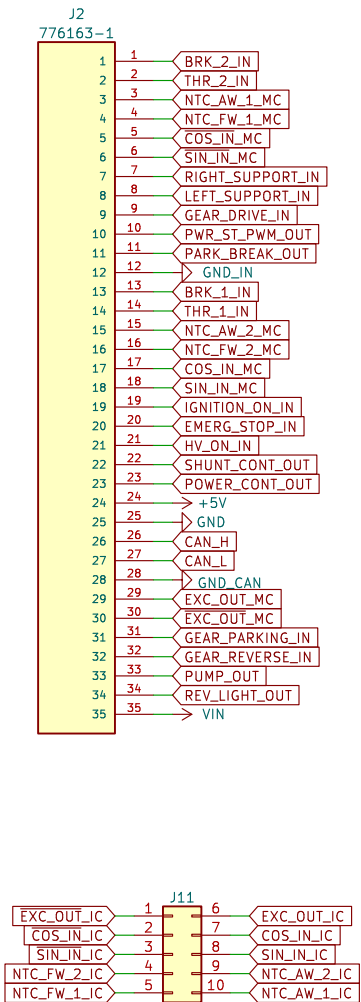
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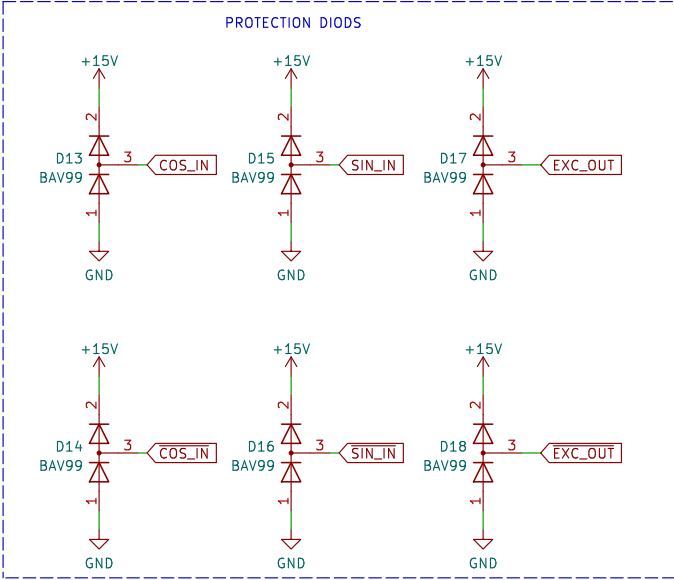
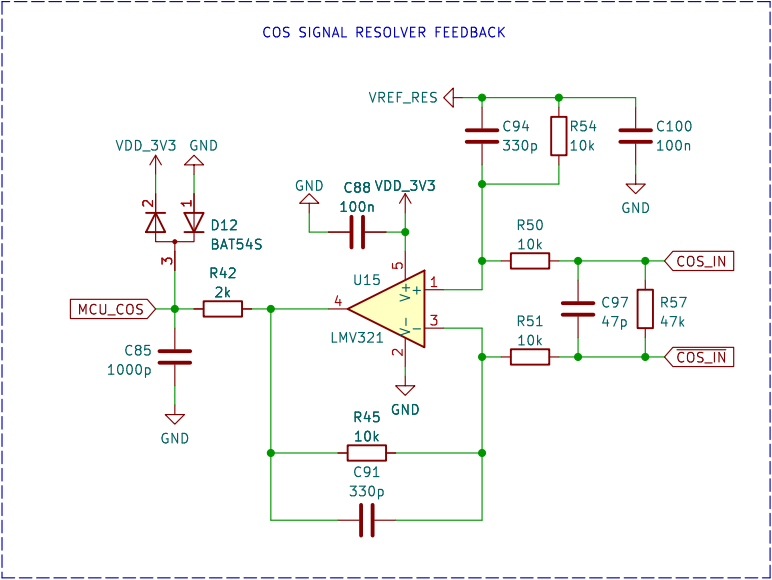
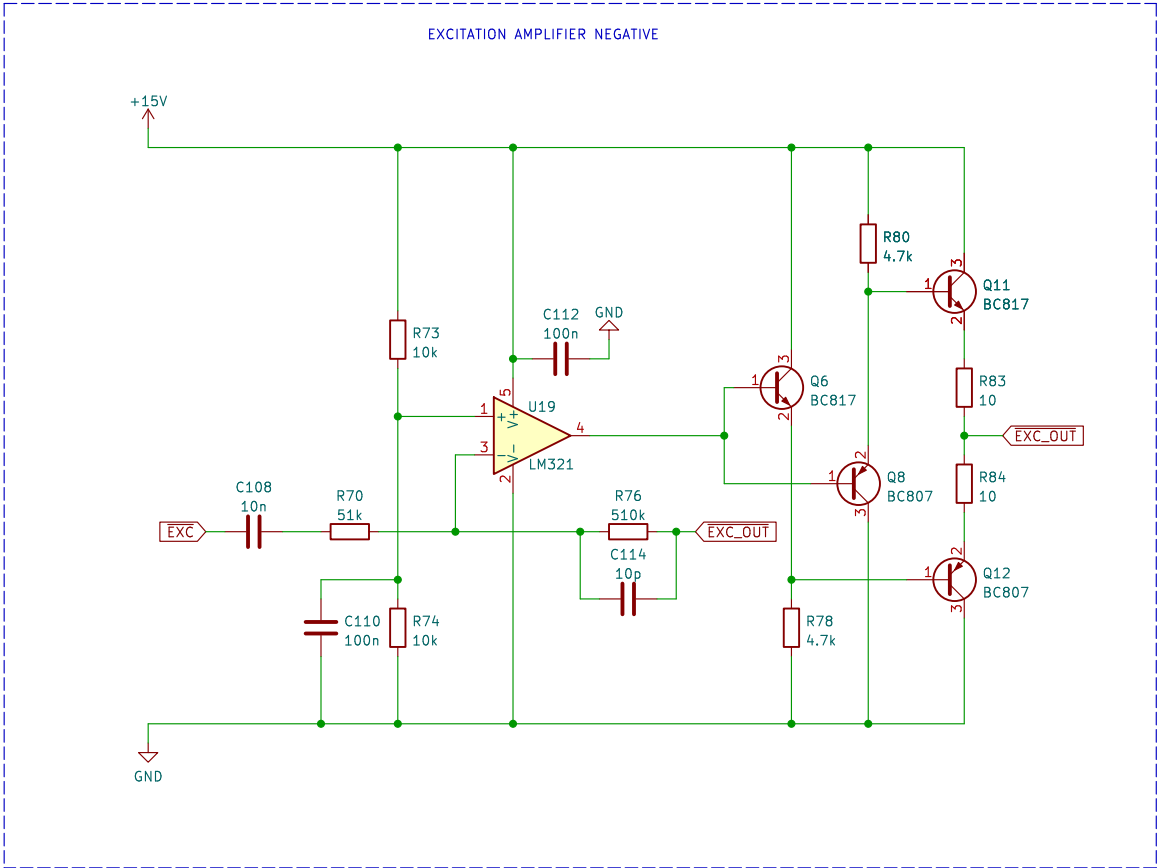
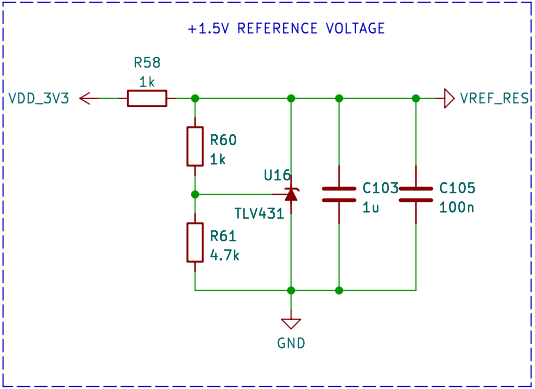
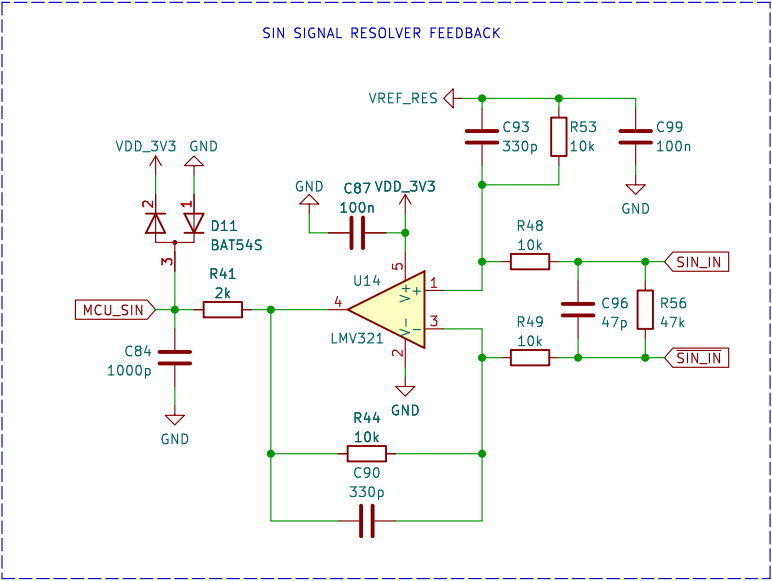
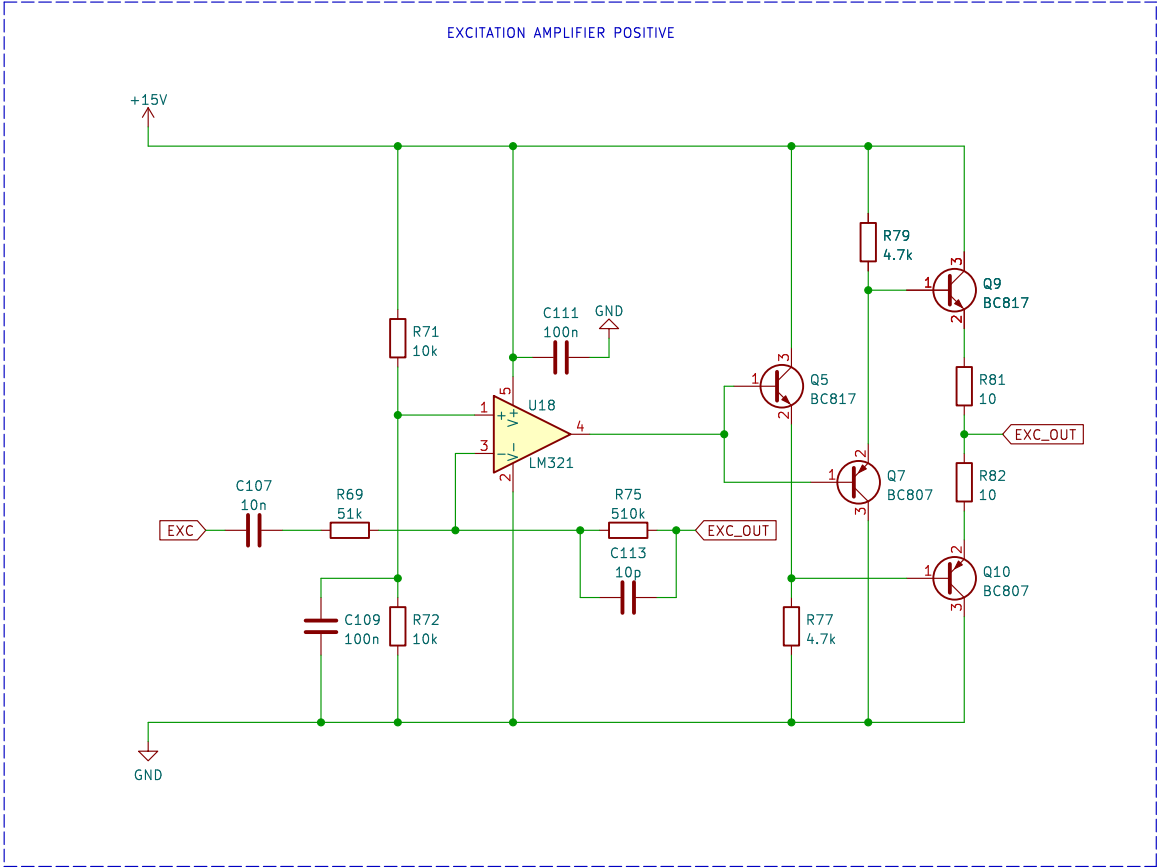
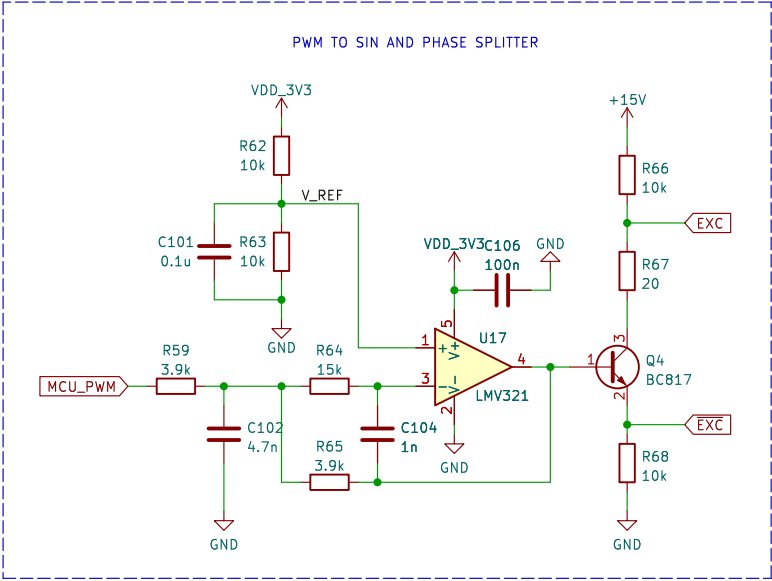
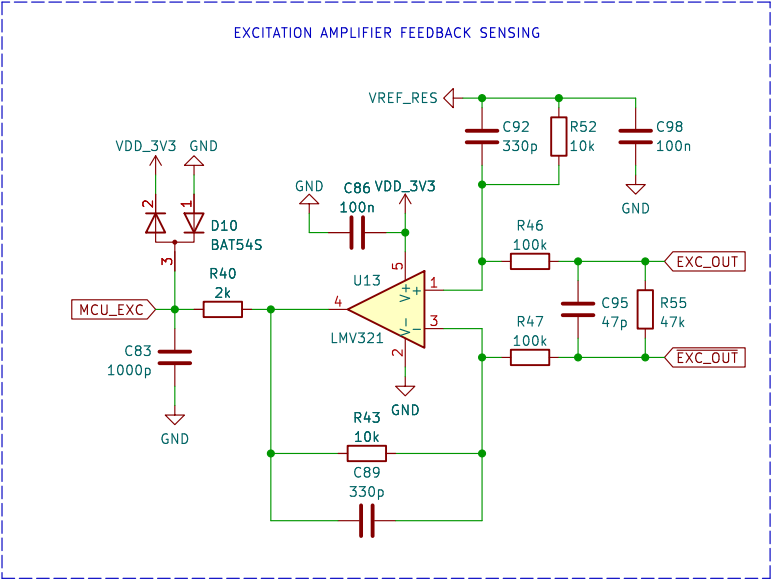
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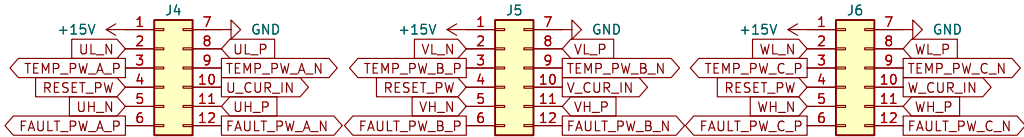
Копировал

Формат А4

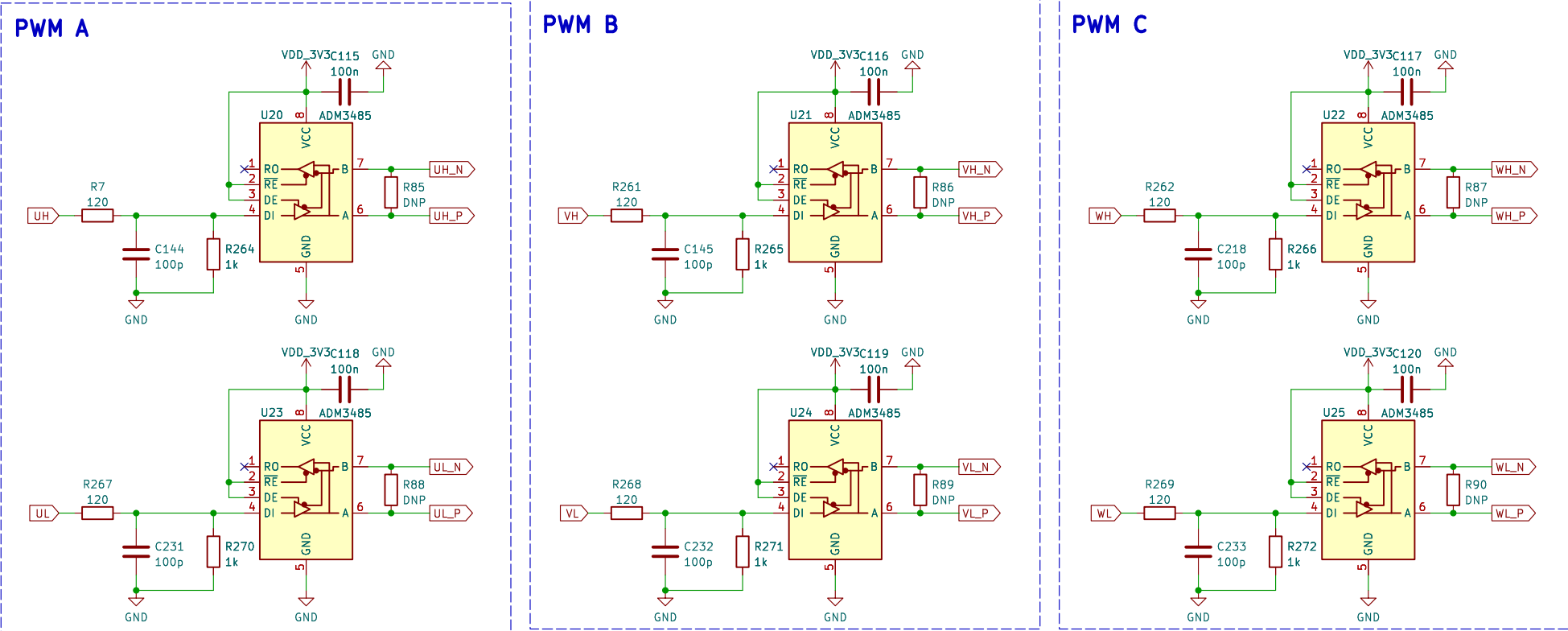
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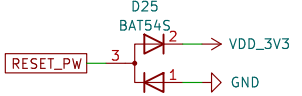




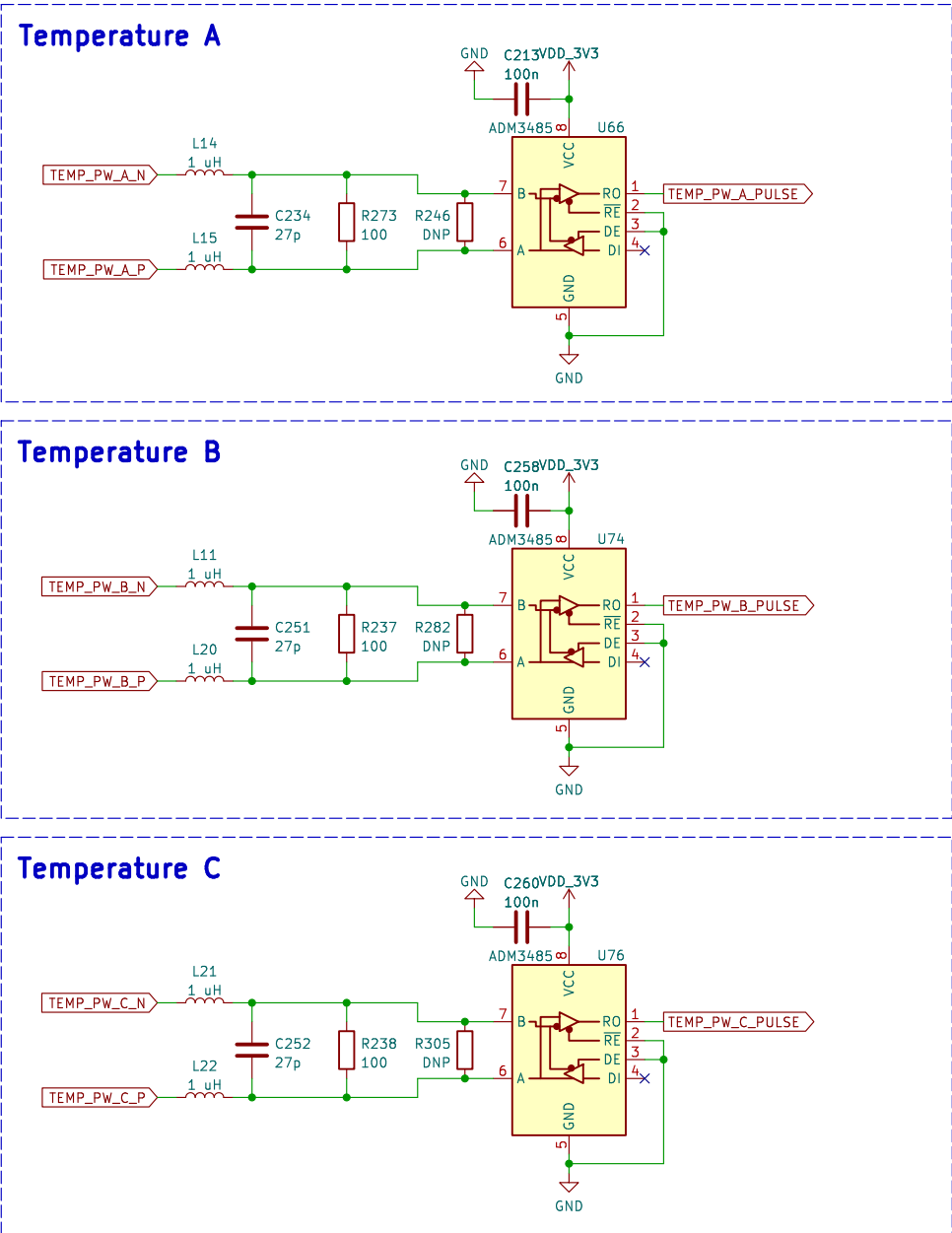
PWM Power Module



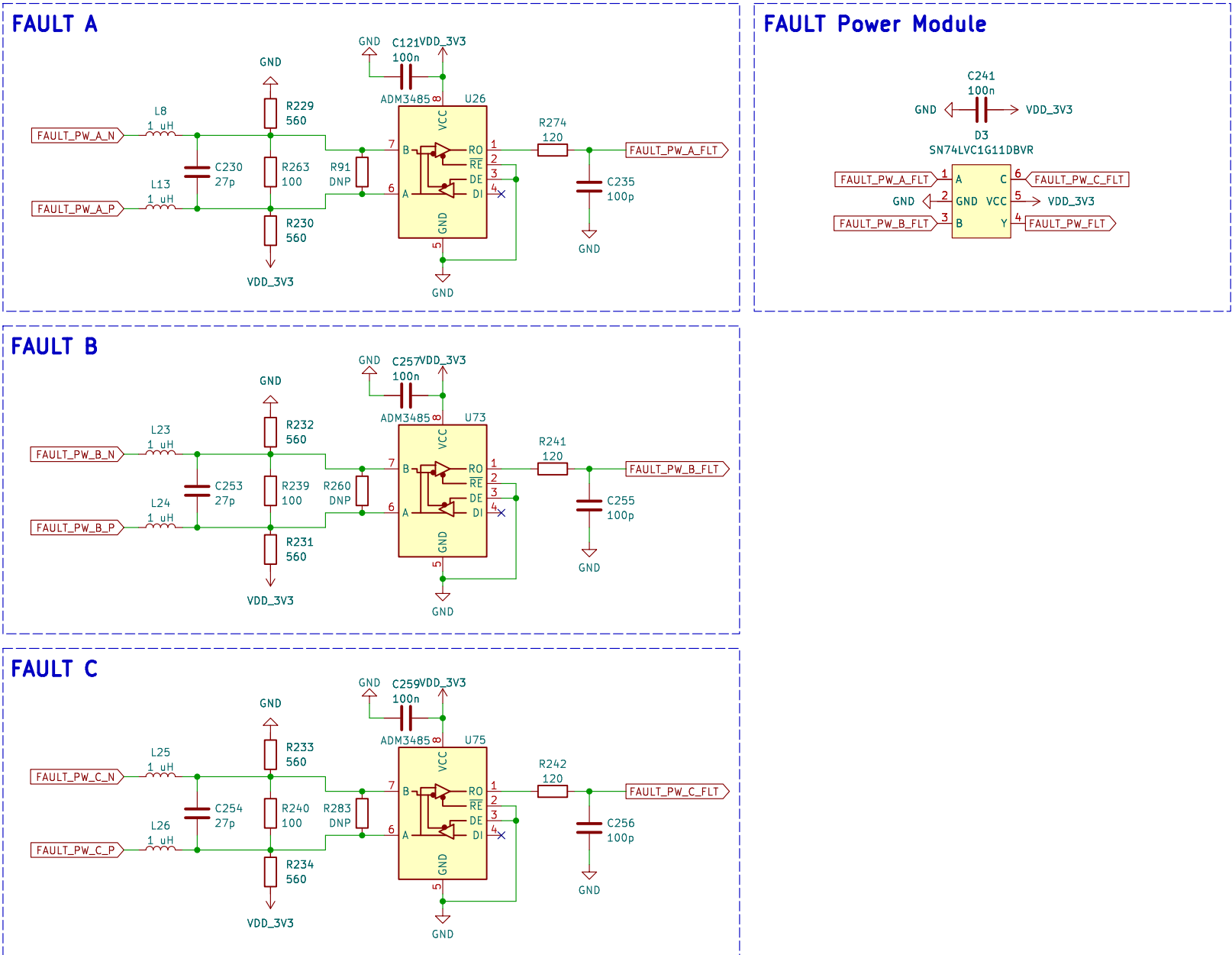
RESET Power Module



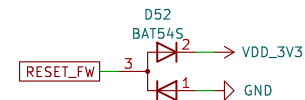
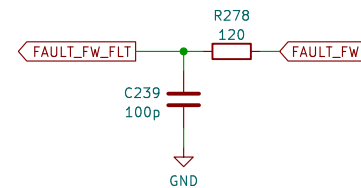
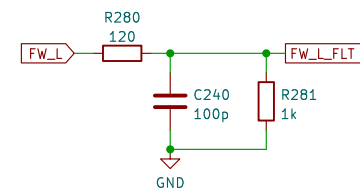
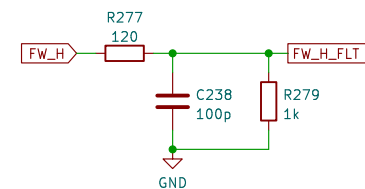
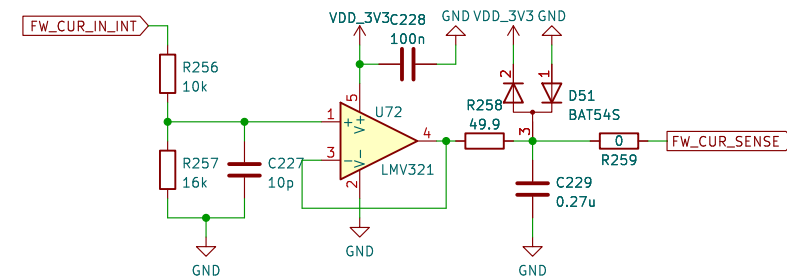
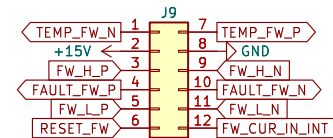
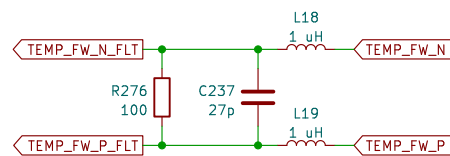
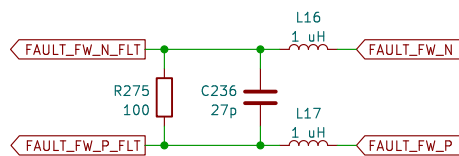
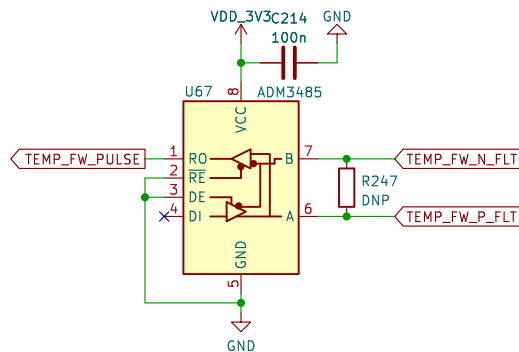
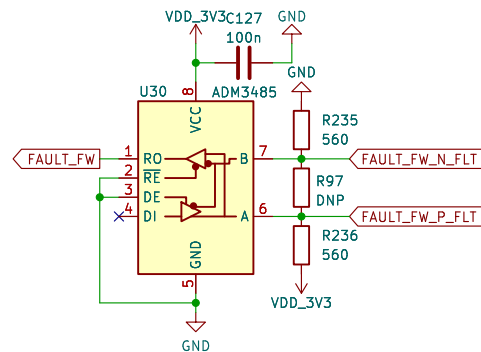
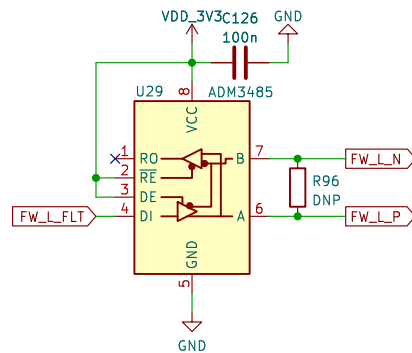
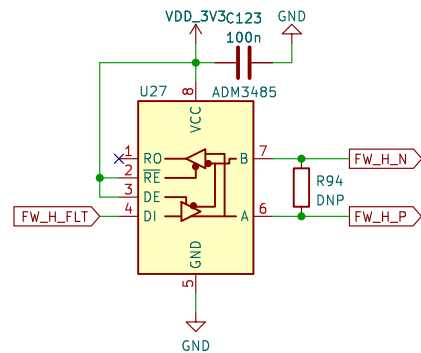
Temperature Power Module



FAULT Power Module



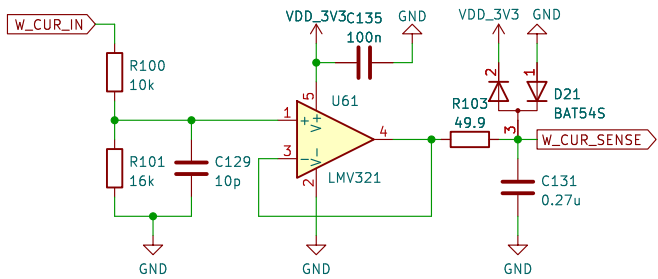
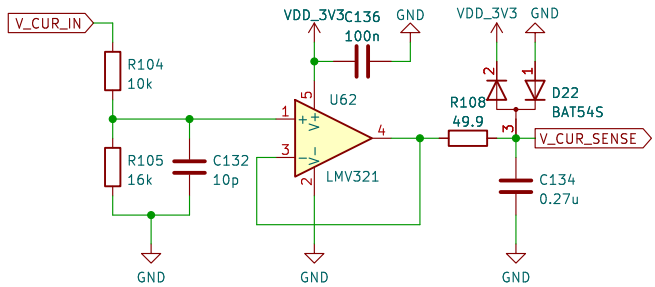
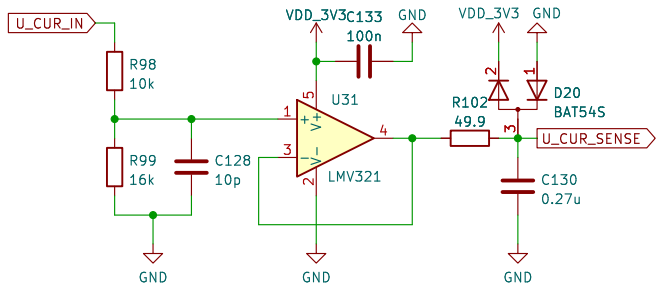
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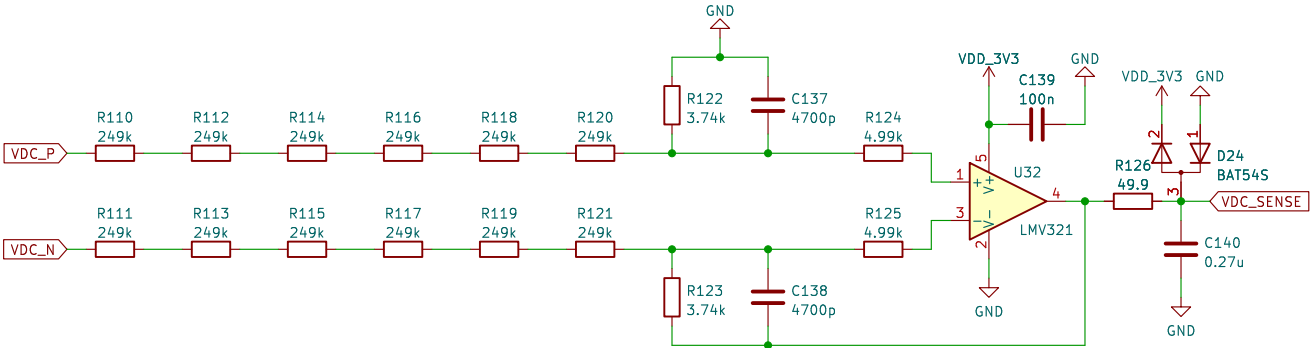
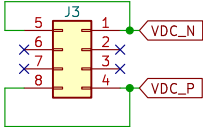
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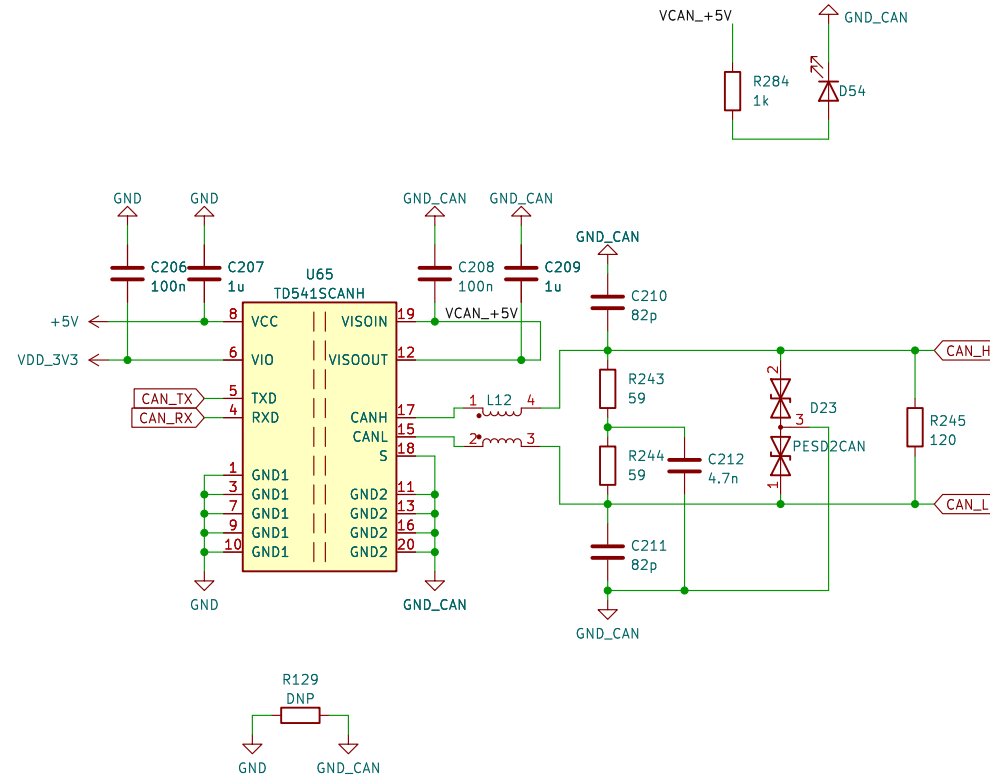


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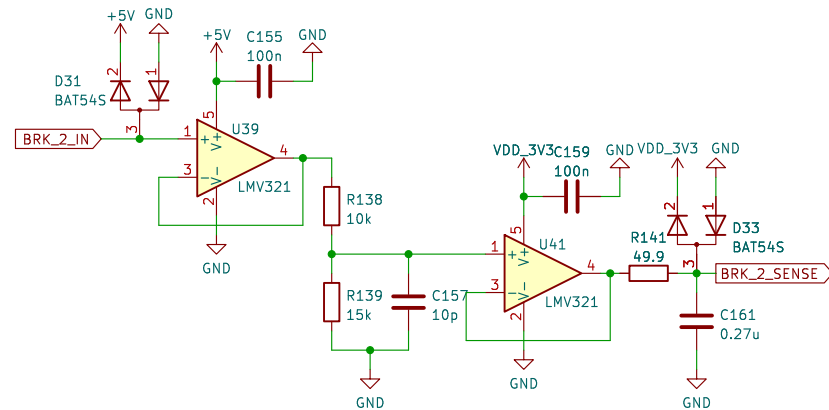
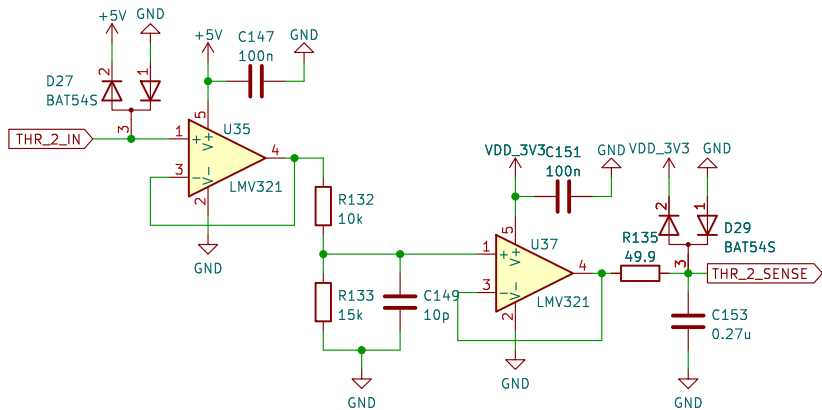
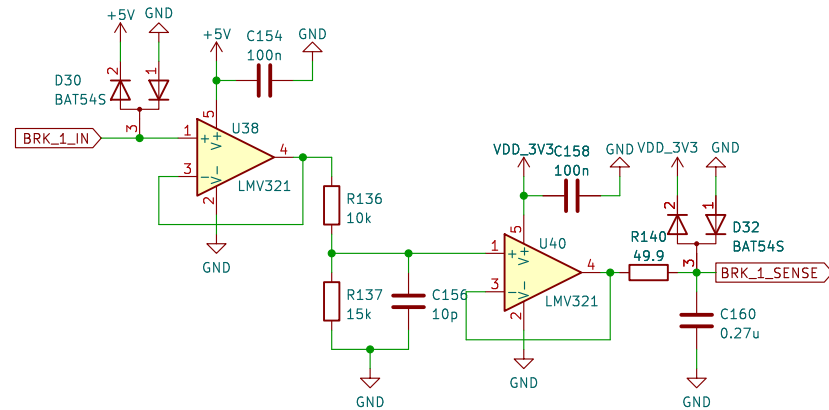
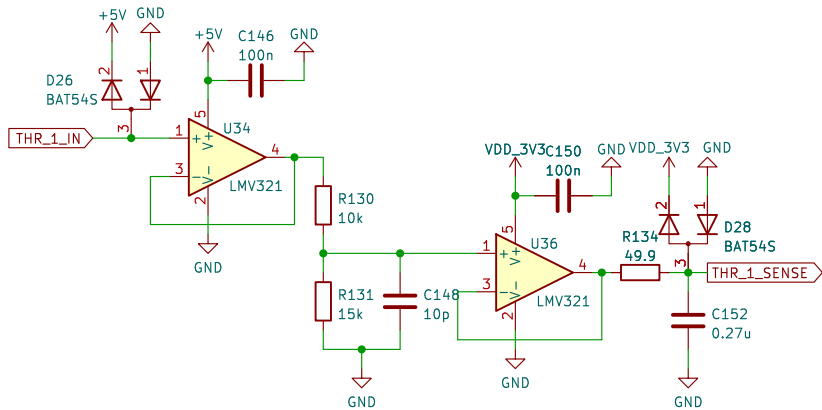
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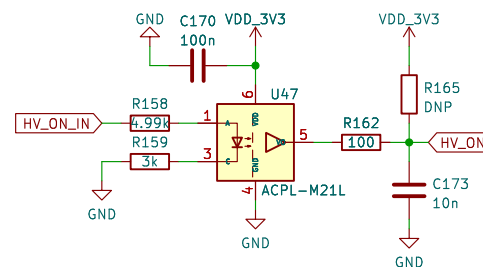
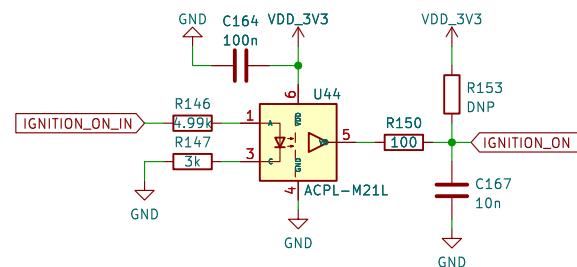
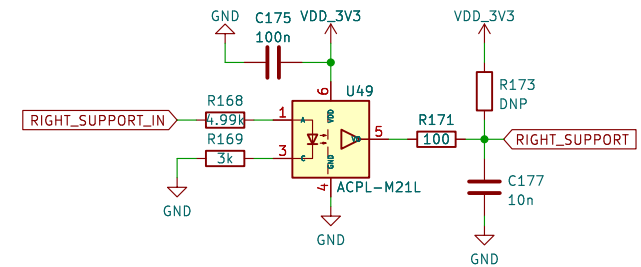
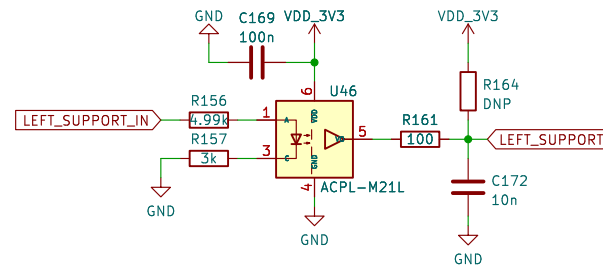
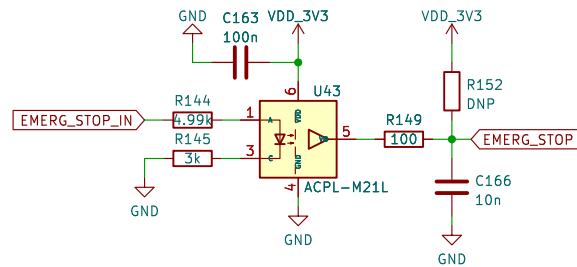
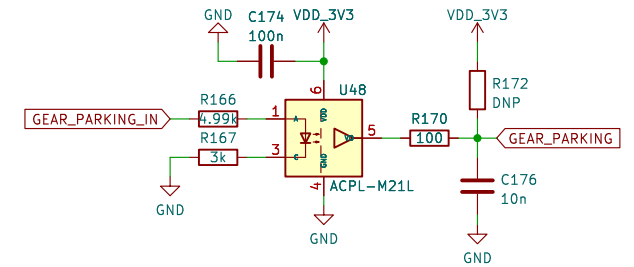
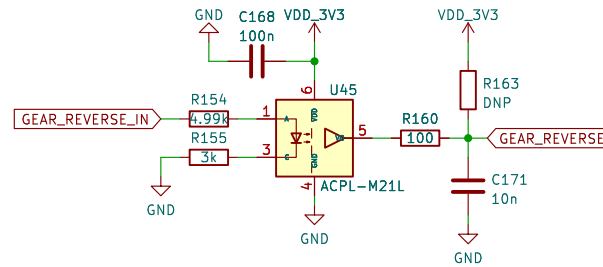
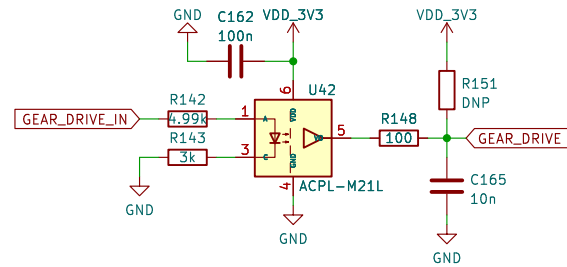
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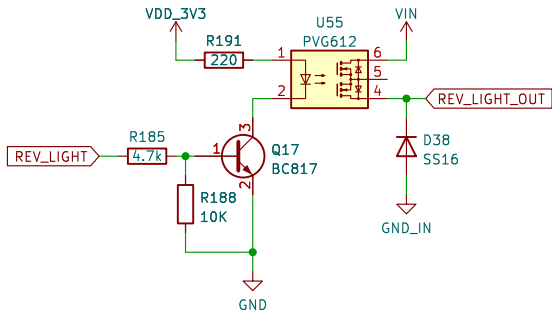
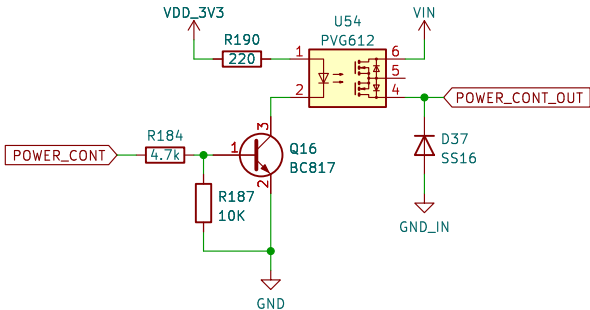
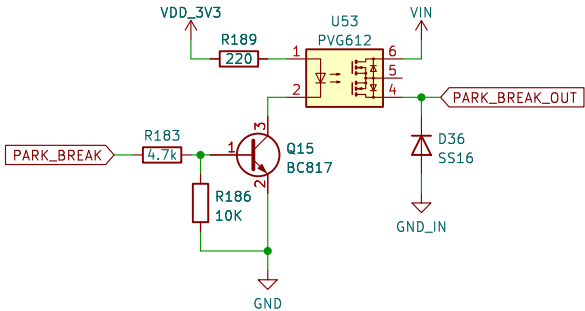
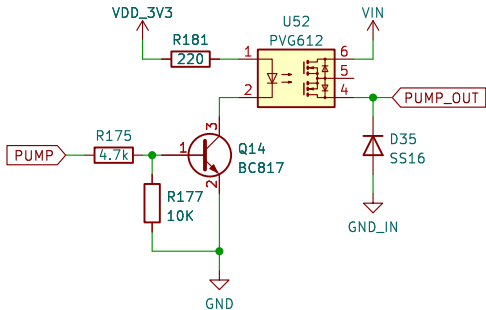
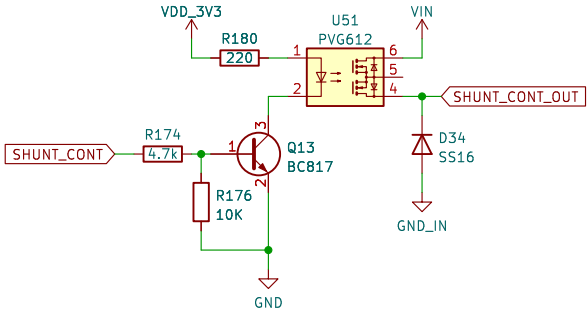
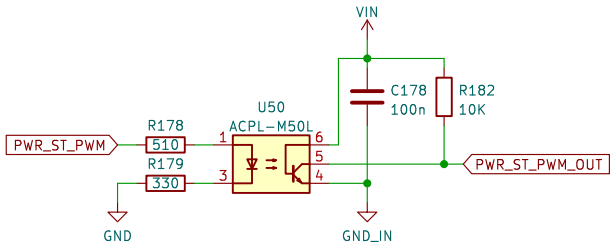


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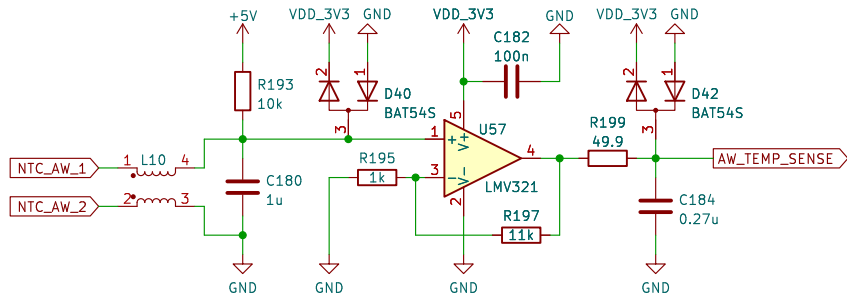
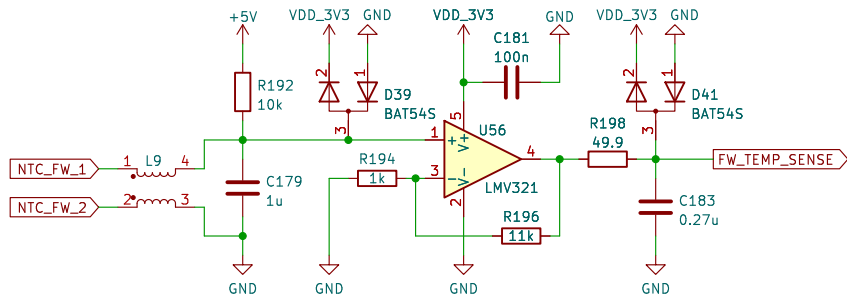


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Копировал

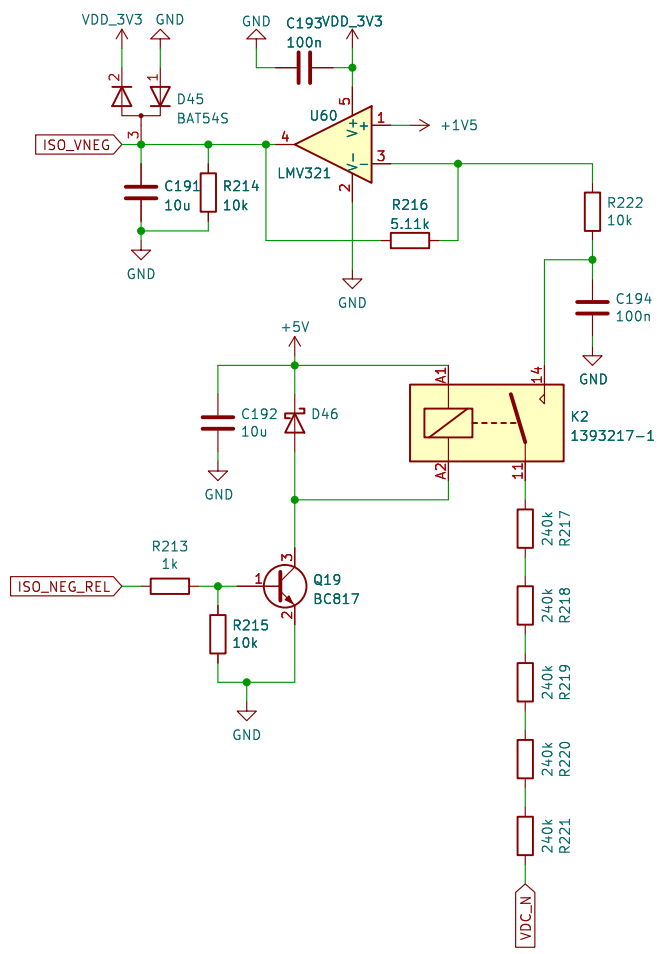
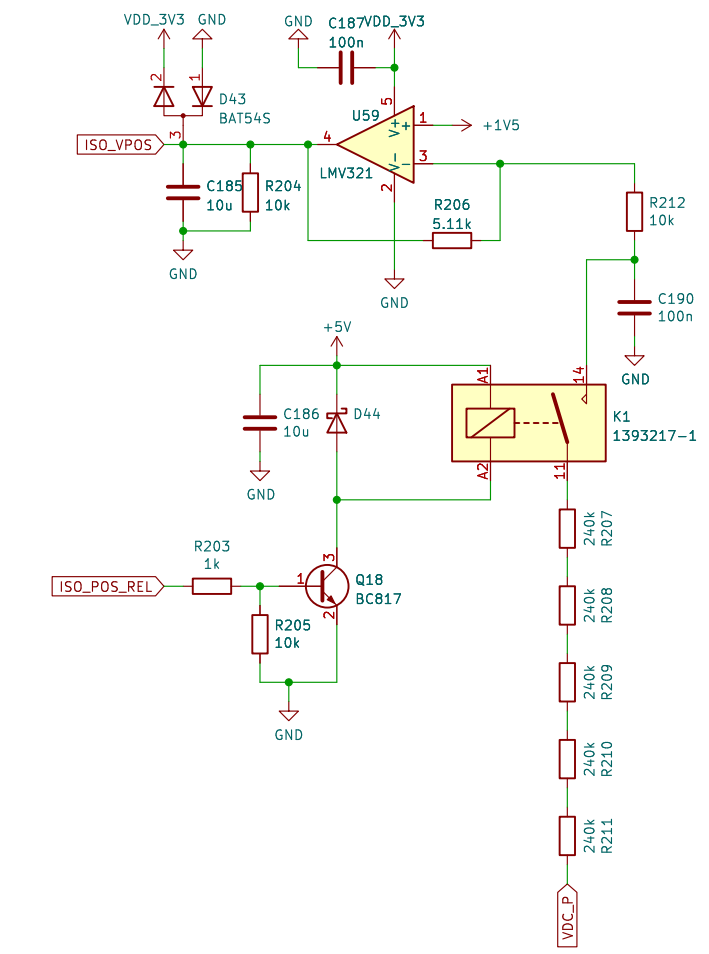
Формат А4

The top diagram shows a temperature sensor circuit. It features a 74HC123 (U33A) configured as a monostable multistable. The input is a pulse from TEMP_FW_PULSE. The output of the monostable is connected to the input of an LMV321 comparator (U70). The comparator's output is connected to a sense line TEMP_FW_SENSE. The circuit is powered by VDD_3V3 and includes various passive components like resistors (R248, R250, R252, R254) and capacitors (C215, C219, C221, C223, C225).

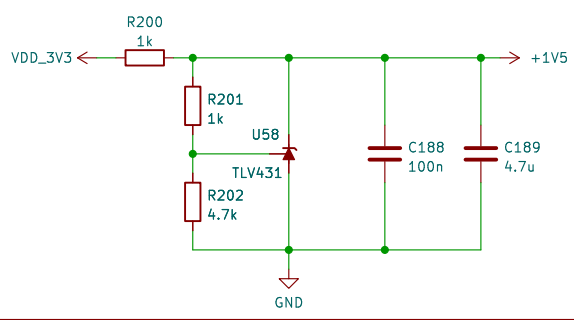
The middle diagram shows a similar temperature sensor circuit. It features a 74HC123 (U33B) configured as a monostable multistable. The input is a pulse from TEMP_PW_A_PULSE. The output of the monostable is connected to the input of an LMV321 comparator (U71). The comparator's output is connected to a sense line TEMP_PW_A_SENSE. The circuit is powered by VDD_3V3 and includes various passive components like resistors (R249, R251, R253, R255) and capacitors (C216, C220, C222, C224, C226).

The bottom diagram shows a simple circuit for a 74HC123 (U33C). The input is connected to VDD_3V3. The output is connected to GND. The circuit includes a capacitor (C217) and a resistor (R247).

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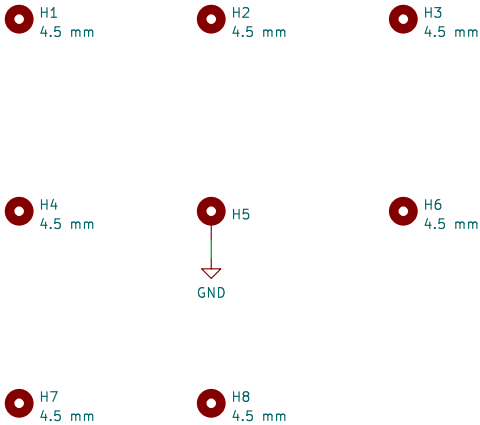
+1.5V Reference



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Mounting Holes



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