STM8 instruction set PM0044

JRA Jump Relative Always JRA

Syntax JRA dst e.g. JRA loop

Operation PC = PC + lgth

PC <= PC + dst, if Condition is True

Description Unconditional relative jump. PC is updated by the signed addition of PC

and dst. Control then passes to the statement addressed by the program

counter. Else, the program continues normally.

Instruction overview

mnom	dst	Affected condition flags						
mnem	ası	٧	I1	Н	10	N	z	С
JRA	Mem	-	-	-	-	-	-	-

Detailed description

dst	Asm	су	lgth	Op-code(s)			ST7	
shortoff	JRA \$2B	2	2	20	XX			X

See also: JP

PM0044 STM8 instruction set

JRxx Conditional Jump JRxx
Relative Instruction

Syntax JRxx dst e.g. JRxx loop

Operation PC = PC + lgth

PC <= PC + dst, if Condition is True

Description Conditional relative jump. PC is updated by the signed addition of PC and

dst, if the condition is true. Control, then passes to the statement

addressed by the program counter. Else, the program continues normally.

Instruction overview

mnom	dst	Affected condition flags						
mnem	ust	V	I1	Н	10	N	Z	С
JRxx	Mem	-	-	-	-	-	-	-

Instruction List

mnem
JRC
JREQ
JRF
JRH
JRIH
JRIL
JRM
JRMI
JRNC
JRNE
JRNH
JRNM
JRNV
JRPL
JRSGE
JRSGT
JRSLE
JRSLT
JRT
JRUGE
JRUGT
JRULE
JRC
JRULT

meaning	sym
Carry	
Equal	=
False	
Half-Carry	
Interrupt Line is High	
Interrupt Line is Low	
Interrupt Mask	
Minus	< 0
Not Carry	
Not Equal	<> 0
Not Half-Carry	
Not Interrupt Mask	
Not Interrupt Mask Not Overflow	
	>= 0
Not Overflow	>= 0 >=
Not Overflow Plus	
Not Overflow Plus Signed Greater or Equal	>=
Not Overflow Plus Signed Greater or Equal Signed Greater Than	>= >
Not Overflow Plus Signed Greater or Equal Signed Greater Than Signed Lower or Equal	>= > <=
Not Overflow Plus Signed Greater or Equal Signed Greater Than Signed Lower or Equal Signed Lower Than	>= > <=
Not Overflow Plus Signed Greater or Equal Signed Greater Than Signed Lower or Equal Signed Lower Than True	>= > <=
Not Overflow Plus Signed Greater or Equal Signed Greater Than Signed Lower or Equal Signed Lower Than True Unsigned Greater or Equal	>= > <= <
Not Overflow Plus Signed Greater or Equal Signed Greater Than Signed Lower or Equal Signed Lower Than True Unsigned Greater or Equal Unsigned Greater Than	>= > > <= < > > > > > > > > > > > > > >
Not Overflow Plus Signed Greater or Equal Signed Greater Than Signed Lower or Equal Signed Lower Than True Unsigned Greater or Equal Unsigned Greater Than Unsigned Lower or Equal	>= > > <= < > > > > > > > > > > > > > >

Condition
C = 1
Z = 1
False
H = 1
l = 1
N = 1
C = 0
Z = 0
H = 0
l = 0
V = 0
N = 0
(N XOR V) = 0
(Z OR (N XOR V)) = 0
(Z OR (N XOR V)) = 1
(N XOR V) = 1
True
C = 0
C = 0 and Z = 0
C = 1 or Z = 1
C = 1
C = 1
V = 1

25 27 27 21 90 29 90 2F 90 2E 90 2D 2B 24 26 90 28 90 2C 28 2A 2A 2E 2C 2D 2F 20 24 22 23 25 25 29	Op-cod	de (OC)
21 90 29 90 2F 90 2E 90 2D 2B 24 26 90 28 90 2C 28 2A 2A 2E 2C 2D 2F 20 24 22 23 25 25		25
90 29 90 2F 90 2E 90 2D 2B 24 26 90 28 90 2C 28 20 2A 2E 20 2D 21 22 23 25 25		27
90 2F 90 2E 90 2D 2B 24 26 90 28 90 2C 28 20 22 22 20 24 22 23 25 25		21
90 2E 90 2D 2B 24 26 90 28 90 2C 28 20 20 21 22 20 24 22 23 25 25	90	29
90 2D 2B 24 26 90 28 90 2C 28 20 2D	90	2F
2B 24 26 90 28 90 2C 28 2A 2A 2E 2C 2D 2D 2F 20 24 22 23 25 25	90	2E
24 26 90 28 90 2C 28 2A 2A 2E 2C 2D 2P 22F 20 24 22 23 25 25	90	2D
26 90 28 90 2C 28 2A 2A 2E 2C 2D 2F 20 24 22 23 25 25		2B
90 28 90 2C 28 2A 2E 2C 2D 2F 20 24 22 23 25 25		24
90 2C 28 2A 2E 2C 2D 2F 20 24 22 23 25 25		26
28 2A 2E 2C 2D 2F 20 24 22 23 25 25	90	28
2A 2E 2C 2D 2F 20 24 22 23 25 25	90	2C
2E 2C 2D 2F 20 24 22 23 25 25		28
2C 2D 2F 20 24 22 23 25 25		2A
2D 2F 20 24 22 23 25 25		
2F 20 24 22 23 25 25		2C
20 24 22 23 25 25		2D
24 22 23 25 25		2F
22 23 25 25		20
23 25 25		24
25 25		22
25		23
		25
29		25
		29

Detailed description

dst	Asm
shortoff	JRxx \$15
shortoff	JRxx \$15

су	lgth
1/2	2
1/2	3

	ST7			
	Op-code	XX		×
90	Op-code	XX		X