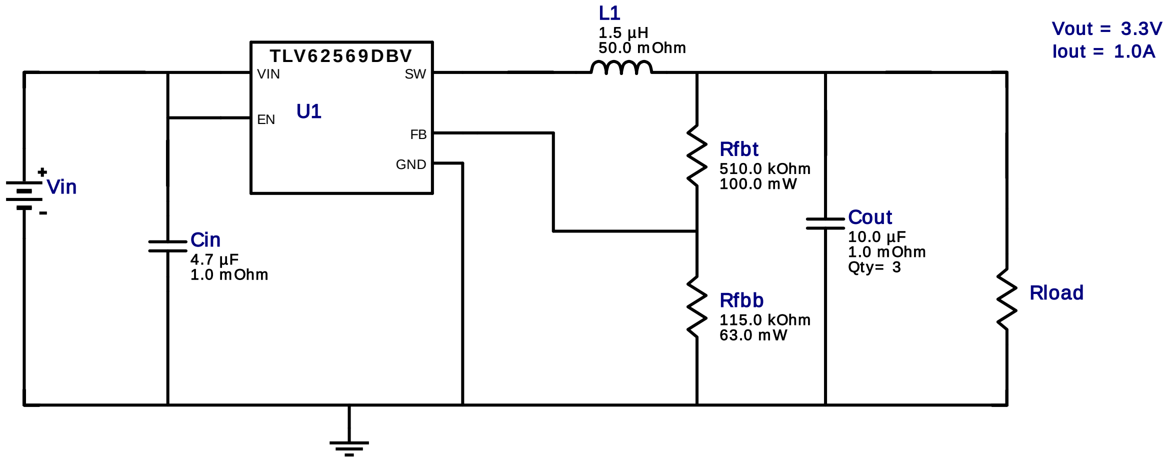
VinMin = 3.5V
VinMax = 4.2V
Vout = 3.3V
Iout = 1.0A

Device = TLV62569DBVR
Topology = Buck
Created = 2022-02-06 08:15:09.066
BOM Cost = $0.33
BOM Count = 8
Total Pd = 0.22W

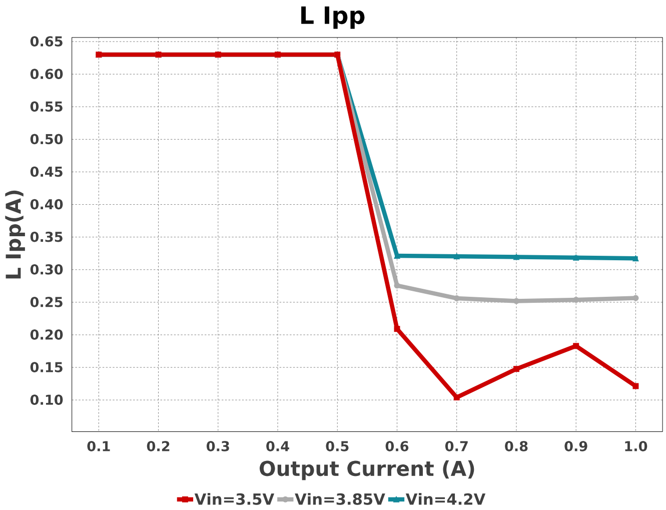
WEBENCH ® Design Report

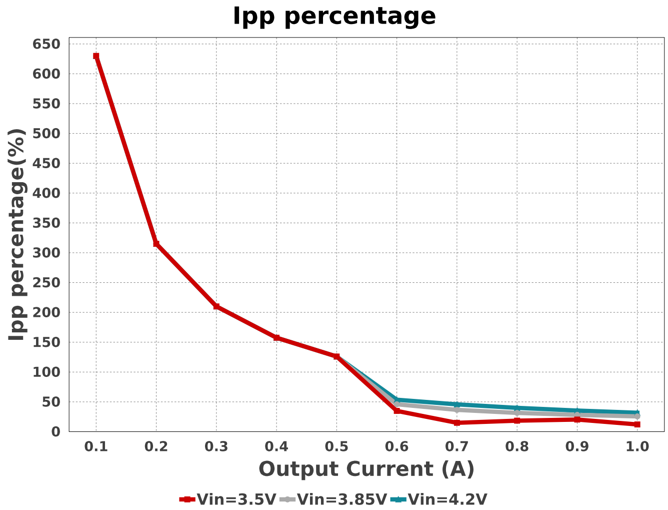
Design : 10 TLV62569DBVR
TLV62569DBVR 3.5V-4.2V to 3.30V @ 1A

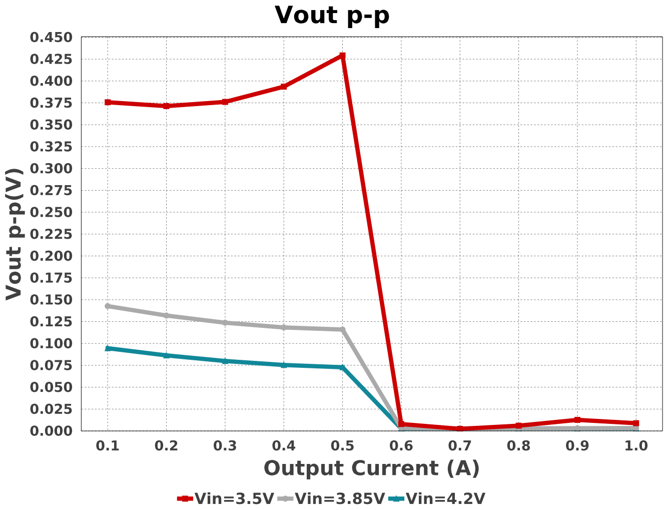


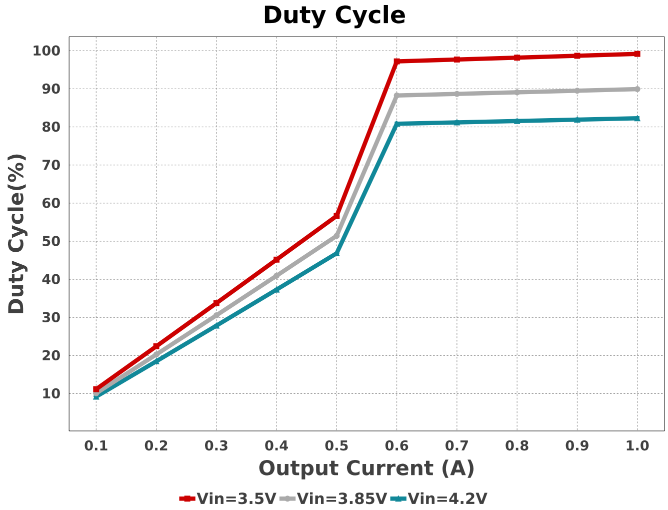
Electrical BOM

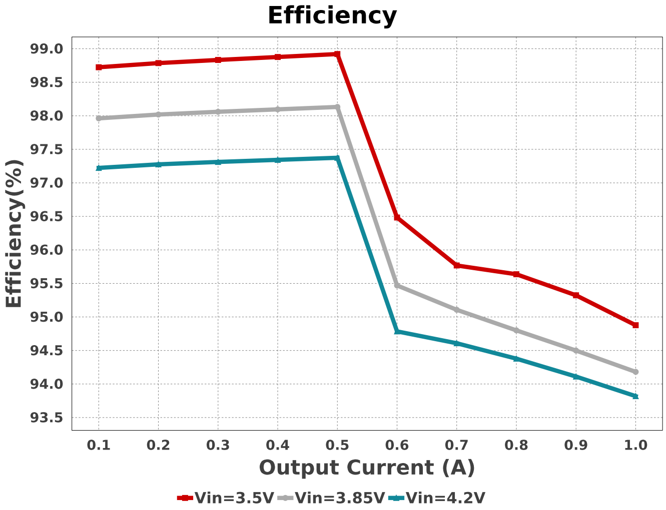
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cin	MuRata	GRM155R61A475MEAAD Series= X5R	Cap= 4.7 uF ESR= 1.0 mOhm VDC= 10.0 V IRMS= 0.0 A	1	$0.03	0402_065 3 mm^2
Cout	MuRata	GRJ155R60J106ME11D Series= X5R	Cap= 10.0 uF ESR= 1.0 mOhm VDC= 6.3 V IRMS= 0.0 A	3	$0.02	0402_070 3 mm^2
L1	CUSTOM	CUSTOM	L= 1.5 µH 50.0 mOhm	1	$0.05	ASPI-0530HI 9 mm^2
Rfbb	Vishay-Dale	CRCW0402115KFKED Series= CRCW..e3	Res= 115.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm^2
Rfbt	Yageo	RC0603FR-07510KL Series= ?	Res= 510.0 kOhm Power= 100.0 mW Tolerance= 1.0%	1	$0.01	0603 5 mm^2
U1	Texas Instruments	TLV62569DBVR	Switcher	1	$0.16	DBV0005A 15 mm^2

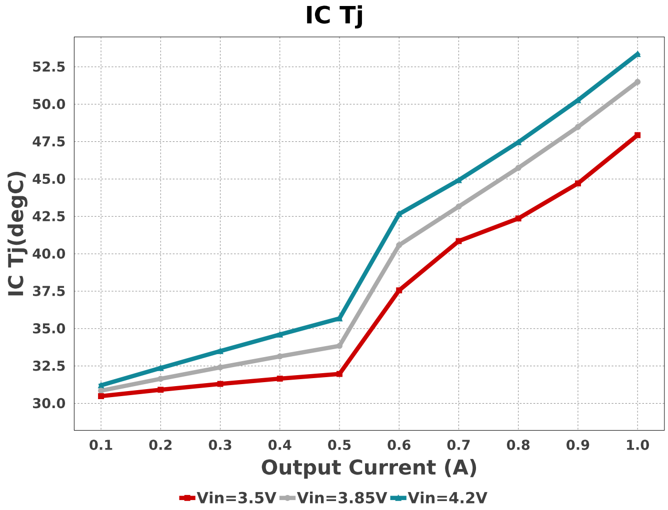

L Ipp
L Ipp(A)
Output Current (A)
Vin=3.5V Vin=3.85V Vin=4.2V

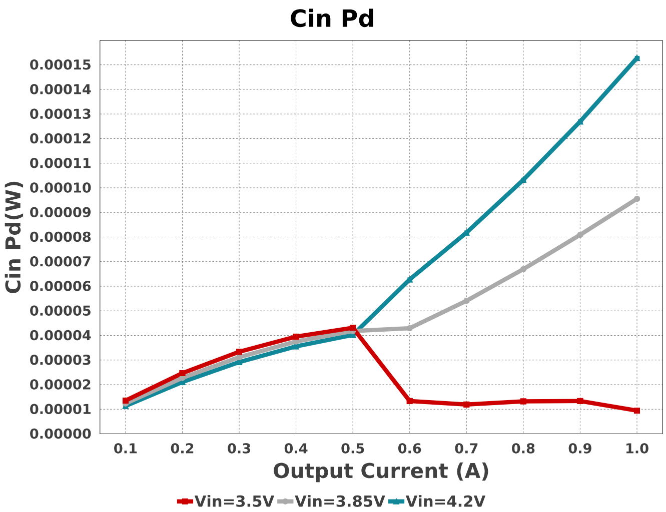

Ipp percentage
Ipp percentage(%)
Output Current (A)
Vin=3.5V Vin=3.85V Vin=4.2V

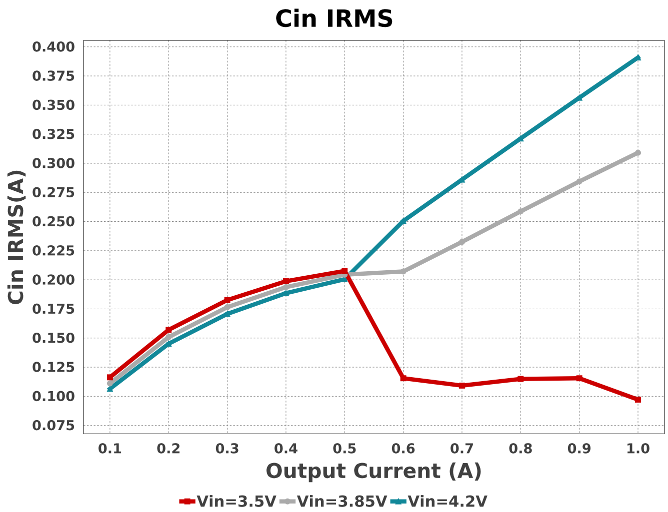

Vout p-p
Vout p-p(V)
Output Current (A)
Vin=3.5V Vin=3.85V Vin=4.2V

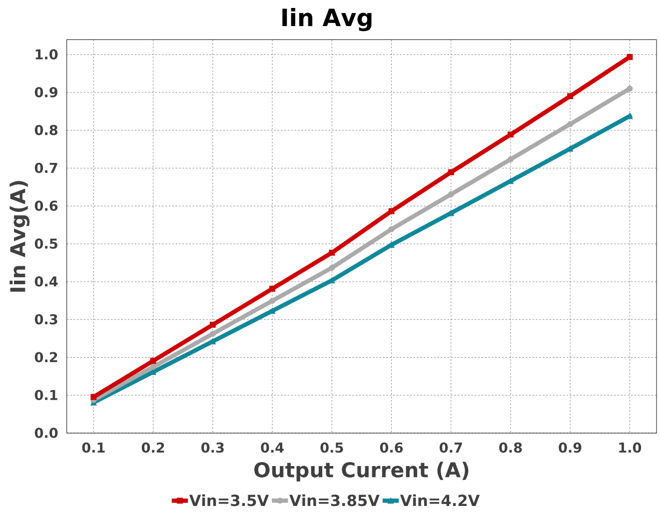

Duty Cycle
Duty Cycle(%)
Output Current (A)
Vin=3.5V Vin=3.85V Vin=4.2V

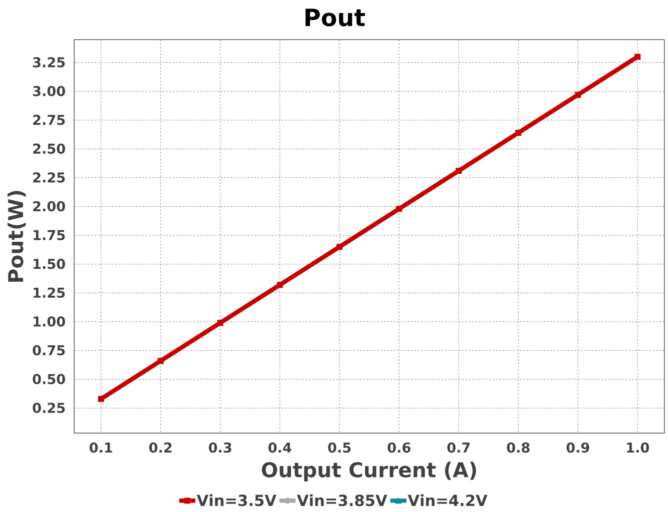

Efficiency
Efficiency(%)
Output Current (A)
Vin=3.5V Vin=3.85V Vin=4.2V

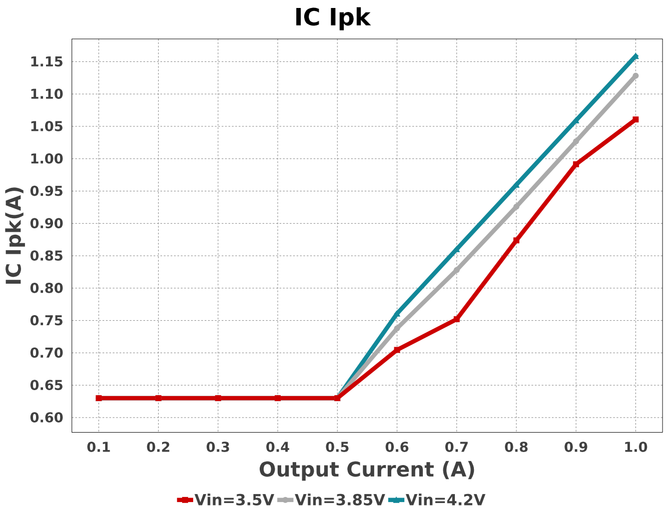

IC Tj
IC Tj(degC)
Output Current (A)
Vin=3.5V Vin=3.85V Vin=4.2V

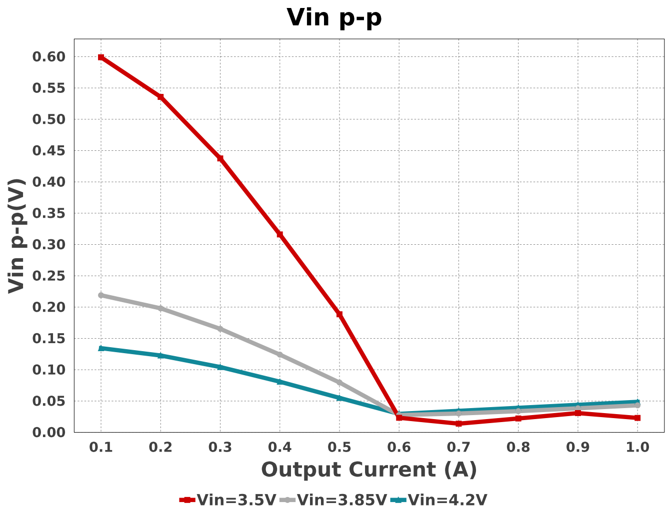

Cin Pd
Cin Pd(W)
Output Current (A)
Vin=3.5V
Vin=3.85V
Vin=4.2V

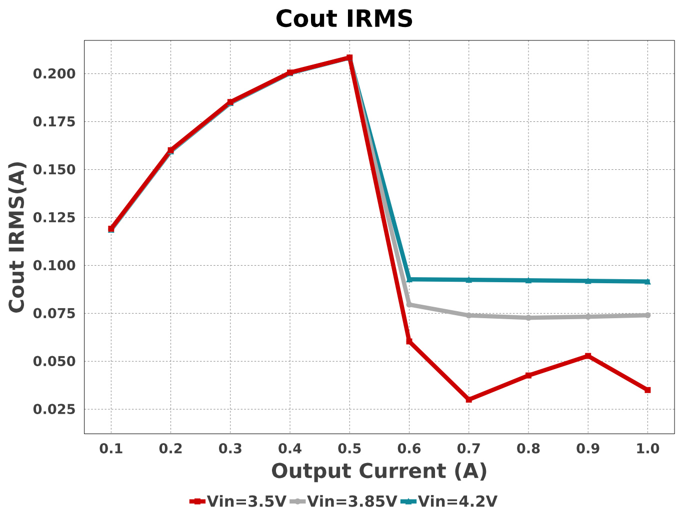

Cin IRMS
Cin IRMS(A)
Output Current (A)
Vin=3.5V
Vin=3.85V
Vin=4.2V

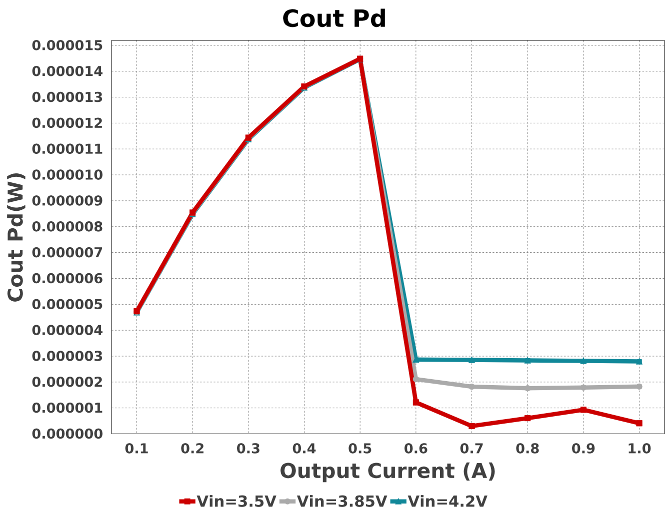

Iin Avg
Iin Avg(A)
Output Current (A)
Vin=3.5V
Vin=3.85V
Vin=4.2V

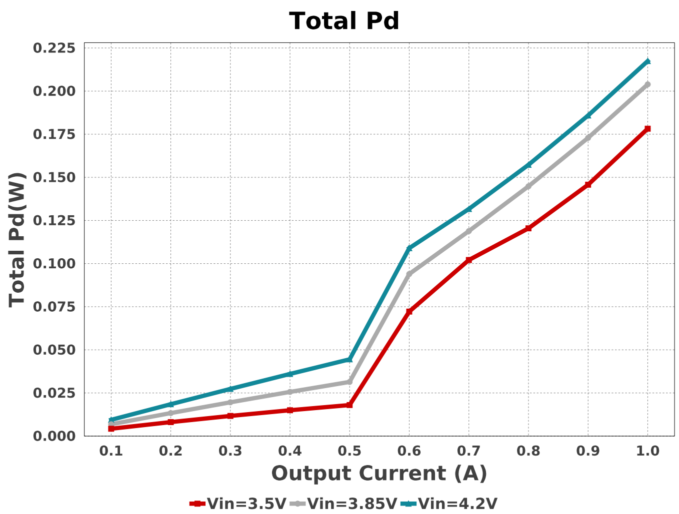

Pout
Pout(W)
Output Current (A)
Vin=3.5V
Vin=3.85V
Vin=4.2V

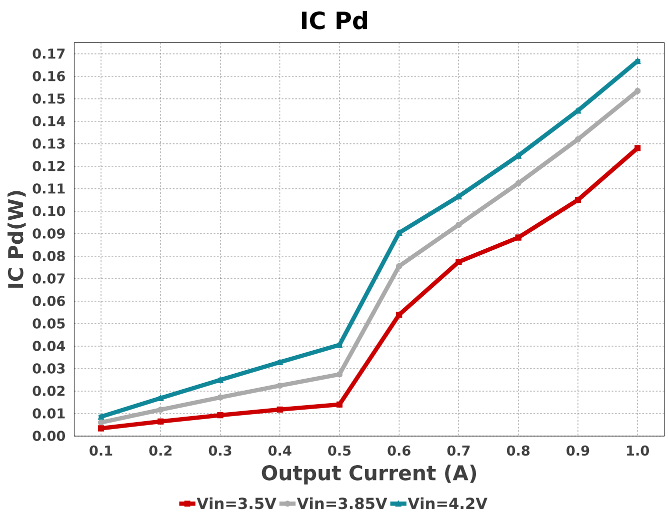

IC Ipk
IC Ipk(A)
Output Current (A)
Vin=3.5V
Vin=3.85V
Vin=4.2V

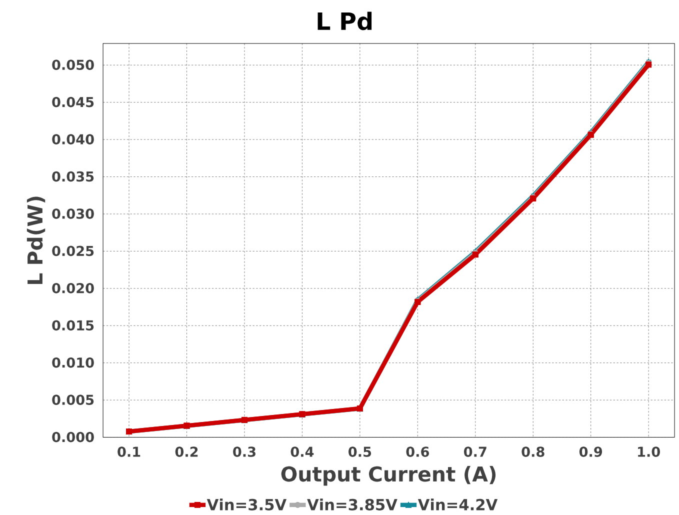

Vin p-p
Vin p-p(V)
Output Current (A)
Vin=3.5V
Vin=3.85V
Vin=4.2V

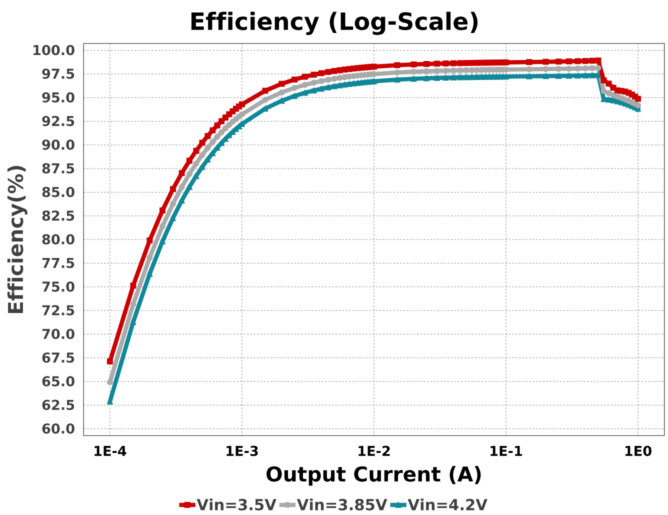












Operating Values

#	Name	Value	Category	Description
1.	BOM Count	8		Total Design BOM count
2.	Total BOM	$0.325		Total BOM Cost
3.	Cin IRMS	390.834 mA	Capacitor	Input capacitor RMS ripple current
4.	Cin Pd	152.75 µW	Capacitor	Input capacitor power dissipation
5.	Cout IRMS	91.6 mA	Capacitor	Output capacitor RMS ripple current
6.	Cout Pd	2.797 µW	Capacitor	Output capacitor power dissipation
7.	IC Ipk	1.159 A	IC	Peak switch current in IC
8.	IC Pd	166.82 mW	IC	IC power dissipation
9.	IC Tj	53.355 degC	IC	IC junction temperature
10.	IC Tolerance	20.0 mV	IC	IC Feedback Tolerance
11.	ICThetaJA	140.0 degC/W	IC	IC junction-to-ambient thermal resistance

#	Name	Value	Category	Description
12.	Iin Avg	837.48 mA	IC	Average input current
13.	Ipp percentage	31.731 %	Inductor	Inductor ripple current percentage (with respect to average inductor current)
14.	L Ipp	317.312 mA	Inductor	Peak-to-peak inductor ripple current
15.	L Pd	50.42 mW	Inductor	Inductor power dissipation
16.	Cin Pd	152.75 µW	Power	Input capacitor power dissipation
17.	Cout Pd	2.797 µW	Power	Output capacitor power dissipation
18.	IC Pd	166.82 mW	Power	IC power dissipation
19.	L Pd	50.42 mW	Power	Inductor power dissipation
20.	Total Pd	217.411 mW	Power	Total Power Dissipation
21.	Duty Cycle	82.273 %	System Information	Duty cycle
22.	Efficiency	93.819 %	System Information	Steady state efficiency
23.	FootPrint	89.0 mm^2	System Information	Total Foot Print Area of BOM components
24.	Frequency	1.277 MHz	System Information	Switching frequency
25.	Iout	1.0 A	System Information	Iout operating point
26.	Mode	CCM	System Information	Conduction Mode
27.	Pout	3.3 W	System Information	Total output power
28.	Vin	4.2 V	System Information	Vin operating point
29.	Vin p-p	48.732 mV	System Information	Peak-to-peak input voltage
30.	Vout	3.3 V	System Information	Operational Output Voltage
31.	Vout Actual	3.261 V	System Information	Vout Actual calculated based on selected voltage divider resistors
32.	Vout Tolerance	5.037 %	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
33.	Vout p-p	2.824 mV	System Information	Peak-to-peak output ripple voltage

Design Inputs

Name	Value	Description
Iout	1.0	Maximum Output Current
VinMax	4.2	Maximum input voltage
VinMin	3.5	Minimum input voltage
Vout	3.3	Output Voltage
base_pn	TLV62569	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

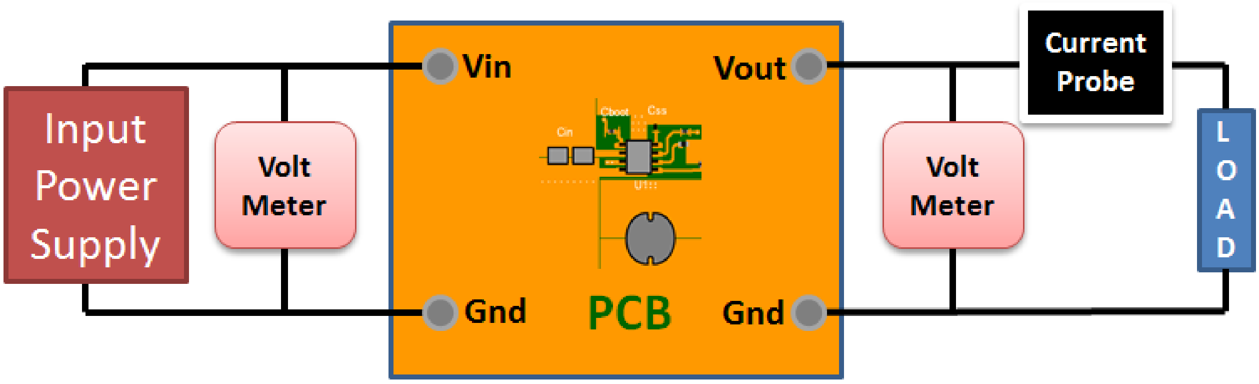
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 3.5V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum Iout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.

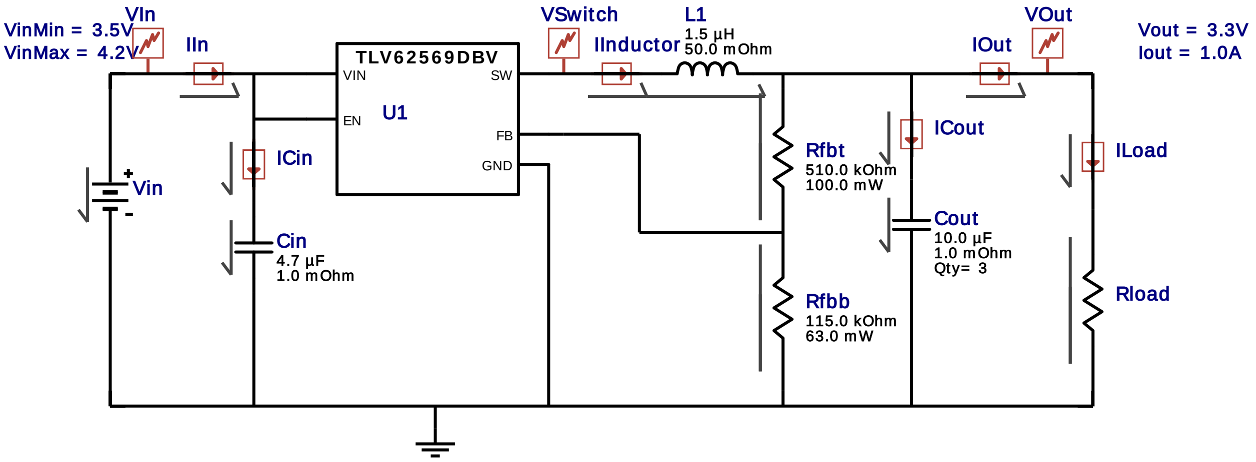


WEBENCH® Electrical Simulation Report

Design Id = 10

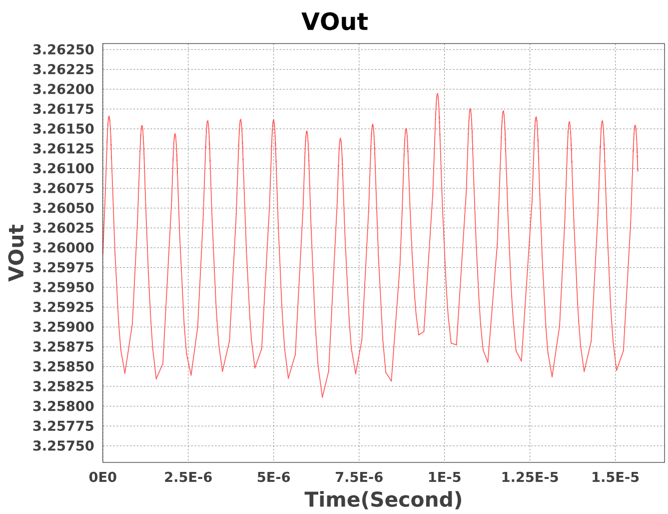
sim_id = 1

Simulation Type = Steady State



Simulation Parameters

#	Name	Parameter Name	Description	Values
1.	Rload	R	no description	3.3



Design Assistance

1. Feature Highlights: Constant Average Current, up to 2A, 2.5 V to 5.5 V Input Voltage Range, Integrated Thermal ProtectionIdeal for portable applications

2. Master key : 80A8A632ED49114A[v1]

3. **TLV62569** Product Folder : http://www.ti.com/product/TLV62569 : contains the data sheet and other resources.

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