A7-LITE Block Diagram

REV	DATE	PAGES	DESCRIPTION
1.0	1/30/2019	All	Rev 1.0 Release
1.1	5/30/2019	All	Rev 1.1 Release

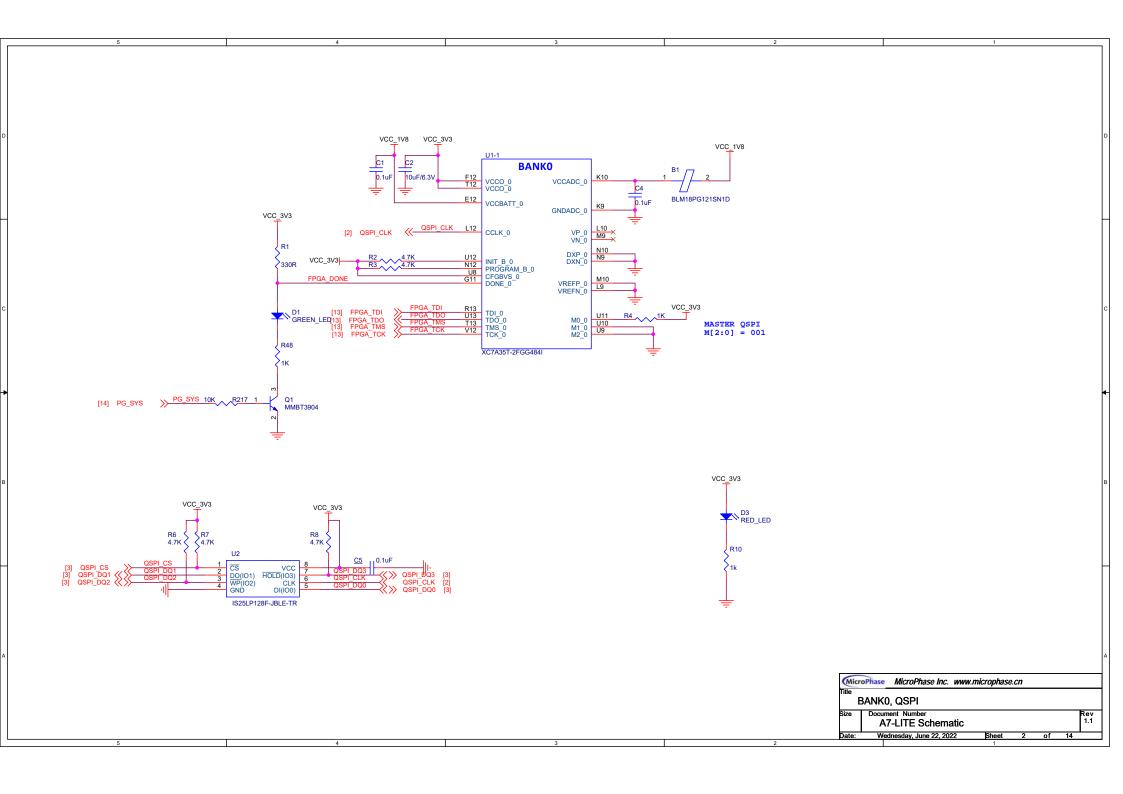
PAGE	DESCRIPTION
1	Title, Notes, Block Diagram, Revision History
2	BANKO, QSPI
3	BANK14
4	BANK15, BANK16
5	BANK34, BANK35
6	BANK216
7	FPGA Power, GND
8	DDR3 RAM
9	KEY LED USB-UART SD
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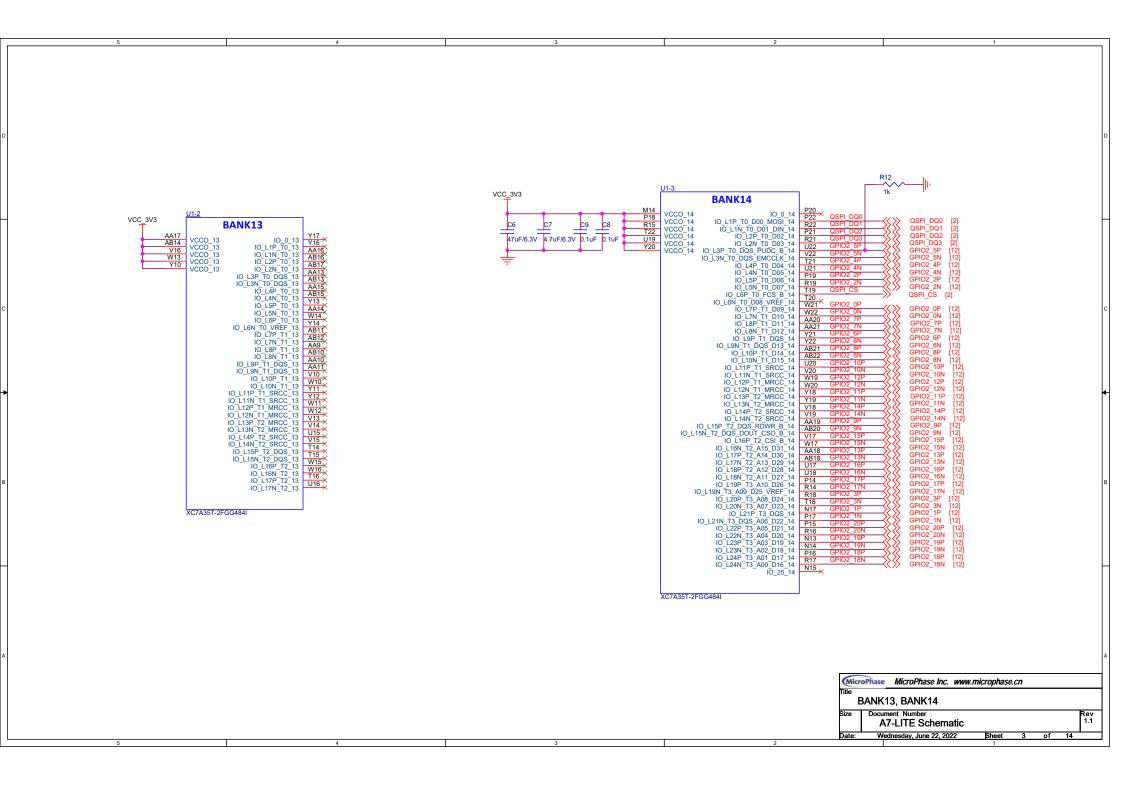
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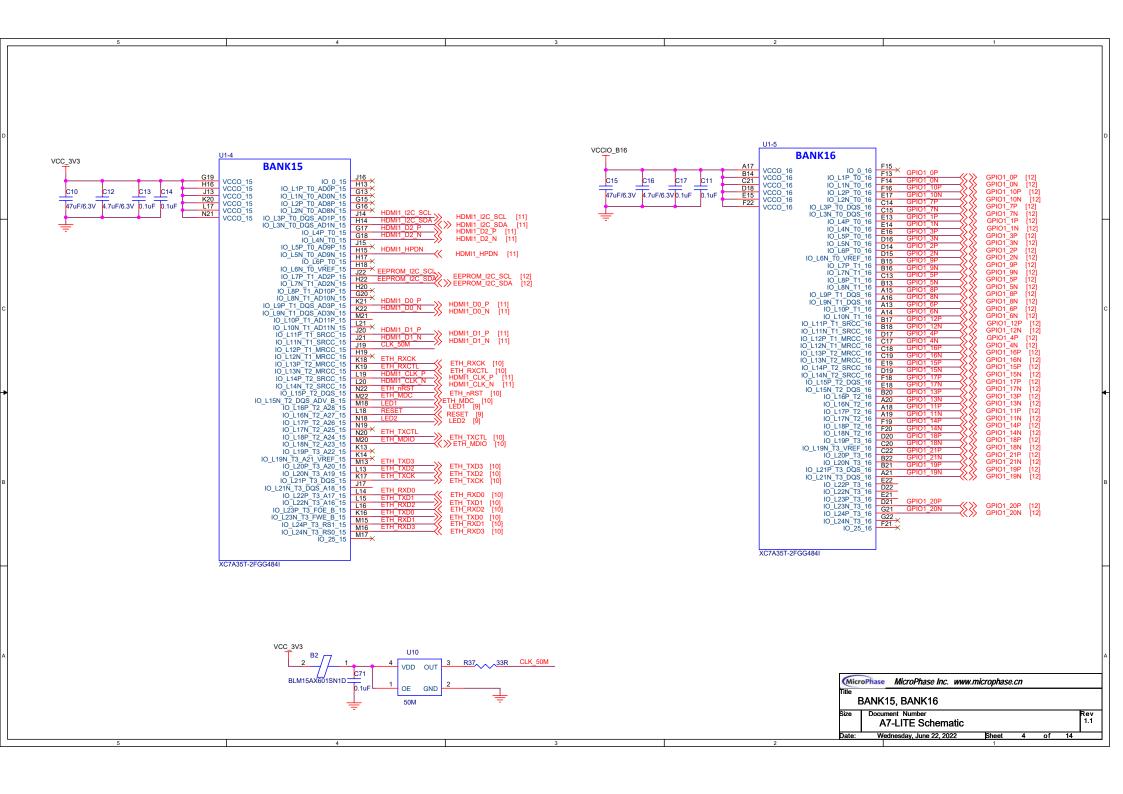
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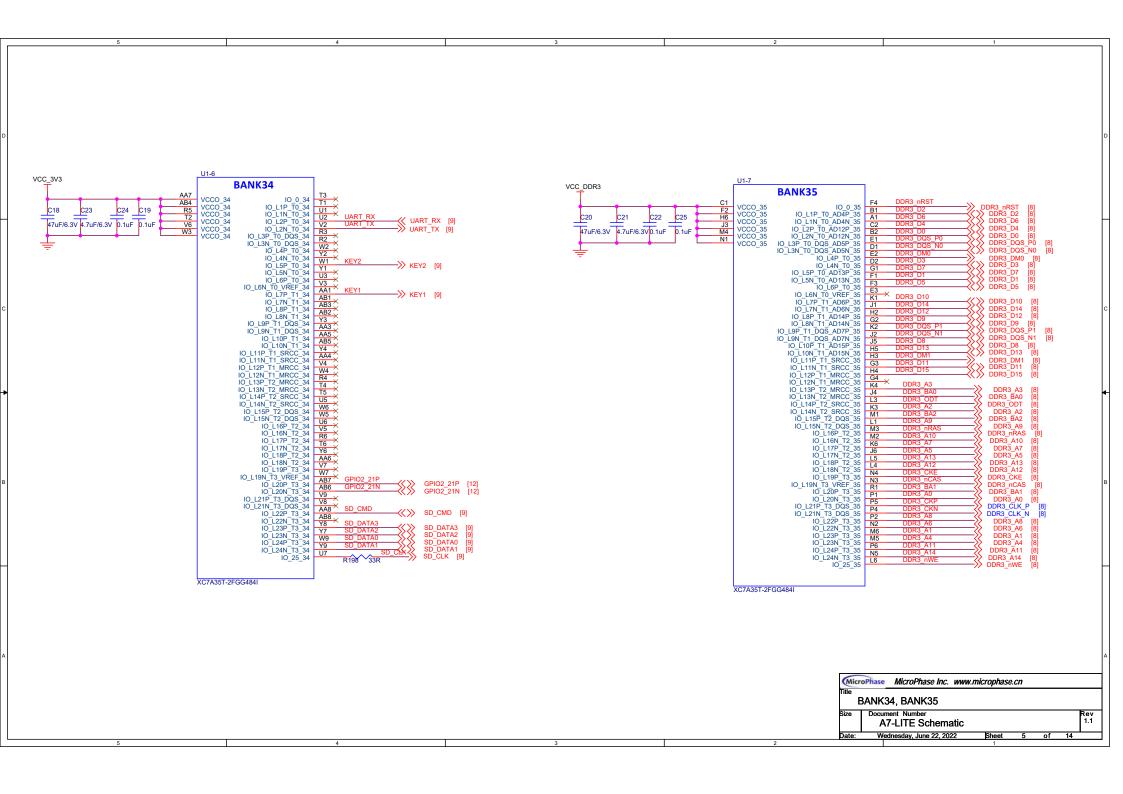


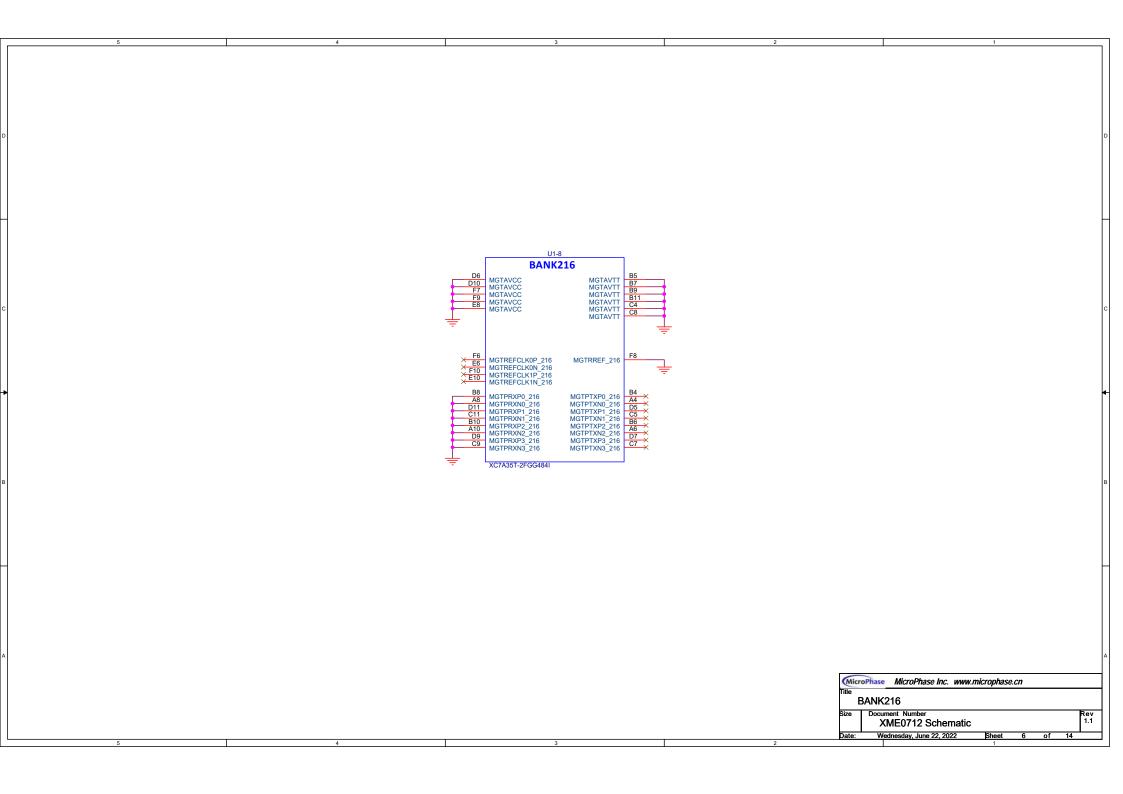
Micr	oPhase	MicroPhase Inc. www	microphase	e.cn			
Title E	Block	Diagram					
Size B		ment Number 7-LITE Schematic					Rev 1.1
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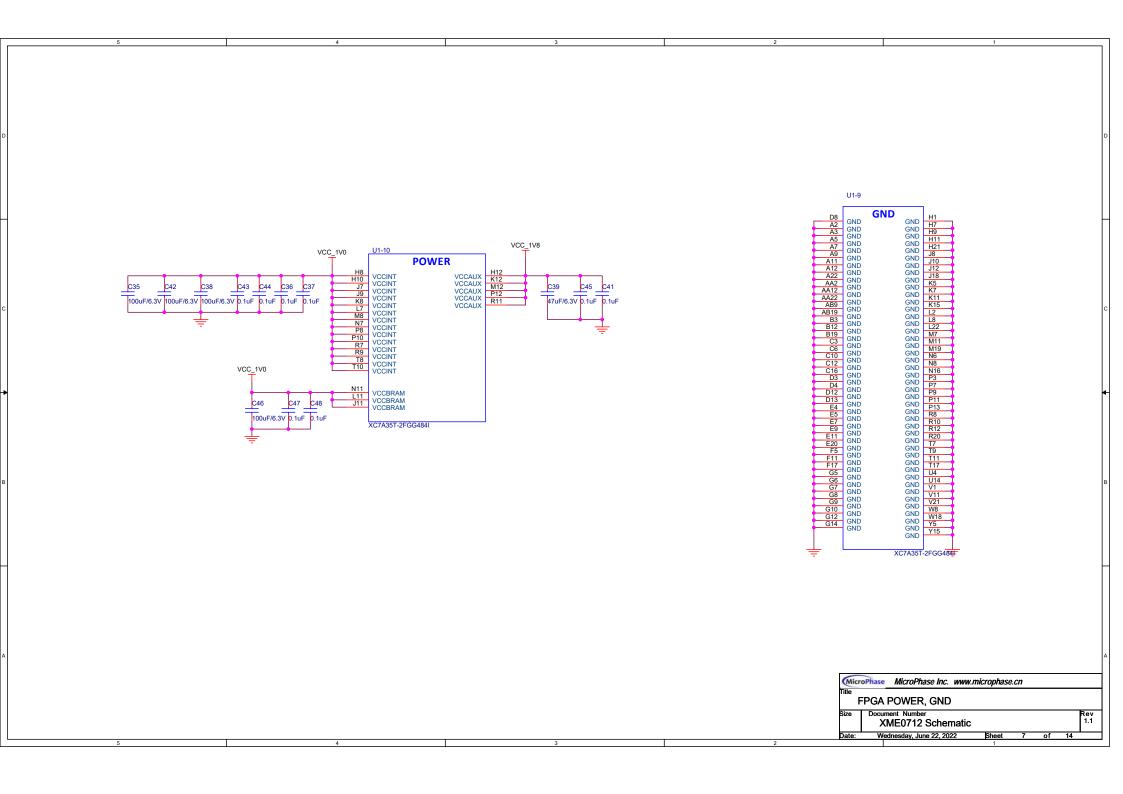


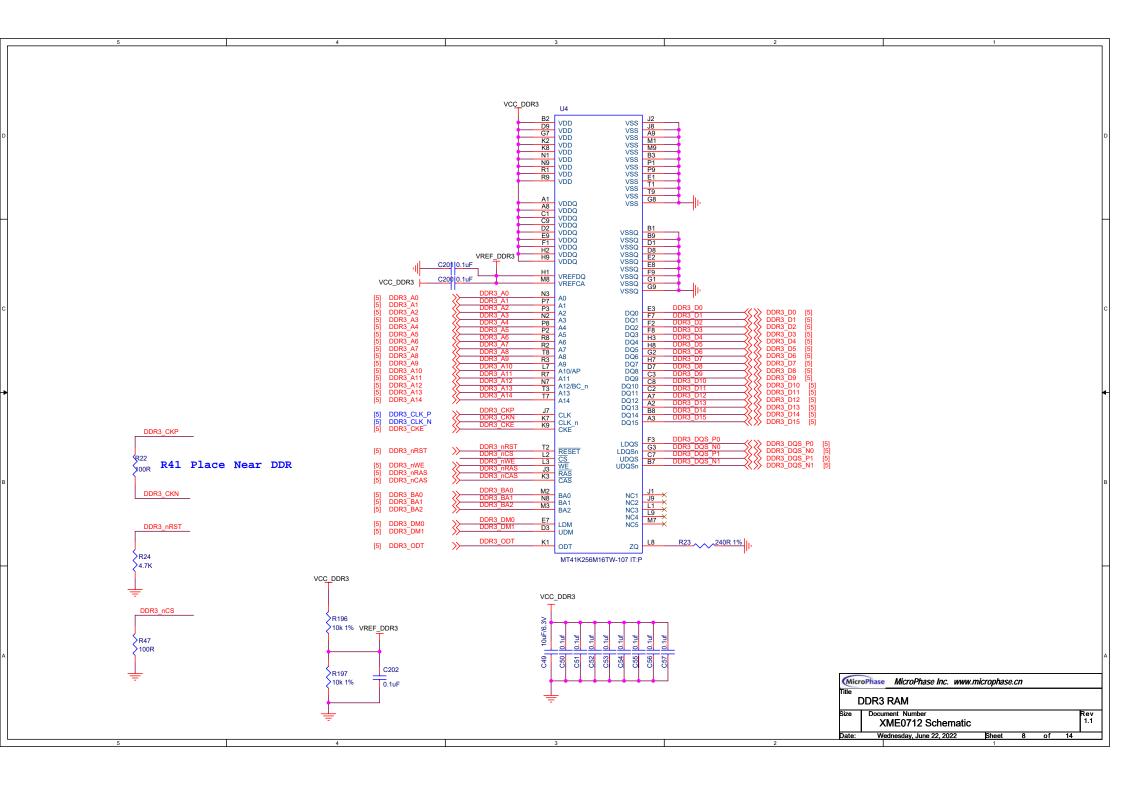




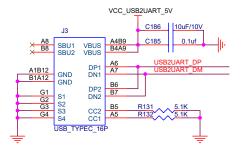


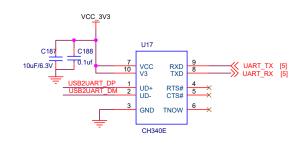




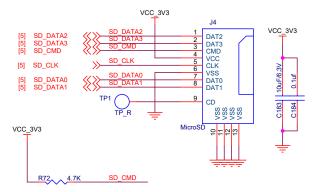


USB-UART

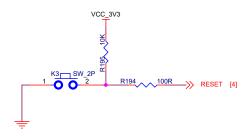


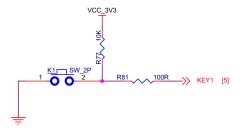


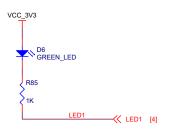
SD Card

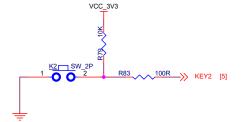


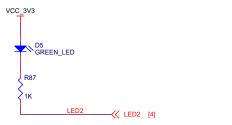
KEY & LED



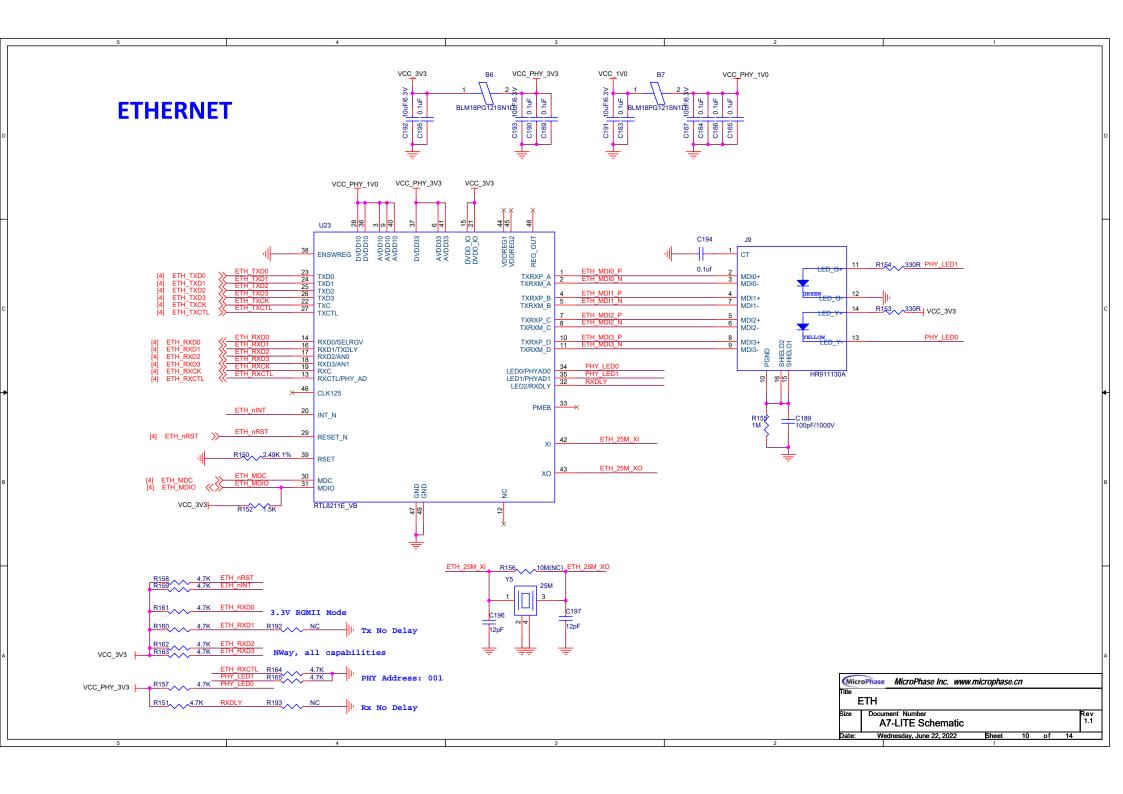


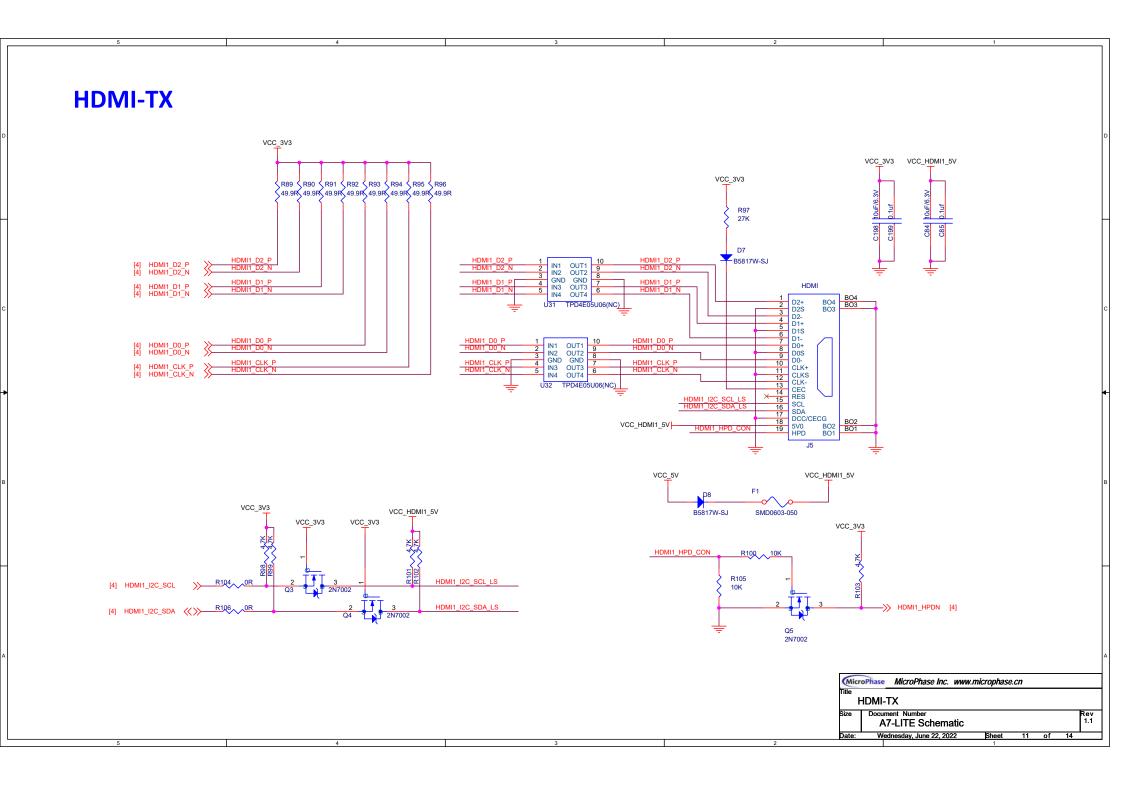




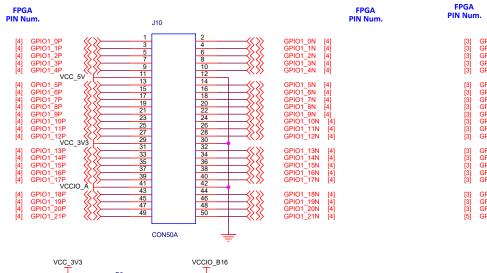


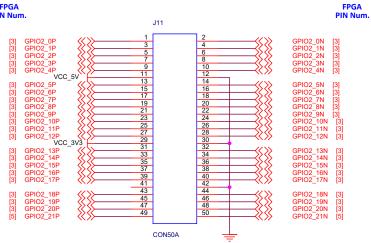
Micr	oPhase MicroPhase Inc. www.	microphase	.cn			
Title (9 KEY LED USB-UART S	SD				
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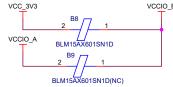




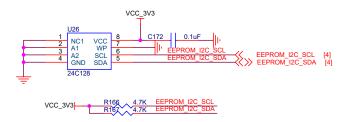
GPIO Interface







EEPROM



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Fitle 1	2 GPIO Connector_EEP	ROM				
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