Memory System

Francisco Alexis Lopez Avila

Simulation Report
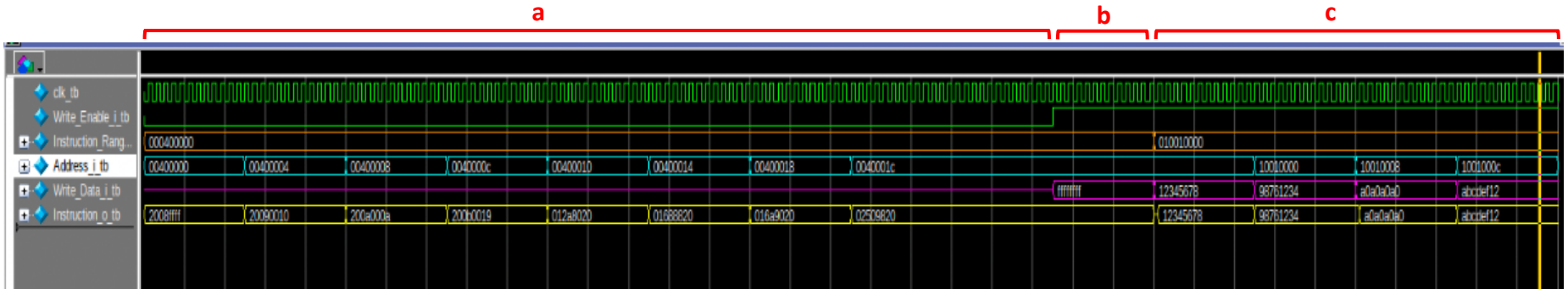
Result: Passed

Requirements for access to:

   ROM: *Instruction_Range* == **0x400000**

   RAM: *Instruction_Range* == **0x10010000** AND *Write_Enable* == **1**

Simulation

a. ROM is being accessed and returning pre-loaded instructions.

b. Even *Write_Enable* changed to 1, *Write_Data* can be written in RAM because the address is not in the correct range.

c. When the address range change to 0x10010000, *Write_Data* can be written to and read from RAM.