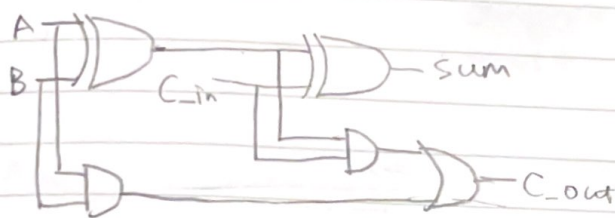
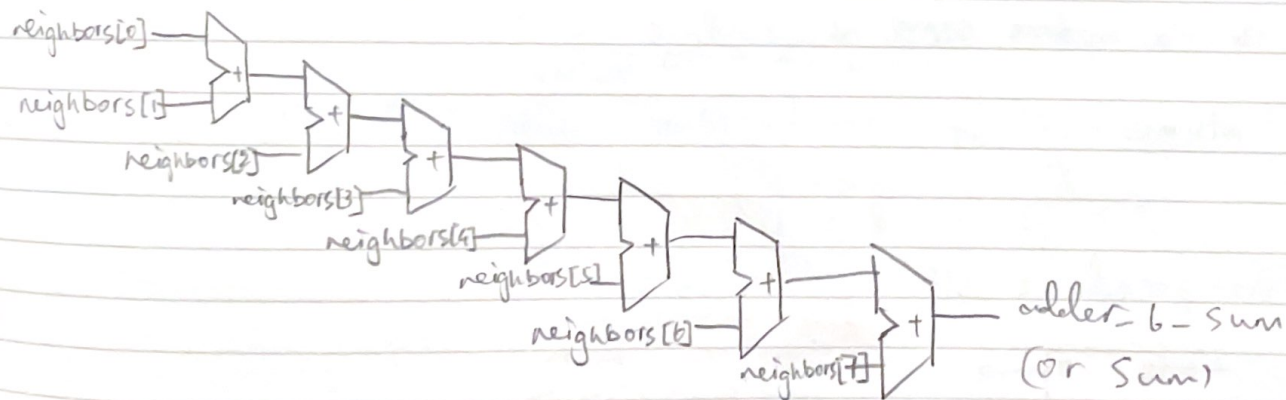
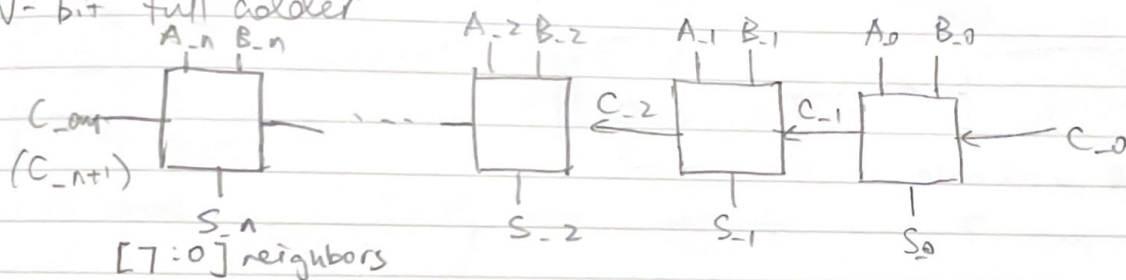


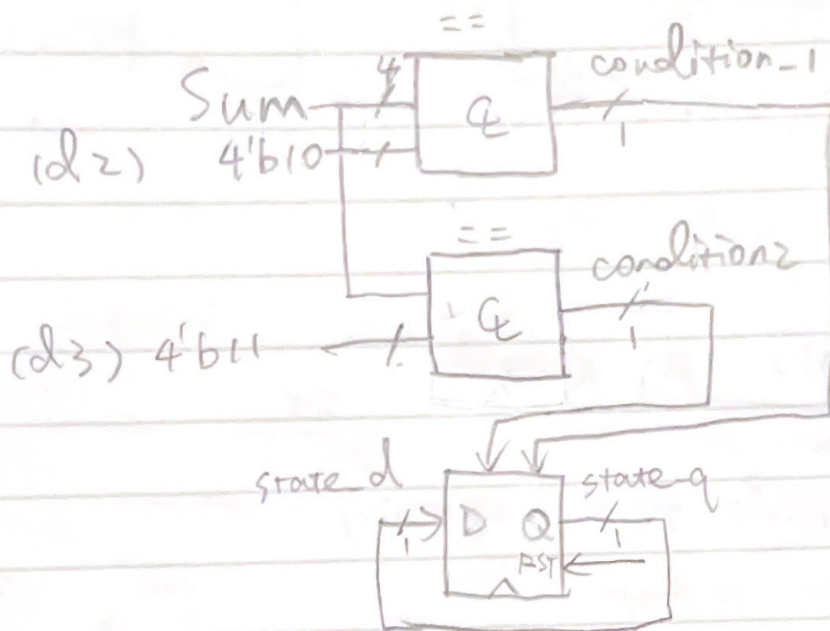
one Full adder



N-bit full adder



decimal	Sum of neighbors	out
0	0000	0
1	0001	0
2	0010	1 ✓
3	0011	1 ✓
4	0100	0



included Avi's led_array_driver.sv in hdl for flashing FPGA