Alex Picard

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EDUCATION

University of Maine - Bachelor of Science, Computer Engineering, GPA (3.00) 2021-2025

Specialties are in software, firmware, and AI development

TECHNICAL SKILLS

Programming Languages | C, C++, Python, Java, Assembly, Matlab, and Verilog

Web Development | Javascript, HTML/CSS

Developer Tools| Git, GitLab

AI development | PyTorch, Numpy, Pandas

WORK EXPERIENCE

Research Assistant | ASCC **May 2023 - Present**

Using different AI techniques to develop software Python | PyTorch | Numpy | Pandas | Matlab

- Translated code from Matlab into Python using Numpy, Pandas, and Matplotlib.
- Developed various neural networks like physics-informed neural networks in Python using PyTorch to develop a software package for the research sponsor company.
- Enhanced proficiency in conducting effective meetings and consistently met project deadlines.

Stocking 2 Associate | Walmart **July 2022 - Present**

Leadership, teamwork, and effective communication

- Developed basic computer skills, including proficiency in Microsoft Office and experience using inventory management software.
- Improved efficiency and productivity by suggesting and implementing process improvements in the stocking department.

PROJECTS

Front-End Software Development | Portfolio **May 2022 - Present**

Front-end design for personal portfolio website | Javascript | HTML/CSS | URL: www.alexpicard.info

- Created a personal portfolio from scratch using basic web development coding languages. The webpage includes my classes, about me, and extra projects. It will change with the more that I learn with plans on introducing more projects, converting it into a full-stack project by introducing frameworks like react and node.js.

Software and Firmware Development|RISC-V Processor **October 2022 - Nov 2022**

Pong game design using software and firmware design techniques Verilog | Quartus II IDE

- This project was the final project for ECE 473 Computer Arch and Organization. The tools used include Quartus and an Altera Cyclone V FPGA. We used Verilog and block diagrams to build a simple 5-stage pipeline processor that has the implemented instructions from RISC-V. The processor has passed all 9 benchmark problems to test the processor including, but not limited to binary search and computing the Fibonacci sequence. The code should work on Git Hub. If needed a zip file can be sent of 100% working design.