

SEMICONDUCTOR KIA7805AF/API~KIA7824AF/API **TECHNICAL DATA**

BIPOLAR LINEAR INTEGRATED CIRCUIT

THREE TERMINAL POSITIVE VOLTAGE REGULATORS 5V, 6V, 7V, 8V, 9V, 10V, 12V, 15V, 18V, 20V, 24V.

FEATURES

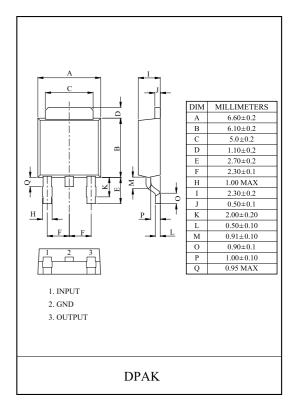
- · Internal Thermal Overload Protection.
- · Internal Short Circuit Current Limiting.
- · Output Current in Excess of 1A.
- · Satisfies IEC-65 Specification. (International Electronical Commission).
- · Package is DPAK, TO-220IS
- · Suitable for MLCC, Tantalum and Low ESR Electrolytic Capacitors.
- · Suffix U : Qualified to AEC-Q100(Grade 0)
 - : Automotive and standard product are electrically and thermally the same, except where specified. ex) KIA78**AF-RTF/PU.

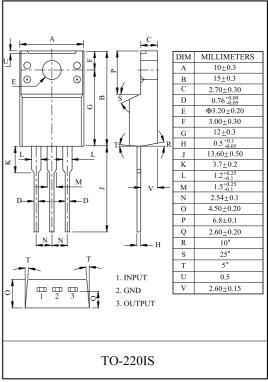
LINE-UP

OUTPUT VOLTAGE (Typ.)	UNIT
5	
6	
7	
8	
9	4.E. BB444
10	AF : DPAK API : TO-220IS
12	7111.10 22015
15	
18	
20	
24	
	5 6 7 8 9 10 12 15 18 20

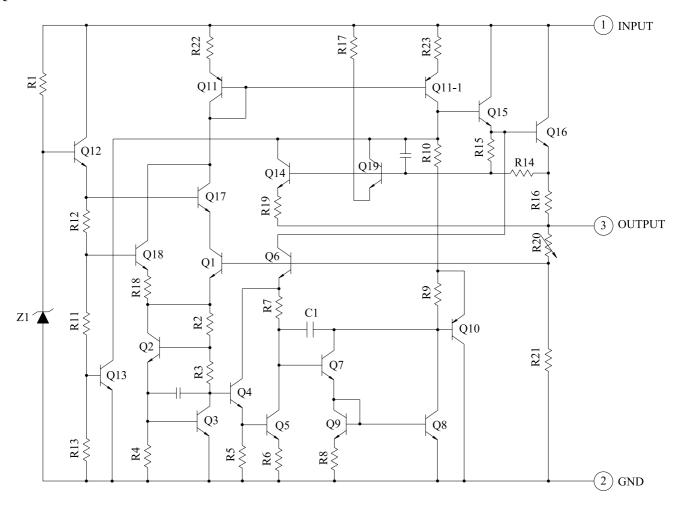
MAXIMUM RATINGS (Ta=25)

CHARACTERIS	TIC	SYMBOL	RATING	UNIT
	KIA7805		35	
Input Voltage	KIA7815	$V_{\rm IN}$	33	V
input voltage	KIA7818	V IN	40	•
	KIA7824		70	
Power Dissipation-1	AF	P_{D1}	1.3	w
(No Heatsink)	API	1 DI	2.0	**
Power Dissipation-2	AF	P _{D2}	12.0	W
(Infinite Heatsink)	API	1 D2	20.8	, vv
Operating Junction Tempe	erating Junction Temperature		-40 150	
Storage Temperature		T_{stg}	-55 150	





EQUIVALENT CIRCUIT



KIA7805AF/API ELECTRICAL CHARACTERISTICS (V_{IN} =10V, I_{OUT} =500mA, 0 T_j 125)

CHARACTERISTIC	SYMBOL	TEST CIRCUIT	Tl	EST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _{OUT}	Fig. 1	T _j =25 , 1	I _{OUT} =100mA	4.8	5.0	5.2	V
Lauret Describetion	Dan En	Fi- 1	Т-25	7.0V V _{IN} 25V	-	3	100	
Input Regulation	Reg line	Fig. 1	T _j =25	8.0V V _{IN} 12V	-	1	50	mV
I ID I	D. L. I	F' 1	Т. 25	5mA I _{OUT} 1.4A	-	15	100	7.7
Load Regulation	Reg load	Fig. 1	T _j =25 250mA I _{OUT} 750mA		-	5	50	mV
Output Voltage	V _{OUT}	Fig. 1	7.0V V _{IN} 20V 5.0mA I _{OUT} 1.0A, Po 15W		4.75	-	5.25	V
Quiescent Current	I_{B}	Fig. 1	T _j =25 , 1	T _j =25 , I _{OUT} =5mA		4.2	8.0	mA
Quiescent Current Change	I_{B}	Fig. 1	7.0V V _{IN}	25V	-	-	1.3	mA
Output Noise Voltage	V _{NO}	Fig. 2	Ta=25 , I _{OUT} =50m	10Hz f 100kHz A	-	50	-	$\mu V_{ m rms}$
Ripple Rejection Ratio	RR	Fig. 3	f=120Hz, I _{OUT} =50m/	8.0V V _{IN} 18V, A, T _j =25	62	78	-	dB
Dropout Voltage	V _D	Fig. 1	I_{OUT} =1.0A, T_j =25		-	2.0	-	V
Short Circuit Current Limit	I _{SC}	Fig. 1	T _j =25		-	1.6	-	A
Average Temperature Coefficient of Output Voltage	TC _{VO}	Fig. 1	I _{OUT} =5mA	, 0 T _j 125	-	-0.6	-	mV/

KIA7806AF/API ELECTRICAL CHARACTERISTICS (V_{IN} =11V, I_{OUT} =500mA, 0 T_j 125)

CHARACTERISTIC	SYMBOL	TEST CIRCUIT	T	EST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _{OUT}	Fig. 1	T _j =25 ,]	I _{OUT} =100mA	5.75	6.0	6.25	V
Input Regulation	Reg line	Fig. 1	T _i =25	8.0V V _{IN} 25V	-	4	120	- mV
input regulation	Keg IIIc	1 Ig. 1	1j=25	9V V _{IN} 13V	-	2	60	111 V
Load Regulation	Reg load	Fig. 1	T-25	5mA I _{OUT} 1.4A	-	15	120	mV
Load Regulation	Keg load	rig. i	T _j =25	250mA I _{OUT} 750mA	-	5	60	IIIV
Output Voltage	V _{OUT}	Fig. 1	8V V _{IN} 21V 5.0mA I _{OUT} 1.0A, Po 15W		5.7	-	6.3	V
Quiescent Current	I_{B}	Fig. 1	T _j =25 , 1	$T_j=25$, $I_{OUT}=5mA$		4.3	8.0	mA
Quiescent Current Change	I_{B}	Fig. 1	8V V _{IN}	25V	-	-	1.3	mA
Output Noise Voltage	V _{NO}	Fig. 2	Ta=25 , I _{OUT} =50m.	10Hz f 100kHz A	-	55	-	$\mu_{ m V_{rms}}$
Ripple Rejection Ratio	RR	Fig. 3	f=120Hz, I _{OUT} =50mA	9V V _{IN} 19V, A, T _j =25	61	77	-	dB
Dropout Voltage	V_{D}	Fig. 1	$I_{OUT}=1.0A, T_j=25$		-	2.0	-	V
Short Circuit Current Limit	I _{SC}	Fig. 1	T _j =25		-	1.5	-	A
Average Temperature Coefficient of Output Voltage	TC_{VO}	Fig. 1	I _{OUT} =5mA	., 0 T _j 125	-	-0.7	-	mV/

KIA7807AF/API ELECTRICAL CHARACTERISTICS (V_{IN} =12V, I_{OUT} =500mA, 0 T_j 125)

CHARACTERISTIC	SYMBOL	TEST CIRCUIT	Tì	EST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _{OUT}	Fig. 1	T _j =25 , 1	I _{OUT} =100mA	6.72	7.0	7.28	V
I. (D. L.)	D. I'	F' 1	Т. 25	9V V _{IN} 25V	-	5	140	17
Input Regulation	Reg line	Fig. 1	T _j =25	10V V _{IN} 14V	-	2	70	mV
I ID 16	D. L. I	F' 1	Т. 25	5mA I _{OUT} 1.4A	-	15	140	17
Load Regulation	Reg load	Fig. 1	T _j =25	250mA I _{OUT} 750mA	-	5	70	- mV
Output Voltage	V _{OUT}	Fig. 1	9V V _{IN} 22V 5.0mA I _{OUT} 1.0A, Po 15W		6.65	-	7.35	V
Quiescent Current	I_B	Fig. 1	T _j =25 , 1	T _j =25 , I _{OUT} =5mA		4.3	8.0	mA
Quiescent Current Change	I_{B}	Fig. 1	9V V _{IN}	25V	-	-	1.3	mA
Output Noise Voltage	V _{NO}	Fig. 2	Ta=25 , I _{OUT} =50m	10Hz f 100kHz A	-	60	-	$\mu V_{ m rms}$
Ripple Rejection Ratio	RR	Fig. 3	f=120Hz, I _{OUT} =50m/	10V V _{IN} 20V, A, T _j =25	59	75	-	dB
Dropout Voltage	V _D	Fig. 1	$I_{OUT}=1.0A, T_j=25$		-	2.0	-	V
Short Circuit Current Limit	I_{SC}	Fig. 1	T _j =25		-	1.3	-	A
Average Temperature Coefficient of Output Voltage	TC _{VO}	Fig. 1	I _{OUT} =5mA	, 0 T _j 125	-	-0.8	-	mV/

KIA7808AF/API ELECTRICAL CHARACTERISTICS (V_{IN} =14V, I_{OUT} =500mA, 0 T_j 125)

CHARACTERISTIC	SYMBOL	TEST CIRCUIT	TI	EST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _{OUT}	Fig. 1	T _j =25 , I	T _{OUT} =100mA	7.7	8.0	8.3	V
I. (D. 1)	D. I'	F' 1	Т. 25	10.5V V _{IN} 25V	-	6	160	37
Input Regulation	Reg line	Fig. 1	T _j =25	11V V _{IN} 17V	-	2	80	mV
I ID I	D 1 1	F' 1	Т. 25	5mA I _{OUT} 1.4A	-	12	160	37
Load Regulation	Reg load	Fig. 1	T _j =25	250mA I _{OUT} 750mA	-	4	80	- mV
Output Voltage	V _{OUT}	Fig. 1	10.5V V _{IN} 23V 5.0mA I _{OUT} 1.0A, Po 15W		7.6	-	8.4	V
Quiescent Current	I_{B}	Fig. 1	T _j =25 , I	T _j =25 , I _{OUT} =5mA		4.3	8.0	mA
Quiescent Current Change	I_{B}	Fig. 1	10.5V V _I	_N 25V	-	-	1.0	mA
Output Noise Voltage	V _{NO}	Fig. 2	Ta=25 , I _{OUT} =50mA	10Hz f 100kHz A	-	70	-	$\mu_{\rm rms}$
Ripple Rejection Ratio	RR	Fig. 3	f=120Hz, I _{OUT} =50mA	11.5V V _{IN} 21.5V, A, T _j =25	58	74	-	dB
Dropout Voltage	V_{D}	Fig. 1	$I_{OUT}=1.0A, T_j=25$		-	2.0	-	V
Short Circuit Current Limit	I _{SC}	Fig. 1	T _j =25		-	1.1	-	A
Average Temperature Coefficient of Output Voltage	TC _{VO}	Fig. 1	I _{OUT} =5mA	, 0 T _j 125	-	-1.0	-	mV/

KIA7809AF/API ELECTRICAL CHARACTERISTICS (V_{IN} =15V, I_{OUT} =500mA, 0 T_j 125)

CHARACTERISTIC	SYMBOL	TEST CIRCUIT	T	EST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _{OUT}	Fig. 1	T _j =25 , 1	I _{OUT} =100mA	8.64	9.0	9.36	V
I. (D. L.)	D. I'	F' 1	т 25	11.5V V _{IN} 26V	-	7.0	180	17
Input Regulation	Reg line	Fig. 1	T _j =25	13V V _{IN} 19V	-	2.5	90	mV
I ID I	D 1 1	F' 1	T. 25	5mA I _{OUT} 1.4A	-	12	180	3.7
Load Regulation	Reg load	Fig. 1	T _j =25	250mA I _{OUT} 750mA	-	4.0	90	mV
Output Voltage	V _{OUT}	Fig. 1	11.5V V _{IN} 26V 5.0mA I _{OUT} 1.0A, Po 15W		8.55	-	9.45	V
Quiescent Current	I_B	Fig. 1	T _j =25 , 1	T _j =25 , I _{OUT} =5mA		4.3	8.0	mA
Quiescent Current Change	I_{B}	Fig. 1	11.5V V	_{IN} 26V	-	-	1.0	mA
Output Noise Voltage	V _{NO}	Fig. 2	Ta=25 , I _{OUT} =50m.	10Hz f 100kHz A	-	75	-	$\mu V_{ m rms}$
Ripple Rejection Ratio	RR	Fig. 3	f=120Hz, I _{OUT} =50m.	12.5V V _{IN} 22.5V, A, T _j =25	56	72	-	dB
Dropout Voltage	V _D	Fig. 1	$I_{OUT}=1.0A, T_j=25$		-	2.0	-	V
Short Circuit Current Limit	I _{SC}	Fig. 1	T _j =25		-	1.0	-	A
Average Temperature Coefficient of Output Voltage	TC _{VO}	Fig. 1	I _{OUT} =5mA	, 0 T _j 125	-	-1.1	-	mV/

KIA7810AF/API ELECTRICAL CHARACTERISTICS (V_{IN} =16V, I_{OUT} =500mA, 0 T_j 125)

CHARACTERISTIC	SYMBOL	TEST CIRCUIT	TI	EST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _{OUT}	Fig. 1	T _j =25 , I	OUT=100mA	9.6	10.0	10.4	V
L (D 1)	D. I'	F' 1	т 25	12.5V V _{IN} 27V	-	8	200	17
Input Regulation	Reg line	Fig. 1	T _j =25	14V V _{IN} 20V	-	2.5	100	mV
I ID I	D 1 1	E' 1	T. 25	5mA I _{OUT} 1.4A	-	12	200	17
Load Regulation	Reg load	Fig. 1	T _j =25	250mA I _{OUT} 750mA	-	4	100	mV
Output Voltage	V _{OUT}	Fig. 1	12.5V V _{IN} 25V 5.0mA I _{OUT} 1.0A, Po 15W		9.5	-	10.5	V
Quiescent Current	I_B	Fig. 1	T _j =25 , I _{OUT} =5mA		-	4.3	8.0	mA
Quiescent Current Change	I_{B}	Fig. 1	12.5V V _I	_N 27V	-	-	1.0	mA
Output Noise Voltage	V _{NO}	Fig. 2	Ta=25 , I _{OUT} =50m/	10Hz f 100kHz A	-	80	-	$\mu_{ m rms}$
Ripple Rejection Ratio	RR	Fig. 3	f=120Hz, I _{OUT} =50m/	13.5V V_{IN} 23.5V, A, $T_j=25$	55	72	-	dB
Dropout Voltage	V _D	Fig. 1	$I_{OUT}=1.0A, T_j=25$		-	2.0	-	V
Short Circuit Current Limit	I_{SC}	Fig. 1	T _j =25		-	0.9	-	A
Average Temperature Coefficient of Output Voltage	TC _{VO}	Fig. 1	I _{OUT} =5mA,	, 0 T _j 125	-	-1.3	-	mV/

KIA7812AF/API ELECTRICAL CHARACTERISTICS (V_{IN} =19V, I_{OUT} =500mA, 0 T_j 125)

CHARACTERISTIC	SYMBOL	TEST CIRCUIT	Tì	EST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _{OUT}	Fig. 1	T _j =25 , 1	I _{OUT} =100mA	11.5	12.0	12.5	V
I D	D. II	F' 1	T. 25	14.5V V _{IN} 30V	-	10	240	***
Input Regulation	Reg line	Fig. 1	T _j =25	16V V _{IN} 22V	-	3	120	mV
I ID I I	D 1 1	F' 1	T. 25	5mA I _{OUT} 1.4A	-	12	240	***
Load Regulation	Reg load	Fig. 1	T _j =25	250mA I _{OUT} 750mA	-	4	120	mV
Output Voltage	V _{OUT}	Fig. 1	14.5V V _{IN} 27V 5.0mA I _{OUT} 1.0A, Po 15W		11.4	-	12.6	V
Quiescent Current	I_{B}	Fig. 1	T _j =25 , 1	T _j =25 , I _{OUT} =5mA		4.3	8.0	mA
Quiescent Current Change	I_{B}	Fig. 1	14.5V V ₁	_{IN} 30V	-	-	1.0	mA
Output Noise Voltage	V _{NO}	Fig. 2	Ta=25 , I _{OUT} =50m	10Hz f 100kHz A	-	90	-	$\mu_{\rm rms}$
Ripple Rejection Ratio	RR	Fig. 3	f=120Hz, I _{OUT} =50m/	15V V _{IN} 25V, A, T _j =25	55	71	-	dB
Dropout Voltage	V_{D}	Fig. 1	$I_{OUT}=1.0A, T_j=25$		-	2.0	-	V
Short Circuit Current Limit	I _{SC}	Fig. 1	T _j =25		-	0.7	-	A
Average Temperature Coefficient of Output Voltage	TC_{VO}	Fig. 1	I _{OUT} =5mA	, 0 T _j 125	-	-1.6	-	mV/

KIA7815AF/API ELECTRICAL CHARACTERISTICS (V_{IN} =23V, I_{OUT} =500mA, 0 T_j 125)

CHARACTERISTIC	SYMBOL	TEST CIRCUIT	TI	TEST CONDITION		TYP.	MAX.	UNIT
Output Voltage	V _{OUT}	Fig. 1	T _j =25 , I	OUT=100mA	14.4	15.0	15.6	V
I (D. LC	D 1	F' 1	Т. 25	17.5V V _{IN} 30V	-	11	300	17
Input Regulation	Reg line	Fig. 1	T _j =25	20V V _{IN} 26V	-	3	150	mV
I In I	D 1 1	F' 1	T. 25	5mA I _{OUT} 1.4A	-	12	300	3.7
Load Regulation	Reg load	Fig. 1	T _j =25	250mA I _{OUT} 750mA	-	4	150	mV
Output Voltage	V _{OUT}	Fig. 1	17.5V V _{IN} 30V 5.0mA I _{OUT} 1.0A, Po 15W		14.25	-	15.75	V
Quiescent Current	I_{B}	Fig. 1	T _j =25 , I	T _j =25 , I _{OUT} =5mA		4.4	8.0	mA
Quiescent Current Change	I_{B}	Fig. 1	17.5V V _I	_N 30V	-	-	1.0	mA
Output Noise Voltage	V _{NO}	Fig. 2	Ta=25 , I _{OUT} =50m/	10Hz f 100kHz A	-	110	-	$\mu V_{ m rms}$
Ripple Rejection Ratio	RR	Fig. 3	f=120Hz, I _{OUT} =50mA	18.5V V _{IN} 28.5V, A, T _j =25	54	70	-	dB
Dropout Voltage	V _D	Fig. 1	$I_{OUT}=1.0A, T_j=25$		-	2.0	-	V
Short Circuit Current Limit	I _{SC}	Fig. 1	T _j =25		-	0.5	-	A
Average Temperature Coefficient of Output Voltage	TC _{VO}	Fig. 1	I _{OUT} =5mA	, 0 T _j 125	-	-2.0	-	mV/

KIA7818AF/API ELECTRICAL CHARACTERISTICS (V_{IN} =27V, I_{OUT} =500mA, 0 T_j 125)

CHARACTERISTIC	SYMBOL	TEST CIRCUIT	T	EST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _{OUT}	Fig. 1	T _j =25 ,]	I _{OUT} =100mA	17.3	18.0	18.7	V
Louist Decodetion	Dan line	Fi- 1	т –25	21V V _{IN} 33V	-	13	360	
Input Regulation	Reg line	Fig. 1	T _j =25	24V V _{IN} 30V	-	4	180	- mV
I In It	D. L. I	F' 1	т 25	5mA I _{OUT} 1.4A	-	12	360	
Load Regulation	Reg load	Fig. 1	T _j =25	250mA I _{OUT} 750mA	-	4	180	- mV
Output Voltage	V _{OUT}	Fig. 1	21V V _{IN} 33V 5.0mA I _{OUT} 1.0A, Po 15W		17.1	-	18.9	V
Quiescent Current	I_{B}	Fig. 1	T _j =25 ,]	I _{OUT} =5mA	-	4.5	8.0	mA
Quiescent Current Change	I_{B}	Fig. 1	21V V _{IN}	33V	-	-	1.0	mA
Output Noise Voltage	V _{NO}	Fig. 2	Ta=25 , I _{OUT} =50m	10Hz f 100kHz A	-	125	-	$\mu_{ m V}_{ m rms}$
Ripple Rejection Ratio	RR	Fig. 3	f=120Hz, I _{OUT} =50m	22V V _{IN} 32V, A, T _j =25	52	68	-	dB
Dropout Voltage	V_{D}	Fig. 1	$I_{OUT}=1.0A, T_j=25$		-	2.0	-	V
Short Circuit Current Limit	I _{SC}	Fig. 1	T _j =25		-	0.4	-	A
Average Temperature Coefficient of Output Voltage	TC_{VO}	Fig. 1	I _{OUT} =5mA	, 0 T _j 125	-	-2.5	-	mV/

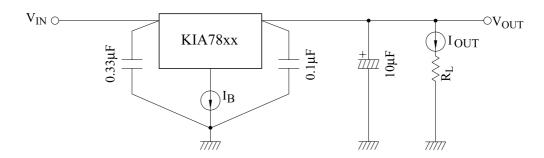
KIA7820AF/API ELECTRICAL CHARACTERISTICS (V_{IN} =29V, I_{OUT} =500mA, 0 T_j 125)

CHARACTERISTIC	SYMBOL	TEST CIRCUIT	TI	EST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _{OUT}	Fig. 1	T _j =25 , I	OUT=100mA	19.2	20.0	20.8	V
Input Regulation	Reg line	Eig 1	Т -25	23V V _{IN} 35V	-	15	400	mV
input Regulation	Keg IIIIe	Fig. 1	T _j =25	26V V _{IN} 32V	-	5	200	IIIV
Lord Domistics	Dagland	Eig 1	Т -25	5mA I _{OUT} 1.4A	-	12	400	mV
Load Regulation	Reg load	Fig. 1	T _j =25	250mA I _{OUT} 750mA	-	4	200	mv
Output Voltage	V _{OUT}	Fig. 1	23V V _{IN} 35V 5.0mA I _{OUT} 1.0A, Po 15W		19.0	-	21.0	V
Quiescent Current	I_{B}	Fig. 1	T _j =25 , I	T _j =25 , I _{OUT} =5mA		4.6	8.0	mA
Quiescent Current Change	I_{B}	Fig. 1	23V V _{IN}	35V	-	-	1.0	mA
Output Noise Voltage	V _{NO}	Fig. 2	Ta=25 , I _{OUT} =50m/	10Hz f 100kHz A	-	135	-	$\mu V_{ m rms}$
Ripple Rejection Ratio	RR	Fig. 3	f=120Hz, I	24V V _{IN} 34V, A, T _j =25	50	66	-	dB
Dropout Voltage	V _D	Fig. 1	$I_{OUT}=1.0A, T_j=25$		-	2.0	-	V
Short Circuit Current Limit	I_{SC}	Fig. 1	T _j =25		-	0.4	-	A
Average Temperature Coefficient of Output Voltage	TC _{VO}	Fig. 1	I _{OUT} =5mA	, 0 T _j 125	-	-3.0	-	mV/

KIA7824AF/API ELECTRICAL CHARACTERISTICS (V_{IN} =33V, I_{OUT} =500mA, 0 T_j 125)

CHARACTERISTIC	SYMBOL	TEST CIRCUIT	TEST CONDITION		MIN.	TYP.	MAX.	UNIT
Output Voltage	V _{OUT}	Fig. 1	T _j =25 , I _{OUT} =100mA		23.0	24.0	25.0	V
Input Regulation	Reg line	Fig. 1	T _j =25	27V V _{IN} 38V	-	18	480	- mV
				30V V _{IN} 36V	-	6	240	
Load Regulation	Reg load	Fig. 1	T _j =25	5mA I _{OUT} 1.4A	-	12	480	- mV
				250mA I _{OUT} 750mA	-	4	240	
Output Voltage	V _{OUT}	Fig. 1	27V V _{IN} 38V 5.0mA I _{OUT} 1.0A, Po 15W		22.8	-	25.2	V
Quiescent Current	I_{B}	Fig. 1	$T_j=25$, $I_{OUT}=5mA$		-	4.6	8.0	mA
Quiescent Current Change	I_{B}	Fig. 1	27V V _{IN} 38V		-	-	1.0	mA
Output Noise Voltage	V _{NO}	Fig. 2	Ta=25 , 10Hz f 100kHz I _{OUT} =50mA		-	150	-	$\mu V_{ m rms}$
Ripple Rejection Ratio	RR	Fig. 3	f=120Hz, 28V V_{IN} 38V, I_{OUT} =50mA, T_j =25		50	66	-	dB
Dropout Voltage	V_{D}	Fig. 1	I _{OUT} =1.0A, T _j =25		-	2.0	-	V
Short Circuit Current Limit	I _{SC}	Fig. 1	T _j =25		-	0.3	-	A
Average Temperature Coefficient of Output Voltage	TC_{VO}	Fig. 1	I_{OUT} =5mA, 0 T_j 125		-	-3.5	-	mV/

Fig. 1 Standard Test Circuit & Application Circuit



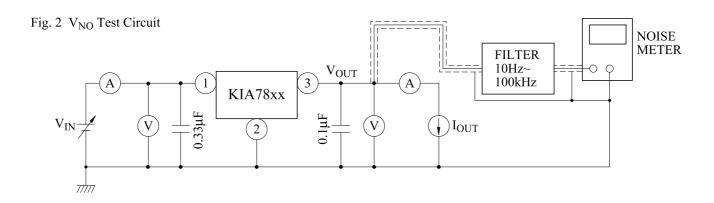
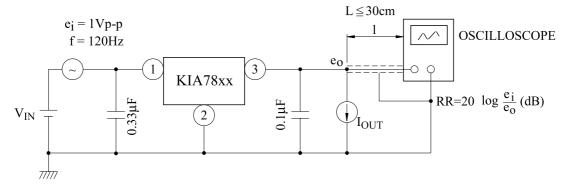


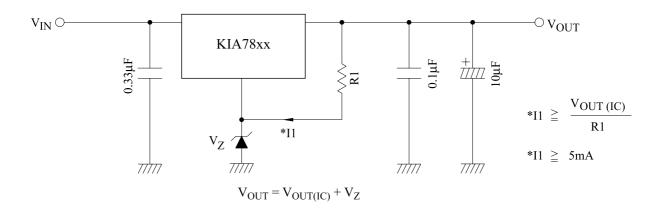
Fig. 3 Ripple Rejection Test Circuit



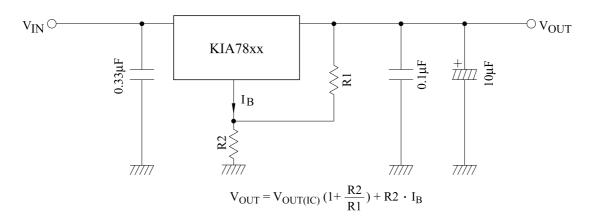
APPLICATION CIRCUIT

(1) VOLTAGE BOOST REGULATOR

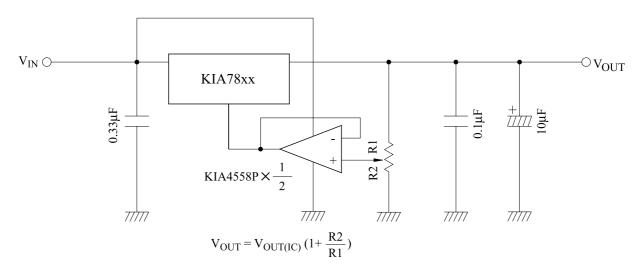
(a) Voltage boost by use of zener diode



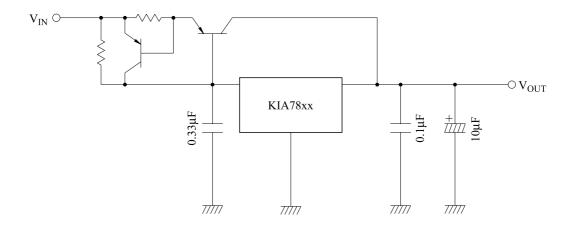
(b) Voltage boost by use of resistor



(c) Adjustable output regulator

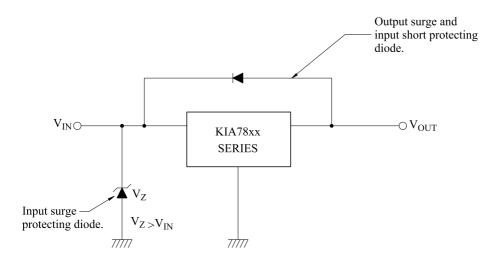


(2) CURRENT BOOST REGULATOR

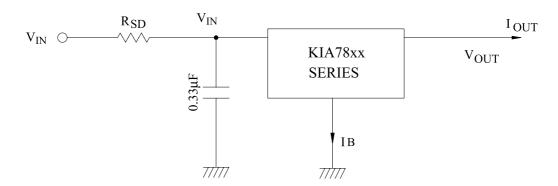


PRECAUTIONS ON APPLICATION

- (1) In regard to GND, be careful not to apply a negative voltage to the input/output terminal. Further, special care is necessary in case of a voltage boost application.
- (2) When a surge voltage exceeding maximum rating is applied to the input terminal or when a voltage in excess of the input terminal voltage is applied to the output terminal, the circuit may be destroyed. Specially, in the latter case, great care is necessary Further, if the input terminal shorts to GND in a state of normal operation, the output terminal voltage becomes higher than the input voltage (GND potential), and the electric charge of a chemical capacitor connected to the output terminal flows into the input side, which may cause the destruction of circuit. In these cases, take such steps as a zener diode and a general silicon diode are connected to the circuit, as shown in the following figure.



(3) When the input voltage is too high, the power dissipation of three terminal regulator increase because of series regulator, so that the junction temperature rises. In such a case, it is recommended to reduce the power dissipation by inserting the power limiting resistor R_{SD} in the input terminal, and to reduce the junction temperature as a result.



The power dissipation P_D of IC is expressed in the following equation.

$$P_D = (V_{IN}'-V_{OUT}) \cdot I_{OUT} + V_{IN}' \cdot I_B$$

If V_{IN} ' is reduced below the lowest voltage necessary for the IC, the parasitic oscillation will be caused according to circumstances. In determining the resistance value of R_{SD} , design with margin should be made by making reference to the following equation.

$$R_{SD} < \frac{V_{IN} - V_{IN}'}{I_{OUT} + I_{B}}$$

(4) Connect the input terminal and GND, and the output terminal and GND, by capacitor respectively.

The capacitances should be determined experimentally because they depend on printed patterns. In particular, adequate investigation should be made so that there is no problem even at time of high or low temperature.

(5) Installation of IC for power supply

For obtaining high reliability on the heat sink design of the regulator IC, it is generally required to derate more than 20% of maximum junction temperature (T_i MAX.) Further, full consideration should be given to the installation of IC to the heat sink.

(a) Heat sink design

The thermal resistance of IC itself is required from the viewpoint of the design of elements, but the thermal resistance from the IC package to the open air varies with the contact thermal resistance. Table 1 shows how much the value of the contact thermal resistance ($_{\rm C}^+$ s) is changed by insulating sheet (mica) and heat sink grease.

TABLE 1. UNIT: /W

PACKAGE	MODEL NO.	TORQUE	MICA	c+ s
TO-220IS	KIA78xxAPI	6kg ⋅ cm	Not Provided	0.3 0.5(1.5 2.0)
			Provided	2.0 2.5(4.0 6.0)

The figures given in parentheses denote the values at time of no grease.

The package of regulator IC serves as GND, therefore, usually use the value at time of "no mica"

(b) Silicon grease

When a circuit not exceeding maximum rating is designed, it is to be desired that the grease should be used if possible.

If it is required that the contact thermal resistance is reduced from the view-point of the circuit design,

It is recommended that the following methods be adopted.

A: Use Thercon (Fuji High Polymer Kogyo K.K)

B: Use SC101 (Torei Silicon) or G-640 (GE), if grease is used.

(c) Torque

When installing IC on a heat sink or the like, tighten the IC with the torque of less than the rated value. If it is tightened with the torque in excess of the rated value, sometimes the internal elements of the IC are adversely affected. Therefore, great care should be given to the installing operation. Further, if polycarbonate screws are used, the torque causes a change with the passage of time, which may lessen the effect of radiation.

- (6) IEC (International Electronical Commission)-65 Specification.
- (a) IEC (International Electronical Commission)-65 is the standard, parts testing method, machinery and tolls (used in connecting main power directly and indirectly) Which are used at home and general building. The purpose of the above standard is not to breaking out the risk which is related to an electric shock, a heating, a fire and the damage of surrounding parts in the case of normal or abnormal operating.
- (b) In case temperature is limited by temperature overheating prevention device, fuse or the operation of fuse resistor One must calculate the temperature of PCB substrate in 2 minute.
 - T 110 regulated
 - T=T(The PCB substrate temperature in 2 minute)
 - -Ta(Ambient temperature)
- (c) Graph

As the territory of the deviant line appear by the heat, as the area is wider, T(The PCB substrate temperature in 2 minute) is becoming high.

