

Process of threads.

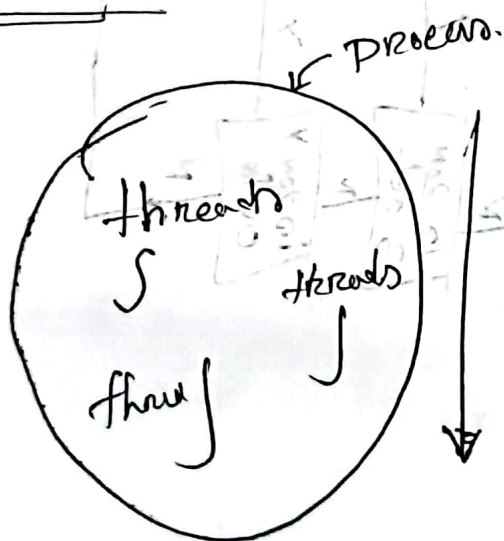
In computing, a process is an instance of a computer program that is being executed. Contains the program code and its current activity.

[Compute program or process will execute 2 (6h)]

A threads of execution is the smallest sequence of programmed instructions that can be managed by an OS. planner.

[The OS planner instruction will OS planner execute too]

A process may be made up with multiple threads of execution that execute instruction concurrently.



lower process made up with

any threads.

L1 and L2 Cache

L1 It is some kind of memory which is built into the CPU. It is placed very close to the CPU. This is built into the CPU. [For CPU, it is not true that it is built into the CPU.]

L2 It is another memory but instead of feeding CPU directly, this feeds the L1 cache.

L2 L2 is on a separate chip that can be accessed more quickly than the larger main memory. Memory size of L2 is 1024 kb [separate chip, not main memory (not in the same chip as L1)]

It

Core 2 and Dual Core

• It usually refers to a series of processors developed by Intel.	• It generally refers to a specific type of processor, where 2 processing cores on a single chip.
• Here 6 MB of L2 cache memory.	• Here 3 MB of L2 cache memory.
• Core 2 is better performer than dual core.	• Previous processor comparison, dual core best.
• Example: Intel Core 2	• Example: Intel G530 dual core

Bit slicing: Bit slicing is a technique for constructing a processor from modules of smaller bit width. It usually consists of an ALU of 1, 2, 4, 8 bits and control lines.

Example: Consider 4 bit binary: 1101.

In bit slicing this number could be processed as 4 separate bits (1, 1, 0, 1) and operation can be performed on each bit individually.

Embedded system is a dedicated computer system, integrated into device for specific function.

Arithmetic processor (ALU) is a component of CPU which performs arithmetic and logical operations.

Itanium processor is a 64 bit MP, designed for high performance computing.

Merced processor is a 32 bit processor of the Itanium, representing a significant development in 64 bit computing.

Embedded Microprocessor

It is a MP that used in embedded system. It is usually smaller, and it consumes less power.

Coprocessor: It is a special set of circuits in MP that designed to perform some of the specialized tasks more quickly than basic MP could perform the task smoothly.