Seg 8 (mbedded system (F3)
Combagnated anto device for specific * Embedded system means som-thing that no attached another thing. * An embedded sayotem & Con be thought of on a Computer. natidioarce rayotem having dofficiere embedded mit. · II haro & components. Nardware Software. Teel time operating system (ROOS) and all of the contractions defines the way the sporton works

Its sets the toules during Execution of MAR. 1704. application program. je ouspie out Aloso referred frictroware, for perponning the ropeler fix function, · Example of the andustries where it is used, home application section, automobile section, agricultural, medical section.

Es model

Two types Es. Two types Es. · Handware mont sell out of ramable. . Software wink to other synha

THO treas high IVI code anto Low IVI. lenguage.

Chanacteristies of Embedded system.

· Es must be reliable.

. Ito con't crosh.

· Softwork con't be updated in many Es. divicus

many device have critical performance & power diagon

Constraints.

. teed time constraints occor In mony rapplication. So used for flexibility, handware used for penyment

Handwork - oftwork parchitioning. · Madi-Ronal Design, Just: Design, Juis.

Design Integration and system test Conser Dis Hos Edition Sis your - Courses Sister Shows could be updated in many Es charen of dence have orthan sperformance & power duffers whoologue your ne noon bound + com much de manufacture of the same of the property of the same of

CS CamScanner

- and more a marger Co America. Co. Deorgan · HW /3W . As opening telestion believes this and all La de la pécification de partifiquing à sulleme ... Simulation Co - verui) Scinulation

Simulation

A test

Three radice 22

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19 Why Co-design weeful? roming, performance safé p. teliability, cast constraints. =) Ito also made possible by the advance in disign methodologies and tools

=> better sulation can found.

inglas T. Extre co-disign Es amobies. · Co opecification: talation between Hw and so. · Co - spn-theoria: Awdomatic duagn of the and sw meet co Bimulation: Bimulation of Hw and Sw. Simulalini op-18mizing Designia mit in the designia. Combur wit. . Detail memory access (Dins) . Homd Coded assembly. · chose corract algorithm Orfin rubbarp - 2) type une boxup tables. ene lookup tou.

Avoid high fraquery.

Simply your code

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alod born ariginalist. Syntom On Chip (300) miss on the A system on a chip (60c) a's an Integrated Circustic)

That antegrates all components of a computer or other electronic Byotem Onto a single Chip. Gopu rorethbrudge

Opp Commetterly mells The confront with a Me on one of degree.

Me Apically home under 100 kB of RAM and after telly single-chip-brotom, whereo 600 900 typically used with more powerful processor. Capable of Turning Software such as the devotop version of windows and Innex which mud external.

memory chips (flook, RAM) to be weful.

Application Domain > 500 where used? Comorcee excen etc. Comorcee excen etc. , smort cords, bonking. & telephone Tele communication : Nedworking, Mobile Computing, worchers Comm unications, ele.

They are an satellite and minister.

They are an satellite and minister. # Components of 300!

A microcontroller von microprocessor

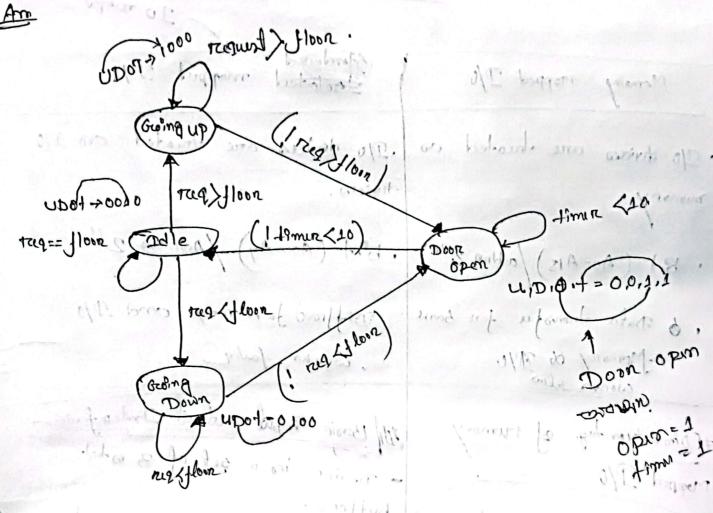
Memory blocks Including Rom, RAM affash morning Mining Sairces Cincluding Oscillotors. External antériface such as MB. Elhermet. · Mottage telgishations it (MASI Most) agrido

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