



Global LCD Panel Exchange Center

ED060SC8

Version: 2.0

# **TECHNICAL SPECIFICATION**

**MODEL NO: ED060SC8** 

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Customer's Confirmat	ion
Customer	
Date	
Ву	
☐E Ink's Confirmation	

Confirmed B

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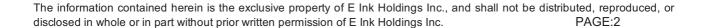




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**Revision History** 

Rev.	Issued Date	Revised Contents
1.0	Oct 11,2010	New
2.0	July.28.2011	Update to E Ink logo







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# TECHNICAL SPECIFICATION CONTENTS

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# 1. Application

The display is a TFT active matrix electrophoresis display, with associated interface and control logic, and a reference system design.

The 6" active area contains 800x600 pixels, the display is capable to display images at 2-16 gray levels (1-4 bits) depending on the display controller and the associated waveform file used.(Note 1-1)

#### Note 1-1

The standard waveforms provided by EIH are 4bits waveform for ISIS and broadsheet, 3 bits waveform for broadsheet & 8 track, 2 bits waveform for PVI-6001A.

#### 2. Features

- High contrast TFT electrophoresis
- 800x600 display
- > High reflectance
- Ultra wide viewing angle
- Ultra low power consumption
- Pure reflective mode
- Bi-stable
- Commercial temperature range
- Landscape, portrait mode
- Antiglare hard-coated front-surface

#### 3. Mechanical Specifications

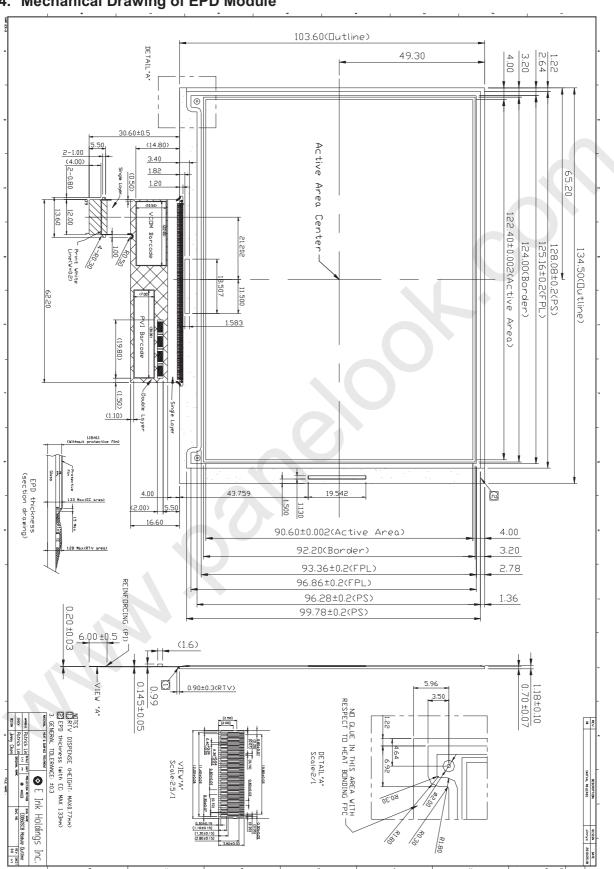
Parameter	Specifications	Unit	Remark
Screen Size	6.0 (4:3 diagonal)	Inch	
Display Resolution	800 (H) ×600(V)	Pixel	
Active Area	122.4 (H)×90.6 (V)	mm	
Pixel Pitch	0.153 (H)×0.151 (V)	mm	
Pixel Configuration	Rectangle		
Outline Dimension	134.50(W)×103.60(H)×1.18(D)	mm	
Module Weight	32 <u>+</u> 3	g	





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# 4. Mechanical Drawing of EPD Module



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# 5. Input / Output Interface

## 5-1) Pin Assignment

Pin#	Signal	Description
1	VNEG	Negative power supply source driver
2	VPOS	Positive power supply source driver
3	VSS	Ground
4	VDD	Digital power supply drivers
5	CKH	Clock source driver
6	LEH	Latch enable source driver
7	OEH	Output enable source driver
8	NC	NO Connection
9	NC	NO Connection
10	NC	NO Connection
11	STH	Start pulse source driver
12	D0	Data signal source driver
13	D1	Data signal source driver
14	D2	Data signal source driver
15	D3	Data signal source driver
16	D4	Data signal source driver
17	D5	Data signal source driver
18	D6	Data signal source driver
19	D7	Data signal source driver
20	NC	NO Connection
21	NC	NO Connection
22	VCOM	Common voltage
23	VGG	Positive power supply gate driver
24	VEE	Negative power supply gate driver
25	NC	NO Connection
26	NC	NO Connection
27	NC	NO Connection driver
28	MODE1	Output mode selection gate driver
29	NC	NO Connection
30	NC	NO Connection
31	NC	NO Connection
32	STV	Start pulse gate driver
33	CKV	Clock gate driver
34	BORDER	Border connection
35	NC	NO Connection
36	NC	NO Connection
37	NC	NO Connection
38	NC	NO Connection
39	VSS	Ground

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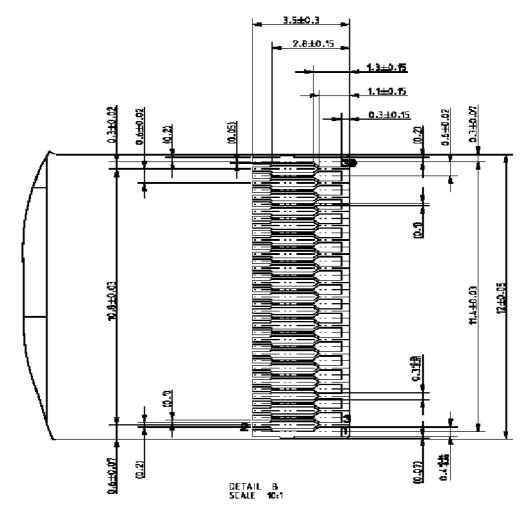


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# 5-2) Panels Electrical Connection

SERVICE	CONNECTOR	TYPE NUMBER	NUMBER OF PINS	MATING CONNECTOR
Interface	JST	39FXL-RSM1-S-H-TB	39	Copper foil 0.3mm pitch







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#### 6. Electrical Characteristics

#### 6-1) Panel interface description

This panel is driven by broadsheet display controller ASIC. See control product specification for details.

# 6-2) Absolute maximum rating

Parameter	Symbol	Rating	Unit
Logic Supply Voltage	VDD	-0.3 to +7	V
Positive Supply Voltage	V <sub>POS</sub>	-0.3 to +18	V
Negative Supply Voltage	$V_{NEG}$	+0.3 to -18	V
Max .Drive Voltage Range	V <sub>POS</sub> - V <sub>NEG</sub>	36	V
Supply Voltage	VGG	-0.3 to +45	V
Supply Voltage	VEE	-25.0 to +0.3	V
Supply Range	VGG-VEE	-0.3 to +45	V
Operating Temp. Range	TOTR	0 to +50	°C
Storage Temperature	TSTG	-25 to +70	$^{\circ}\mathbb{C}$

# 6-3) Panel DC characteristics

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Signal ground	V <sub>SS</sub>		-	0	-	V
	$V_{DD}$		3.0	3.3	3.6	V
Logic Voltage supply	$I_{VDD}$	V <sub>DD</sub> =3.3V	-	1	3	mA
Gate Negative supply	V <sub>EE</sub>		-21	-20	-19	V
Gate Negative supply	I <sub>EE</sub>	$V_{EE} = -20V$	-	0.6	1.8	mA
Gate Positive supply	$V_{GG}$		21	22	23	V
Gate i ositive supply	$I_{GG}$	$V_{GG} = 22V$	-	0.6	1.8	mA
Source Negative supply	$V_{NEG}$		-15.4	-15	-14.6	V
Source Negative Supply	I <sub>NEG</sub>	$V_{NEG} = -15V$	-	18	36	mA
Source Positive supply	$V_{POS}$		14.6	15	15.4	V
Source i ositive supply	I <sub>POS</sub>	$V_{POS} = 15V$	-	15	30	mA
Border supply	V	$V_{POS} = 15V$	14.6	15	15.4	V
	V <sub>Border</sub>	$V_{NEG} = -15V$	-15.4	-15	-14.6	V
Asymmetry source	$V_{Asym}$	$V_{POS}+V_{NEG}$	-800	0	800	mV
Common voltage	$V_{COM}$		-2.5	Adjusted	-1.0	V
Common voltage	I <sub>COM</sub>		-	0.14	ı	mA
Panel power consumption	Р		-	530	1080	mW
Standby power panel	P <sub>STBY</sub>		-	-	0.4	mW
Operating temperature			0	-	50	$^{\circ}\mathbb{C}$
Storage temperature			-25	-	70	$^{\circ}\mathbb{C}$
Maximum image update time at 25°C			-	600	-	ms

- The Typical power consumption is measured using 85Hz WF with following pattern transition: from horizontal 4 gray scale pattern to vertical 4 gray scale pattern.(Note 6-1)
- The standby power is the consumed power when the panel controller is in standby mode.

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- The listed electrical/optical characteristics are only guaranteed under the controller & waveform provided by E Ink

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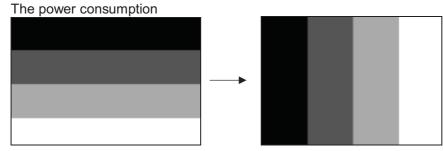


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- Vcom is recommended to be set in the range of assigned value  $\pm 0.1 \text{V}$
- The maximum I<sub>COM</sub> inrush current is about 600 mA

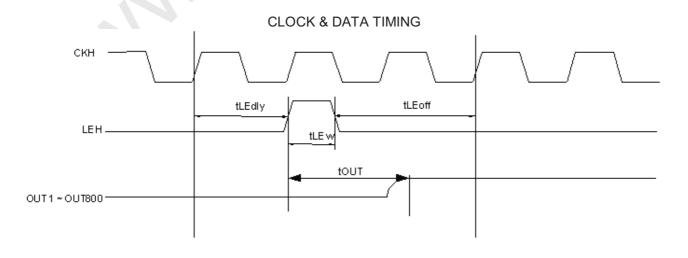




# 6-4) Panel AC characteristics

VDD=3.0V to 3.6V, unless otherwise specified.

Parameter	Symbol	Min.	Тур.	Max.	Unit
Clock frequency	fckv	-	-	200	kHz
Minimum "L" clock pulse width	twL	0.5	-	-	us
Clock rise time	trckv	-	-	100	ns
Clock fall time	tfckv	-	-	100	ns
Data setup time	tSU	100	-	-	ns
Data hold time	tH	100	-	-	ns
Pulse rise time	trspv	-	-	100	ns
Pulse fall time	tfspv	-	-	100	ns
Clock CKH cycle time	tcy	16.7	-	-	ns
D0 D7 setup time	tsu	8	-	-	ns
D0 D7 hold time	th	8	-	-	ns
STH setup time	tstls	8	-	-	ns
STH hold time	tstlh	8	-	-	ns
LEH on delay time	tLEdly	40	-	-	ns
LEH high-level pulse width	tLEw	40	-	-	ns
LEH off delay time	tLEoff	200	-	-	ns
Output setting time to +/- 30mV(C <sub>load</sub> =200pF)	tout		-	12	us



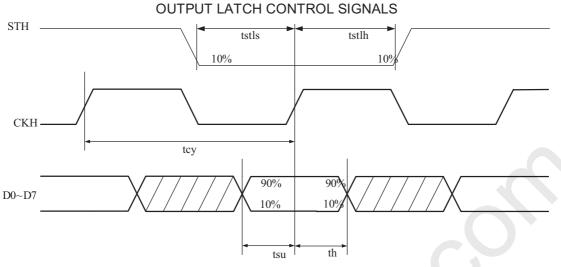
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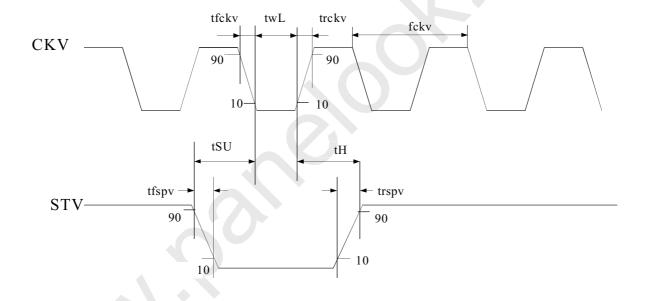




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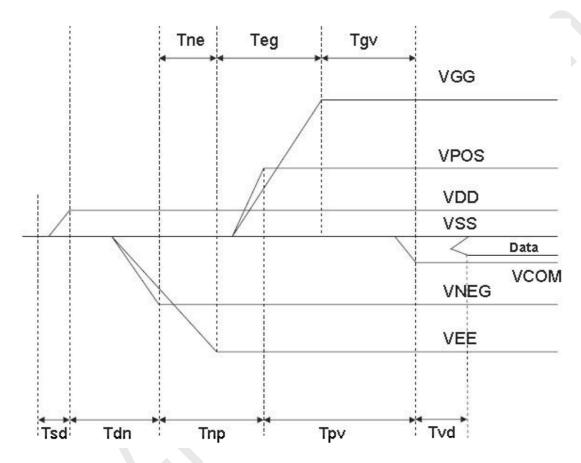
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# 7. Power on Sequence

Power Rails must be sequenced in the following order:

- 1. VSS  $\rightarrow$  VDD  $\rightarrow$  VNEG  $\rightarrow$  VPOS (Source driver)  $\rightarrow$  VCOM
- 2. VSS  $\rightarrow$  VDD  $\rightarrow$  VEE  $\rightarrow$  VGG (Gate driver)

#### **POWER ON**



	Min	Max
Tsd	$100 \mu\mathrm{s}$	-
Tdn	$100 \mu\mathrm{s}$	ı
Tnp	$1000 \mu\mathrm{s}$	-
Tpv	$100 \mu\mathrm{s}$	1
Tvd	$100 \mu\mathrm{s}$	ı
Tne	$0 \mu\mathrm{s}$	1
Teg	$1000 \mu\mathrm{s}$	-
Tgv	$100 \mu\mathrm{s}$	-

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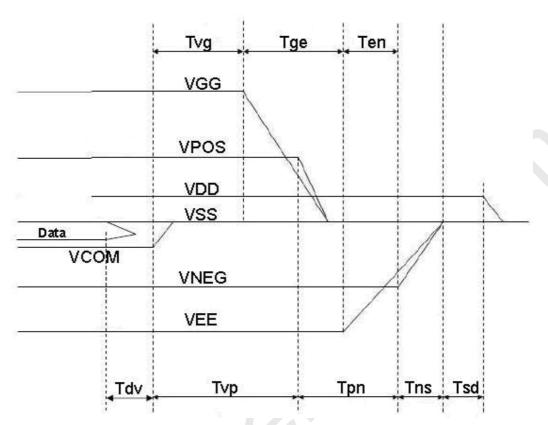


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#### **POWER DOWN**



	Min	Max
Tdv	$100 \mu\mathrm{s}$	-
Tvp	$0 \mu\mathrm{s}$	-
Tpn	0 μ s	-
Tns	-	1000ms
Tsd	$100 \mu\mathrm{s}$	-
Tvg	0 μ s	
Tge	$0 \mu\mathrm{s}$	
Ten	$0 \mu\mathrm{s}$	-

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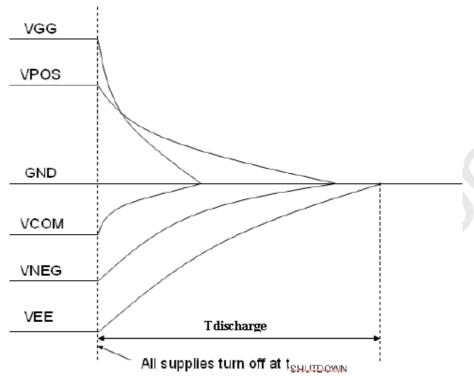
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\*Discharge time Sequence

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Note7-1: Supply voltages decay through pull down resistors.

Note7-2: VEE must remain negative of all other supplies during decay period.

	Min	Max
Tdischarge	-	1000ms

Refresh Rate: The module ED60SC8 is applied at a maximum screen refresh rate of 85Hz.



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# 8. Optical characteristics

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## 8-1) Specification

Measurements are made with that the illumination is under an angle of 45 degrees, the detection is perpendicular unless otherwise specified.

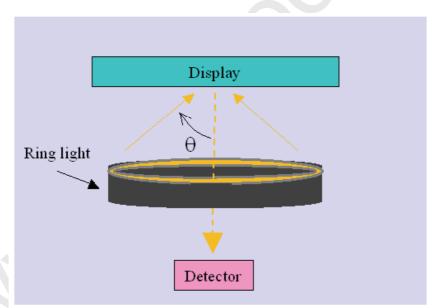
SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT	Note
R	Reflectance	White	30	35	-	%	Note 8-1
Gn	N <sub>th</sub> Grey Level	-	-	DS+(WS-DS)×n/(m-1)	-	L*	-
CR	Contrast Ratio	-	10	12	-		-
T <sub>update</sub>	Update time	2~4-bit mode 1-bit mode		600 250	-	ms ms	-

WS: White state, DS: Dark state, Gray state from Dark to White: DS \ G1 \ G2... \ Gn... \ Gm-2 \ WS m: 4 \ 8 \ 16 when 2 \ 3 \ 4 bits mode

Note 8-1: Luminance meter: Eye – One Pro Spectrophotometer

## 8-2) Definition of contrast ratio

The contrast ratio (CR) is the ratio between the reflectance in a full white area (RI) and the reflectance in a dark area (Rd): CR = RI / Rd





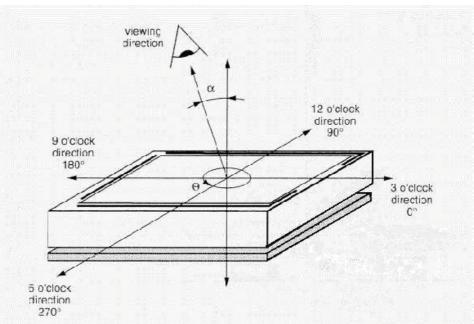
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#### 8-3) Reflection Ratios

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The reflection ratio is expressed as:

R = Reflectance Factor<sub>white board</sub>  $x (L_{center} / L_{white board})$ L<sub>center</sub> is the luminance measured at center in a white area (R=G=B=1). L<sub>white board</sub> is the luminance of a standard white board. Both are measured with equivalent illumination source. The viewing angle shall be no more than 2 degrees.



 $\alpha$  = declination /  $\theta$  = azimuth



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#### HANDLING, SAFETY AND ENVIROMENTAL REQUIREMENTS

#### **WARNING**

The display glass may break when it is dropped or bumped on a hard surface. Handle with care. Should the display break, do not touch the electrophoresis material. In case of contact with electrophoresis material, wash with water and soap.

#### **CAUTION**

The display module should not be exposed to harmful gases, such as acid and alkali gases, which corrode electronics components.

Disassembling the display module can cause permanent damage and invalidates the warranty agreements.

Observe general precautions that are common to handling delicate electronic components. The glass can break and front surfaces can easily be damaged. Moreover the display is sensitive to static electricity and other rough environmental conditions.

## **Data sheet status**

Product specification This data sheet contains final product specifications.

#### **Limiting values**

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification are not implied. Exposure to limiting values for extended periods may affect device reliability.

## **Application information**

Where application information is given, it is advisory and does not form part of the specification.





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## 10. Reliability test

	TEST	CONDITION	METHOD
1	High-Temperature Operation	T = +50℃, RH = 30% for 240 hrs	IEC 60 068-2-2Bp
2	Low-Temperature Operation	T = 0℃ for 240 hrs	IEC 6 0 068-2-2Ab
3	High-Temperature Storage	T = +70℃, RH=23% for 240 hrs (Test In White Pattern)	IEC 60 068-2-2Bp
4	Low-Temperature Storage	T = -25℃ for 240 hrs (Test In White Pattern)	IEC 60 068-2-1Ab
5	High-Temperature, High-Humidity Operation	T = +40℃, RH = 90% for 168 hrs	IEC 60 068-2-3CA
6	High Temperature, High- Humidity Storage	T = +60°C, RH=80% for 240hrs (Test In White Pattern)	IEC 60 068-2-3CA
7	Temperature Cycle	1 cycle:[-25°ℂ 30min]→ [+70°ℂ 30 min] :100 cycles (Test In White Pattern)	IEC 60 068-2-14
8	UV exposure Resistance storage	765 W/m² for 168hrs,40°C (Test In White Pattern)	IEC60 068-2-5Sa
9	Package Vibration	1.04G, Frequency: 10~500Hz Direction: X,Y,Z Duration: 1 hours in each direction	Full packed for shipment
10	Package Drop Impact	Drop from height of 122 cm on concrete surface.  Drop sequence: 1 corner, 3 edges, 6 faces One drop for each.	Full packed for shipment
11	Electrostatic Effect (non-operating)	(Machine model) +/- 250V 0Ω, 200pF	IEC 62179 IEC 62180
12	Altitude test Operation	700hPa ( = 3000m )48hr	
13	Altitude test Storage	260hPa ( = 10000m )48hr (Test In White Pattern)	
14	Stylus Tapping	POLYACETAL Pen: Top R 0.8mm Load: 200gf ,Speed: 30 times/min	

Total 13,500times

Actual EMC level to be measured on customer application

Note: The protective film must be removed before temperature test.

#### < Criteria >

- a. Module should normally work under the normally condition
- b. After reliability test, there is no display NG including line and no image.
- c. Stylus Tapping: no glass breakage or damage to microcapsules.



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## **Bar Code definition**

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<u>E2T</u>	<u>00</u>	<u>4</u>	<u>01</u>	<u>1</u>	<u>l</u> _	<u> 7</u>	<u>4</u>	<u>00361</u>	<u>A</u>	
1	2	3	4	2	5	6	2	7	2	8

#### 1-EPD Model Code

<b>EPD Model Code</b>	Part Number		
E2T	ED060SC8		

# 2-Internal Control Codes

DO NOT CARE

#### 3-FPL Version Code

<b>FPL Version Code</b>	Platform
3	Eink ver. 2.3
4	Eink ver. V110
5	Eink ver. V110A
6	Eink ver. V220C
7	Eink ver. V250
8	Eink ver. V220E

#### 4-FPL Batch Code

FPL Batch Code	Translation
01-99	001-099
A0-A9	100-109
B0-B9	110-119
•	•
Z0-Z9	320-329

#### 5-Year

Year	Translation
F	2005
G	2006
Н	2007
	2008
J	2009
K	2010
Z	2025

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#### 6-Month

Month	Translation
1	Jan
2	Feb
3	Mar
4	Apr
5	May
6	Jun
7	Jul
8	Aug
9	Sep
Α	Oct
В	Nov
С	Dec

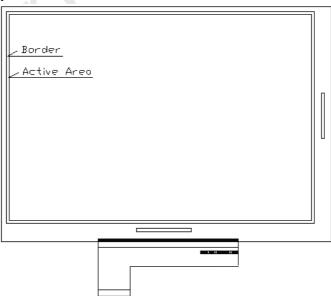
#### 7-Serial Number

ı	Serial Number
ı	00000-99999

## 8-Module Manufacturer Code

Module	Translation
Manufacturer	
Code	
Т	TOC
Р	EIH

#### 12. Border definition



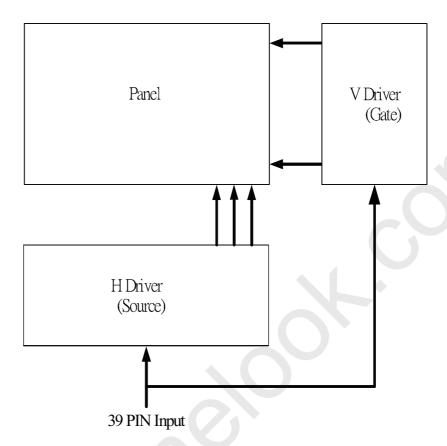
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## 13. Block Diagram

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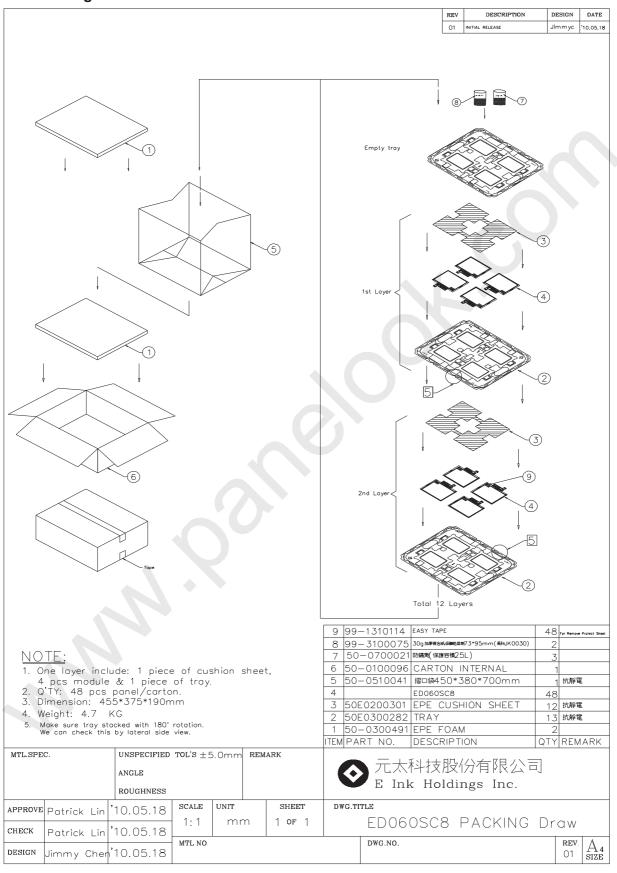






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# 14. Packing



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