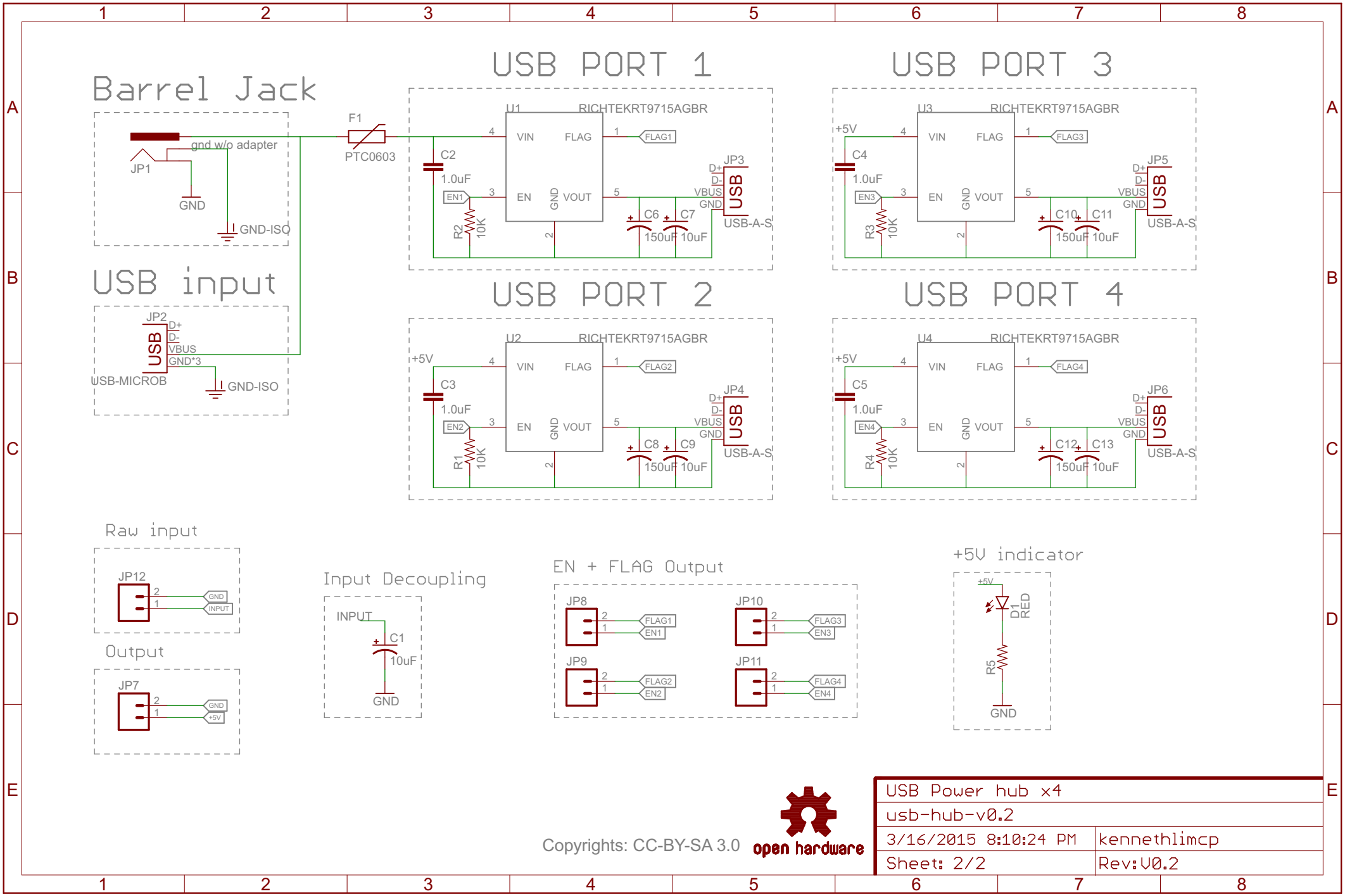
Improvements from V0.1:

- Fixed GND vias for USB port (some were not named as GND and left isolated)
- Add in silkscreen for input + output header pins (clearer for users)
- Spaced out USB ports to have 6mm distance in between (support more devices)
- increased thickness for most power traces (improve power dissipation)
- Increase Isolate to 15mil for 5V plane (increase manufacturability)
- Added bStop for USB pads! (expose the USB pins on bottom layer)
- version silkscreen on pcb to v0.2
- reduced plane size to prepare for panelizing


Barrel Jack
JP1
gnd w/o adapter
GND
GND-ISO
F1
PTC0603
USB input
JP2
USB
D+
D-
VBUS
GND*3
USB-MICROB
GND-ISO
USB PORT 1
U1
RICHTEKRT9715AGBR
VIN
FLAG
EN
GND
VOUT
FLAG1
EN1
C2
1.0uF
R2
10K
C6
150uF
C7
10uF
JP3
VBUS
GND
USB-A-S
USB PORT 2
U2
FLAG2
EN2
C3
R1
C8
C9
JP4
USB PORT 3
U3
FLAG3
EN3
C4
R3
C10
C11
JP5
USB PORT 4
U4
FLAG4
EN4
C5
R4
C12
C13
JP6
+5V
Raw input
JP12
INPUT
Output
JP7
Input Decoupling
C1
EN + FLAG Output
JP8
JP9
JP10
JP11
+5V indicator
D1
RED
R5
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open hardware
USB Power hub x4
usb-hub-v0.2
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Sheet: 2/2
Rev:V0.2