Version: <u>**2.0**</u>

TECHNICAL SPECIFICATION

MODEL NO: ED060SC7

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Customer's Confirmation				
Customer				
Date				
Ву				
EIH's Confirmation				

Confirmed By 申婚神.



Revision History

Rev.	Issued Date	Revised Contents
1.0	September, 14,2010	New
2.0	Nov 15, 2010	<u>Modify</u>
		Page 5 4. Mechanical Drawing of EPD module
		Outline drawing delete VCOM LABEL
		Page 14 7. Power on Sequence
		<u>Add</u>
		Page 16 8. Discharge time Sequence

TECHNICAL SPECIFICATION <u>CONTENTS</u>

NO.	ITEM	PAGE
-	Cover	1
-	Revision History	2
-	Contents	3
1	Application	4
2	Features	4
3	Mechanical Specifications	4
4	Mechanical Drawing of EPD module	5
5	Input/Output Terminals	7
6	Electrical Characteristics	9
7	Power on Sequence	14
8	Discharge time Sequence	16
9	Optical Characteristics	17
10	Handing, Safety and Environment Requirements	19
11	Reliability test	20
12	Bar Code definition	21
13	Border definition	21
14	Block Diagram	22
15	Packing	23

1. Application

The display is a TFT active matrix electrophoretic display, with associated interface and control logic, and a reference system design.

The 6" active area contains 600 x 800 pixels, the display is capable to display images at 2-16 gray levels (1-4 bits) depending on the display controller and the associated waveform file used.

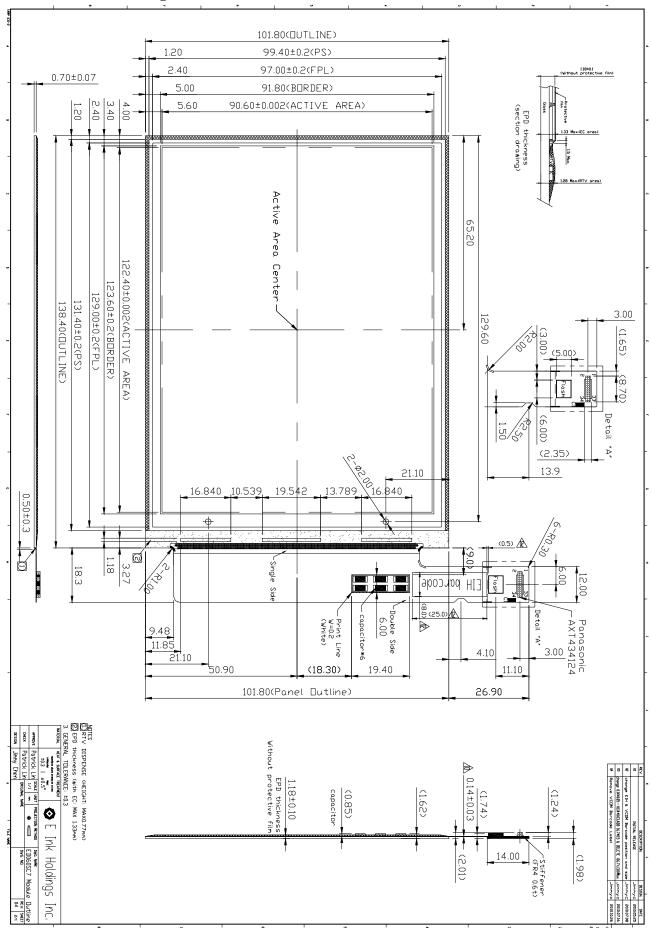
2. Features

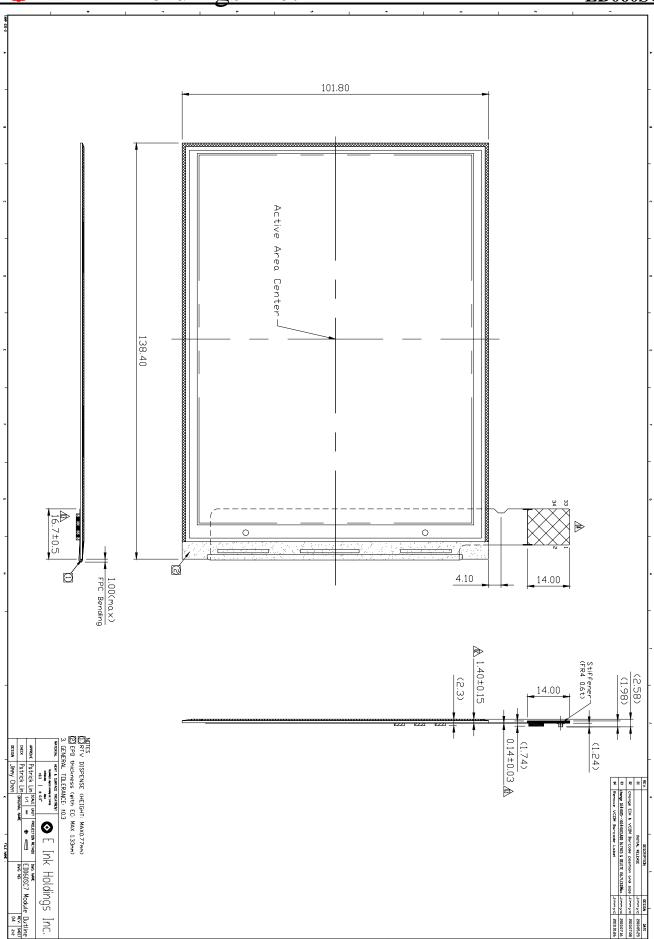
- ➤ High contrast TFT electrophoretic
- ➤ 600 x 800 display
- ➤ High reflectance
- ➤ Ultra wide viewing angle
- ➤ Ultra low power consumption
- > Pure reflective mode
- ➤ Bi-stable
- Commercial temperature range
- ➤ Landscape, portrait mode
- ➤ Antiglare hard-coated front-surface

3. Mechanical Specifications

Parameter	Specifications	Unit	Remark
Screen Size	6.0 (3:4 diagonal)	Inch	
Display Resolution	600 (H)×800(V)	Pixel	
Active Area	90.6 (H)×122.4 (V)	mm	
Pixel Pitch	0.151 (H)×0.153 (V)	mm	
Pixel Configuration	Rectangle		
Outline Dimension	101.8(W)×138.4(H)×1.18(D) (panel area height)	mm	
Module Weight	34±3.4	g	

4. Mechanical Drawing of EPD Module







5. Input/Output Interface5-1) Connector type: AXT434124

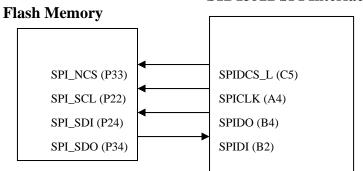
Pin Assignment

Pin#	Signal	Description	Remark
1	VNEG	Negative power supply source driver	
2	VPOS	Positive power supply source driver	
3	VNEG	Negative power supply source driver	
4	VPOS	Positive power supply source driver	
5	VDD	Digital power supply drivers	
6	VSS	Ground	
7	VDD	Digital power supply drivers	
8	VSS	Ground	
9	XCL	Clock source driver	
10	XLE	Latch enable source driver	
11	XOE	Output enable source driver	
12	XSTL	Start pulse source driver	
13	D0	Data signal source driver	
14	D1	Data signal source driver	
15	D2	Data signal source driver	
16	D3	Data signal source driver	
17	D4	Data signal source driver	
18	D5	Data signal source driver	
19	D6	Data signal source driver	
20	D7	Data signal source driver	
21	VCOM	Common connection	
22	SPI_SCL	Serial Data Clock for Flash memory	Note5-1
23	VCOM	Common connection	
24	SPI_SDI	Serial Data Input for Flash memory	Note5-1
25	VGG	Positive power supply gate driver	
26	MODE1	Output mode selection gate driver	
27	VEE	Negative power supply gate driver	
28	CKV	Clock gate driver	
29	VEE	Negative power supply gate driver	
30	SPV	Start pulse gate driver	
31	VSS	Ground	
32	BORDER	Border connection	
33	SPI_NCS	Chip Select for Flash memory	Note5-1
34	SPI_SDO	Serial Data Output for Flash memory	Note5-1

Note 5-1

S1D13521 SPI Interface Flash Memory SPI_NCS (P33) SPI_SCL (P22) SPI_SDI (P24) SPI_SDO (P34) SPI_SDO (P34) SPI_SDO (P34)

S1D13522 SPI Interface



6.Electrical Characteristics

6-1) Absolute maximum rating

Parameter	Symbol	Rating	Unit
Logic Supply Voltage	VDD	-0.3 to +7	V
Positive Supply Voltage	V_{POS}	-0.3 to +18	V
Negative Supply Voltage	$V_{ m NEG}$	+0.3 to -18	V
Max .Drive Voltage Range	V_{POS} - V_{NEG}	36	V
Supply Voltage	VGG	-0.3 to +45	V
Supply Voltage	VEE	-25.0 to +0.3	V
Supply Range	VGG-VEE	-0.3 to +45	V
Operating Temp. Range	TOTR	0 to +50	$^{\circ}\!\mathbb{C}$
Storage Temperature	TSTG	-25 to +70	$^{\circ}\!\mathbb{C}$

6-2) Panel DC characteristics

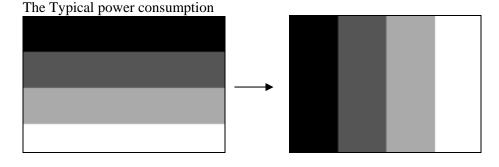
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Signal ground	V_{SS}		-	0	-	V
Lasia Waltana aumulu	V_{DD}		3.0	3.3	3.6	V
Logic Voltage supply	I_{VDD}	$V_{DD}=3.3V$	-	1.05	3.15	mA
Coto Nogotivo gumply	V_{EE}		-21	-20	-19	V
Gate Negative supply	${ m I}_{ m EE}$	$V_{EE} = -20V$	-	0.8	2.4	mA
Coto Docitivo cumply	V_{GG}		21	22	23	V
Gate Positive supply	I_{GG}	$V_{GG} = 22V$	-	0.8	2.4	mA
Course Negative supply	V_{NEG}		-15.4	-15	-14.6	V
Source Negative supply	I_{NEG}	$V_{NEG} = -15V$	-	18	36	mA
Course Desitive supply	V_{POS}		14.6	15	15.4	V
Source Positive supply	I_{POS}	$V_{POS} = 15V$	-	16	32	mA
D 1 1	3 7	$V_{POS} = 15V$	14.6	15	15.4	V
Border supply	V_{Border}	$V_{NEG} = -15V$	-15.4	-15	-14.6	V
Asymmetry source	V_{Asym}	$V_{POS} \!\!+\! V_{NEG}$	-800	0	800	mV
Common voltage	V_{COM}		-2.5	Adjusted	-0.3	V
Common voltage	I_{COM}		_	0.25	-	mA
Maximum power panel	P_{MAX}		-	-	1131	mW
Standby power panel	P _{STBY}		-	-	0.4	mW
Typical power panel	P_{TYP}		-	547	-	mW
Operating temperature			0	-	50	$^{\circ}\!\mathbb{C}$
Storage temperature			-25	-	70	$^{\circ}\mathbb{C}$





- The Typical power consumption is measured with following pattern transition: from horizontal 4 gray scale pattern to vertical 4 gray scale pattern. (Note 6-1)
- The standby power is the consumed power when the panel controller is in standby mode.
- The listed electrical/optical characteristics are only guaranteed under the controller & waveform provided by EIH
- Vcom is recommended to be set in the range of assigned value \pm 0.1V
- The maximum I_{COM} inrush current is about 800 mA

Note 6-1

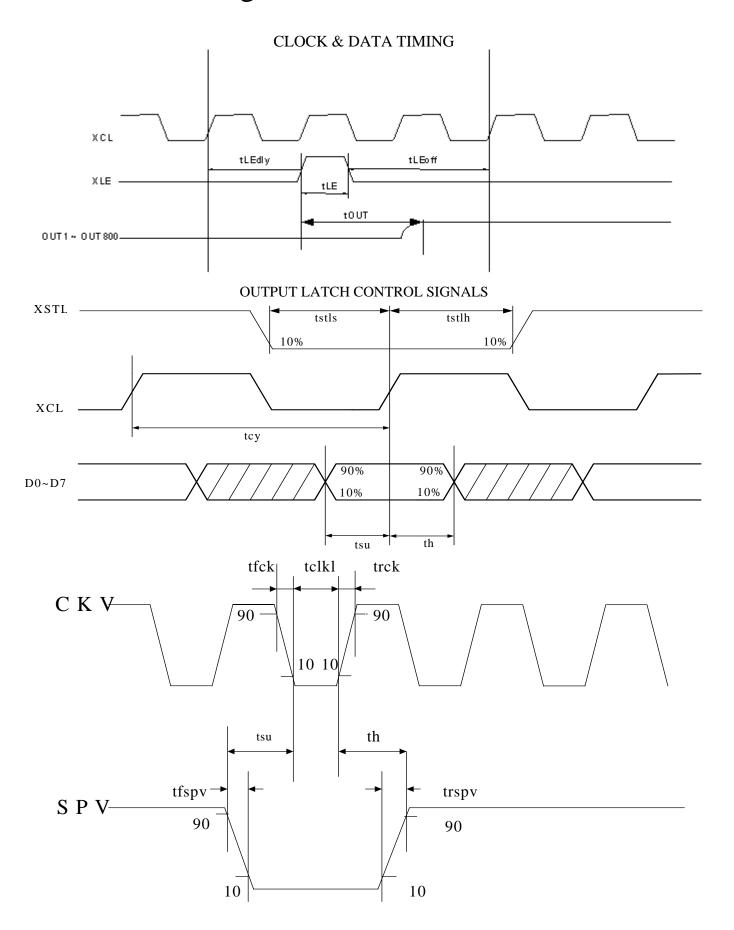




6-3) Panel AC characteristics

VDD=3.0V to 3.6V, unless otherwise specified.

Parameter	Symbol	Min.	Тур.	Max.	Unit	App Pin	
Clock frequency	fckv	-	-	200	kHz		
Minimum "L" clock pulse width	twL	0.5	-	-	us	CKV	
Clock rise time	trckv	-	-	100	ns	CKV	
Clock fall time	tfckv	-	-	100	ns		
Data setup time	tSU	100	-	-	ns	CKM CDM	
Data hold time	tH	100	-	-	ns	CKV, SPV	
Pulse rise time	trspv	-	-	100	ns	CDV	
Pulse fall time	tfspv	-	-	100	ns	SPV	
Clock XCL cycle time	tcy	50	-	DC	ns		
D0D7 setup time	tsu	8	-	-	ns		
D0 D7 hold time	th	1	-	-	ns		
XLE on delay time	tLEdly	40	-	-	ns	Below	
XLE high-level pulse width	tLEw	40	-	-	ns	table	
XLE off delay time	tLEoff	200	-	-	ns		
Output setting time to +/- 30mV(C _{load} =200pF)	tout	-	-	12	us		



6-4) Power Consumption

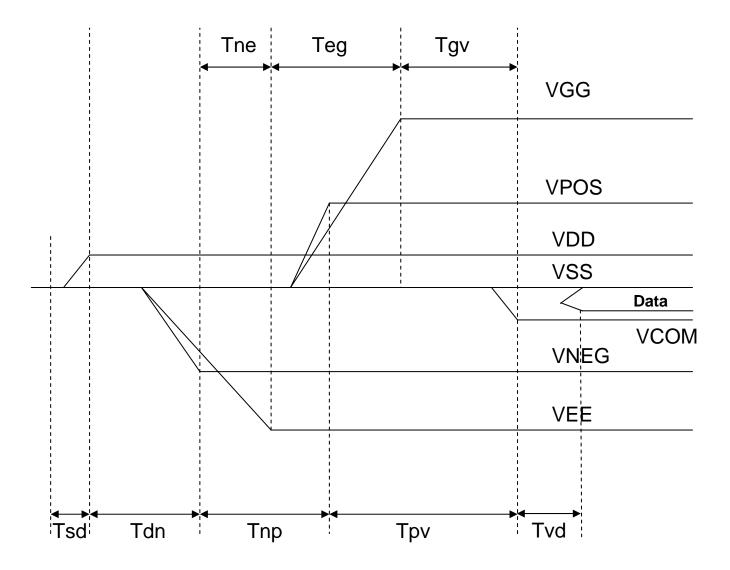
Parameter	Symbol	Conditions	TYP	Max	Unit	Remark
Panel power consumption during update.	-	-	547	1131	mW	
Power consumption in standby mode	-	-	-	0.4	mW	

7.Power on Sequence

Power Rails must be sequenced in the following order:

- 1. VSS → VDD → VNEG → VPOS (Source driver) → VCOM
- 2. VSS → VDD → VEE → VGG (Gate driver)

POWER ON

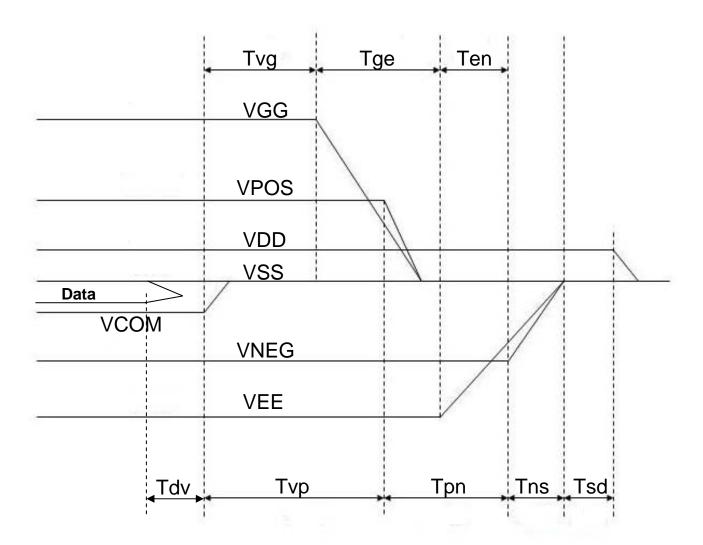


	Min	Max
Tsd	100us	-
Tdn	100us	-
Tnp	1000us	-
Tpv	100us	-
Tvd	100us	-
Tne	0us	-
Teg	1000us	-
Tgv	100us	-

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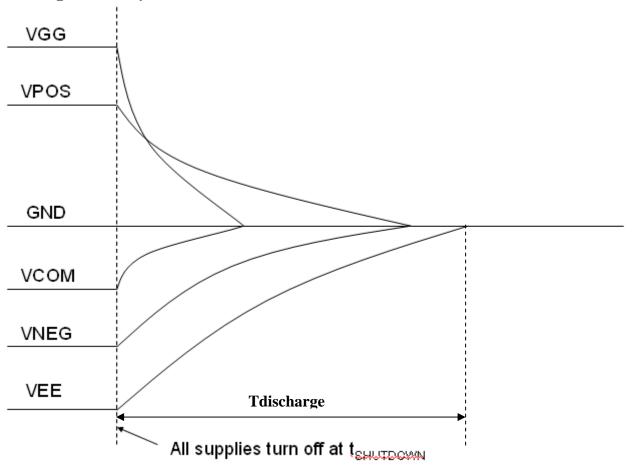
PAGE:14

POWER DOWN



	Min	Max
Tdv	$100\mu\mathrm{s}$	-
Tvp	$0 \mu\mathrm{s}$	-
Tpn	$0\mu\mathrm{s}$	-
Tns	-	1000ms
Tsd	$100\mu\mathrm{s}$	-
Tvg	$0 \mu\mathrm{s}$	-
Tge	$0\mu\mathrm{s}$	-
Ten	$0\mu\mathrm{s}$	-

8. Discharge time Sequence



Note8-1: Supply voltages decay through pulldown resistors.

Note8-2: VEE must remain negative of all other supplies during decay period.

8-1) Refresh Rate

The module ED60SC7 is applied at a maximum screen refresh rate of 85Hz.

	Min	Max
Refresh Rate	-	85Hz

9. Optical characteristics

9-1) Specifications

Measurements are made with that the illumination is under an angle of 30 degrees, the detection is perpendicular unless otherwise specified.

 $T = 25^{\circ}C$

Symbol	Parameter	Conditions	Min	Тур.	Max	Unit	Note
R	Reflectance	White	30	40	-	%	Note 9-1
Gn	N _{th} Grey Level	-	-	DS+(WS-DS) ×n/(m-1)	-	L*	-
CR	Contrast Ratio	-	10	12	-		-

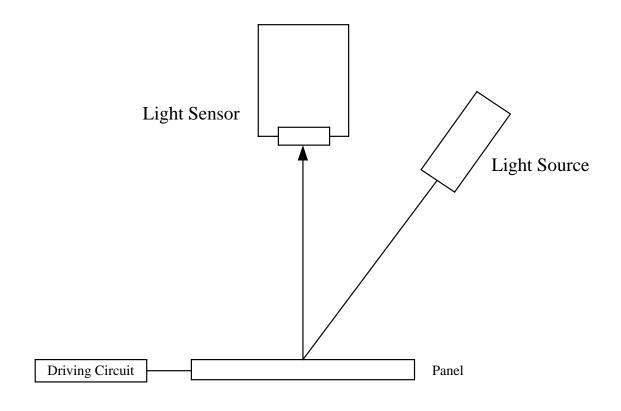
WS: White state , DS: Dark state, Gray state from Dark to White :DS \cdot G1 \cdot G2... \cdot Gn... \cdot Gm-2 \cdot WS m:4 \cdot 8 \cdot 16 when 2 \cdot 3 \cdot 4 bits mode

Note 9-1: Luminance meter: Eye – One Pro Spectrophotometer

9-2) Definition of contrast ratio

The contrast ratio (CR) is the ratio between the reflectance in a full white area (Rl) and the reflectance in a dark area (Rd):

CR = Rl/Rd

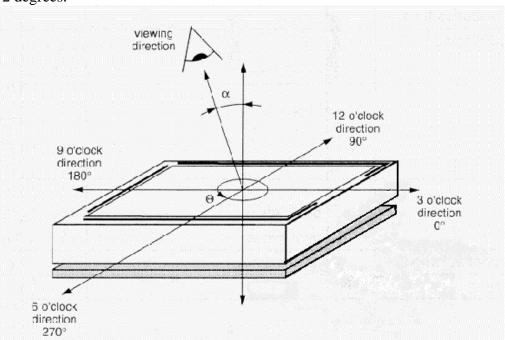


9-3) Reflection Ratio

The reflection ratio is expressed as:

 $R = Reflectance \ Factor_{white \ board} \quad x \quad (\ L_{center} \ / \ L_{white \ board})$

 L_{center} is the luminance measured at center in a white area (R=G=B=1). $L_{white \ board}$ is the luminance of a standard white board. Both are measured with equivalent illumination source. The viewing angle shall be no more than 2 degrees.



 $\alpha = declination / \theta = azimuth$

10.HANDLING, SAFETY AND ENVIROMENTAL REQUIREMENTS

WARNING

The display glass may break when it is dropped or bumped on a hard surface. Handle with care. Should the display break, do not touch the electrophoretic material. In case of contact with electrophoretic material, wash with water and soap.

CAUTION

The display module should not be exposed to harmful gases, such as acid and alkali gases, which corrode electronics components.

Disassembling the display module can cause permanent damage and invalidates the warranty agreements.

Observe general precautions that are common to handling delicate electronic components. The glass can break and front surfaces can easily be damaged. Moreover the display is sensitive to static electricity and other rough environmental conditions.

Data sheet status					
Product	This data sheet contains final product specifications.				
specificatio					
n					

Limiting values

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.



ED060SC7

11. Keli	iability test		
	TEST	CONDITION	METHOD
1	High-Temperature Operation	T = +50°C, RH = 30% for 240 hrs	IEC 60 068-2-2Bp
2	Low-Temperature Operation	T = 0°C for 240 hrs	IEC 60 068-2-2Ab
3	High-Temperature Storage	T = +70°C, RH=23% for 240 hrs Test in white pattern	IEC 60 068-2-2Bp
4	Low-Temperature Storage	T = -25°C for 240 hrs Test in white pattern	IEC 60 068-2-1Ab
5	High-Temperature, High-Humidity Operation	T = +40°C, RH = 90% for 168 hrs	IEC 60 068-2-3CA
6	High Temperature, High- Humidity Storage	T = +60°C, RH=80% for 240hrs Test in white pattern	IEC 60 068-2-3CA
7	Temperature Cycle	-25°C →+70°C, 100 Cycles 30min 30min Test in white pattern	IEC 60 068-2-14
8	UV exposure Resistance	765 W/m² for 168hrs,40°C Test in white pattern	IEC60 068-2-5Sa
9	Package Vibration	1.04G, Frequency: 10~500Hz Direction: X,Y,Z Duration: 1 hours in each direction	Full packed for shipment
10	Package Drop Impact	Drop from height of 122 cm on concrete surface. Drop sequence: 1 corner, 3 edges, 6 faces One drop for each.	full packed for shipment
11	Electrostatic Effect (non-operating)	(Machine model)+/- 250V 0 Ω , 200pF	IEC 62179, IEC 62180
12	Altitude test Operation	700hPa (= 3000m),48Hr	
13	Altitude test Storage	260hPa (= 10000m),48Hr Test in white pattern	
14	Stylus Tapping	POLYACETAL Pen: Top R:0.8mm Load: 300gf Speed: 2 times/sec Total 13,500times,	

Actual EMC level to be measured on customer application Note: The protective film must be removed before temperature test.

12.Bar Code definition

E01 00 4 01 1 <u>7</u> <u>A</u> 5 1 2 3 4 2 6 2 7 2 8

1 : EPD model code:

ED060SC7: E2N/E32/E31/E30

2 : Internal control codes:

3 : FPL reversion code

V110:4 V110A:5 V220:6 V250:7 V220E:8

4 : FPL batch code:

 $(BL/P/B...)001 \sim 009:01 \sim 99,\ 100 \sim 109:A0 \sim A9,\ 110 - 119:B0 \sim B9...\ 320 \sim 329:Z0 \sim Z9$

5 : Year:

F: 2005 / G: 2006 / H: 2007 / I: 2008 /... / Z: 2025

6 : Month:

1:Jan. 2:Feb. ... 9:Sep. A:Oct. B:Nov. C:Dec.

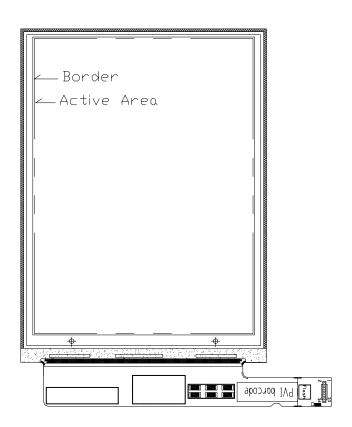
7 : Serial number

00000-99999

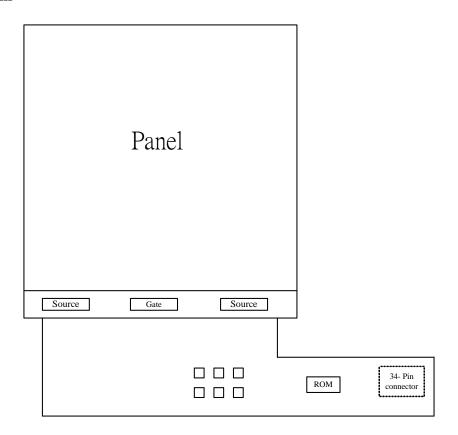
8 : MFG code:

TOC:M, EIH:N

13. Border definition



14.Block Diagram



15.Packing

