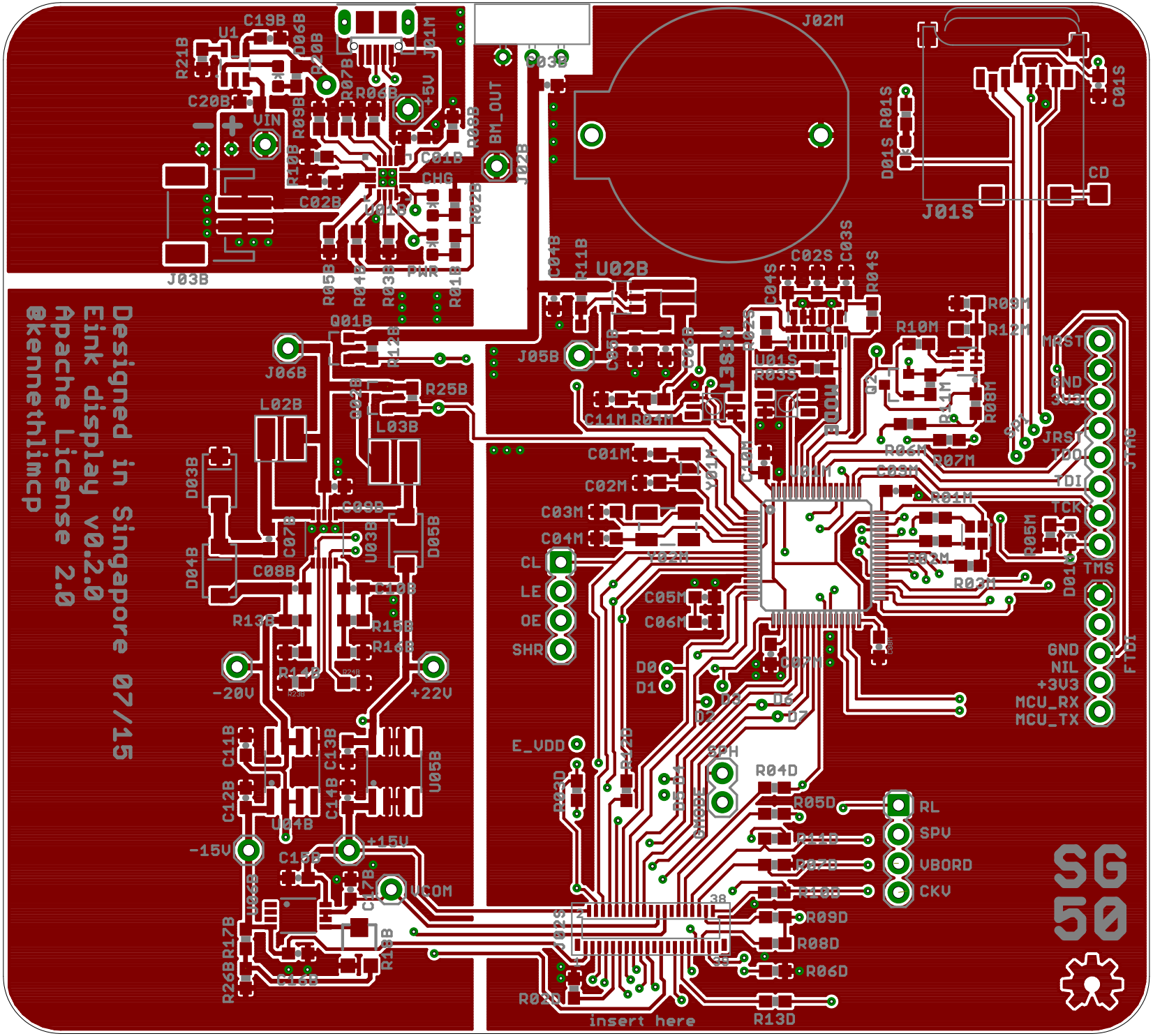
Designed in Singapore 07/15
Eink display v0.2.0
Apache License 2.0
@kennethlimcp
J02M
J01S
J03B
J01M
J02B
J05B
J06B
U02B
U01S
U01M
U02M
U03B
U04B
U05B
VIN
BM_OUT
+5V
CHG
PWR
RESET
BUSY
CL
LE
OE
SHR
E_VDD
SPH
GATE
RL
SPV
VBORD
CKV
-20V
+22V
-15V
+15V
VCOM
MOST
GND
3V3
JRST
TDO
TDI
TCK
TMS
JTAG
GND
NIL
+3V3
MCU_RX
MCU_TX
FTDI
CD
D01S R01S
insert here
SG
50