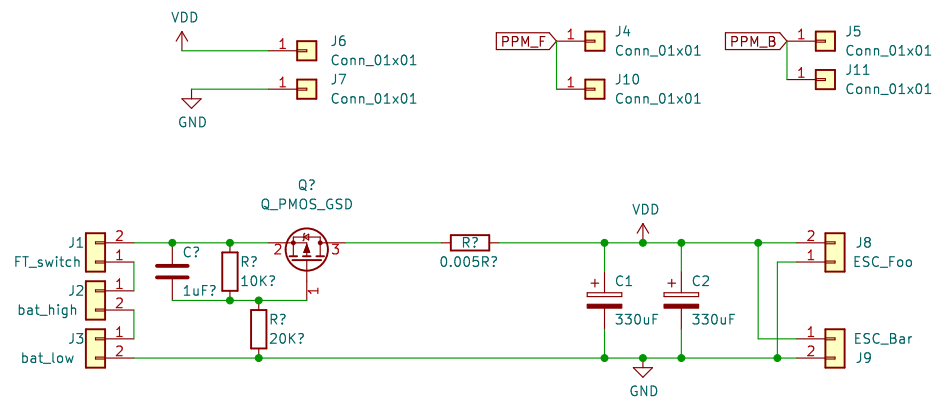


TODO:
current sensing
removable current limiting – <https://www.powerctc.com/en/node/4564>

Errata:
JUST FLIP AN ESC UPSIDE DOWN TO FIX TOPOLOGY ISSUES
Investigate using traces with vias for high current
10mm 2oz traces on both layers should carry enough current
OR route all non-trace wires (2.5mm) on back side
raise signal wire ziptie holes
XT-30 FP polarity is wrong



$70A/4096 = 0.017A$ per div, but you def need an amp for this.

Sheet: /
File: spin-tgd-power.kicad_sch

Title:

Size: A4
KiCad E.D.A. kicad (6.0.5)

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