Section: 6A

## National University of Computer and Emerging Sciences, Lahore Campus



Course: Program: Duration:

Paper Date:

Section:

Exam:

Operating Systems BS(Computer Science)

30 Minutes 6-Feb-2025

6A Quiz 1 Course Code: Semester:

CS 2006 Spring 2025

Total Marks:

10

Weight Page(s):

2

Answer the questions in the given available space. CLO1 [2+2+2+2 = 10 Marks]

Q:1 When an interrupt occurs then how CPU manages to handle that interrupt?

interrupt is a signal that demands CPU attention to solve any as function/tosk. when an interrupt occur CPU save the current instruction address and executes the ISR Cinterrupt service routine) ISR unch the Interrupt type and executes the interrupt. (Pu then continues from where it has shopped.

Q:2 How does Direct Memory Access (DMA) improve system performance?

DHA is a technique in which hardware device directly interacts with the memory with at any implement of CPU. So since Mare no incomment of CPU it 1/8 has less workload and have less three is consumed in data transfer

Q:3 What is the role of a device driver in managing the interaction between the operating system and a hardware device?

device driver is a sufficient programe that helps the commonwheath between Os and device controller.

for e.g. device controller is connected to each device.

when a read | with operation commes it translate it so made the consuler commes it translate it.

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Q:4 What is the main purpose of the Device Status Table (DST) in an operating system? What kind of information does a Device Status Table typically?

Device Status Table holds the status of the device so eg if it is take or not our (fundaring or not hundring).

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1.5

Q:5 Why use APIs (Application Programming Interfaces) rather than direct system calls?

System caus provides on interface betwee user and as

it pupose is no provide allow program to request services eng

fre mandling. API make the usage of System calls- easier

eng

win 32 API (for windows).

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