# Project 1: Spartan6 - DSP48A1

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| RTL code | DFFlop: mux\_2:  **D_FFlop_Codemux_2_Code**Common unit: mux\_4:  Common_unit_Code  mux_4_Code |
| Code_1Spartan6 - DSP48A1:  Code_2 |
| Code_3  Code_4 |
| Testbench | Tb_1  Tb_2 |
| Waveform | WF_1  WF_2WF_3 |
| **DO** | Do_file |
| **Elaboration** | Schematic:  EB_SCH  Message window  EB_Mess |
| **Synthesis** | SYN_SCH  SYN_Mess  SYN_Time  SYN_UTI |
| **Implement** | Imp_Device  Imp_Mess  Imp_Time  Imp_UTI |
| **Constraint**  **file** | Const |