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SPI Driver Instance 0

SPI Module ID

SPI_ID_1

+

Driver Mode

SPI Interrupt Priority

INT_PRIORITY_LEVEL1

SPI Interrupt Sub-priority

INT_SUBPRIORITY_LEVEL0

+

Master \Slave Mode

+

Data Width

+

Buffer Mode

☐

Allow Idle Run

Protocol Type

DRV_SPI_PROTOCOL_TYPE_STANDARD

Baud Clock Source

SPI_BAUD_RATE_PBCLK_CLOCK

Clock \Baud Rate - Hz

1000000

Clock Mode

DRV_SPI_CLOCK_MODE_IDLE_LOW_EDGE_FALL

Input Phase

SPI_INPUT_SAMPLING_PHASE_AT_END

Dummy Byte Value

0xFF

Max Jobs In Queue

10

Minimum Number Of Job Queue Reserved For Instance

1