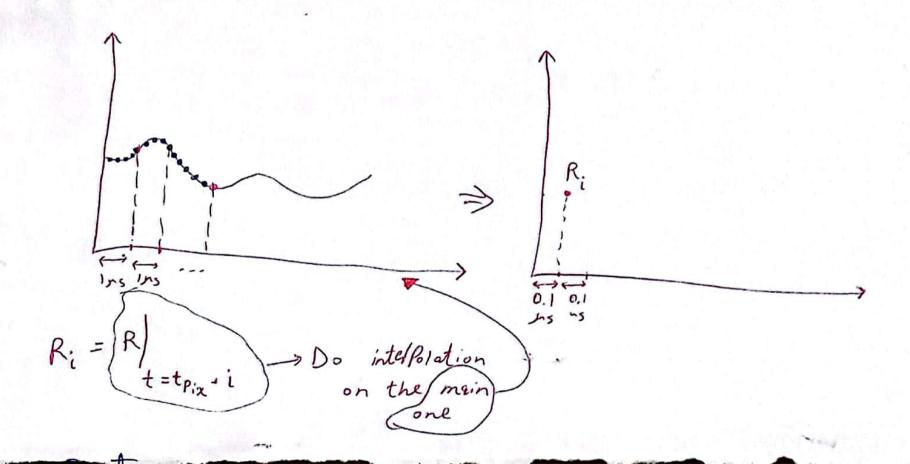


somple Predection/Interpolation

tif N-Samples \* t Pix \* if tpix < Ins (min sample + Conv. time)

\* Then: A number of samples to be Predected = N. sample ( Per frame)



## Sample Prediction (For signals to Frank)

```
* Cafture samples at maximum late (continions mode)
* Get the interPolated "sampleBuffer []" using the function:
    "interPolate (Sample Buffer, tp:v)
* The function "interPolate ()" works as follows:
    u16 Sample Buffer InterPolated [N_Samples];
    for (us i=0; i< N_Samples; i++)
       t = i * tpix ;
       t = (t/tcox) * tconv.
        to t + tony.
        S. = Sample Buffer [i];
        Sz = Sample Buffer [i+1];
       \frac{s-s_1}{t-t_1} = \frac{+s_1+s_1}{t_1-t_1} \Rightarrow s = \frac{(s_1-s_1)}{t_1-t_1} + s_1
                                        MUST
Be "signed"
        sample Buffer InterPolated [i] = 5 i
```

**CS** CamScanner

How to Sync?

wait for rising effe with a timeout ();

Enable DMA to copy from ADC to buffer;

Wait for DMA TC;

Draw i

RWM @ F. SAMPILITO ADC

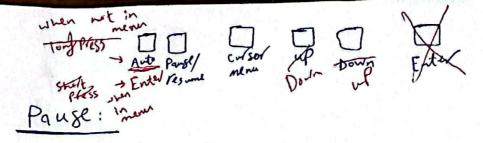
Buffer

init

init

init

init



On Press, EXTI ISR is executed.

if Previously Panget, Toggle "Pangey" flag.

In main loop drawing, let the code segment of taking new array of samples be conditioned with that flag. Drawing is still on, but for the same values.

\* When "Consor" button is Pressed:

OPEN Consor button is Pressed:

Add Ist's' (~)

\* Fence of the 4 has 2 values : move I ver's (ii doed)

\* Eeach consor of the 4 has 2 values : move I ver's (ii doed)

\* In main loop drawing, check "enabled" of each of the consors and draw than (if enabled) according to their "Pos"

\* Drawing voltage consor: After done drawing "PixAM-i[]"

• Draw a dashed line @: PixAM [#][Post of the consor)

with color of "consor1\_color"

· i.e.: @ Pix Arr[y][Pos,]; y ∈ [[o:dash\_ungth],[2+dash\_ungth:3+dash\_ungth],...]

· Notice: - (n/dash-length) must be an integer.
- last/max 'y' must be n-dash-length.

this will slav. line of anily PIXANT] Drawing time cursor: Draw a dashed line ( Pix Arr [ pos ] [x] ) only if f (Pos == reg(Count) · XE { [o:cutsor-with], [2\*cutsor-with:3+cutsor-wigth], ...} · Notice: - (128/cursor-length) must be an integer.

moving bottoms

on press, ext ISR is executed. increment or decrement "Pos" of the "selected" if "enabled" When in cursor menu, disable the interrult of the these buttons temprary.

I Notice that drawing a dashed time cursor Will Slow down like drawing, faster to evid this, make it a solid line from a=0 1 x=127 by DMA

Change divs Stime

Time

No Itale (if not addy)

Add Store (if not addy)

Time (if not addy)

Controls

Time (if not addy)

2 (if not addy) Pemove Stoled (if added)

Pemove (if added)

Time (if added)

2 (if added)

> select (if added)

Time (if added)

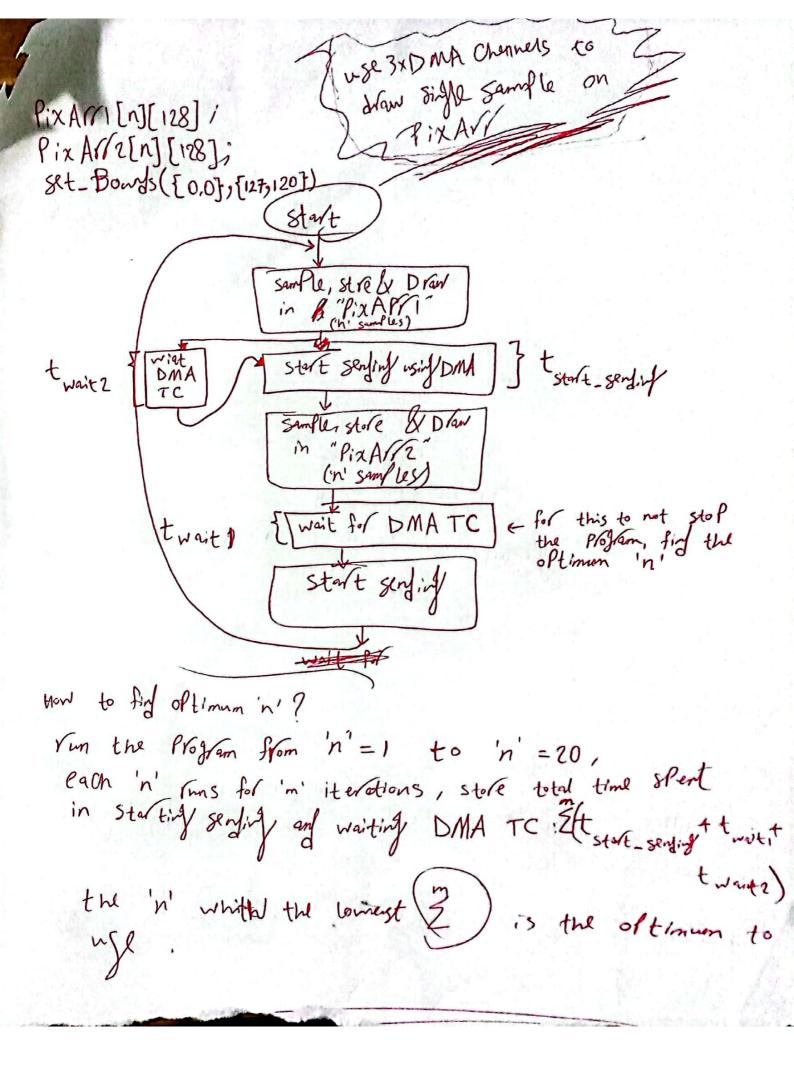
> clif added)

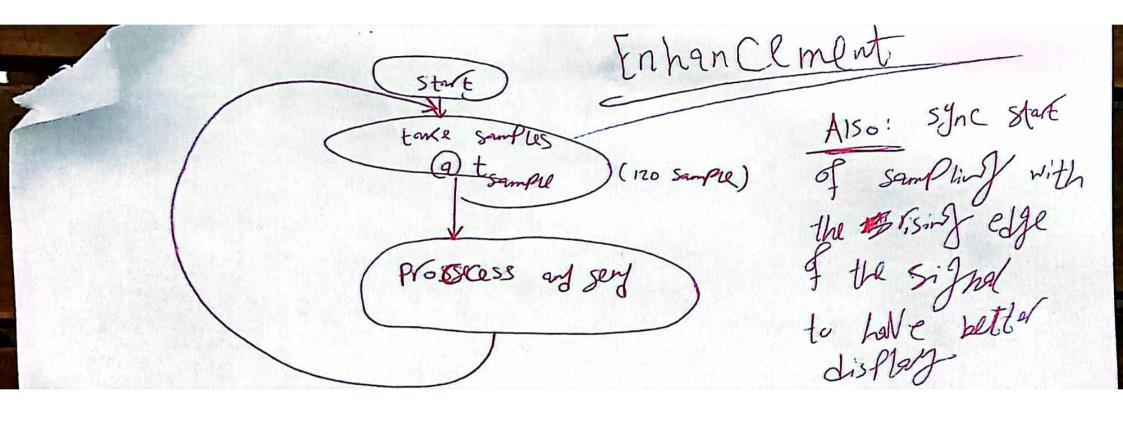
Time (if added)

> clif added)

> clif added)

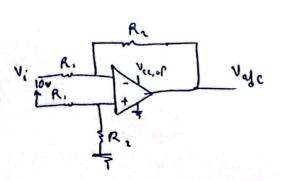
> clif added) ATTHE (+), (-) buttons can either move a sciencely cursor, or charge a science div, based on the last sciences one of the two. \* Charging Voltage div may change infut channel.







For Vin € (-10:410)



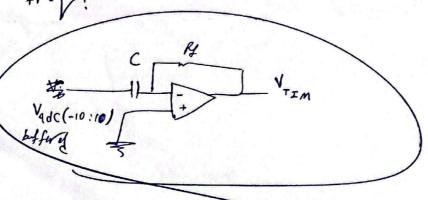
$$\frac{R_1}{R_1} = 0.165$$

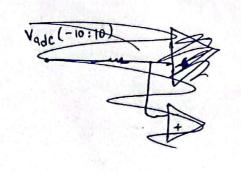
Notice: Vec, of is the Vec value that make the of amp output 3,3 v (2) saturation

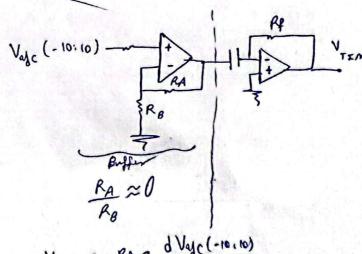
(tested to and found:

v )









VIEW = - REC d Vayc (-10,10)



* Action	timl	•
Reglig ADC_DR	2~3 ms	
redily redister		
scroll (using your func.	5 2-5	
GPIO set Pin	USS than Ins	Disply man
SPI Set frame format macro	7 ms	
SPI transmit usiff for loop for 128+ 16-bit	128 25	
SPI transmit using for bold for 2.128 g-blt	147 ms > = 0,574 ms	(
sot bords	2~3~9	
w/t Cod	2~345	
0 0	\$000	

Exellent works

\* Now devide drawing process to 3 Parts:

- (1) SCroll, regADC, set smallest & burgest, set bond.s, d'en from 0 to 'smallest
- (3) Draw from smallest to largest.
- 3 Draw from largest+1 to 127.

As noticed, one line draw time is not detrministic. Hence, make the whole operation of drawing a line (the previous 3 Parts) times triggered, to obtain Constant timing of them. Here's a Q: why isn't it determined the min: 3 ns, and: 50 ~ 60 ms, mad: 90 ms.

+ Note: Hay to Charge man ackney to 125, and add 2 Pixels to any DMA transfer, so that the smallest Possible number of data to be transferry (0) never harpens, as it does not trigger FC interret, which causes flow fault lefic.

+ Note: To trigger start of operation, use basic timer, more eco.

