

Pin diagram of the FTSH-105-01-LD microcontroller. The diagram shows a 48-pin package with pins numbered 1 to 48. Pins 1-10 are labeled with functions: RESETn, SWCLK, SWDIO, I2C_GND, VDD_ANALOG, and FTDSP. Pins 11-16 are labeled with functions: ADC_CS, ADC_RESET, ADC_START, ADC_DRDY, RS485_DIR, and SWDIO. Pins 17-22 are labeled with functions: SWCLK, PA0, PA1, PA2, PA3, PA4, PA5, PA6, PA7, PA8, PA9, PA10, PA11, PA12, PA13, PA14, and PA15. Pins 23-28 are labeled with functions: PB0, PB1, PB2, PB3, PB4, PB5, PB6, PB7, PB8, PB9, PB10, PB11, PB12, PB13, PB14, and PB15. Pins 29-34 are labeled with functions: SPI_TEMP, SPI_CS, SPI_MOSI, SPI_MISO, I2C_SDA, I2C_SCL, and I2C_GND. Pins 35-40 are labeled with functions: I2C_GND, I2C_SDA, I2C_SCL, I2C_GND, I2C_SDA, I2C_SCL, I2C_GND, and I2C_SDA. Pins 41-46 are labeled with functions: I2C_GND, I2C_SDA, I2C_SCL, I2C_GND, I2C_SDA, I2C_SCL, I2C_GND, and I2C_SDA. Pins 47-48 are labeled with functions: I2C_GND and I2C_SDA.

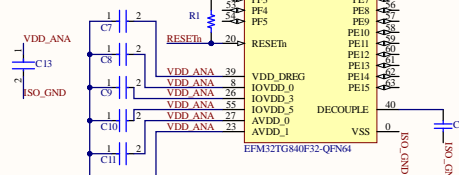


Figure 1 shows the pin connections for the AD124S08IPBSR. The pinout table lists pins 1 through 26, including power supply pins (AVDD, DVDD, IOVDD), ground pins (ISO_GND, REF0_GND, REF1_GND, D_GND), and signal pins (CLK, CS, SCK, MOSI, MISO, RESET, REFP0, REFP1, DRDY, ADC_START, AVSS, DQND). The schematic shows the internal connections for these pins, including decoupling capacitors (C14, C15, C16, C17, C18, C19) and the connection of the REF0 pin to the REFP0 pin.

[illegible]

DS18B20

U6

DQ

TEMP_D

R4 4.7 kOhms

VDD_ANA

ISO_GND

DS18B20+

Title ISE-to-Modbus-RTU-Converter			
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