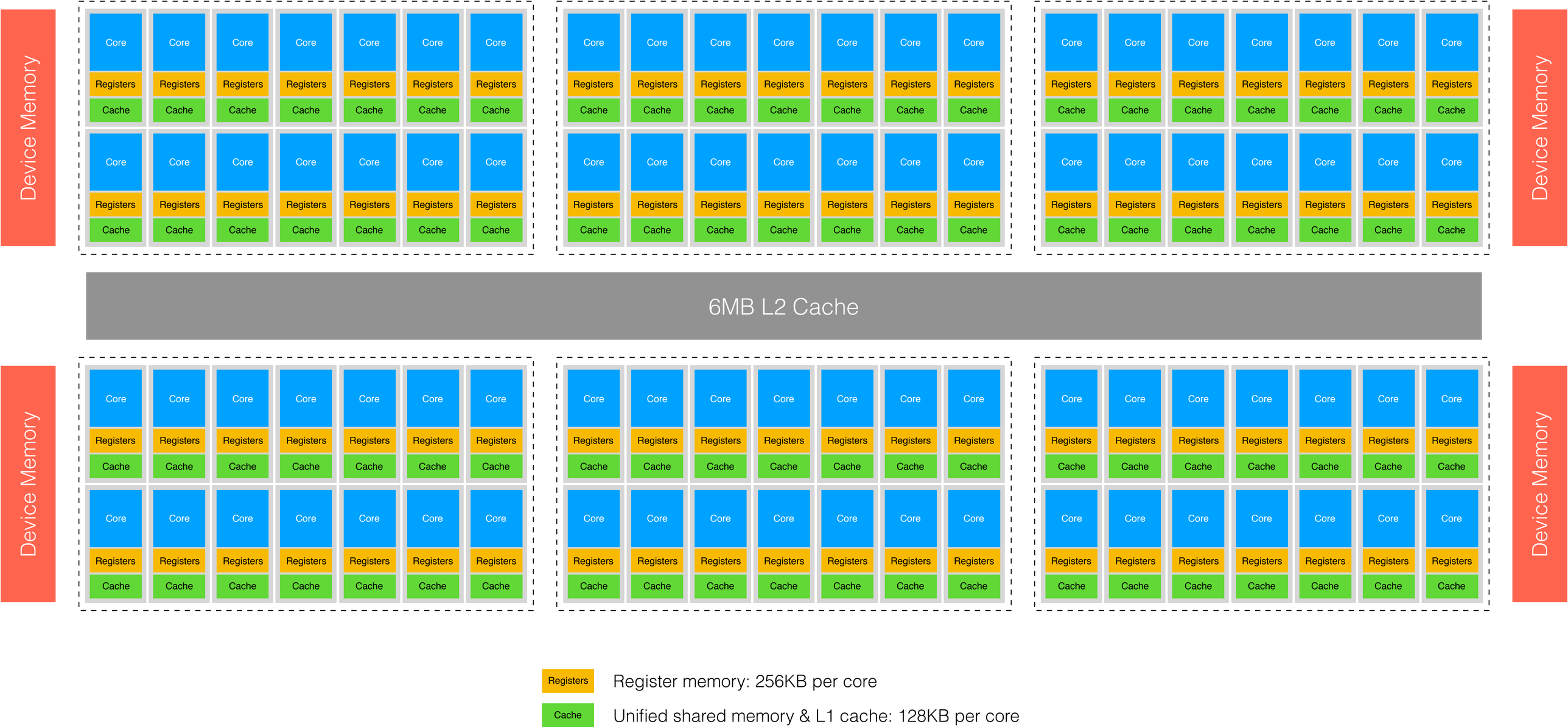
Device Memory
Core
Registers
Cache
6MB L2 Cache
Device Memory
Device Memory
Device Memory
Registers
Register memory: 256KB per core
Cache
Unified shared memory & L1 cache: 128KB per core