



Mentor® Verification IP Altera® Edition Release Highlights

Software Version 10.2b

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Introduction

The Mentor® Verification IP Altera® Edition provides Bus Functional Models (BFMs) that simulate the behavior and facilitate the verification of design IP. This release includes support for the ARM AMBA® AXI3™ and AXI4™ bus interfaces. These BFMs conform to the *AMBA® AXI™ and ACE™ Protocol Specification, AXI3™, AXI4™, and AXI4-Lite™, ACE and ACE-Lite™* (ARM IHI 0022D) with restrictions to AMBA AXI3 and AXI4 functionality, which are described in the section “Protocol Restrictions” in the *Mentor Verification IP Altera Edition AXI3/AXI4 User Guide*.

Note

This release supports only the AMBA AXI3 and AXI4 protocols; AMBA AXI4-Lite and ACE protocols are not supported in this release.

Release V10.2b includes the following features:

- AMBA AXI3 master, monitor, and slave bus functional models (BFMs).
- AMBA AXI4 master, monitor, and slave bus functional models (BFMs).
- Protocol checks that monitor and report any protocol violations.
- Added 64-bit address support for the AXI3 protocol.
- Support for both SystemVerilog (Verilog) and VHDL testbenches.
- Ready-to-use AXI3 and AXI4 master, monitor, and slave components for the Qsys integration tool included with the Altera Quartus II software.
- Support for the Altera Quartus II software, version 13.1.
- Support for the following functional simulation tools:
 - Mentor Graphics ModelSim/Questa Sim 10.2b
 - Synopsys VCS/VCSMX 2013.06
 - Cadence IES 12.20.014
- Examples showing how to connect and use the BFMs including
 - Qsys system with master, monitor, and slave BFMs connected back to back.
 - SystemVerilog and VHDL master, monitor, and slave test programs.
- User Guide with tutorials.

These release highlights summarize the [Authorization Codes](#) required and [Support Information](#).

Authorization Codes

The following authorization codes are required for this release:

- mgcvipae, required to use Mentor Verification IP Altera Edition
- mgcvipaeaxi, required to use AXI3 functionality
- mgcvipaeaxi4, required to use AXI4 functionality

You may request authorization from your distributor.

Mentor Verification IP (VIP) Altera Edition is a separately licensed product that is not part of the Modelsim-Altera simulator. Licenses need to be requested from Altera, similar to licenses for the Modelsim-Altera subscription edition.

Installation Information

This release of Mentor Verification IP Altera Edition is designed to be included with an Altera software release.

Support Information

Mentor Verification IP Altera Edition is supported by Altera Corporation Altera World-Wide-Web Support: <http://www.altera.com/mySupport>.