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Therefore, in the compatibility graph there are nine vertices.

In the given machine,

(BD) is the implied pair for (AB). So, a directed arc is drawn from (AB) to (BD).

(CE) is the implied pair for (AF). So, a directed arc is drawn from (AF) to (CE).

(CE) is the implied pair for (AF). So, a directed arc is drawn from (AF) to (CE).

(DE) is the implied pair for (BC). So, a directed arc is drawn from (BC) to (DE).



(CE) is the implied pair for (AF). So, a directed arc is drawn from (AF) to (CE).

(DE) is the implied pair for (BC). So, a directed arc is drawn from (BC) to (DE).

(BD) is the implied pair for (BD). As $(S_i, S_j) \neq (S_p, S_q)$, no arc is drawn from (BD) to (BD).



(CE) is the implied pair for (AF). So, a directed arc is drawn from (AF) to (CE).

(DE) is the implied pair for (BC). So, a directed arc is drawn from (BC) to (DE).

(BD) is the implied pair for (BD). As $(S_i, S_j) \neq (S_p, S_q)$, no arc is drawn from (BD) to (BD).

(AC) and (BF) are implied pair for (DE). So, two directed arcs is drawn from (DE) to (AC) and (BF).

(CE) is the implied pair for (AF). So, a directed arc is drawn from (AF) to (CE).

(DE) is the implied pair for (BC). So, a directed arc is drawn from (BC) to (DE).

(BD) is the implied pair for (BD). As $(S_i, S_j) \neq (S_p, S_q)$, no arc is drawn from (BD) to (BD).

(AC) and (BF) are implied pair for (DE). So, two directed arcs is drawn from (DE) to (AC) and (BF).

The compatibility graph for the given machine is given in Fig.4.17.

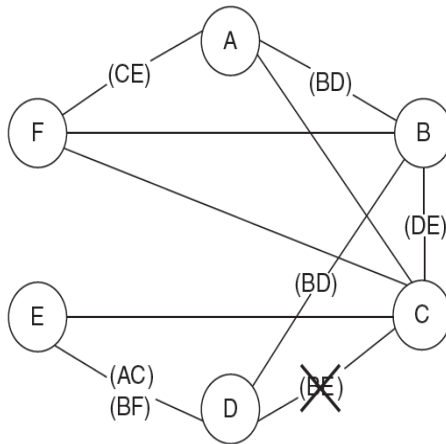


Fig.4.16 Merger Graph

4.7.4 Minimal Machine Construction

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To construct the minimal machine, a compatible graph construction is an essential part. After this, we have to find closed compatible, closed covering, and from there minimal closed covering.

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A subgraph of a compatibility graph is called closed if, for every vertex in the subgraph, all outgoing arcs and the terminal vertices of the arcs also belong to the subgraph. The pair of states that belong to the subgraph as terminal vertices are called closed compatible.

if a subgraph is closed, and if every state of the machine is covered by at least one vertex of the sub-graph of a compatibility graph, then the subgraph forms a closed covering of the machine.

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To find a minimal machine, we need to find the subgraphs which closed cover the machine. Then, we need to find the subgraph which contains less number of vertices and which can generate a simpler machine. The states of the minimal machine are the vertices of the subgraph. From these states, the transition functions are constructed. By this process, a minimal machine of the given machine is constructed.

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The minimal machine is constructed by the following process for the machine given as an example in the compatibility graph section.

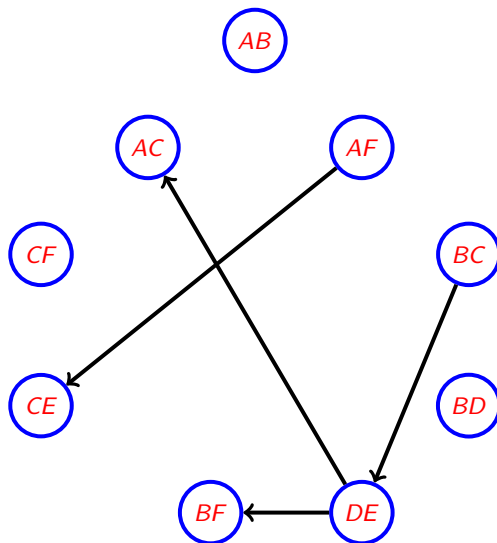


Fig.4.17 Compatible Graph

4.7.5 Minimal Machine

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The subgraphs (AC), (DE), (BF) or (AB), (BD), (AF), (CE) or (AF), (CE), (BC), (DE) are closed subgraphs of the compatibility graph and forms a closed cover of the machine (here every state of the machine is covered by at least one vertex of the subgraph). Among them, the subgraph (AC)(DE)(BF) contains less number of vertices.

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In the minimal machine, the states are (AC), (BF), and (DE). Let us rename them as S_1, S_2, S_3 , respectively. The minimal machine becomes

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<i>PresentState</i>	<i>NextState, z</i>			
	I_1	I_2	I_3	I_4
$S_1(AC)$	$S_3, 1$	$S_1, 1$	$S_3, 0$	$S_2, 1$
$S_2(BF)$	$S_1, 1$	$-, -$	$S_3, 0$	$S_3, 1$
$S_3(DE)$	$S_1, 0$	$S_2, 1$	$S_2, 0$	$S_3, 0$

Example 4.11 Draw a compatible graph for the following machine. Hence, find the minimal machine.

Solution:

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Solution:

<i>PresentState</i>	<i>NextState, z</i>			
	I_1	I_2	I_3	I_4
<i>A</i>	—, —	<i>D</i> , 0	<i>D</i> , 0	<i>C</i> , —
<i>B</i>	<i>A</i> , 1	—, —	—, —	<i>D</i> , —
<i>C</i>	<i>B</i> , 1	<i>C</i> , 0	<i>E</i> , 0	—, —
<i>D</i>	<i>E</i> , 1	<i>C</i> , 0	—, —	<i>D</i> , 0
<i>E</i>	—, —	—, —	—, —	<i>A</i> , 1

To find the compatible pairs of a machine, we need to construct the merger graph first. According to the rules for the construction of the merger graph, the following graph as shown in Fig. 4.18 is constructed.

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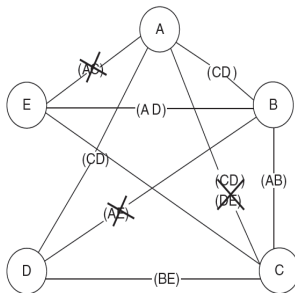


Fig.4.18 Merger Graph

The pair of states which are connected by undirected arcs (not crossed in the interrupted portion) are compatible pairs.

The compatible pairs of the given machine are (AB), (AD), (BC), (BE), (CD), and (CE). Therefore, in the compatibility graph of the previous machine, there are nine vertices.

(CD) is the implied pair for (AB). A directed arc is drawn from (AB) to (CD).

(CD) is the implied pair for (AD). A directed arc is drawn from (AD) to (CD).

(AB) is the implied pair for (BC). A directed arc is drawn from (BC) to (AB).

(AD) is the implied pair for (BE). A directed arc is drawn from (BE) to (AD).

(BE) is the implied pair for (CD). A directed arc is drawn from (CD) to (BE).

The compatibility graph is given in Fig. 4.19.

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The compatible pairs of the given machine are (AB), (AD), (BC), (BE), (CD), and (CE). Therefore, in the compatibility graph of the previous machine, there are nine vertices.

(CD) is the implied pair for (AB). A directed arc is drawn from (AB) to (CD).

(CD) is the implied pair for (AD). A directed arc is drawn from (AD) to (CD).

(AB) is the implied pair for (BC). A directed arc is drawn from (BC) to (AB).

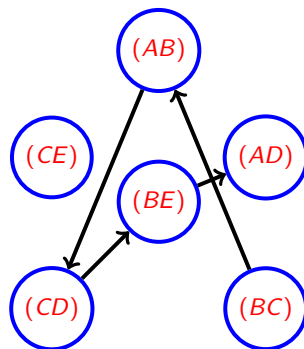
(AD) is the implied pair for (BE). A directed arc is drawn from (BE) to (AD).

(BE) is the implied pair for (CD). A directed arc is drawn from (CD) to (BE).

The compatibility graph is given in Fig. 4.19.

Minimal Machine: In the previous compatibility graph, the subgraph (AD), (CD), (BE) forms a closed covering of the given machine.

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**Fig.4.19** Compatible Graph

The states of the minimal machine are (AD) , (CD) , and (BE) . Let us rename them as S_1 , S_2 , and S_3 , respectively. The minimal machine of this machine is

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<i>PresentState</i>	<i>NextState, z</i>			
	I_1	I_2	I_3	I_4
$S_1(AD)$	$S_3, 1$	$S_2, 0$	$S_1/S_2, 0$	$S_2, 0$
$S_2(CD)$	$S_3, 1$	$S_2, 0$	$S_3, 0$	$S_1/S_2, 0$
$S_3(BE)$	$S_1, 1$	$-, -$	$-, -$	$S_1, 1$

4.8 Merger Table

The merger table is a substitute application of the merger graph. Similar to the merger graph from the merger table, we can also get the compatible pairs and implied pairs.

If the number of states of a machine increases, the number of combination pair increases. For a machine of n states, the number of two state combinations are nC_2 , i.e., $n(n-1)/2$. If $n = (n-1)$, the number of combinations are $(n-1)(n-2)/2$. The number of combinations increases to $(n-1)$ if the number of states increases from $(n-1)$ to n . It is difficult to connect two states by arcs if the number of states increases. In substitute of that, the merger table is an easier process to find compatible pairs and implied pairs.

4.8.1 Construction of a Merger Table

A merger table can be constructed by the following way.

- ▶ Make a table of $(n - 1) \times (n - 1)$, where the left hand side is labelled by the 2nd state to the n th state and the right hand side is labelled by the 1st state to the $(n - 1)$ th state.
- ▶ Each box represents a pair of state combination.

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- ▶ Put a cross in the box if the outputs conflict for the pair of states.
- ▶ Put a right sign in the box if the outputs as well as next states do not conflict for the pair of states.
- ▶ If the outputs do not conflict but the next states conflict for the pair of states, put the conflicting next states in the box.

The following examples describe the process in detail.

Example 4.12 Construct a merger table of the following machine and find the compatible pairs.

Solution:

<i>PresentState</i>	<i>NextState, z</i>		
	I_1	I_2	I_3
<i>A</i>	$E, 0$	$B, -$	$C, -$
<i>B</i>	$-, -$	$D, -$	$B, 0$
<i>C</i>	$E, -$	$D, -$	$C, 0$
<i>D</i>	$C, 0$	$-, -$	$B, 1$
<i>E</i>	$C, 0$	$-, -$	$B, -$

Make a table like the following:

B				
C				
D				
E				
	A	B	C	D

Consider the state combination (AB). Here, the outputs do not conflict but the next states for l_2 and l_3 conflict. So, the conflicting next state combinations, (BD) and (BC), are placed in the box labelled (AB).

Consider the state combination (AC). Here, the outputs do not conflict but the next states for l_2 conflict. So, the conflicting next state combination, (BD), is placed in the box labelled (AC).

Consider the state combination (AD). The outputs do not conflict, but the next states for l_1 and l_3 conflict. The conflicting next state combinations (CE) and (BC) are placed in the box labelled (AD).

Consider the state combination (AE). The outputs for the states do not conflict, but the next states for input I_1 and I_3 conflict. (CE) and (BC) are placed in the box labelled (AE).

Consider the state combination (BD). Here, the outputs for input I_3 conflict. There will be an \times (cross) in the box labelled (BD).

Consider state combination (DE). Here, both the outputs and the next state combination are the same for all the inputs. So a \checkmark (tick) is placed in the box labelled (DE).