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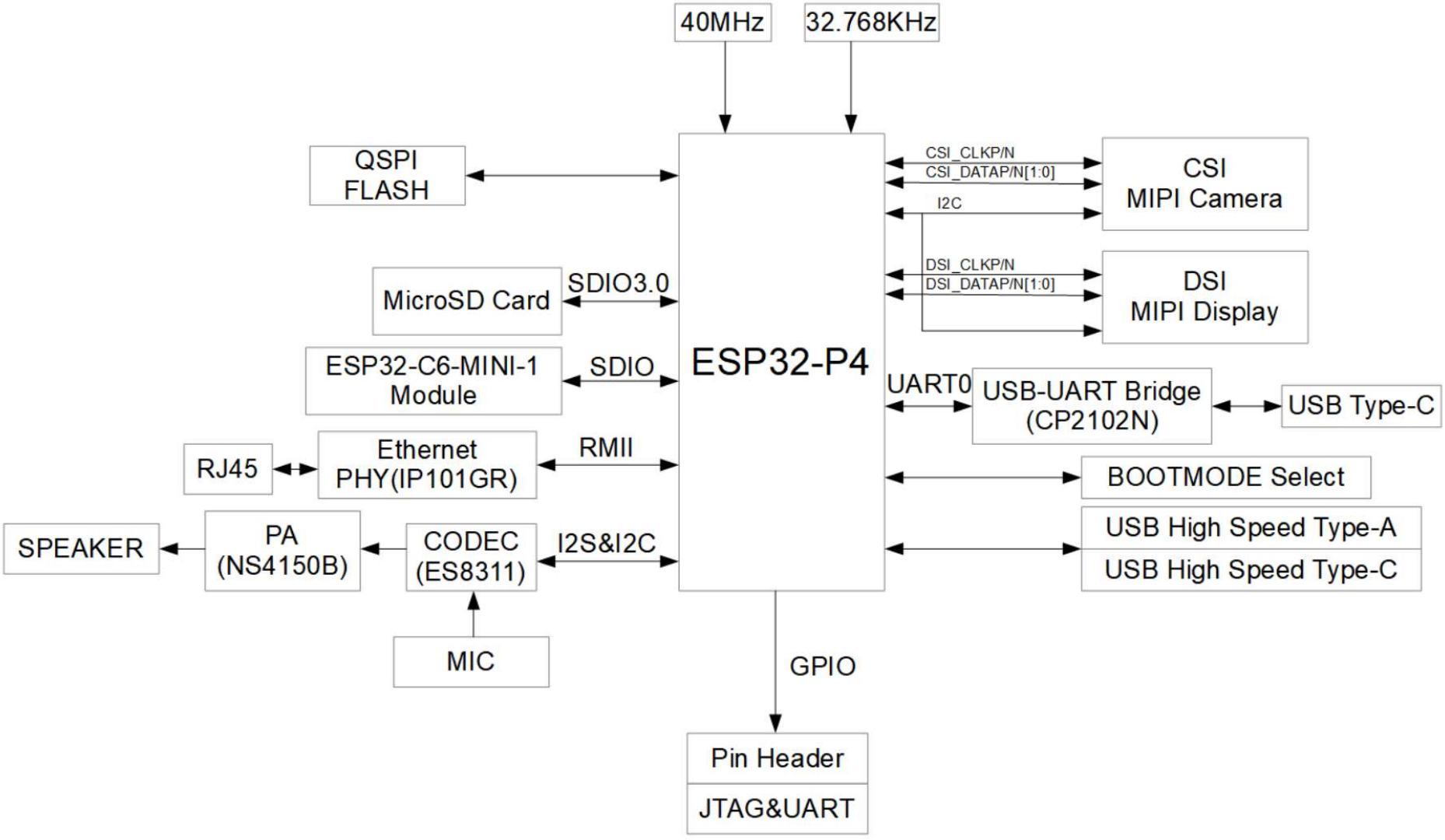

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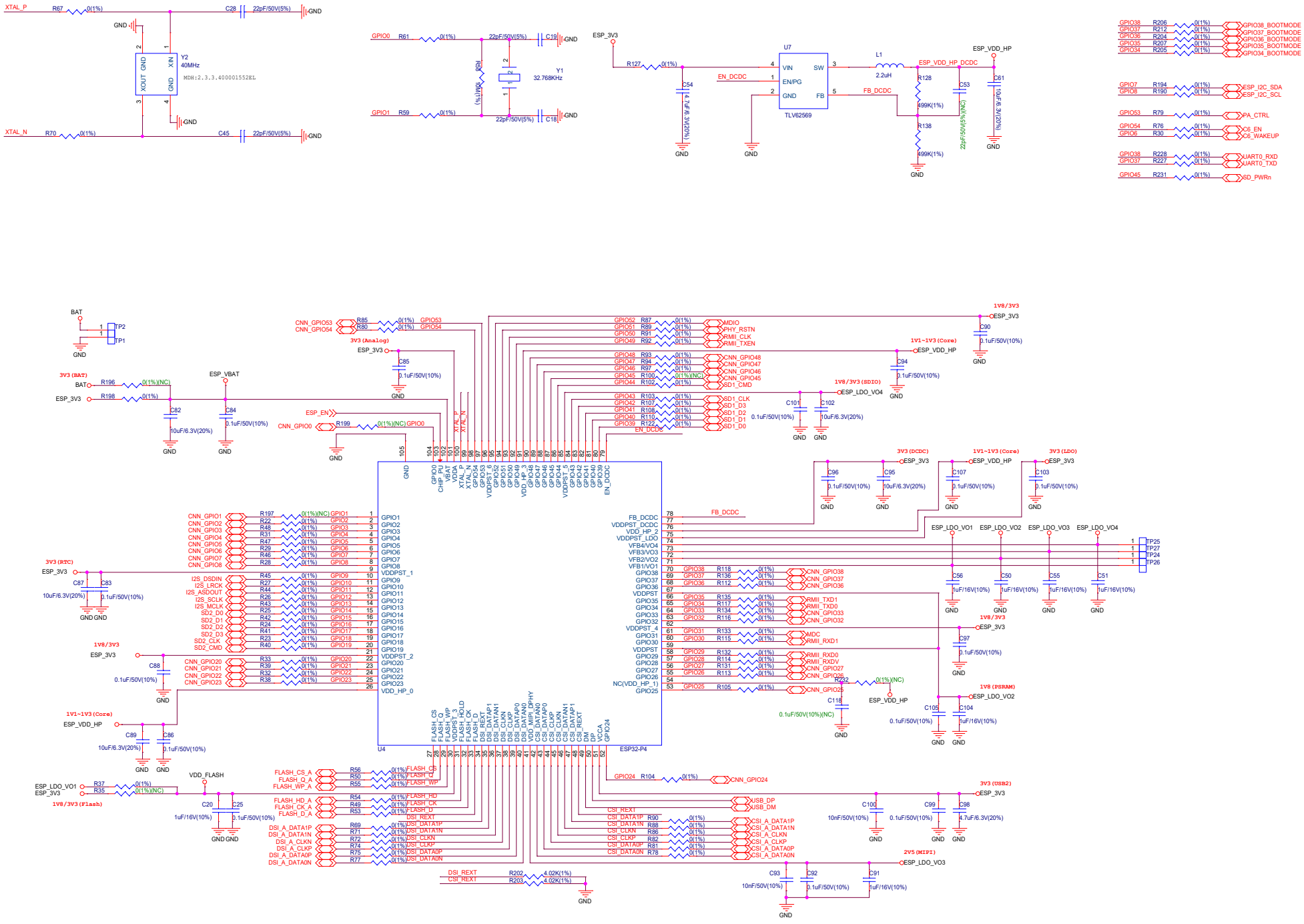
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XTAL_N
Y2
40MHz
Y1
32.768KHz
U7
TLV62569
ESP_VDD_HP
ESP_VDD_HP_DCDC
L1
2.2uH
FB_DCDC
EN_DCDC
BAT
ESP_VBAT
ESP_EN
U4
ESP32-P4
FB_DCDC
ESP_LDO_VO1
ESP_LDO_VO2
ESP_LDO_VO3
ESP_LDO_VO4
VDD_FLASH
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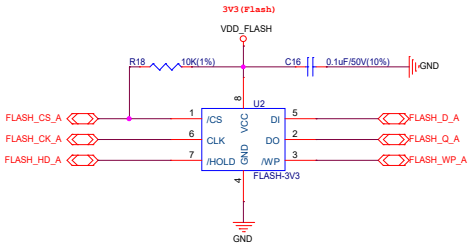
ESP32-P4_FUNCTION_EV_BOARD
Tuesday, July 02, 2024

FLASH:



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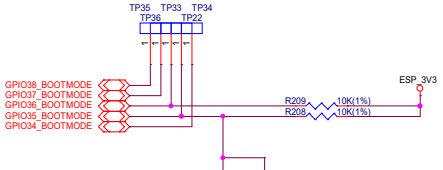

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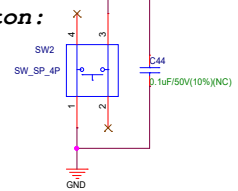
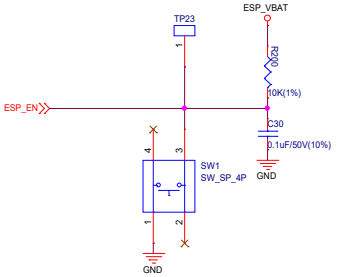
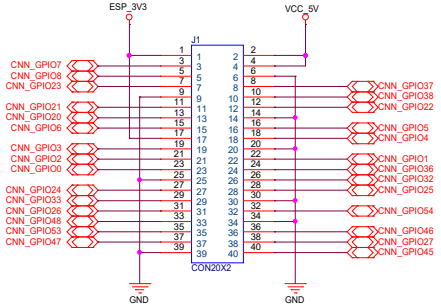


Reset Button:



Pin Header For GPIOs:



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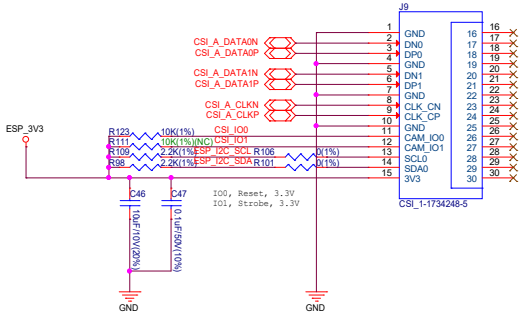

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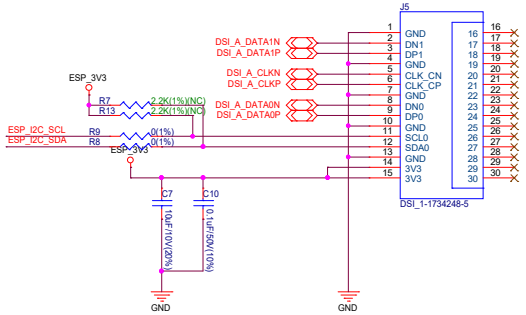

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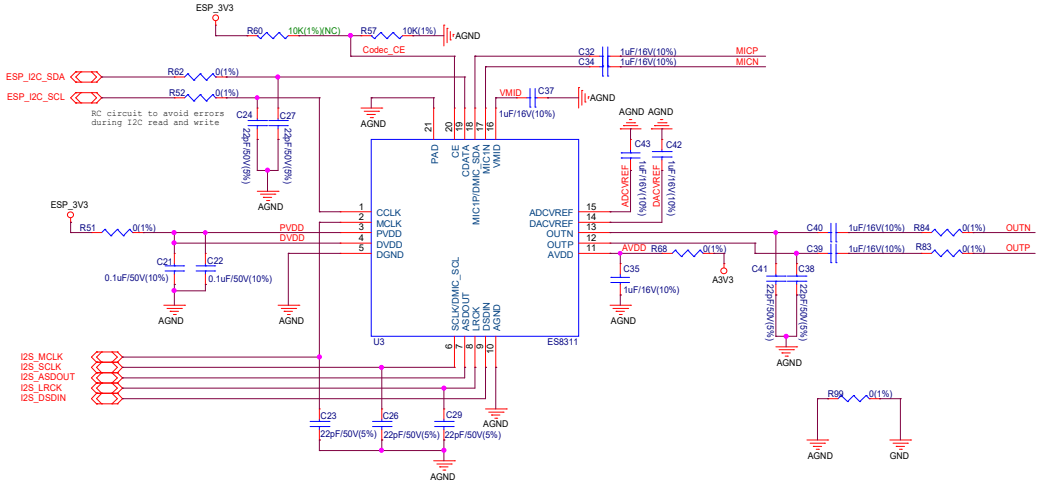
Codec:



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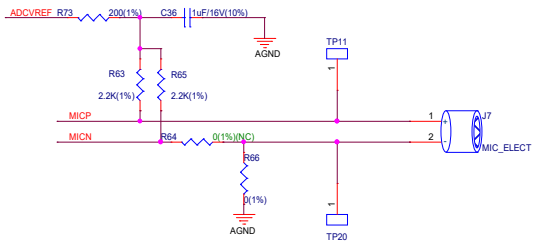

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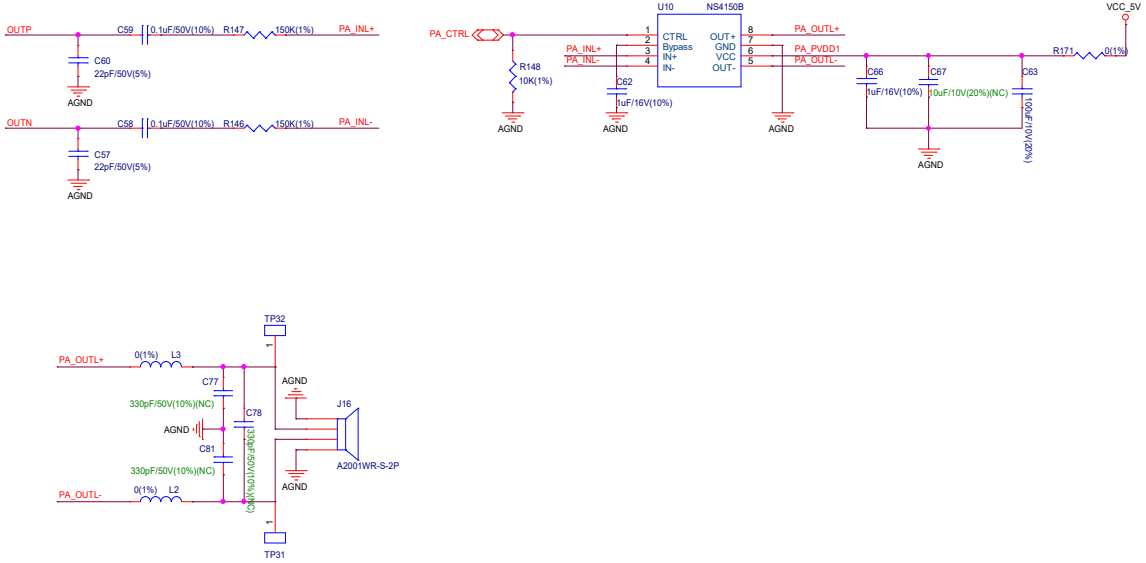

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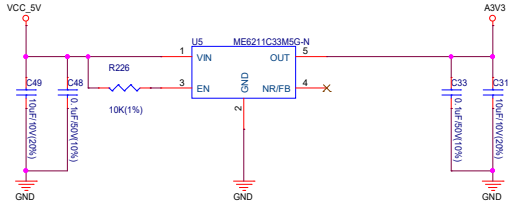

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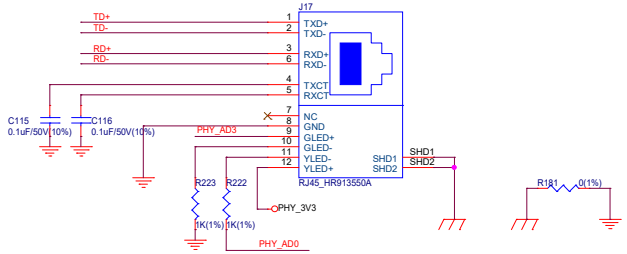
RJ45 with Trans



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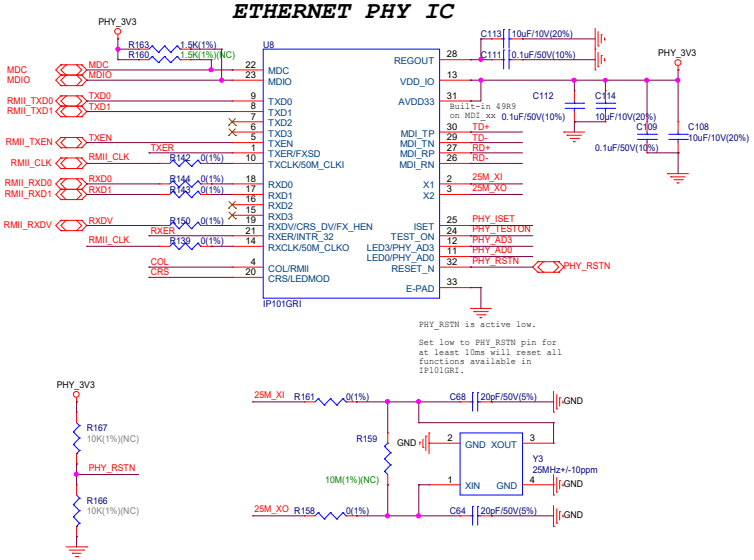
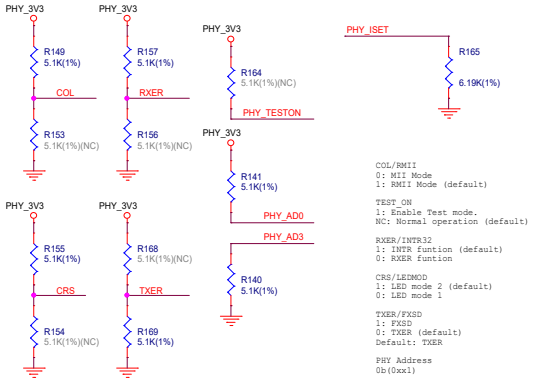


PHY MODE CONFIG



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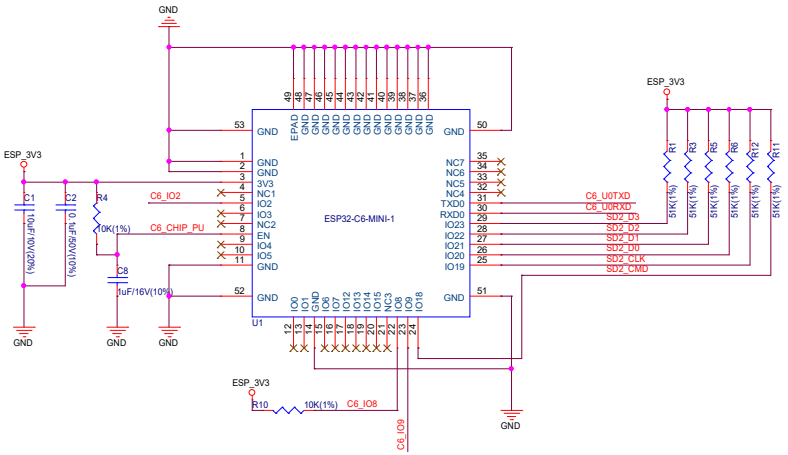

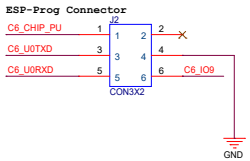

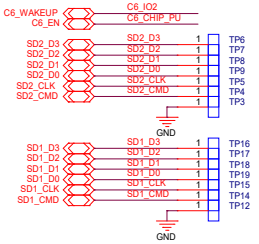

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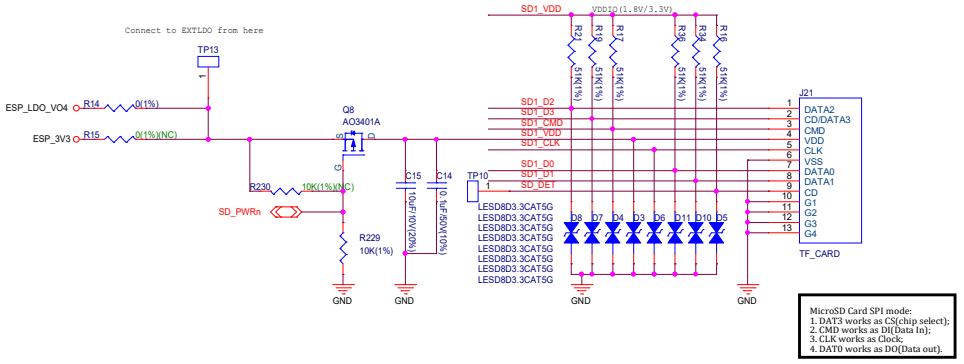

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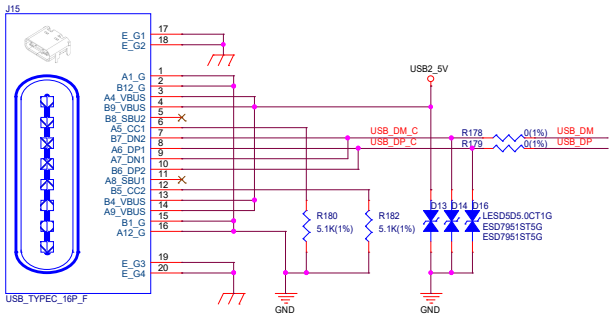
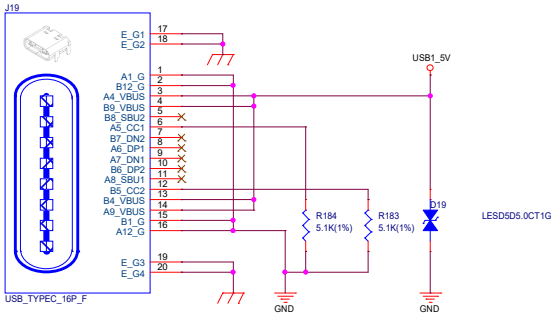
USB High Speed & Power in:



USB Full Speed Slave & Power in (24&25):



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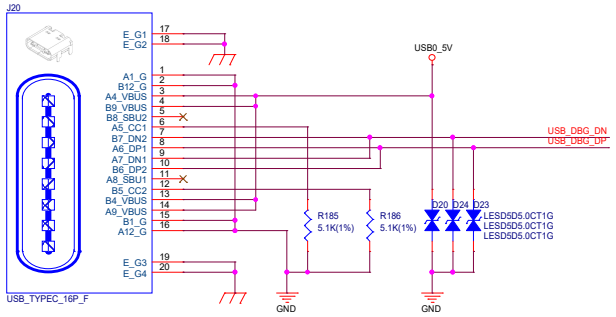

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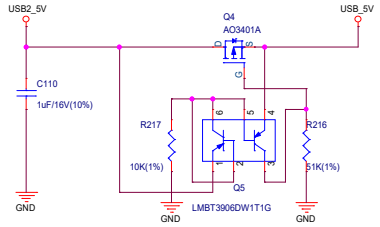

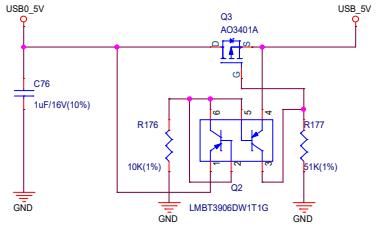

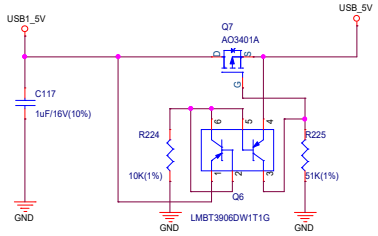

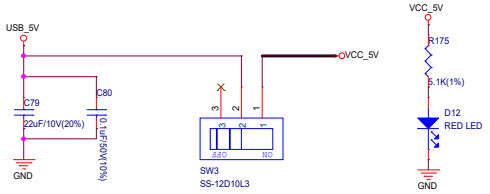

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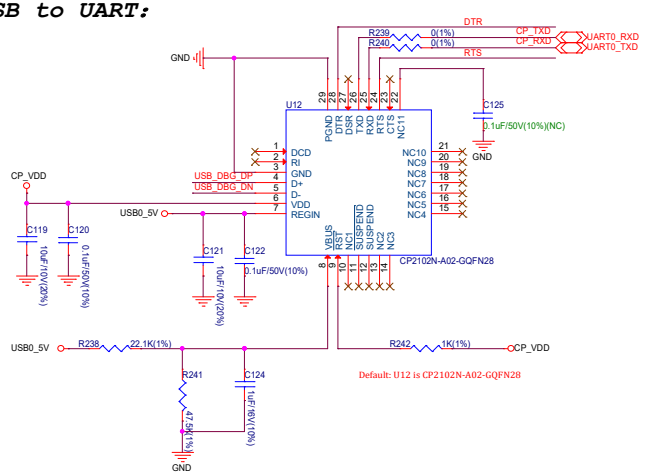

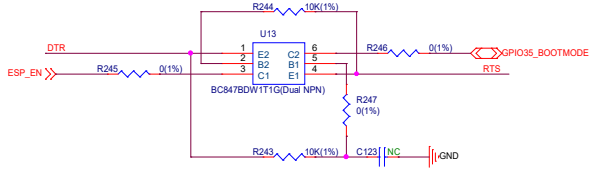

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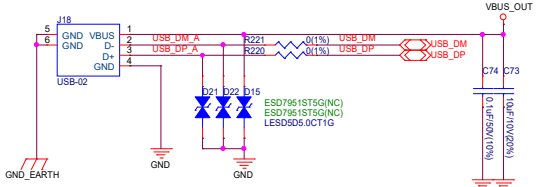

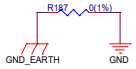

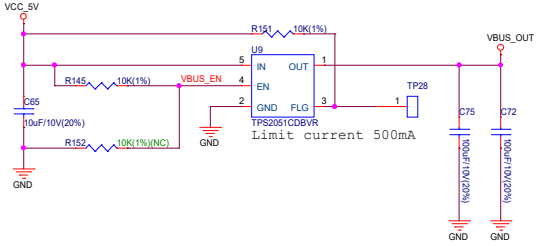

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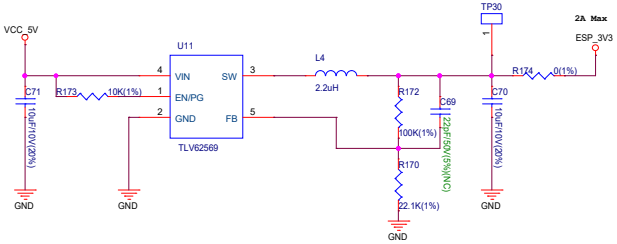

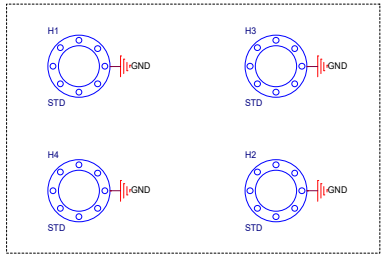

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