

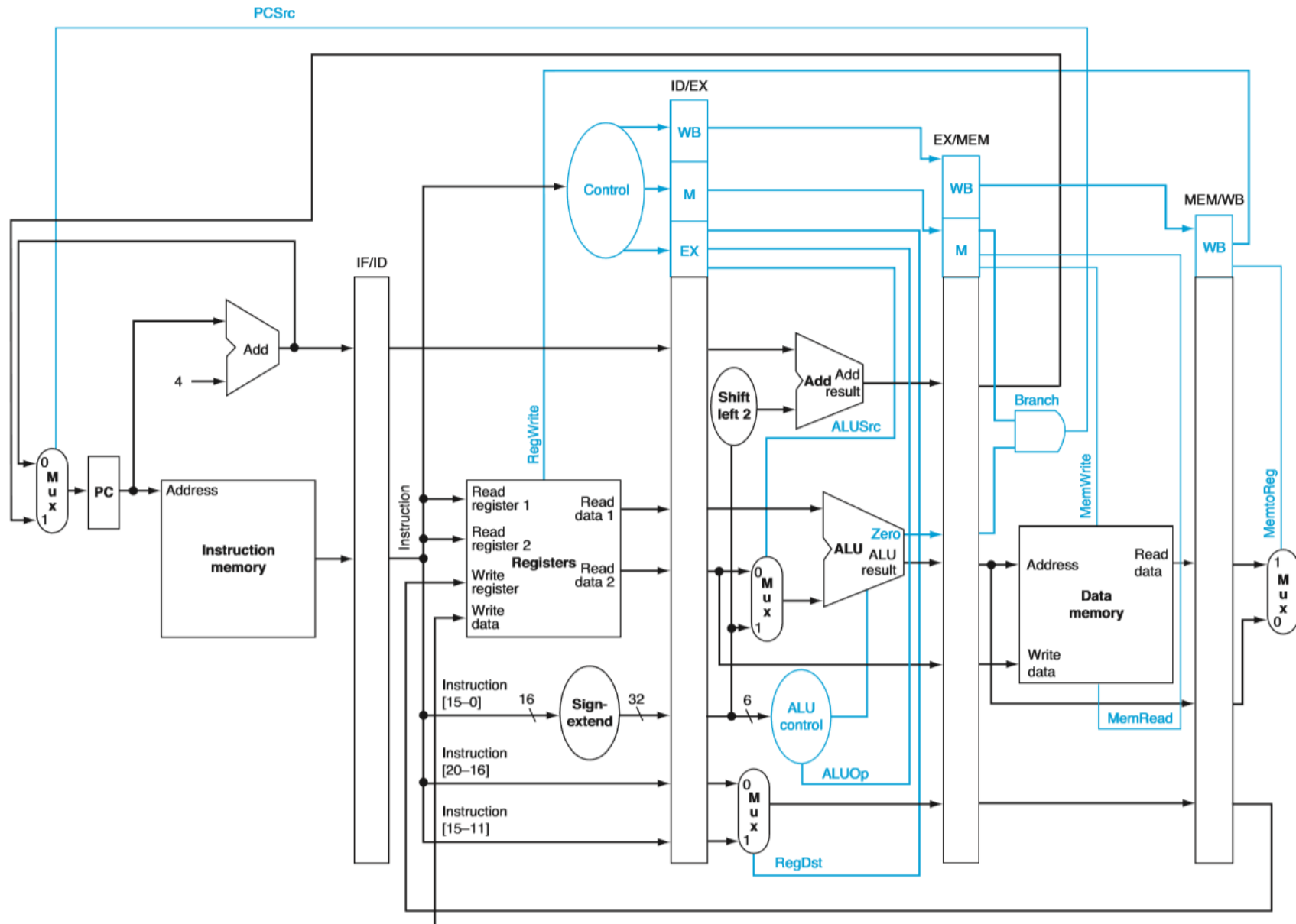
IN THE NAME OF GOD

Architecture Lab Description, Session #2

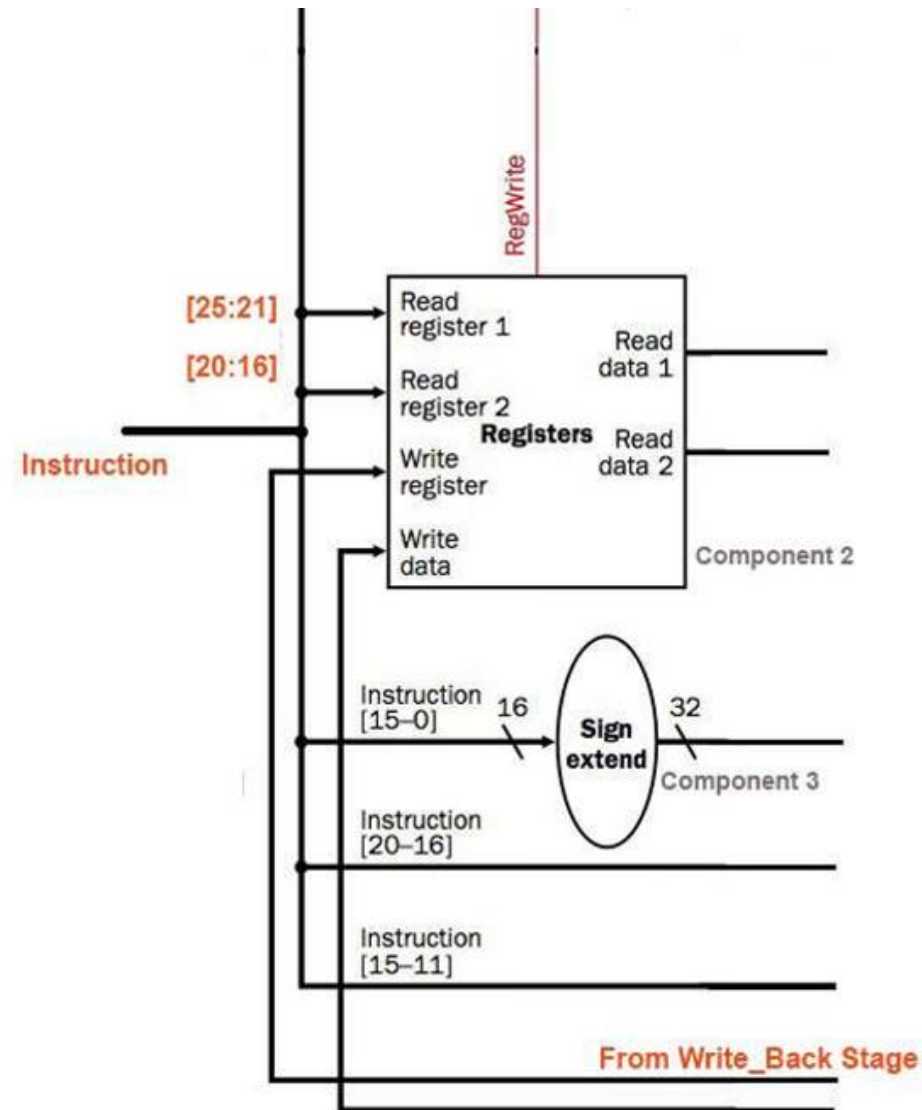
Session Abstract

- Implementing Instruction Decode Pipe Stage
- Testing it

MIPS DataPath



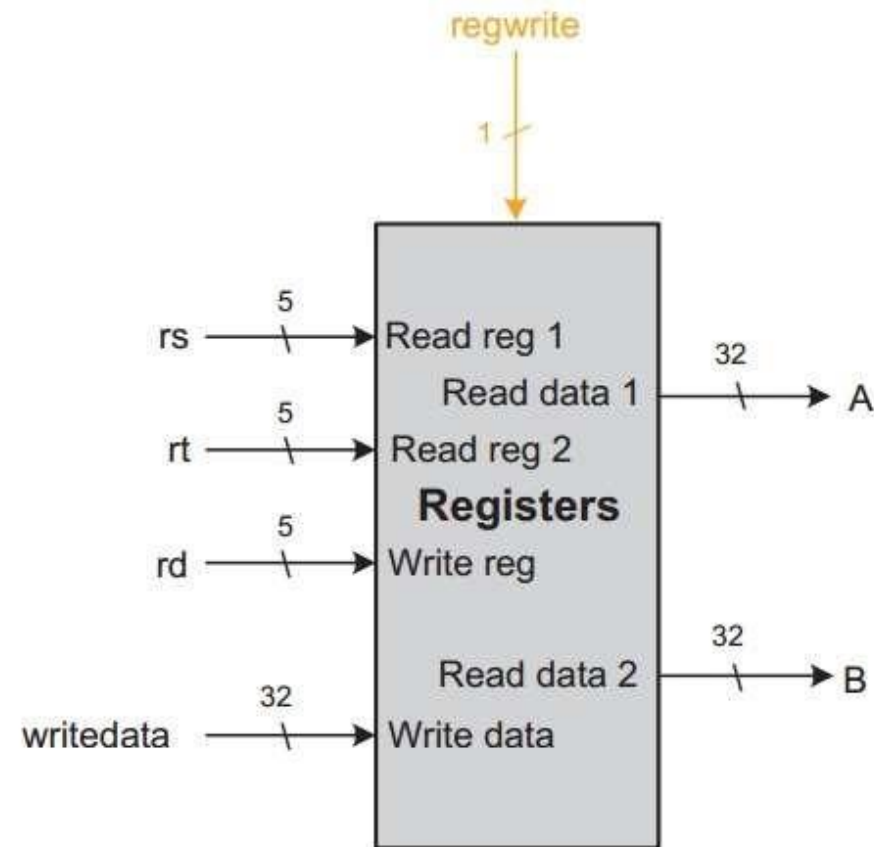
Instruction Decode Pipeline



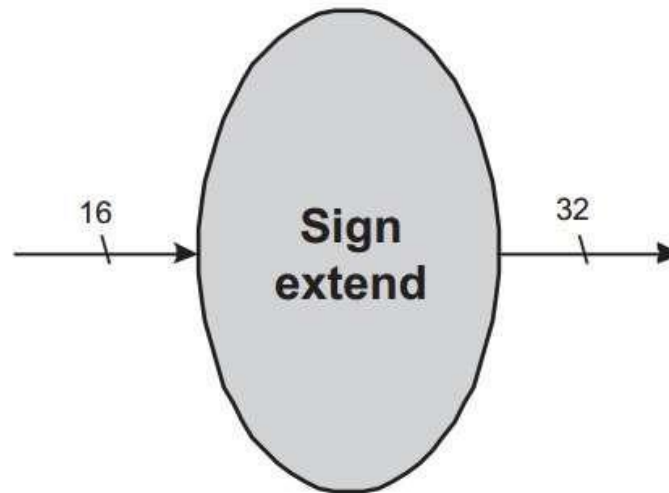
Decode Pipe I/O

- Inputs:
 - Instruction
 - Mux(From Write Back)
 - Write Register
 - Control Unit (RegWrite)
- Outputs
 - Read Data(1 & 2)
 - Instruction Address(1 & 2)
 - Instruction(to control unit)
 - Sign Extended Number

Module: Register File



Module: Sign Extender



END!