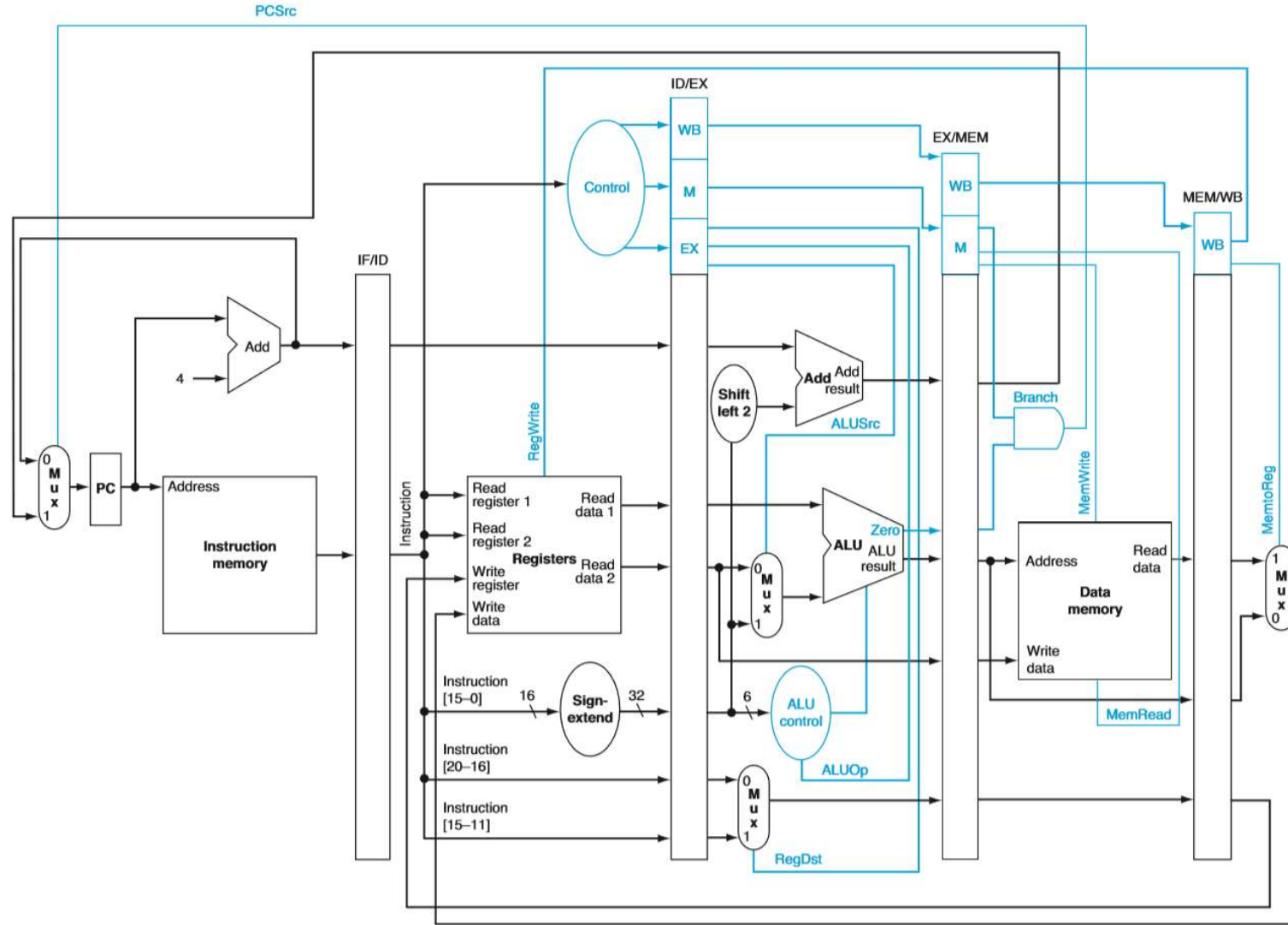# IN THE NAME OF GOD

Architecture Lab Description, Session #4
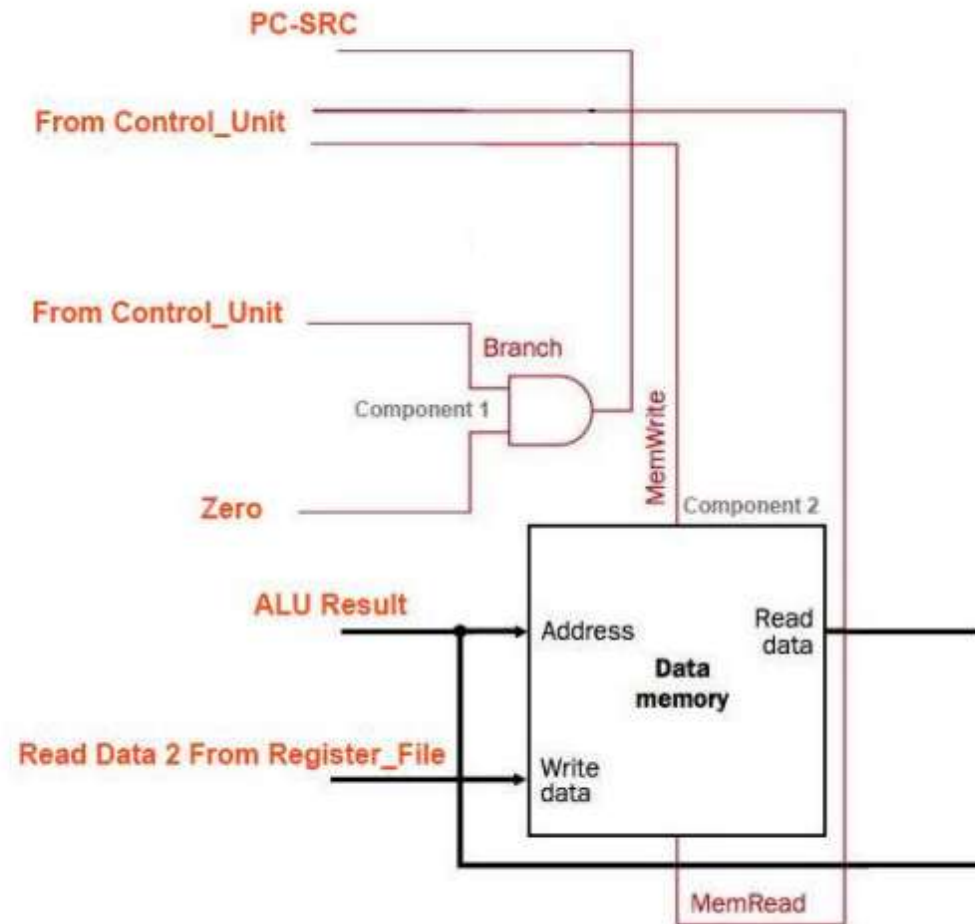
Session Abstract

- Implementing Data Memory Pipe Stage
- Creating test bench and testing it
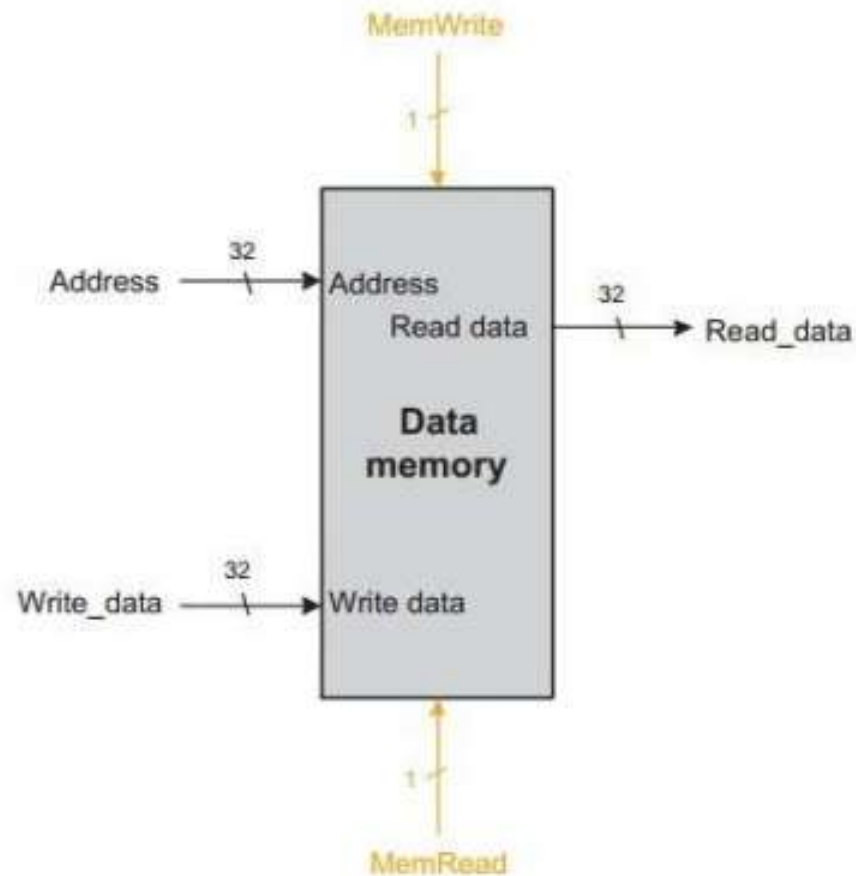
MIPS DataPath

Data Memory Pipeline

Data Memory Pipe I/O

- Inputs:
  - Branch (From CU)
  - Mem Write (From CU)
  - Mem Read (From CU)
  - Zero
  - ALU Result
  - Read Data 2 From Register File
- Outputs
  - PC-SRC
  - Read Data
  - ALU Result

Module: Data Memory

END!