Lab Report

Ali Yıldırım

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Chapter 1

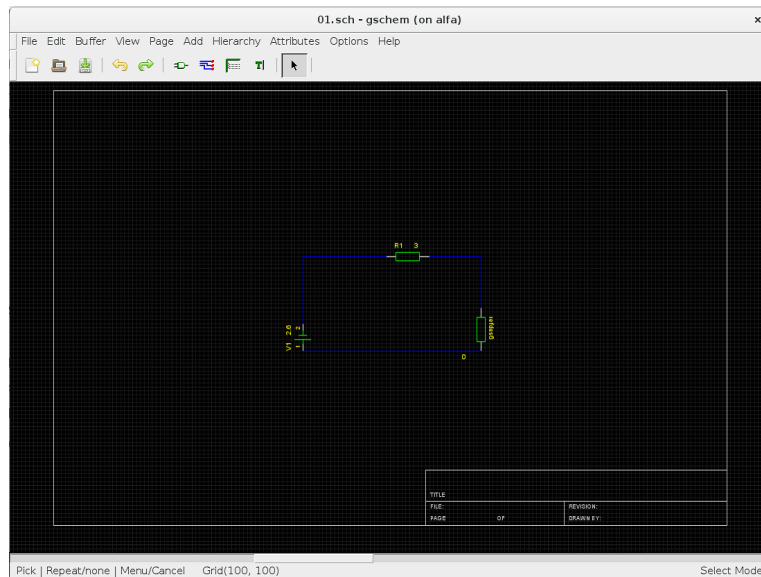# Theoretical part

R1	3
R2	6
V1	2,6
UR1	0,86
UR2	1,73

Chapter 2

Practical part

2.1 Work with GEDA programs

2.1.1 Work with gschem

2.1.2 Work with gnetlist

```
* Spice netlister for gnetlist
R2 2 0 6
R1 1 2 3
V1 0 1 2.6
.END
```
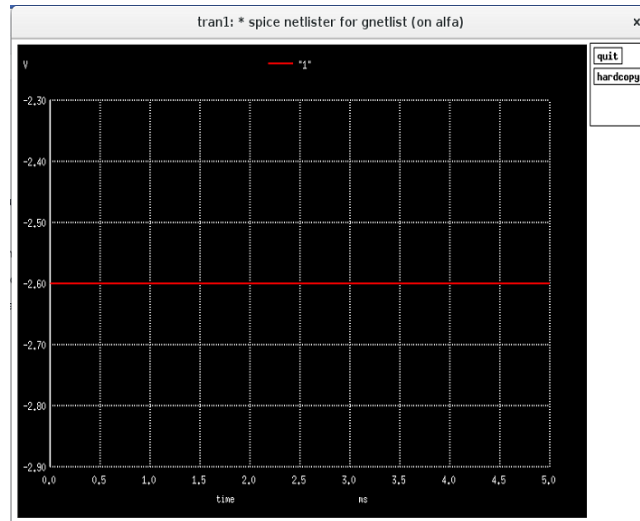
2.1.3 Work with ngspice
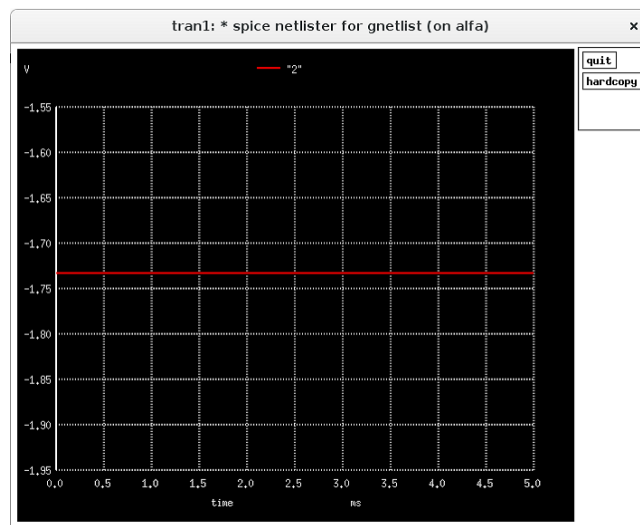

Figure 2.1: Simulation of voltage on resistor R1.

Figure 2.2: Simulation of voltage on resistor R2.

2.2   Work with QUCS programs