Circuit and System-I

LAB # 03



Spring 2022

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Class Section: C

"On my honor, as student of University of Engineering and Technology, I have neither given nor received unauthorized assistance on this academic work."

Student Signature:

Submitted to:

Engr. Faiz Ullah

21 April, 2022

Department of Computer Systems Engineering

ASSESSMENT RUBRICS LAB # 03

Verification of Ohm's law Using PSPICE

	LAB REPORT ASSESSMENT								
	Criteria	Excellent	Average	Nil	Marks Obtai ned				
1.	Objectives of Lab	All objectives of lab are properly covered [Marks 1]	Objectives of lab are partially covered [Marks 0.5]	Objectives of lab are not shown [Marks 0]					
2.	Ohm's Law and Mathematic al Expression.	Correct definition of Ohm's law, mathematical expression and circuit diagram is shown. [Marks 1]	Correct statement of Ohm's law and no mathematical expression and circuit diagram with no labels. [Marks 0.5]	No definition, mathematica l expression and circuit diagram is shown [Marks 0]					
3.	PSPICE and its Procedure.	Elaborate PSPICE software and its procedure for designing circuit. [Marks 2]	PSPICE is not defined while its procedure is not properly discussed [Marks 1]	No Definition or procedure. [Marks 0]					
4.	Circuit Diagram in PSPICE.	Circuit diagrams for all cases of varying resisters and source voltages are shown with titles and labels in PSPICE. [Marks 2]	Some of the cases of varying resisters and source voltages are shown with no titles and labels in PSPICE [Marks 1]	No circuit diagrams are shown [Marks 0]					
5.	Observation s & Calculations	All experimental results are completely shown in form of table. [Marks 2]	Experimental results are partially shown and some of the observations are missing. [Marks 1]	No experimenta l results are shown [Marks 0]					
6.	Graphs	Graphs from experimental results of Ohm's law are shown with labels. [Marks 1]	Graphs from experimental results of Ohm's law are shown with no labels . [Marks 0.5]	No graphs are shown [Marks 0]					

7. Conclusion	Conclusion about experimental results is properly explained and satisfactory. [Marks 1]	Conclusion about experimental results is not properly explained and satisfactory. [Marks 0.5]	No conclusion is shown [Marks 0]	
Total Marks Obtained: Instructor Signature:				

TITLE:

Verification of **OHM's** law using PSPICE

OBJECTIVES:

- ❖ To find current in the wire using PSPICE software.
- ❖ We will be able to use PSPICE.
- ❖ We will be able of to use OHMs Law.

OHMS LAW:

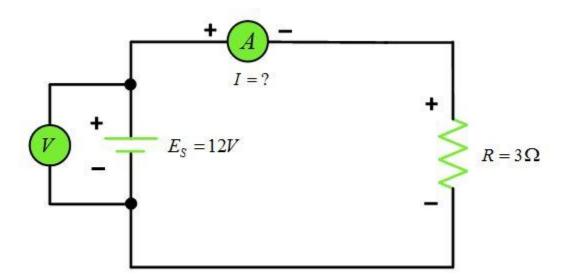
DEFINITION:

Ohm's law states that the current in an electric circuit is proportional to the applied voltage and inversely proportional to its resistance.

Mathematical Expression:

$$V = IR$$

CIRCUIT DIAGRAM:



PSPICE:

INTODUCTION:

PSpice is a SPICE analog circuit and digital logic simulation software that runs on personal computers, hence the first letter "P" in its name. It was developed by MicroSim and is used in electronic design automation. MicroSim was bought by OrCAD which was subsequently purchased by Cadence Design Systems. The name is an acronym for Personal Simulation Program with Integrated Circuit Emphasis. Today it has evolved into an analog mixed signal simulator.

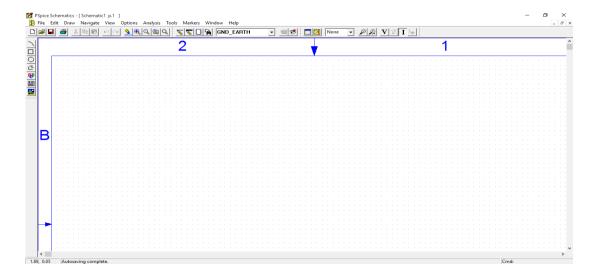
OR

"PSPICE is a circuit analysis tool that allows the user to simulate a circuit and extract key voltages and currents."

STEPS:

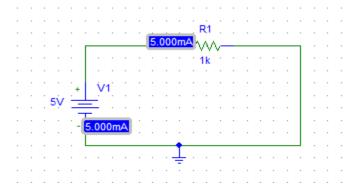
- Go to start menu and search for Schematics.
- Select "Menu Draw". Get new parts.
- Then click the "Libraries" button (You will get a window named as Library Brower).
- ➤ Go to "Part Browser Basic", input the device name in the "Part Name" or select it in the bottom catalog.
- Click "Place" button, then you can put the selected devices into you schematic.
- Connect the devices you have put onto your schematic, use menu "Draw Wire", your cursor would change to be a pencil.
- Check your circuit carefully, compare it with the circuit in your Lab Pak. Check the name and value of every device.
- > Save your schematic.
- Use the menu "Analysis Setup" to set up the simulation condition.
- Use the menu "Analysis Simulation".





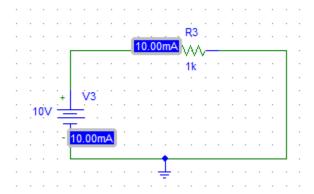
CASE 1:

Voltage of 5v and resistance of $1k\Omega$. The current in this circuit is 5.00mA.



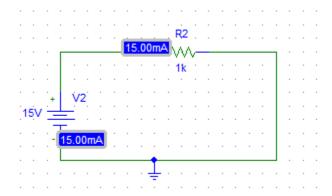
CASE 2:

Voltage of 10v and resistance of $1k\Omega$. The current in this circuit is 10.00mA.



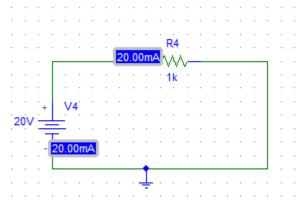
CASE 3:

Voltage of 15v and resistance of $1k\Omega$. The current in this circuit is 15.00mA.



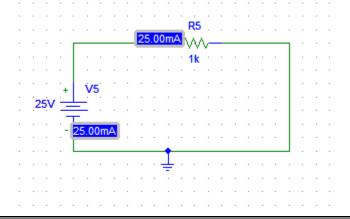
CASE 4:

Voltage of 20v and resistance of $1k\Omega$. The current in this circuit is 20.00mA.



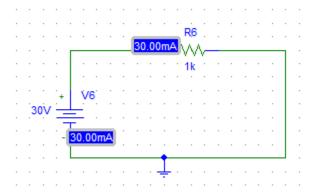
CASE 5:

Voltage of 25v and resistance of $1k\Omega$. The current in this circuit is 25.00mA.



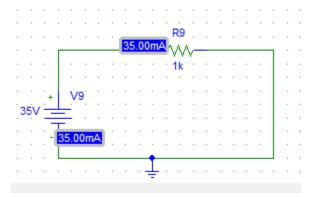
CASE 6:

Voltage of 30v and resistance of $1k\Omega$. The current in this circuit is 30.00mA.



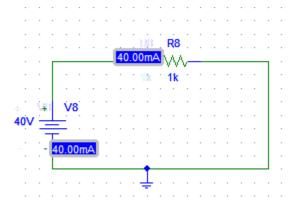
CASE 7:

Voltage of 35v and resistance of $1k\Omega$. The current in this circuit is 35.00mA.



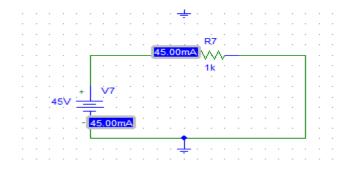
CASE 8:

Voltage of 40v and resistance of $1k\Omega$. The current in this circuit is 40.00mA.



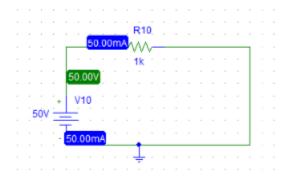
CASE 9:

Voltage of 45v and resistance of $1k\Omega.$ The current in this circuit is 45.00mA.



CASE 10:

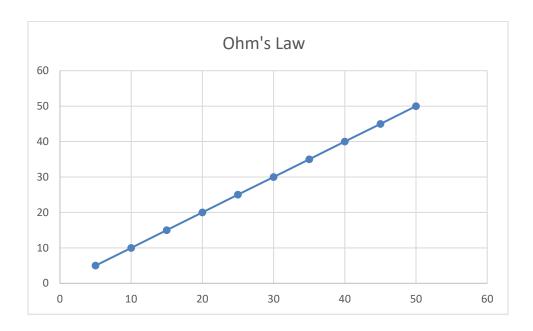
Voltage of 50v and resistance of $1k\Omega$. The current in this circuit is 50.00mA.



OBSERVATION:

S.No	Voltage (Volts)	Resistance (Ohms)	Current (Amperes)
1	5 V	1k Ω	5 mA
2	10 V	1k Ω	10 mA
3	15 V	1k Ω	15 mA
4	20 V	1kΩ	20 mA
5	25 V	1kΩ	25 mA
6	30 V	1k Ω	30 mA
7	35 V	1kΩ	35 mA
8	40 V	1kΩ	40 mA
9	45 V	1kΩ	45 mA
10	50 V	1kΩ	50 mA

GRAPH BETWEEN VOLTAGE AND CURRENT:



CONCLUSION:

From this we concluded that voltage is directly proportional to the current when the resistance is constant. Or if we keep any one of these constant, other two are proportional.