# Circuit and System-I

**LAB # 07** 



# Spring 2022

Submitted by: Ali Asghar

Registration No.: 21PWCSE2059

Class Section: C

"On my honor, as student of University of Engineering and Technology, I have neither given nor received unauthorized assistance on this academic work."

Student Signature:

Submitted to:

Engr. Faiz Ullah

2 June, 2022

Department of Computer Systems Engineering

# ASSESSMENT RUBRICS LAB # 7

# **Node Voltage Analysis using PSPICE**

LAB REPORT ASSESSMENT									
	Criteria	Excellent	Average	Nill	Marks Obtained				
1. (	Objectives of Lab	All objectives of lab are properly covered [Marks 0.5]	Objectives of lab are partially covered [Marks 0.25]	Objectives of lab are not shown [Marks 0]					
	Node Voltage Analysis (Theory, Circuit Diagram )	Brief introduction about Node Voltage Analysis (what is Node voltage analysis, What are nodes, How to apply KCL equations at each node) is shown along with properly labeled circuit diagram [Marks 1]	Some of the points about Node Voltage Analysis are missing and circuit diagram is not properly labeled [Marks 0.5]	Introduction about Node Voltage Analysis and circuit diagram is not shown [Marks 0]					
	PSPICE Simulator	Brief introduction of PSPICE simulator [Marks 1]	Brief introduction of PS Is not shown [Marks 0]						
4. 1	Procedure	All experimental steps are shown in detail along with how to verify Node Voltage Analysis. [Marks 1.5]	Some of the experimental steps are missing [Marks 1]	Experimenta 1 steps are missing [Marks 0]					
	Observations & Calculations	Mathematical calculations are shown and comparison with PSPICE results. [Marks 5]	Mathematical calculations are shown but no comparison with PSPICE results [Marks 2.5]	No mathematica 1 calculations are shown [Marks 0]					
6.	Conclusion	Conclusion about experiment is shown [Marks 1]	Conclusion about experiment is partially shown [Marks 0.5]	Conclusion about experiment is not shown [Marks 0]					

Total Marks Obtained:\_\_\_\_\_
Instructor Signature: \_\_\_\_\_

## TITLE:

# Node Voltage Analysis using PSPICE

## **OBJECTIVES:**

- ❖ To know about Node voltage method.
- ❖ How to calculate node voltages in a circuit.
- Verifying Node Voltage Method.

# **NODE VOLTAGE ANALYSIS:**

The mathematical method for calculating the distribution of voltage between the nodes in a circuit.

### NODE:

A point at which two or more elements are joints together is called node.

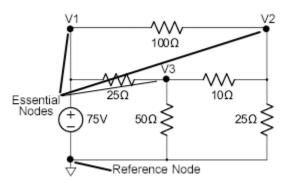


Figure 1

# WRITING NODE VOLTAGE METHOD FOR FIGURE 1:

## AT NODE V2:

$$\frac{V_2 - 75}{100} + \frac{V_2}{25} + \frac{V_2 - V_3}{10} = 0$$

## AT NODE V3:

$$\frac{V_3 - 75}{25} + \frac{V_3}{50} + \frac{V_3 - V_2}{10} = 0$$

# KIRCHHOFF'S CURRENT LAW (KCL):

In 1847, Gustav Robert Kirchhoff, a professor at the University of Berlin, formulated two important laws that provide the foundation for analysis of electric circuits. These laws are referred to as Kirchhoff's current law (KCL) and Kirchhoff's voltage law (KVL) in his honor. Kirchhoff's laws are a consequence of conservation of charge and conservation of energy. Kirchhoff's current law states that the algebraic sum of the currents entering any node is identically zero for all instants of time.

#### STATEMENT:

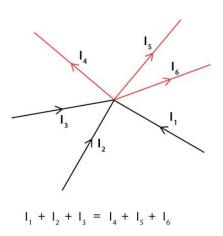
The algebraic sum of the currents into a node at any instant is zero.

#### **MATHEMATICAL FORM:**

$$\sum I = 0$$

#### **CIRCUIT DLAGRAM:**

#### Kirchhoff's Current Law



#### **PSPICE:**

**PSpice** is a SPICE analog circuit and digital logic simulation software that runs on personal computers, hence the first letter "P" in its name. It was developed by MicroSim and is used in electronic design automation. MicroSim was bought by OrCAD which was subsequently purchased by Cadence Design Systems. The name is an acronym for Personal Simulation Program with Integrated Circuit Emphasis. Today it has evolved into an analog mixed signal simulator.

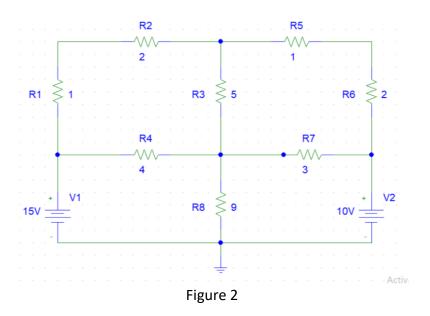
OR

"PSPICE is a circuit analysis tool that allows the user to simulate a circuit and extract key voltages and currents."

## NODE YOLTAGE METHOD:

The Node Voltage Method breaks down circuit analysis into this sequence of steps,

- Assign a reference node (ground).
- Assign node voltage names to the remaining nodes.
- Solve the easy nodes first, the ones with a voltage source connected to the reference node.
- ❖ Write Kirchhoff's Current Law for each node. Do Ohm's Law in your head.
- Solve the resulting system of equations for all node voltages.
- Solve for any currents you want to know using Ohm's Law.



# APPLYING NODE YOLTAGE METHOD ON FIGURE 1:

### **SUPPOSITION:**

Let's suppose in figure 2.

 $i_1$  is flowing through  $R_1 \& R_2$ .  $i_3$  is flowing through  $R_3$ .  $i_4$  is flowing through  $R_4$ .

 $i_6$  is flowing through  $R_6$  &  $R_5$ .  $i_7$  is flowing through  $R_7$ .  $i_8$  is flowing through  $R_8$ .

Now applying KCL at node V<sub>2</sub> and V<sub>3</sub>.

# AT NODE V2:

$$i_1 + i_6 - i_3 = 0$$

# AT NODE Y3:

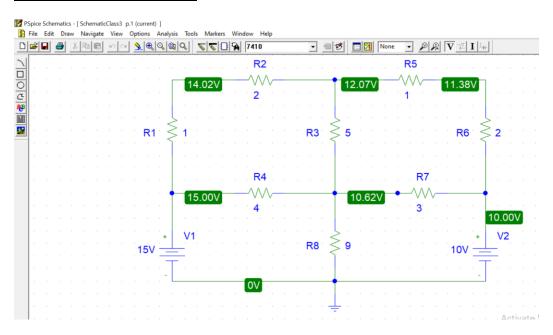
$$i_4 + i_7 + i_3 - i_8 = 0$$

Now after applying Ohm's Law in above equations, I calculated  $V_2$ 

And V<sub>3</sub> as:

$$V_2 = 12.093V \& V_3 = 10.736V$$

## **VERIFICATION IN PSPICE:**

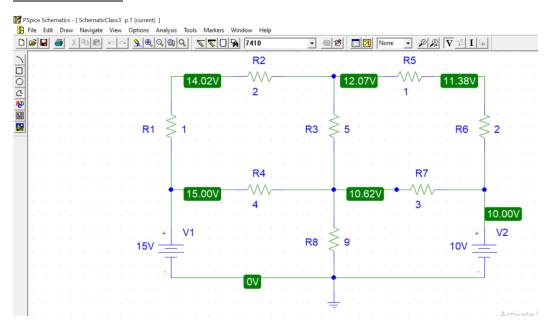


## PROCEDURE:

- 1) Open schematic program of PSpice
- 2) Click on the "Get New Part" button on the toolbar
- 3) Type 'r' in the search bar and place the eight resistors on the white sheet

- 4) Type 'vdc' in the search bar and place two of them on the white sheet
- 5) Type 'gnd-earth' and place it on the white sheet
- 6) Now arrange these components on the white sheet according to the circuit diagram as following
- 7) After arranging click on simulate button and the following results are generated

### **OBSERVATION:**



V <sub>2</sub> (Calculated)	V₃(Calculated)	V <sub>2</sub> (PSPICE)	V₃(PSPICE)	Error In V <sub>2</sub>	Error In V₃
12.093V	10.736V	12.07V	10.62V	$\frac{12.07V - 12.093V}{12.093V} \times 100$	10.62V - 10.736V
				12.07V	10.62V
				= 0.19%	$\times 100 = 1.09\%$

#### **CONCLUSION:**

In accordance with the Node Voltage Analysis, we made use of Kirchhoff's Current law to arrive at a system of equations which aims to calculate the node voltages. The calculated voltages were then verified in PSPICE Simulator. PSCPICE Confirmed the calculations of Node Voltage Method. Errors were also arised in the calculated values because of low precision. The errors were negligibly small to ignore.