Functionally-Complete Boolean Logic in Real DRAM Chips: Experimental Characterization and Analysis

Paper Review by:
Ali Asghar(21PWCSE2059)

CSE-420 Embedded Systems
Course Instructor: Dr. Asif Ali Khan

**DEPARTMENT OF COMPUTER SYSTEMS ENGINEERING
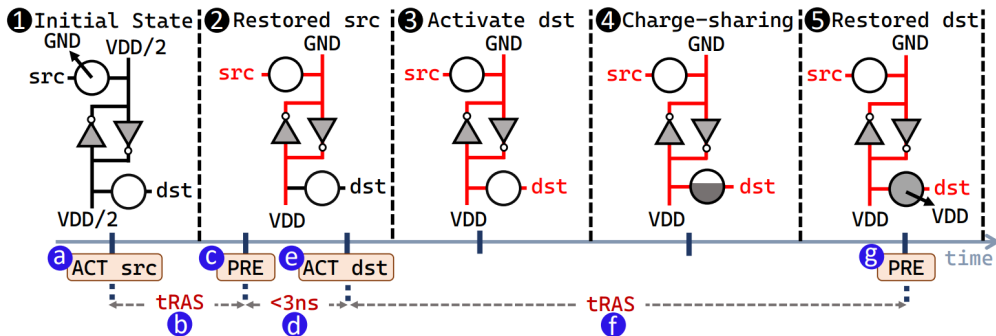UNIVERSITY OF ENGINEERING & TECHNOLOGY,
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Introduction

- **Problem:** Data movement
- **Solution:** Processing-using-DRAM (PuD)
- **Previous Work:** Basic bitwise operations (AND, OR) but lacked functionally complete logic.
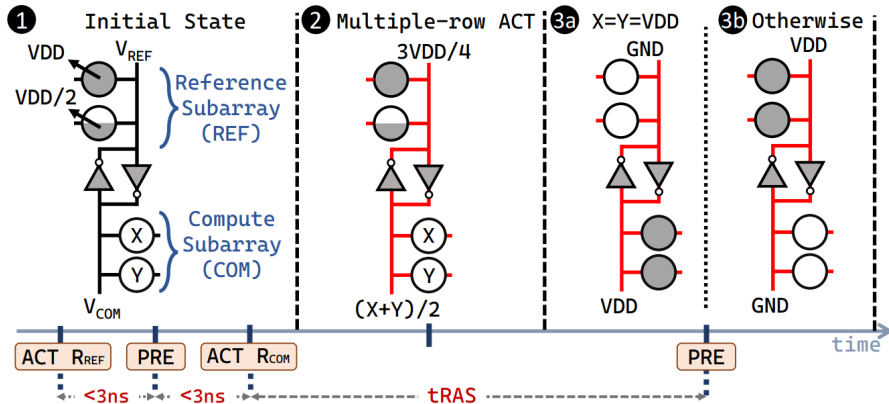
NOT Gate Implementation

AND and NAND Gate Implementation

Thank you!