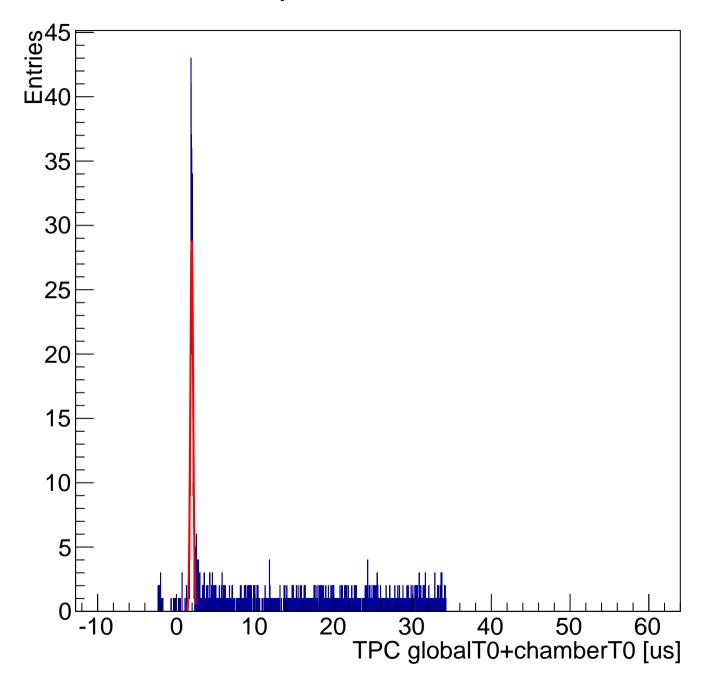
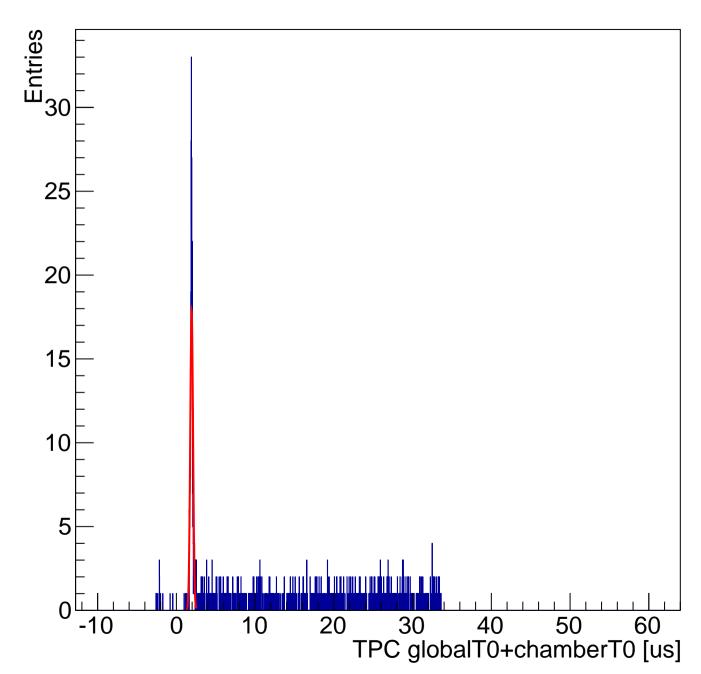
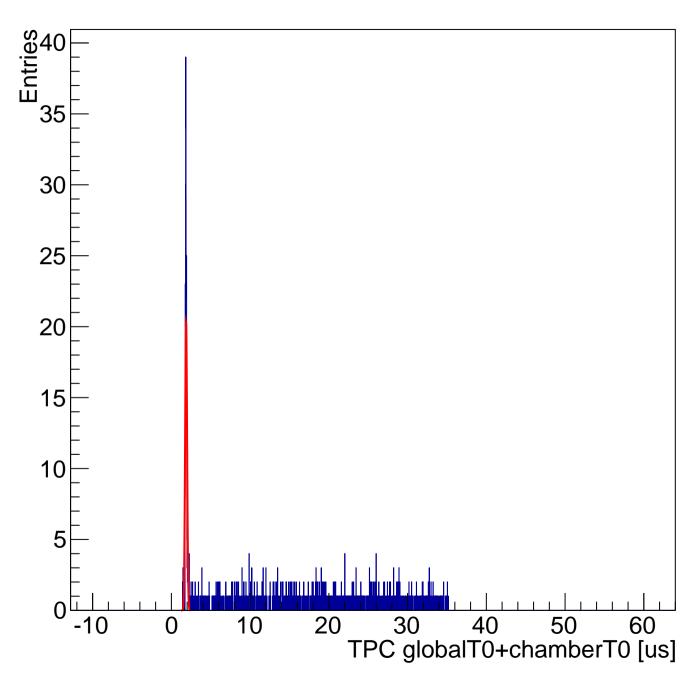
# tpcT0\_VTPC1



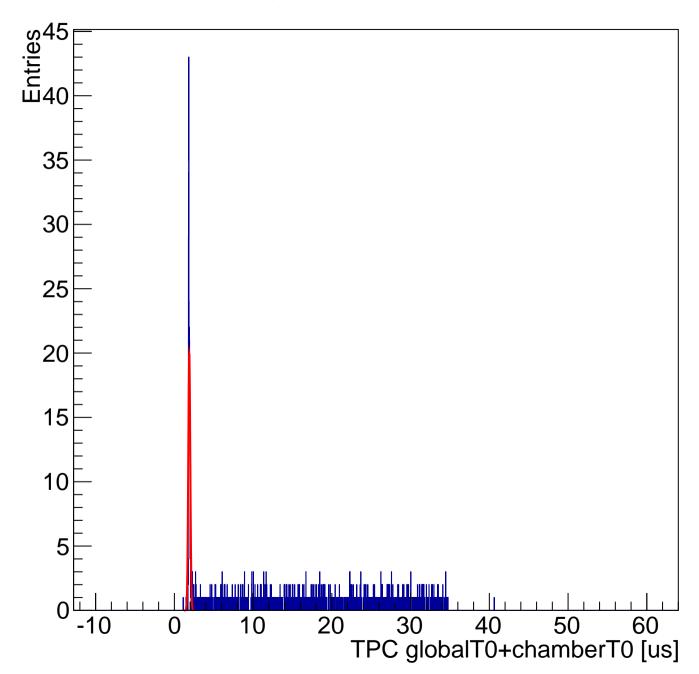
# tpcT0\_VTPC2



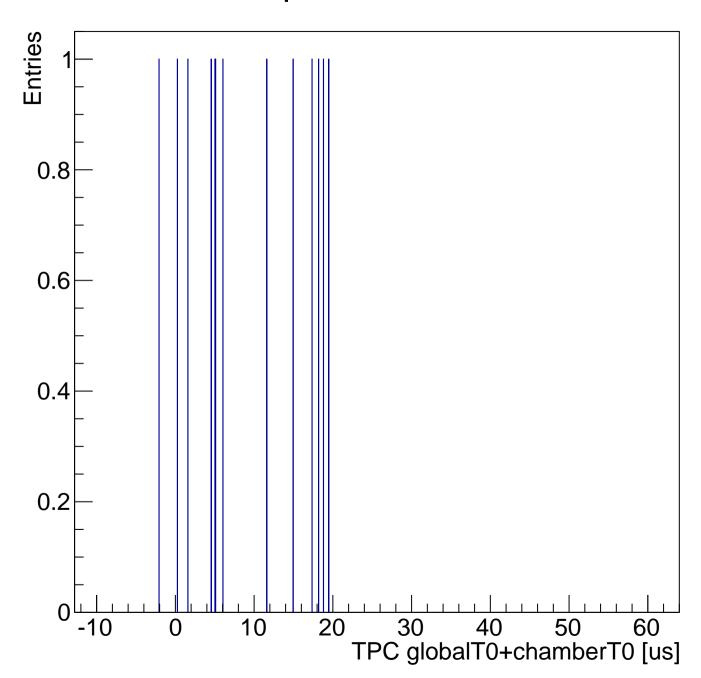
# tpcT0\_MTPCL



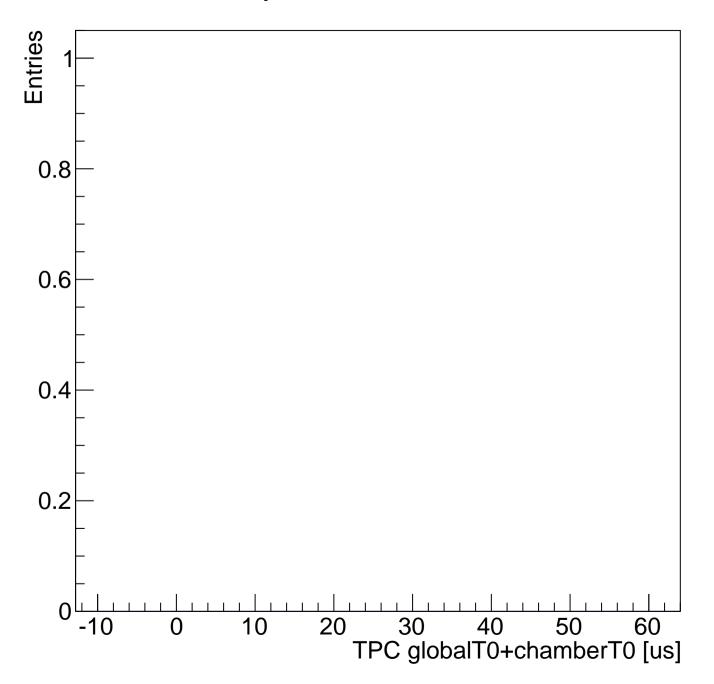
## tpcT0\_MTPCR



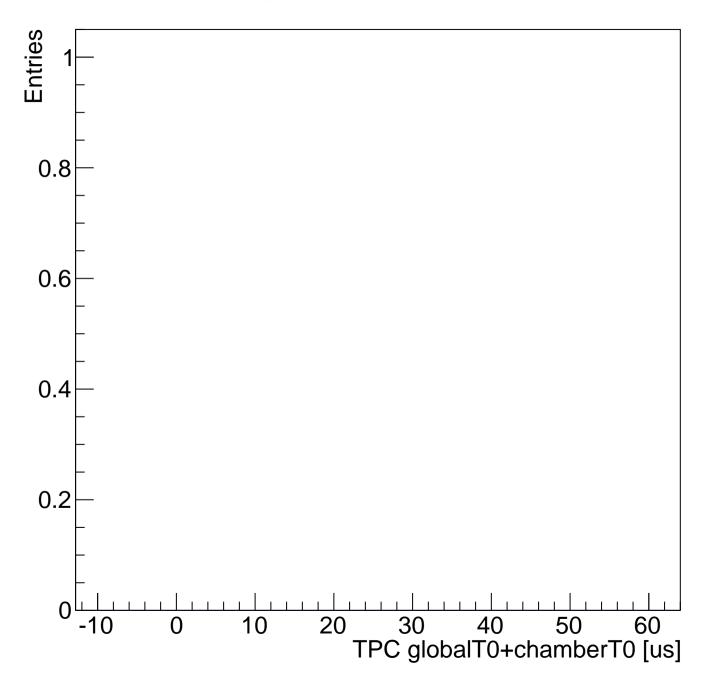
## tpcT0\_GTPC



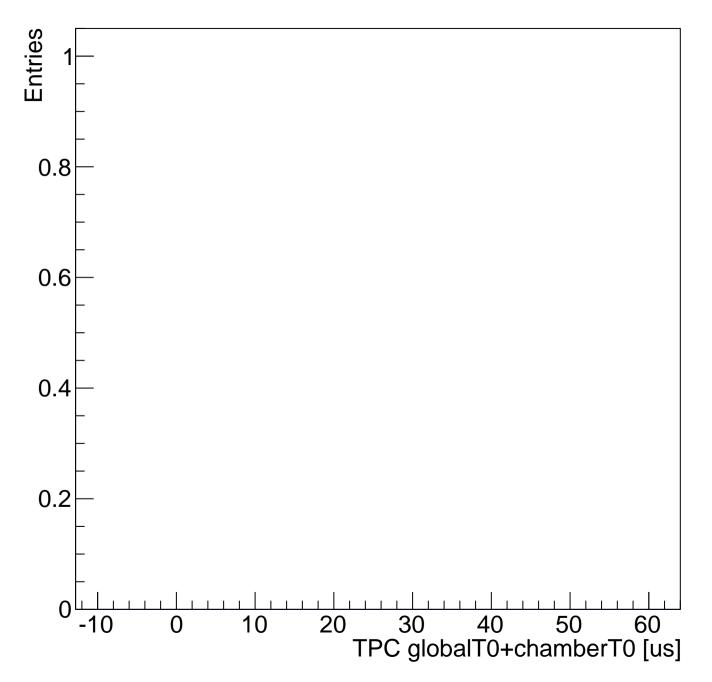
## tpcT0\_LMPDJD



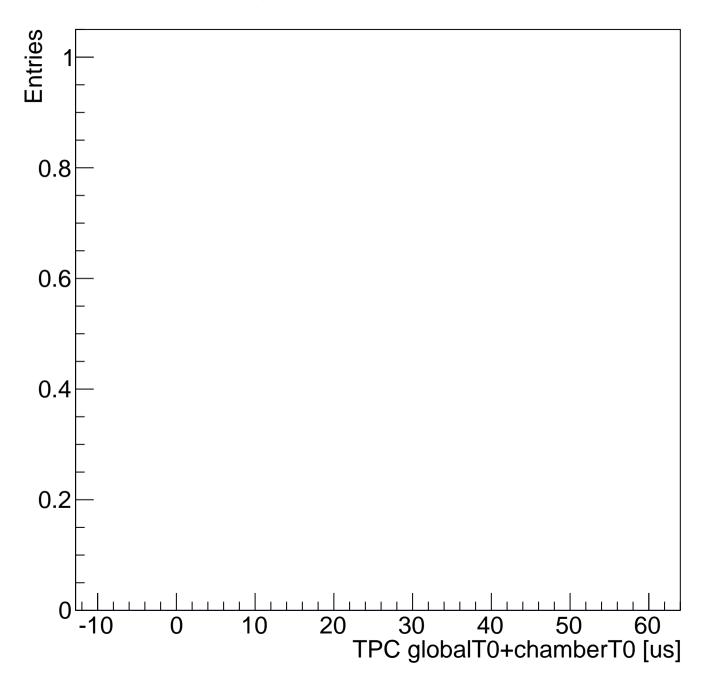
## tpcT0\_LMPDJU



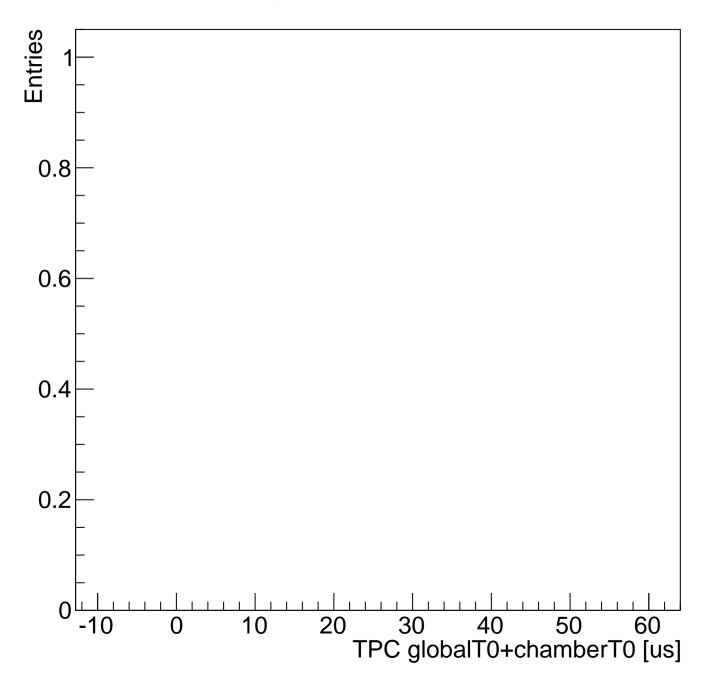
## tpcT0\_LMPDSU



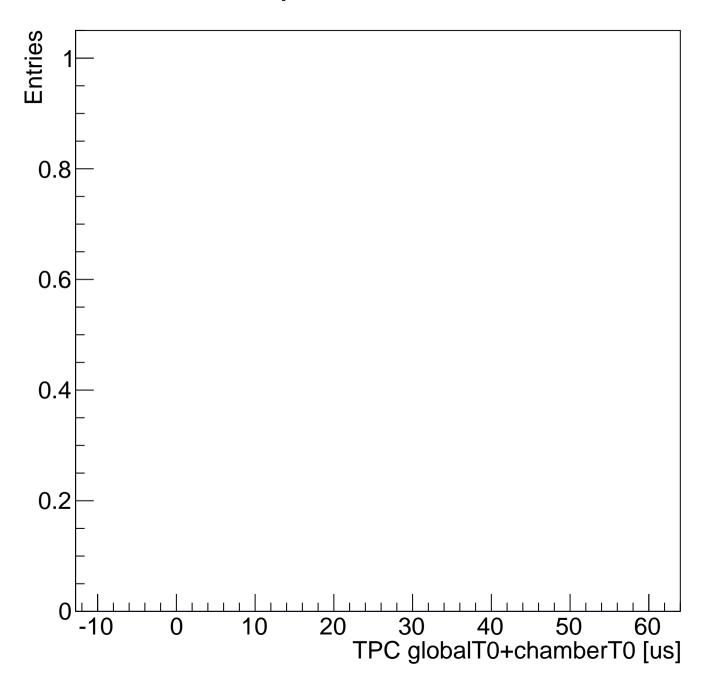
## tpcT0\_LMPDSD



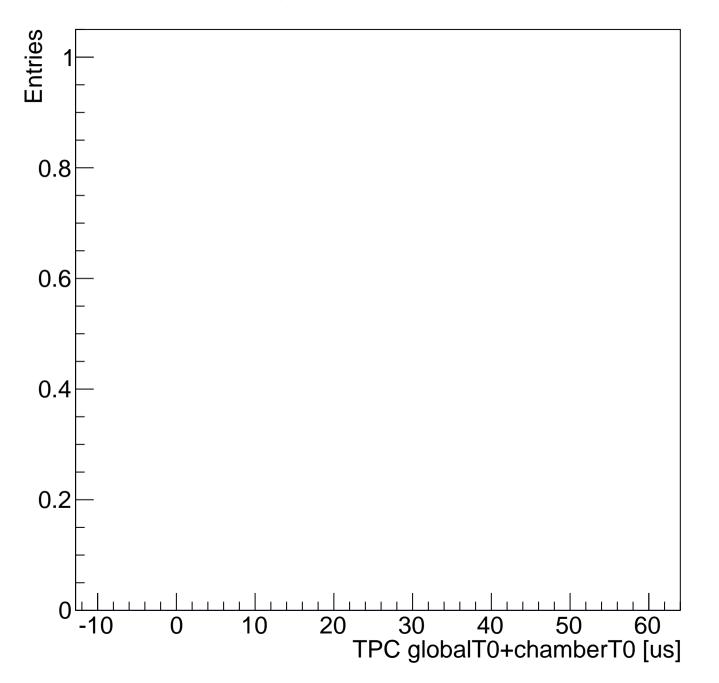
# tpcT0\_LMPDP



# tpcT0\_FTPC1



## tpcT0\_FTPC2



## tpcT0\_FTPC3

