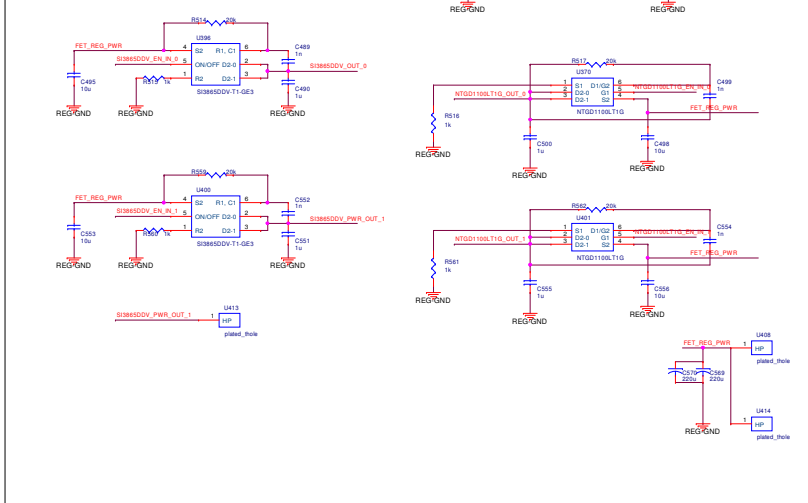


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The schematic diagram illustrates the internal circuitry of the PCB054WP4N module. It features two RS-485 transceivers, IC1 and IC2, which are connected to a 3.3V supply and a 100Ω termination resistor. The module also includes a USB-to-UART bridge, IC3, which is connected to a USB port and a 3.3V supply. The schematic shows the internal wiring of the module, including the RS-485 transceivers, the USB-to-UART bridge, and the power supply connections. The module is powered by a 3.3V supply and has a GND connection. The schematic shows the internal wiring of the module, including the RS-485 transceivers, the USB-to-UART bridge, and the power supply connections.

The top diagram shows the IC4053PW (CMOS 3-input NAND) connected to the IC4053PW (CMOS 3-input NAND). The IC4053PW is connected to the IC4053PW via a 5.2V supply and a 10k resistor. The IC4053PW is connected to the IC4053PW via a 5.2V supply and a 10k resistor.

The bottom diagram shows the IC4053PW (CMOS 3-input NAND) connected to the IC4053PW (CMOS 3-input NAND). The IC4053PW is connected to the IC4053PW via a 5.2V supply and a 10k resistor. The IC4053PW is connected to the IC4053PW via a 5.2V supply and a 10k resistor.

Figure 10 consists of four circuit diagrams, each showing a different method to connect a differential signal source to a differential signal sink.

- Diagram 1 (Top Left):** A differential signal source (DIFFAMPLI_P, DIFFAMPLI_N) is connected to a differential signal sink (DIFFAMPLI_P_IN, DIFFAMPLI_N_IN) via two resistors, R413 (1k) and R412 (1k). A capacitor C437 (100n) is connected between the two input lines of the sink. The source is connected to GND.
- Diagram 2 (Top Right):** A differential signal source (DIFFAMPLI_P, DIFFAMPLI_N) is connected to a differential signal sink (DIFFAMPLI_P_IN, DIFFAMPLI_N_IN) via two resistors, R413 (1k) and R412 (1k). A capacitor C437 (100n) is connected between the two input lines of the sink. The source is connected to GND.
- Diagram 3 (Bottom Left):** A differential signal source (DIFFAMPLI_P, DIFFAMPLI_N) is connected to a differential signal sink (DIFFAMPLI_P_IN, DIFFAMPLI_N_IN) via two resistors, R413 (1k) and R412 (1k). A capacitor C437 (100n) is connected between the two input lines of the sink. The source is connected to GND.
- Diagram 4 (Bottom Right):** A differential signal source (DIFFAMPLI_P, DIFFAMPLI_N) is connected to a differential signal sink (DIFFAMPLI_P_IN, DIFFAMPLI_N_IN) via two resistors, R413 (1k) and R412 (1k). A capacitor C437 (100n) is connected between the two input lines of the sink. The source is connected to GND.