

CPSC440 Project (Task 2)

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Ning Chen, Ph.D. Prof. of Computer Science Department, California State University, Fullerton

Project Title:

An Introduction of “Internet of Things”

In this project you have a chance to have a holistic view on the “Internet of Things.”

Step 2 Task

In Step 2 Task, your group needs to study the MC1322x datasheet (available from the class website) and submit a presentation (ppt and mp3) that covers the following topics:

1. What is the memory architecture (or design) of the MC1322x? You may want to read Section 4 Memory, Section 4.1 RAM and ROM, Section 4.2 Serial FLASH (NVM) of the MC1322x datasheet.
2. What are GPIO, UART and Timer (TMR) Module? You may want to read Section 5.1 Parallel IO (GPIO), Section 5.3 Timer (TMR) Module and Section 5.4 UART Modules.

Q. You only need one presentation per group. Is this correct?

A. Yes.

Q. What is the title of my group’s presentation?

A. The title of your presentation is “The Hardware of MC1322x Part 1.”

Q. Is this task related to CPSC440?

A. Yes. This task allows us to learn the hardware using a top-down approach. You will get a better overall view on the system (hardware and software).

Q. Why do you want us to study Memory, TMR, GPIO and UART?

A. They have some relationship with the software issues that we will discuss later.

Q. How long is my group’s presentation?

A. For Task2, the minimum is 20 minutes (if you plan to have a longer presentation, don’t go over 40 minutes).

Q. What do you want to hear from us?

A. Actually I want to hear what you have to say (anything is fine) after reading those sections.
The main purpose of the project is to promote self-learning (instead of you learn to satisfy me).

Q. What is the grading rubric of this assignment?

A.

Category	Emphasis	
Presentation (10 points)	Effort	
	References used	
	Contents	
Insights (15 points)	Own way of expressing information (not just cut-and-paste)	
	Integration of different sources of information	
	Own thoughts and opinions	
Technical Issues (5 points)	Aesthetics	
	No failing issues on uploading, downloading, opening, ...	