

Northern University Bangladesh



Lab report: 04

Department: CSE

Course title: Digital Electronics & Pulse Technique Lab Work

Experiment no: CSE 2366

Experiment name: Implement Diode Transistor Logic (DTL) NOR, Transistor Transistor Logic (TTL) NOT gate in Tinkercad

Submitted by

Submitted to

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Date of Experiment: 01/7/2024

Date of submission: 7/7/2024

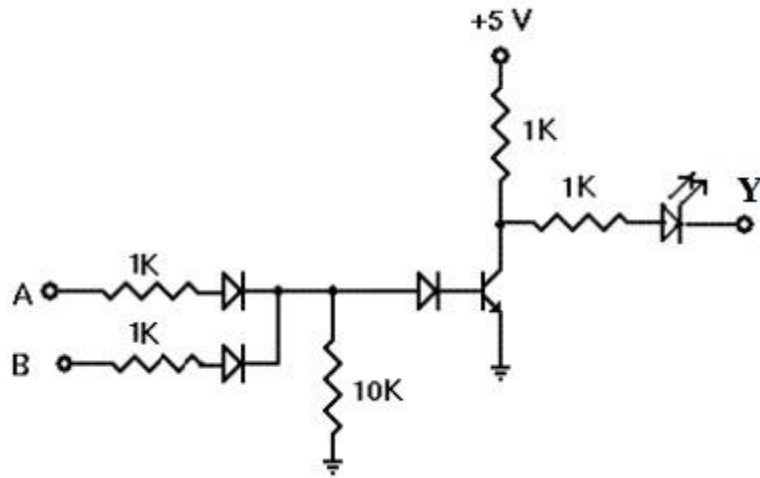
Name of teacher: Nizia Nahyan

Designation: Lecturer

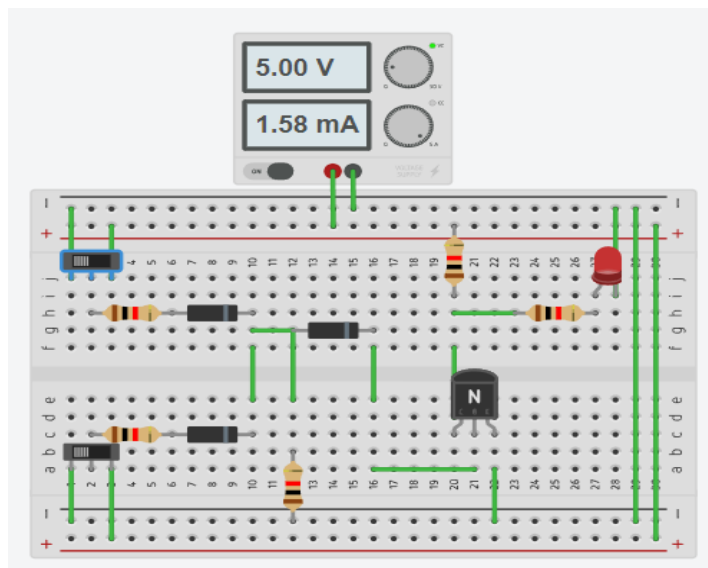
Signature:

DTL NOR gate

Circuit diagram:



Circuits Screenshot:

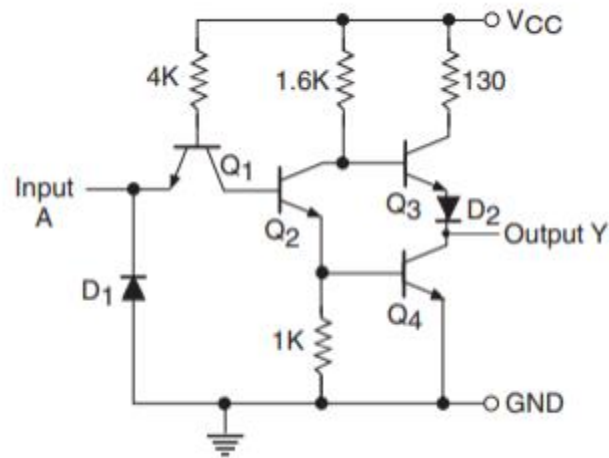


Tinkercad circuit link:

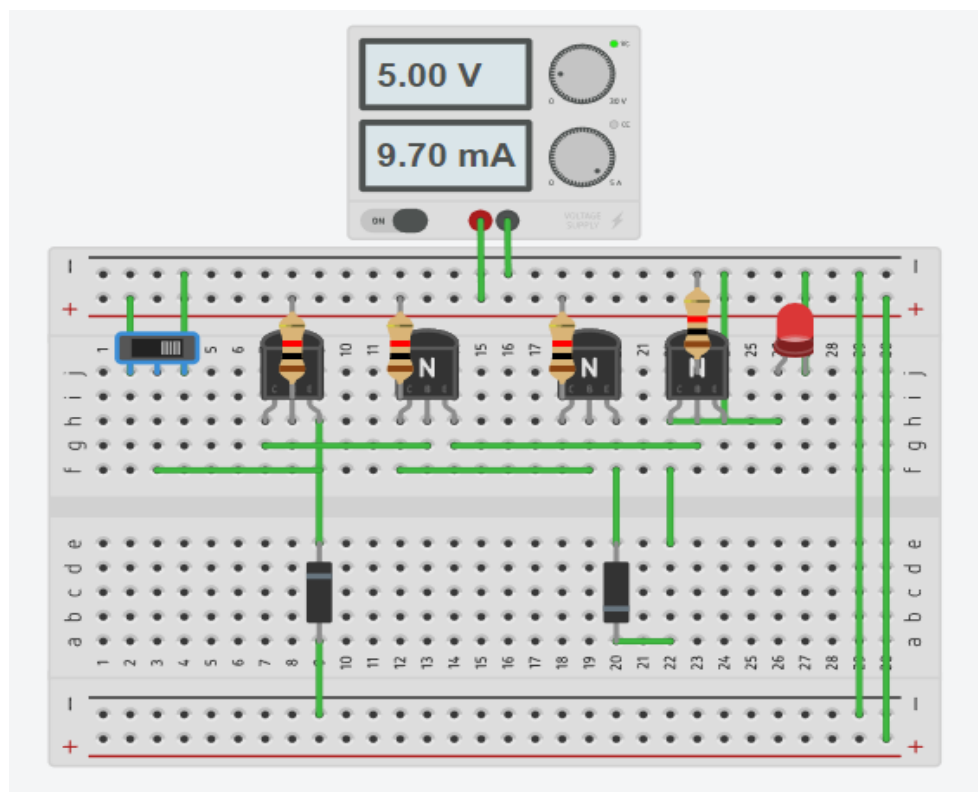
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TTL NOT gate

Circuit diagram:



Circuit screenshot:



Tinkercad circuit link:

https://www.tinkercad.com/things/8hWCwrv1LUsttl-not-1615?sharecode=CXD3XrlbgKHVrdeCHDFepEWISpyqHY_azTGf97Slqpg