

Northern University Bangladesh



Lab report: 03

Department: CSE

Course title: Digital Electronics & Pulse Technique Lab Work

Experiment no: CSE 2366

Experiment name: Implement Diode Transistor Logic NAND, NOT gate in Tinkercad

Submitted by

Submitted to

Name: Fardeen Ahmed

Id: 41210301615

Date of Experiment: 24/6/2024

Date of submission: 27/6/2024

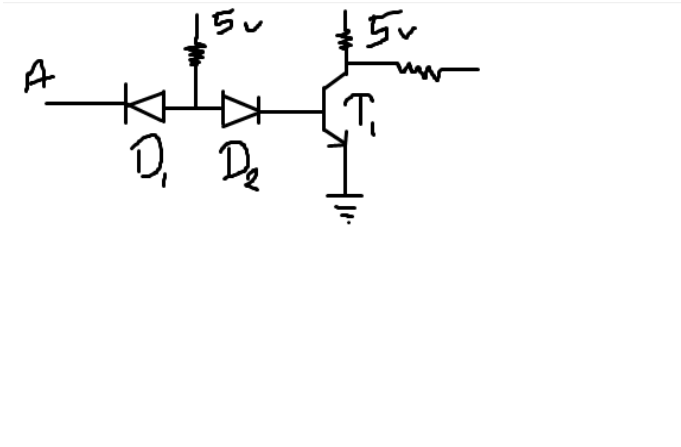
Name of teacher: Nizia Nahyan

Designation: Lecturer

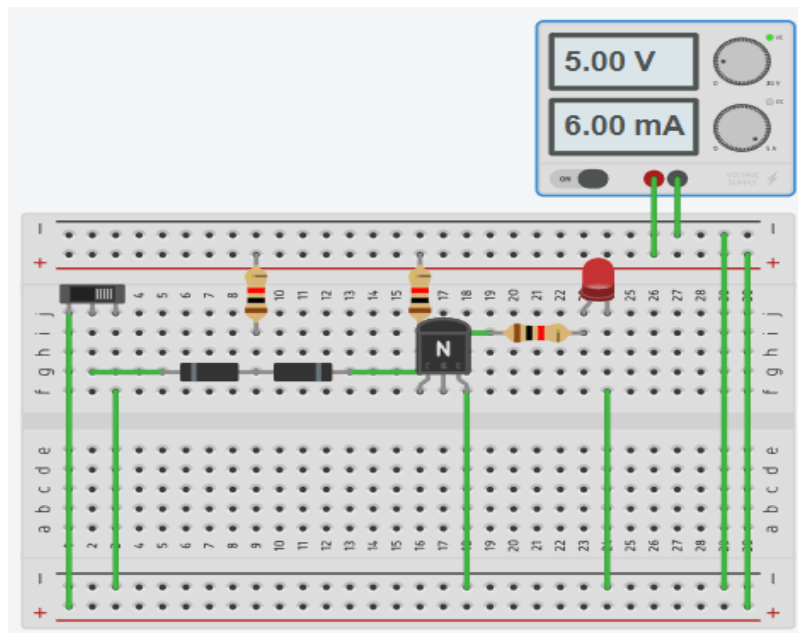
Signature:

DTL NOT gate

Circuit diagram:



Circuits Screenshot:

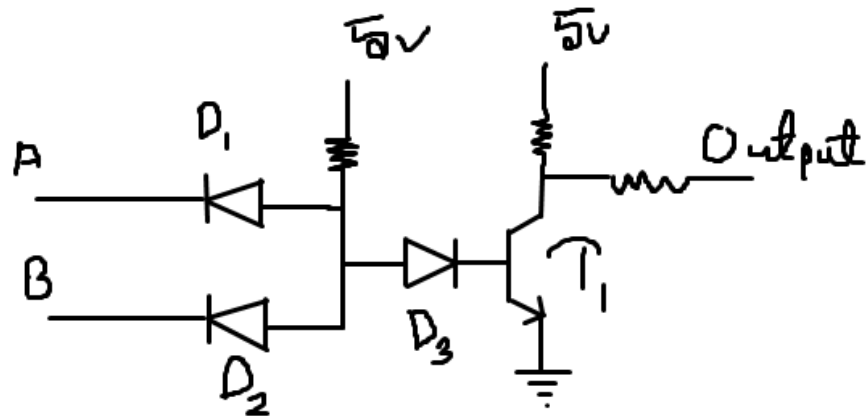


Tinkercad circuit link:

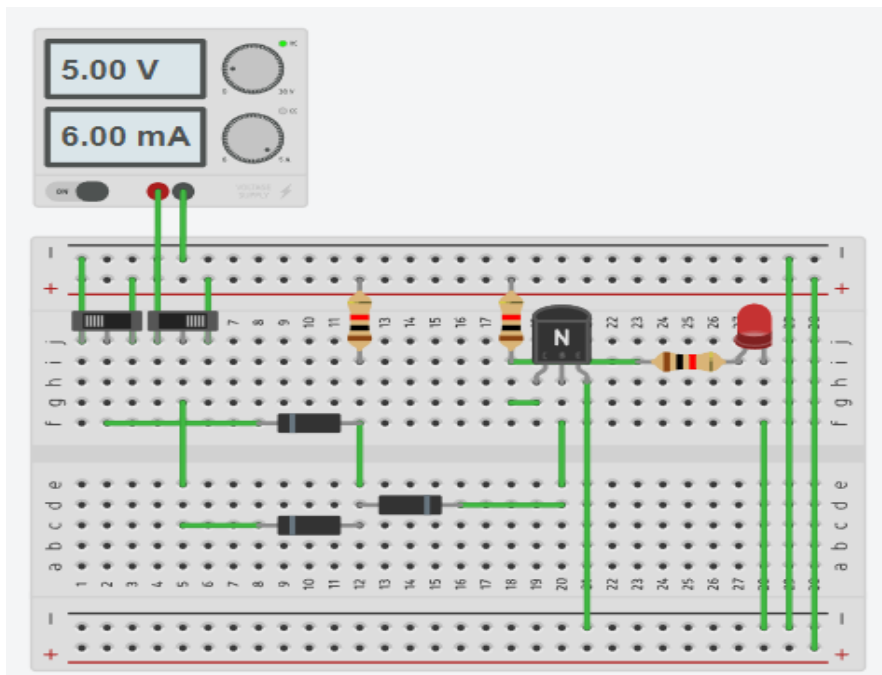
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DTL NAND gate

Circuit diagram:



Circuit screenshot:



Tinkercad circuit link:

<https://www.tinkercad.com/things/ixPrXUCA9qO-dtl-nand-1615?sharecode=Sgk8z7S9ILWINmn8lI00aGny9gtPwRpHp1wH3msOq-l>