

**COMSATS University Islamabad, Lahore Campus**  
**Department of Electrical and Computer Engineering**  
**M. A. Jinnah Campus, Lahore.**

**EEE241 – Digital Logic Design**

**SP23-BCS-A Fall 2023**

**Total Marks 20**

**Wednesday 29 November 2023**

**Assignment 3**

**Resource Person:** Dr. Muhammad Farooq-i-Azam

**Submission Deadline:** **Wednesday 13 December 2023**

The project comprises of two components:

1. Written Report
2. Hardware Project

**Written Report**

- (a) Describe the circuit that you have chosen as your project in a clear manner. Describe the complete function of the circuit.
- (b) Draw a clear circuit diagram of the project. Label all the components appropriately. Draw your own diagram using suitable software. Do not use a copied diagram for your report.
- (c) Describe any integrated circuits that are used in the project along with their pin description and functions.
- (d) Include a picture of your project in the report.
- (e) Use appropriate captions for the figures, pictures and diagrams.

**Hardware**

- (a) The hardware should be assembled on prototyping breadboard.
- (b) Use appropriate labels on the circuit board.

Copied work will be penalized and may be awarded zero marks.