Name: _____   **Registration No:** _____

Class:	**SP23-BCS-A**	**Marks: 10**
Subject:	**Digital Logic Design**	**Due Date: 23-11-2023**
Instructor:	**Sarmad Hassan**	

INSTRUCTIONS

- **Do all the necessary workings and attach it with answer sheet.**
- **No marks will be given without complete working and complete simulation window screenshot.**
- **Add the screenshot of complete window, half/cropped screenshot is not acceptable.**
- **Copied assignments will be marked as zero.**

Question: 1

    A. *Design* a Half Adder and Half Subtractor using 74153 IC.

    B. *Design* a Full Adder and Full Subtractor using 74153 IC.

    C. Implement the designed circuits in part A and B on proteus to verify the output.