

Game Project Status (06/29/2021 - 22:51:54)			
Project File:	Q1.xise	Parser Errors:	No Errors
Module Name:	Game	Implementation State:	Synthesized
Target Device:	xc6slx16-3csg324	• Errors:	No Errors
Product Version:	ISE 14.7	• Warnings:	2 Warnings (0 new)
Design Goal:	Balanced	• Routing Results:	
Design Strategy:	Xilinx Default (unlocked)	• Timing Constraints:	
Environment:	System Settings	• Final Timing Score:	

Device Utilization Summary (estimated values)			[-]
Logic Utilization	Used	Available	Utilization
Number of Slice Registers	40	18224	0%
Number of Slice LUTs	137	9112	1%
Number of fully used LUT-FF pairs	32	145	22%
Number of bonded IOBs	28	232	12%
Number of BUFG/BUFGCTRLs	1	16	6%

Detailed Reports					[-]
Report Name	Status	Generated	Errors	Warnings	Infos
Synthesis Report	Current	Tue Jun 29 22:51:53 2021	0	2 Warnings (0 new)	1 Info (0 new)
Translation Report					
Map Report					
Place and Route Report					
Power Report					
Post-PAR Static Timing Report					
Bitgen Report					

Secondary Reports			[-]
Report Name	Status	Generated	

Date Generated: 06/29/2021 - 23:38:34