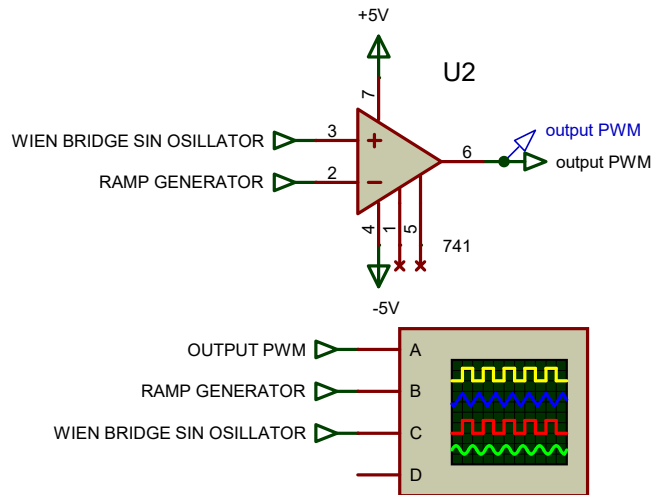
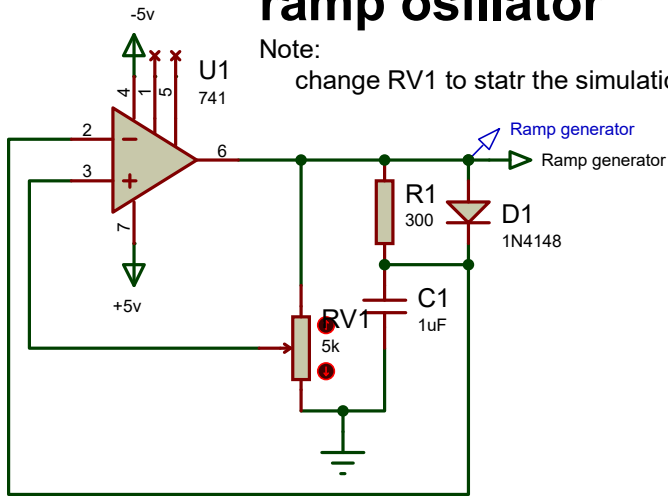


ramp osillator

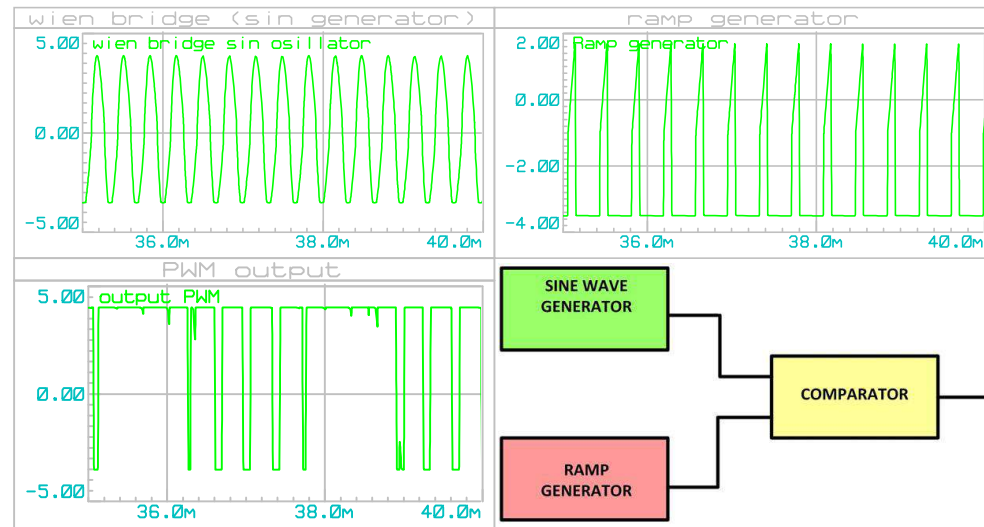
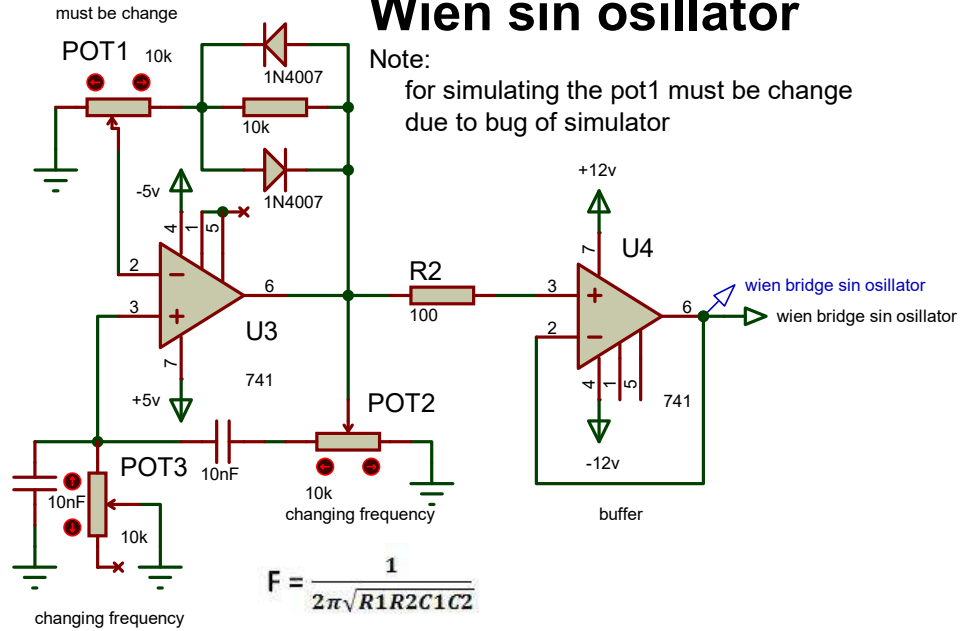
Note:
change RV1 to statr the simulation



comparator using op-Amp

Wien sin osillator

Note:
for simulating the pot1 must be change
due to bug of simulator



PWM modulation
Pulse & Technik Project

simulated by:
Alireza Sotoodeh