#### 2015 Test beam Run Control

Generated by Doxygen 1.6.1

Wed Jul 29 19:40:20 2015

### **Contents**

1	Mod	lule Ind	ex		1
	1.1	Modul	es		. 1
2	Nan	nespace	Index		3
	2.1	Names	space List		. 3
3	Data	a Struct	ure Index	· ·	5
	3.1	Class l	Hierarchy		. 5
4	Data	a Struct	ure Index		7
	4.1	Data S	tructures		. 7
5	Mod	lule Do	cumentati	ion	9
	5.1	Socket	communi	cation objects	. 9
6	Nan	<b>nespace</b>	Documen	ntation	11
	6.1	NIM N	Namespace	Reference	. 11
	6.1	NIM N 6.1.1	-	e Reference	
	6.1		-		. 12
	6.1	6.1.1	Enumera 6.1.1.1	ation Type Documentation	. 12 . 12
		6.1.1	Enumera 6.1.1.1 Namespac	tion Type Documentation	. 12 . 12 . 13
		6.1.1 VME	Enumera 6.1.1.1 Namespac	HVModuleN470Opcodes	. 12 . 12 . 13 . 15
		6.1.1 VME	Enumera 6.1.1.1 Namespac Typedef	tion Type Documentation	. 12 . 12 . 13 . 15
		6.1.1 VME	Enumera 6.1.1.1 Namespac Typedef 6.2.1.1	tion Type Documentation	. 12 . 12 . 13 . 15 . 15
		6.1.1 VME	Enumera 6.1.1.1 Namespac Typedef 6.2.1.1 6.2.1.2 6.2.1.3	tion Type Documentation	. 12 . 12 . 13 . 15 . 15 . 15
		6.1.1 VME 1 6.2.1	Enumera 6.1.1.1 Namespac Typedef 6.2.1.1 6.2.1.2 6.2.1.3	tion Type Documentation	. 12 . 12 . 13 . 15 . 15 . 15 . 16

ii CONTENTS

	6.2.2.3	CAENETControllerV288Answer	16
	6.2.2.4	CAENETControllerV288Register	16
	6.2.2.5	CFDV812Register	17
	6.2.2.6	DetectionMode	17
	6.2.2.7	FPGAUnitV1495Register	17
	6.2.2.8	IOModuleV262Register	18
	6.2.2.9	micro_handshake	18
	6.2.2.10	TDCV1x90Register	18
	6.2.2.11	trailead_edge_lsb	19
	6.2.2.12	trig_conf	20
6.3 VME:	::TDCV1x9	000pcodes Namespace Reference	21
6.3.1	Function	Documentation	24
	6.3.1.1	AUTOLOAD_DEF_CONFI	24
	6.3.1.2	AUTOLOAD_USER_CONF	24
	6.3.1.3	CLEAR_KEEP_TOKEN	24
	6.3.1.4	CONT_STOR	24
	6.3.1.5	DEFAULT_SETUP_REG	24
	6.3.1.6	DIS_ALL_CHANNEL	24
	6.3.1.7	DIS_CHANNEL	24
	6.3.1.8	DIS_ERROR_BYPASS	24
	6.3.1.9	DIS_ERROR_MARK	24
	6.3.1.10	DIS_HEAD_TRAILER	24
	6.3.1.11	DIS_SUB_TRG	24
	6.3.1.12	DISABLE_TEST_MODE	24
	6.3.1.13	EN_ALL_CHANNEL	24
	6.3.1.14	EN_CHANNEL	24
	6.3.1.15	EN_ERROR_BYPASS	24
	6.3.1.16	EN_ERROR_MARK	24
	6.3.1.17	EN_HEAD_TRAILER	24
	6.3.1.18	EN_SUB_TRG	24
	6.3.1.19	ENABLE_TEST_MODE	24
	6.3.1.20	LOAD_DEF_CONFIG	24
	6.3.1.21	LOAD_USER_CONFIG	24
	6.3.1.22	READ ACO MOD	24

62122	DEAD ADMICT ON	24
	READ_ADJUST_CH	
6.3.1.24	READ_DETECTION	24
6.3.1.25	READ_DETECTION	
6.3.1.26	READ_DLL_LOCK	
6.3.1.27	READ_EEPROM	
6.3.1.28	READ_EN_PATTERN	
6.3.1.29	READ_EN_PATTERN32	
6.3.1.30	READ_ERROR_STATUS	
6.3.1.31	READ_ERROR_TYPES	24
6.3.1.32	READ_EVENT_SIZE	24
6.3.1.33	READ_FIFO_SIZE	24
6.3.1.34	READ_GLOB_OFFS	24
6.3.1.35	READ_HEAD_TRAILER	24
6.3.1.36	READ_MICRO_REV	24
6.3.1.37	READ_RC_ADJ	24
6.3.1.38	READ_RES	24
6.3.1.39	READ_SETUP_REG	24
6.3.1.40	READ_SETUP_SCANPATH	24
6.3.1.41	READ_SPARE	24
6.3.1.42	READ_STATUS_STREAM	24
6.3.1.43	READ_TDC_ID	24
6.3.1.44	READ_TRG_CONF	24
6.3.1.45	RESET_DLL_PLL	24
6.3.1.46	REV_DATE_MICRO_FW	24
6.3.1.47	SAVE_RC_ADJ	24
6.3.1.48	SAVE_USER_CONFIG	24
6.3.1.49	SET_ADJUST_CH	24
6.3.1.50	SET_DEAD_TIME	24
6.3.1.51	SET DETECTION	24
6.3.1.52	SET_DLL_CLOCK	24
6.3.1.53	SET_ERROR_TYPES	24
6.3.1.54	SET_EVENT_SIZE	24
6.3.1.55	SET_FIFO_SIZE	24
6.3.1.56	SET_GLOB_OFFS	24
5.5.1.50	DET_OROD_OTTO	∠¬

iv CONTENTS

			6.3.1.57	SET_KEEP_TOKEN	24
			6.3.1.58	SET_PAIR_RES	24
			6.3.1.59	SET_RC_ADJ	24
			6.3.1.60	SET_REJ_MARGIN	24
			6.3.1.61	SET_SW_MARGIN	24
			6.3.1.62	SET_TDC_TSET_OUTPUT	24
			6.3.1.63	SET_TR_LEAD_LSB	24
			6.3.1.64	SET_WIN_OFFS	24
			6.3.1.65	SET_WIN_WIDTH	24
			6.3.1.66	TRG_MATCH	24
			6.3.1.67	UPDATE_SETUP_REG	24
			6.3.1.68	UPDATE_SETUP_TDC	24
			6.3.1.69	WRITE_EEPROM	24
			6.3.1.70	WRITE_EN_PATTERN	24
			6.3.1.71	WRITE_EN_PATTERN32	24
			6.3.1.72	WRITE_SETUP_REG	24
			6.3.1.73	WRITE_SPARE	24
7			ure Docur	mentation	25
	7.1		_	718 Class Reference	25
	7.1	VME:: 7.1.1	Detailed	Description	25 26
	7.1		Detailed	Description	
	7.1	7.1.1	Detailed	Description	26
	7.1	7.1.1	Detailed Member 7.1.2.1	Description	26 26
	7.1	7.1.1 7.1.2	Detailed Member 7.1.2.1	Description	26 26 26
	7.1	7.1.1 7.1.2	Detailed Member 7.1.2.1 Construc	Description	<ul><li>26</li><li>26</li><li>26</li><li>27</li></ul>
	7.1	7.1.1 7.1.2	Detailed Member 7.1.2.1 Construc 7.1.3.1 7.1.3.2	Description	26 26 26 27 27
	7.1	7.1.1 7.1.2 7.1.3	Detailed Member 7.1.2.1 Construc 7.1.3.1 7.1.3.2	Description	26 26 26 27 27 27
	7.1	7.1.1 7.1.2 7.1.3	Detailed Member 7.1.2.1 Construc 7.1.3.1 7.1.3.2 Member	Description  Enumeration Documentation  IRQId  tor & Destructor Documentation  BridgeVx718  ~BridgeVx718  Function Documentation	26 26 27 27 27 27
	7.1	7.1.1 7.1.2 7.1.3	Detailed Member 7.1.2.1 Construc 7.1.3.1 7.1.3.2 Member 7.1.4.1	Description  Enumeration Documentation  IRQId  tor & Destructor Documentation  BridgeVx718  ~BridgeVx718  Function Documentation  CheckConfiguration	26 26 27 27 27 27 27
	7.1	7.1.1 7.1.2 7.1.3	Detailed Member 7.1.2.1 Construc 7.1.3.1 7.1.3.2 Member 7.1.4.1 7.1.4.2	Description  Enumeration Documentation  IRQId  tor & Destructor Documentation  BridgeVx718  ~BridgeVx718  Function Documentation  CheckConfiguration  CheckPCIInterface	26 26 27 27 27 27 27 27
	7.1	7.1.1 7.1.2 7.1.3	Detailed Member 7.1.2.1 Construc 7.1.3.1 7.1.3.2 Member 7.1.4.1 7.1.4.2 7.1.4.3	Description  Enumeration Documentation  IRQId  tor & Destructor Documentation  BridgeVx718  ~BridgeVx718  Function Documentation  CheckConfiguration  CheckPCIInterface  GetHandle	26 26 27 27 27 27 27 27 27
	7.1	7.1.1 7.1.2 7.1.3	Detailed Member 7.1.2.1 Construc 7.1.3.1 7.1.3.2 Member 7.1.4.1 7.1.4.2 7.1.4.3 7.1.4.4	Description  Enumeration Documentation  IRQId  tor & Destructor Documentation  BridgeVx718  ~BridgeVx718  Function Documentation  CheckConfiguration  CheckPCIInterface  GetHandle  GetIRQStatus	26 26 27 27 27 27 27 27 27 27 28

<u>CONTENTS</u> v

		7.1.4.8	OutputConf	28
		7.1.4.9	OutputOff	28
		7.1.4.10	OutputOn	28
		7.1.4.11	Reset	28
		7.1.4.12	SetIRQ	28
		7.1.4.13	SinglePulse	28
		7.1.4.14	StartPulser	28
		7.1.4.15	StopPulser	28
		7.1.4.16	TestOutputs	28
		7.1.4.17	WaitIRQ	28
	7.1.5	Field Do	cumentation	28
		7.1.5.1	fHasIRQ	28
7.2	VME::	BridgeVx	718Control Class Reference	30
	7.2.1	Construc	tor & Destructor Documentation	30
		7.2.1.1	BridgeVx718Control	30
		7.2.1.2	$\sim$ BridgeVx718Control	30
	7.2.2	Member	Function Documentation	30
		7.2.2.1	GetAddressIncrement	30
		7.2.2.2	GetArbiterType	31
		7.2.2.3	GetBusReqLevel	31
		7.2.2.4	GetBusTimeout	31
		7.2.2.5	GetInterruptReq	31
		7.2.2.6	GetReleaseType	31
		7.2.2.7	GetRequesterType	31
		7.2.2.8	GetSysRes	32
	7.2.3	Field Do	cumentation	32
		7.2.3.1	fWord	32
7.3	VME::	BridgeVx	718Status Class Reference	33
	7.3.1	Construc	tor & Destructor Documentation	33
		7.3.1.1	BridgeVx718Status	33
		7.3.1.2	~BridgeVx718Status	33
	7.3.2	Member	Function Documentation	33
		7.3.2.1	Dump	33
		7.3.2.2	GetBERR	34

vi CONTENTS

		7.3.2.3 GetDipSwitch	 34
		7.3.2.4 GetDTACK	 34
		7.3.2.5 GetSystemControl	 34
		7.3.2.6 GetSystemReset	 34
		7.3.2.7 GetUSBType	 34
	7.3.3	Field Documentation	 34
		7.3.3.1 fWord	 34
7.4	VME:	:CAENETControllerV288 Class Reference	 35
	7.4.1	Detailed Description	 36
	7.4.2	Constructor & Destructor Documentation	 36
		7.4.2.1 CAENETControllerV288	 36
		7.4.2.2 ~CAENETControllerV288	 36
	7.4.3	Member Function Documentation	 36
		7.4.3.1 FetchBuffer	 36
		7.4.3.2 GetStatus	 36
		7.4.3.3 Reset	 37
		7.4.3.4 SendBuffer	 37
		7.4.3.5 WaitForResponse	 37
	7.4.4	Friends And Related Function Documentation	 37
		7.4.4.1 operator <<	 37
		7.4.4.2 operator>>	 37
7.5	VME:	:CAENETControllerV288Status Class Reference	 38
	7.5.1	Member Enumeration Documentation	 38
		7.5.1.1 OperationStatus	 38
	7.5.2	Constructor & Destructor Documentation	 39
		7.5.2.1 CAENETControllerV288Status	 39
		7.5.2.2 ~CAENETControllerV288Status	 39
	7.5.3	Member Function Documentation	 39
		7.5.3.1 GetOperationStatus	 39
	7.5.4	Field Documentation	 39
		7.5.4.1 fWord	 39
7.6	VME:	:CFDV812 Class Reference	 40
	7.6.1	Detailed Description	 41
	7.6.2	Constructor & Destructor Documentation	 41

CONTENTS	vii

	7.6.2.1	CFDV812	41
	7.6.2.2	~CFDV812	41
7.6.3	Member	Function Documentation	41
	7.6.3.1	CheckConfiguration	41
	7.6.3.2	DeadTimeCalculator	42
	7.6.3.3	GetFixedCode	42
	7.6.3.4	GetManufacturerId	42
	7.6.3.5	GetModuleType	42
	7.6.3.6	GetModuleVersion	42
	7.6.3.7	GetSerialNumber	42
	7.6.3.8	OutputWidthCalculator	43
	7.6.3.9	SetDeadTime	43
	7.6.3.10	SetOutputWidth	43
	7.6.3.11	SetPOI	43
	7.6.3.12	SetThreshold	44
NIM::I	HVModule	eN470Values::ChannelStatus Class Reference	45
7.7.1	Member	Enumeration Documentation	46
	7.7.1.1	SignalStandard	46
7.7.2	Construc	tor & Destructor Documentation	48
	7.7.2.1	ChannelStatus	48
	7.7.2.2	~ChannelStatus	48
7.7.3	Member	Function Documentation	48
	7.7.3.1	Alarm	48
	7.7.3.2	Dump	48
	7.7.3.3	Enabled	48
	7.7.3.4	HVEnabled	48
	7.7.3.5	Isel	48
	7.7.3.6	Kill	48
	7.7.3.7	MaxV	48
	7.7.3.8	NonCalibrated	48
	7.7.3.9	OVC	48
	7.7.3.10	OVV	48
	7.7.3.11	Polarity	48
	7.7.3.12	RampDown	48
	NIM::17.7.17.7.2	7.6.2.2 7.6.3 Member 7.6.3.1 7.6.3.2 7.6.3.3 7.6.3.4 7.6.3.5 7.6.3.6 7.6.3.7 7.6.3.8 7.6.3.9 7.6.3.10 7.6.3.11 7.6.3.12 NIM::HVModule 7.7.1 Member 7.7.1.1 7.7.2 Construct 7.7.2.1 7.7.2.2 7.7.3 Member 7.7.3.1 7.7.3.2 7.7.3.3 7.7.3.4 7.7.3.5 7.7.3.6 7.7.3.7 7.7.3.8 7.7.3.9 7.7.3.10 7.7.3.11	7.6.2.2 ~CFDV812  7.6.3 Member Function Documentation 7.6.3.1 CheckConfiguration 7.6.3.2 DeadTimeCalculator 7.6.3.3 GetFixedCode 7.6.3.4 GetManufacturerId 7.6.3.5 GetModuleType 7.6.3.6 GetModuleVersion 7.6.3.7 GetSerialNumber 7.6.3.8 OutputWidthCalculator 7.6.3.9 SetDeadTime 7.6.3.10 SetOutputWidth 7.6.3.11 SetPOI 7.6.3.12 SetThreshold NIM::HVModuleN470Values::ChannelStatus Class Reference 7.7.1 Member Enumeration Documentation 7.7.1.1 SignalStandard 7.7.2.1 ChannelStatus 7.7.2.2 ~ChannelStatus 7.7.3.1 Alarm 7.7.3.2 Dump 7.7.3.3 Enabled 7.7.3.4 HVEnabled 7.7.3.5 Isel 7.7.3.6 Kill 7.7.3.7 MaxV 7.7.3.8 NonCalibrated 7.7.3.9 OVC 7.7.3.10 OVV 7.7.3.11 Polarity

viii CONTENTS

		7.7.3.13 RampUp
		7.7.3.14 Standard
		7.7.3.15 Trip
		7.7.3.16 UNV
		7.7.3.17 Vsel
	7.7.4	Friends And Related Function Documentation
		7.7.4.1 operator <<
	7.7.5	Field Documentation
		7.7.5.1 fWord
7.8	Client	Class Reference
	7.8.1	Detailed Description
	7.8.2	Constructor & Destructor Documentation
		7.8.2.1 Client
		7.8.2.2 Client
		7.8.2.3 ~Client
	7.8.3	Member Function Documentation
		7.8.3.1 Announce
		7.8.3.2 Connect
		7.8.3.3 Disconnect
		7.8.3.4 GetType
		7.8.3.5 ParseMessage
		7.8.3.6 Receive
		7.8.3.7 Receive
		7.8.3.8 Send
		7.8.3.9 Send
		7.8.3.10 SendAndReceive
	7.8.4	Field Documentation
		7.8.4.1 fClientId
		7.8.4.2 fIsConnected
		7.8.4.3 fType
7.9	file_he	eader_t Struct Reference
	7.9.1	Detailed Description
	7.9.2	Field Documentation
		7.9.2.1 acq_mode

	7.9.2.2	det_mode	56
	7.9.2.3	magic	56
	7.9.2.4	num_hptdc	56
	7.9.2.5	run_id	56
	7.9.2.6	spill_id	56
7.10 FileR	eader Class	Reference	57
7.10.	l Detailed	Description	57
7.10.2	2 Construc	ctor & Destructor Documentation	58
	7.10.2.1	FileReader	58
	7.10.2.2	FileReader	58
	7.10.2.3	~FileReader	58
7.10.3	3 Member	Function Documentation	58
	7.10.3.1	Clear	58
	7.10.3.2	Dump	58
	7.10.3.3	GetNextEvent	58
	7.10.3.4	GetNextMeasurement	58
	7.10.3.5	GetNumEvents	59
	7.10.3.6	GetNumTDCs	59
	7.10.3.7	IsOpen	59
	7.10.3.8	Open	59
7.10.4	Field Do	cumentation	59
	7.10.4.1	fFile	59
	7.10.4.2	fHeader	59
	7.10.4.3	fNumEvents	59
	7.10.4.4	fReadoutMode	59
	7.10.4.5	fWriteTime	59
7.11 VME	::FPGAUni	itV1495 Class Reference	60
7.11.	l Detailed	Description	61
7.11.2	2 Member	Enumeration Documentation	61
	7.11.2.1	TDCBits	61
7.11.3	3 Construc	etor & Destructor Documentation	62
	7.11.3.1	FPGAUnitV1495	62
	7.11.3.2	~FPGAUnitV1495	62
7.11.4	4 Member	Function Documentation	62

x CONTENTS

	7.11.4.1	CheckBoardVersion	62
	7.11.4.2	ClearOutputPulser	62
	7.11.4.3	DumpFWInformation	63
	7.11.4.4	GetCAENFirmwareRevision	63
	7.11.4.5	GetControl	63
	7.11.4.6	GetGeoAddress	63
	7.11.4.7	GetHardwareRevision	64
	7.11.4.8	GetInternalClockPeriod	64
	7.11.4.9	GetInternalTriggerPeriod	64
	7.11.4.10	GetOutputDelay	64
	7.11.4.11	GetOutputPulser	65
	7.11.4.12	GetScalerValue	65
	7.11.4.13	GetSerialNumber	65
	7.11.4.14	GetTDCBits	65
	7.11.4.15	GetUserFirmwareRevision	65
	7.11.4.16	PulseTDCBits	66
	7.11.4.17	ResetFPGA	66
	7.11.4.18	SetControl	66
	7.11.4.19	SetInternalClockPeriod	66
	7.11.4.20	SetInternalTriggerPeriod	67
	7.11.4.21	SetOutputDelay	67
	7.11.4.22	SetOutputPulser	67
	7.11.4.23	SetOutputPulserPOI	67
	7.11.4.24	SetTDCBits	68
	7.11.4.25	StartScaler	68
	7.11.4.26	StopScaler	68
7.11.5	Field Doo	cumentation	68
	7.11.5.1	fScalerStarted	68
7.12 VME:	FPGAUnit	V1495Control Class Reference	69
7.12.1	Detailed 1	Description	70
7.12.2	Member 1	Enumeration Documentation	70
	7.12.2.1	ClockSource	70
	7.12.2.2	SignalSource	70
	7.12.2.3	TriggerSource	70

CONTENTS xi

7.12.3	Constructor & Destructor Documentation	71
	7.12.3.1 FPGAUnitV1495Control	71
	7.12.3.2 ~FPGAUnitV1495Control	71
7.12.4	Member Function Documentation	71
	7.12.4.1 Dump	71
	7.12.4.2 GetBit	71
	7.12.4.3 GetClockSource	71
	7.12.4.4 GetScalerStatus	71
	7.12.4.5 GetSignalSource	71
	7.12.4.6 GetTriggerSource	72
	7.12.4.7 GetWord	72
	7.12.4.8 SetBit	72
	7.12.4.9 SetClockSource	72
	7.12.4.10 SetScalerReset	72
	7.12.4.11 SetScalerStatus	73
	7.12.4.12 SetSignalSource	73
	7.12.4.13 SetTriggerSource	73
7.12.5	Field Documentation	73
	7.12.5.1 fWord	73
7.13 VME::	:GenericBoard < Register, am > Class Template Reference	74
7.13.1	Constructor & Destructor Documentation	75
	7.13.1.1 GenericBoard	75
	7.13.1.2 ~GenericBoard	75
7.13.2	Member Function Documentation	75
	7.13.2.1 ReadRegister	75
	7.13.2.2 ReadRegister	75
	7.13.2.3 WriteRegister	75
	7.13.2.4 WriteRegister	76
7.13.3	Field Documentation	76
	7.13.3.1 fBaseAddr	76
	7.13.3.2 fHandle	76
7.14 VME::	:GlobalOffset Struct Reference	77
7.14.1	Field Documentation	77
	7.14.1.1 coarse	77

xii CONTENTS

	7.14.1.2 fine	77
7.15 HTTP	PMessage Class Reference	78
	Detailed Description	78
	2 Constructor & Destructor Documentation	78
	7.15.2.1 HTTPMessage	78
	7.15.2.2 HTTPMessage	79
7.15.3		79
	7.15.3.1 Decode	79
	7.15.3.2 Dump	79
	7.15.3.3 Encode	79
	7.15.3.4 GetKey	79
7.15.4	Field Documentation	79
	7.15.4.1 fOriginalString	79
	7.15.4.2 fWS	79
7.16 NIM::	:HVModuleN470 Class Reference	80
7.16.1	Constructor & Destructor Documentation	81
	7.16.1.1 HVModuleN470	81
	7.16.1.2 ~HVModuleN470	81
7.16.2	2 Member Function Documentation	81
	7.16.2.1 GetFWRevision	81
	7.16.2.2 GetModuleId	81
	7.16.2.3 ReadChannelValues	81
	7.16.2.4 ReadMonitoringValues	82
	7.16.2.5 ReadRegister	82
	7.16.2.6 SetChannelI0	82
	7.16.2.7 SetChannelI1	83
	7.16.2.8 SetChannelV0	83
	7.16.2.9 SetChannelV1	83
	7.16.2.10 WriteRegister	83
	7.16.2.11 WriteRegister	84
7.16.3	Field Documentation	84
	7.16.3.1 fAddress	84
	7.16.3.2 fController	84
7.17 NIM··	·HVModuleN470ChannelValues Class Reference	85

CONTENTS		xiii
7.17.1	Detailed Description	85
7.17.2	Constructor & Destructor Documentation	86
	7.17.2.1 HVModuleN470ChannelValues	86
	7.17.2.2 ~HVModuleN470ChannelValues	86
7.17.3	Member Function Documentation	86
	7.17.3.1 ChannelStatus	86
	7.17.3.2 Dump	86
	7.17.3.3 10	87
	7.17.3.4 I1	87
	7.17.3.5 Imon	87
	7.17.3.6 MaxV	87
	7.17.3.7 RampDown	87
	7.17.3.8 RampUp	87
	7.17.3.9 Trip	87
	7.17.3.10 V0	87
	7.17.3.11 V1	87
	7.17.3.12 Vmon	87
7.17.4	Field Documentation	87
	7.17.4.1 fChannelId	87
	7.17.4.2 fValues	87
7.18 NIM::	HVModuleN470Values Class Reference	88
7.18.1	Detailed Description	88
7.18.2	Constructor & Destructor Documentation	89
	7.18.2.1 HVModuleN470Values	89
	7.18.2.2 ~HVModuleN470Values	89
7.18.3	Member Function Documentation	89
	7.18.3.1 Dump	89
	7.18.3.2 GetChannelStatus	89
	7.18.3.3 Imon	89
	7.18.3.4 Vmax	89
	7.18.3.5 Vmon	89
7.18.4	Field Documentation	89
	7.18.4.1 fValues	89
7.19 VME:	:IOModuleV262 Class Reference	90

xiv CONTENTS

	7.19.1	Constructo	or & Destructor Documentation	90
		7.19.1.1	IOModuleV262	90
		7.19.1.2	~IOModuleV262	90
	7.19.2	Member F	unction Documentation	90
		7.19.2.1	GetIdentifier	90
		7.19.2.2	GetManufacturerId	91
		7.19.2.3	GetModuleType	91
		7.19.2.4	GetModuleVersion	91
		7.19.2.5	GetSerialNumber	91
7.20	Messag	ge Class Ret	ference	92
	7.20.1	Detailed D	escription	92
	7.20.2	Constructo	or & Destructor Documentation	93
		7.20.2.1	Message	93
		7.20.2.2	Message	93
		7.20.2.3	Message	93
		7.20.2.4	~Message	93
	7.20.3	Member F	unction Documentation	93
		7.20.3.1	Dump	93
		7.20.3.2	GetKey	93
		7.20.3.3	GetString	93
		7.20.3.4	IsFromWeb	93
	7.20.4	Field Docu	imentation	93
		7.20.4.1	fString	93
7.21	Messer	iger Class F	Reference	95
	7.21.1	Detailed D	Description	96
	7.21.2	Constructo	or & Destructor Documentation	96
		7.21.2.1	Messenger	96
		7.21.2.2	Messenger	96
		7.21.2.3	~Messenger	97
	7.21.3	Member F	unction Documentation	97
		7.21.3.1	AddClient	97
		7.21.3.2	Broadcast	97
		7.21.3.3	Connect	98
		7.21.3.4	Disconnect	98

CONTENTS xv

	7.21.3.5 Discon	nectClient		 	. 98
	7.21.3.6 GetTy <sub>I</sub>	oe		 	. 99
	7.21.3.7 Proces	sMessage		 	. 99
	7.21.3.8 Receiv	e		 	. 100
	7.21.3.9 Send			 	. 100
	7.21.3.10 SendA	11		 	. 101
	7.21.3.11 StartA	equisition		 	. 101
	7.21.3.12 StopAo	equisition		 	. 101
	7.21.3.13 Switch	ClientType		 	. 101
7.21.4	Field Documenta	tion		 	. 102
	7.21.4.1 fNum	Attempts		 	. 102
	7.21.4.2 fPID			 	. 102
	7.21.4.3 fStderr	Pipe		 	. 102
	7.21.4.4 fStdou	tPipe		 	. 102
	7.21.4.5 fWS			 	. 102
7.22 VME::	PCIInterfaceA281	8 Class Reference		 	. 103
7.22.1	Constructor & D	estructor Documen	itation	 	. 103
	7.22.1.1 PCIInt	erfaceA2818		 	. 103
	7.22.1.2 ∼PCII	nterfaceA2818 .		 	. 103
7.22.2	Member Function	n Documentation		 	. 103
	7.22.2.1 GetFW	Revision		 	. 103
7.22.3	Field Documenta	tion		 	. 103
	7.22.3.1 fHandl	e		 	. 103
7.23 Socket	Class Reference			 	. 104
7.23.1	Detailed Descrip	tion		 	. 105
7.23.2	Member Typedef	Documentation		 	. 106
	7.23.2.1 Socket	Collection		 	. 106
7.23.3	Member Enumer	ation Documentati	on	 	. 106
	7.23.3.1 Socket	Type		 	. 106
7.23.4	Constructor & D	estructor Documen	itation	 	. 106
	7.23.4.1 Socket			 	. 106
	7.23.4.2 Socket			 	. 106
	7.23.4.3 ∼Sock	et		 	. 106
7.23.5	Member Function	n Documentation		 	. 106

xvi CONTENTS

	7.23.5.1	AcceptConnections	106
	7.23.5.2	Bind	107
	7.23.5.3	Configure	107
	7.23.5.4	Create	107
	7.23.5.5	DumpConnected	107
	7.23.5.6	FetchMessage	107
	7.23.5.7	GetPort	107
	7.23.5.8	GetSocketId	107
	7.23.5.9	GetSocketType	107
	7.23.5.10	IsWebSocket	107
	7.23.5.11	Listen	107
	7.23.5.12	PrepareConnection	108
	7.23.5.13	SelectConnections	108
	7.23.5.14	SendMessage	108
	7.23.5.15	SetPort	108
	7.23.5.16	SetSocketId	108
	7.23.5.17	Start	108
	7.23.5.18	Stop	108
7.23.6	Field Doc	eumentation	109
	7.23.6.1	fAddress	109
	7.23.6.2	fBuffer	109
	7.23.6.3	fMaster	109
	7.23.6.4	fPort	109
	7.23.6.5	$fReadFds\dots$	109
	7.23.6.6	fSocketId	109
	7.23.6.7	fSocketsConnected	109
7.24 Socket	Message C	llass Reference	110
7.24.1	Detailed l	Description	111
7.24.2	Construct	or & Destructor Documentation	112
	7.24.2.1	SocketMessage	112
	7.24.2.2	SocketMessage	112
	7.24.2.3	SocketMessage	112
	7.24.2.4	SocketMessage	112
	7.24.2.5	SocketMessage	112

CONTENTS	xvii
CONTENTS	XVII

	7.24.2.6 SocketMessage	112
	7.24.2.7 SocketMessage	112
	7.24.2.8 SocketMessage	112
	7.24.2.9 SocketMessage	113
	7.24.2.10 SocketMessage	113
	7.24.2.11 SocketMessage	113
	7.24.2.12 ~SocketMessage	113
7.24.3	Member Function Documentation	113
	7.24.3.1 Dump	113
	7.24.3.2 GetCleanedValue	113
	7.24.3.3 GetIntValue	113
	7.24.3.4 GetKey	113
	7.24.3.5 GetString	113
	7.24.3.6 GetValue	114
	7.24.3.7 GetVectorValue	114
	7.24.3.8 Object	114
	7.24.3.9 SetKeyValue	114
	7.24.3.10 SetKeyValue	114
	7.24.3.11 SetKeyValue	114
	7.24.3.12 SetKeyValue	114
	7.24.3.13 String	115
7.24.4	Field Documentation	115
	7.24.4.1 fMessage	115
7.25 VME::	:TDCErrorFlag Class Reference	116
7.25.1	Detailed Description	116
7.25.2	Constructor & Destructor Documentation	117
	7.25.2.1 TDCErrorFlag	117
	7.25.2.2 ~TDCErrorFlag	117
7.25.3	Member Function Documentation	117
	7.25.3.1 Dump	117
	7.25.3.2 GetWord	117
	7.25.3.3 HasGroupError	117
	7.25.3.4 HasInternalChipError	117
	7.25.3.5 HasL1BufferOverflow	117

xviii CONTENTS

	7.25.3.6 HasReachedEventSizeLimit	117
	7.25.3.7 HasReadoutFIFOOverflow	117
	7.25.3.8 HasTriggerFIFOOverflow	117
7.25.4	Friends And Related Function Documentation	118
	7.25.4.1 operator<<	118
7.25.5	Field Documentation	118
	7.25.5.1 fWord	118
7.26 VME::	TDCEvent Class Reference	119
7.26.1	Detailed Description	120
7.26.2	Member Enumeration Documentation	120
	7.26.2.1 EventType	120
7.26.3	Constructor & Destructor Documentation	121
	7.26.3.1 TDCEvent	121
	7.26.3.2 TDCEvent	121
	7.26.3.3 TDCEvent	121
	7.26.3.4 ~TDCEvent	121
7.26.4	Member Function Documentation	121
	7.26.4.1 Dump	121
	7.26.4.2 GetBunchId	121
	7.26.4.3 GetChannelId	121
	7.26.4.4 GetErrorFlags	121
	7.26.4.5 GetETTT	121
	7.26.4.6 GetEventCount	122
	7.26.4.7 GetEventId	122
	7.26.4.8 GetGeo	122
	7.26.4.9 GetStatus	122
	7.26.4.10 GetTDCId	122
	7.26.4.11 GetTime	122
	7.26.4.12 GetType	122
	7.26.4.13 GetWidth	123
	7.26.4.14 GetWord	123
	7.26.4.15 GetWordCount	123
	7.26.4.16 IsTrailing	123
	7.26.4.17 SetWord	123

CONTENTS	xix

7.26.5	Field Documentation	123
	7.26.5.1 fWord	123
7.27 VME:	:TDCMeasurement Class Reference	124
7.27.1	Detailed Description	124
7.27.2	Constructor & Destructor Documentation	124
	7.27.2.1 TDCMeasurement	124
	7.27.2.2 TDCMeasurement	124
	7.27.2.3 ~TDCMeasurement	125
7.27.3	Member Function Documentation	125
	7.27.3.1 Dump	125
	7.27.3.2 GetBunchId	125
	7.27.3.3 GetChannelId	125
	7.27.3.4 GetETTT	125
	7.27.3.5 GetEventId	125
	7.27.3.6 GetLeadingTime	125
	7.27.3.7 GetTDCId	125
	7.27.3.8 GetToT	125
	7.27.3.9 GetTrailingTime	126
	7.27.3.10 NumErrors	126
	7.27.3.11 NumEvents	126
	7.27.3.12 SetEventsCollection	126
7.27.4	Field Documentation	126
	7.27.4.1 fEvents	126
	7.27.4.2 fMap	126
7.28 VME:	:TDCV1x90 Class Reference	127
7.28.1	Detailed Description	129
7.28.2	Member Enumeration Documentation	129
	7.28.2.1 DLLMode	129
7.28.3	Constructor & Destructor Documentation	129
	7.28.3.1 TDCV1x90	129
	7.28.3.2 ~TDCV1x90	130
7.28.4	Member Function Documentation	130
	7.28.4.1 abort	130
	7.28.4.2 CheckConfiguration	130

xx CONTENTS

7.28.4.3 DisableChannel	30
7.28.4.4 EnableChannel	30
7.28.4.5 FetchEvents	30
7.28.4.6 GetAcquisitionMode	31
7.28.4.7 GetBLTEventNumberRegister	31
7.28.4.8 GetChannelDeadTime	31
7.28.4.9 GetControl	31
7.28.4.10 GetDetectionMode	32
7.28.4.11 GetDLLClock	32
7.28.4.12 GetErrorMarks	32
7.28.4.13 GetETTT	32
7.28.4.14 GetEventCounter	32
7.28.4.15 GetEventStored	32
7.28.4.16 GetFIFOSize	33
7.28.4.17 GetFirmwareRevision	33
7.28.4.18 GetGlobalOffset	33
7.28.4.19 GetModel	33
7.28.4.20 GetOUI	33
7.28.4.21 GetPoI	34
7.28.4.22 GetRCAdjust	34
7.28.4.23 GetResolution	34
7.28.4.24 GetSerialNumber	34
7.28.4.25 GetStatus	35
7.28.4.26 GetTDCEncapsulation	35
7.28.4.27 GetTestMode	35
7.28.4.28 GetTriggerConfiguration	35
7.28.4.29 GetWindowOffset	36
7.28.4.30 GetWindowWidth	36
7.28.4.31 HardwareReset	36
7.28.4.32 ReadAcquisitionMode	36
7.28.4.33 ReadDetectionMode	36
7.28.4.34 SetAcquisitionMode	36
7.28.4.35 SetBLTEventNumberRegister	37
7.28.4.36 SetChannelDeadTime	37

xxi

7.28.4.37 SetContinuousStorage	137
7.28.4.38 SetControl	137
7.28.4.39 SetDetectionMode	138
7.28.4.40 SetDLLClock	138
7.28.4.41 SetErrorMarks	138
7.28.4.42 SetETTT	138
7.28.4.43 SetFIFOSize	139
7.28.4.44 SetGlobalOffset	139
7.28.4.45 SetLSBTraileadEdge	139
7.28.4.46 SetPairModeResolution	139
7.28.4.47 SetPoI	140
7.28.4.48 SetRCAdjust	140
7.28.4.49 SetStatus	140
7.28.4.50 SetTDCEncapsulation	140
7.28.4.51 SetTestMode	141
7.28.4.52 SetTriggerMatching	141
7.28.4.53 SetVerboseLevel	141
7.28.4.54 SetWindowOffset	141
7.28.4.55 SetWindowWidth	142
7.28.4.56 SoftwareClear	142
7.28.4.57 SoftwareReset	142
7.28.4.58 WaitMicro	142
7.28.5 Field Documentation	143
7.28.5.1 fAcquisitionMode	143
7.28.5.2 fBuffer	143
7.28.5.3 fDetectionMode	143
7.28.5.4 fErrorMarks	143
7.28.5.5 fVerb	143
7.28.5.6 fWindowWidth	143
7.28.5.7 gEnd	143
7.28.5.8 nchannels	143
7.28.5.9 pair_lead_res	143
7.28.5.10 pair_width_res	143
7.29 VME::TDCV1x90Control Class Reference	144

xxii CONTENTS

7.29.1	Detailed Description	144
7.29.2	Constructor & Destructor Documentation	145
	7.29.2.1 TDCV1x90Control	145
	7.29.2.2 ~TDCV1x90Control	145
7.29.3	Member Function Documentation	145
	7.29.3.1 Dump	145
	7.29.3.2 GetAlign64	145
	7.29.3.3 GetBusError	145
	7.29.3.4 GetCompensation	145
	7.29.3.5 GetEmptyEvent	145
	7.29.3.6 GetETTT	145
	7.29.3.7 GetEventFIFO	145
	7.29.3.8 GetMEBAccess	145
	7.29.3.9 GetSRAMCompensation	145
	7.29.3.10 GetSWTermination	145
	7.29.3.11 GetTermination	145
	7.29.3.12 GetTestFIFO	145
	7.29.3.13 GetValue	145
	7.29.3.14 SetAlign64	145
	7.29.3.15 SetBusError	146
	7.29.3.16 SetCompensation	146
	7.29.3.17 SetEmptyEvent	146
	7.29.3.18 SetETTT	146
	7.29.3.19 SetEventFIFO	146
	7.29.3.20 SetMEBAccess	146
	7.29.3.21 SetSRAMCompensation	146
	7.29.3.22 SetSWTermination	146
	7.29.3.23 SetTermination	146
	7.29.3.24 SetTestFIFO	147
7.29.4	Field Documentation	147
	7.29.4.1 fWord	147
7.30 VME::	TDCV1x90Status Class Reference	148
7.30.1	Detailed Description	148
7.30.2	Member Enumeration Documentation	149

CONTENTS	xxiii
----------	-------

	7.30.2.1	TDCResolution	149
7.30.3	Construct	or & Destructor Documentation	149
	7.30.3.1	TDCV1x90Status	149
	7.30.3.2	~TDCV1x90Status	149
7.30.4	Member F	Function Documentation	149
	7.30.4.1	AlmostFull	149
	7.30.4.2	BusError	149
	7.30.4.3	DataReady	149
	7.30.4.4	Dump	149
	7.30.4.5	Error	149
	7.30.4.6	Error	150
	7.30.4.7	Full	150
	7.30.4.8	GetValue	150
	7.30.4.9	HeadersEnabled	150
	7.30.4.10	PairMode	150
	7.30.4.11	Purged	150
	7.30.4.12	Resolution	150
	7.30.4.13	TerminationOn	150
	7.30.4.14	TriggerLost	150
	7.30.4.15	TriggerMatching	150
7.30.5	Field Doc	umentation	150
	7.30.5.1	fWord	150
7.31 VME::	trailead_t S	Struct Reference	151
7.31.1	Field Doc	umentation	151
	7.31.1.1	ettt	151
	7.31.1.2	event_count	151
	7.31.1.3	leading	151
	7.31.1.4	total_hits	151
	7.31.1.5	trailing	151
7.32 VMER	eader Class	s Reference	152
7.32.1	Detailed I	Description	154
7.32.2	Member 7	Typedef Documentation	155
	7.32.2.1	OutputFiles	155
7.32.3	Construct	or & Destructor Documentation	155

xxiv CONTENTS

	7.32.3.1	VMEReader	155
	7.32.3.2	$\sim$ VMEReader	155
7.32.4	Member	Function Documentation	156
	7.32.4.1	Abort	156
	7.32.4.2	AddCFD	156
	7.32.4.3	AddFPGAUnit	156
	7.32.4.4	AddHVModule	156
	7.32.4.5	AddIOModule	157
	7.32.4.6	AddTDC	157
	7.32.4.7	GetCFD	157
	7.32.4.8	GetCFDCollection	157
	7.32.4.9	GetFPGAUnit	157
	7.32.4.10	GetHVModule	158
	7.32.4.11	GetIOModule	158
	7.32.4.12	GetNumCFD	158
	7.32.4.13	GetNumTDC	158
	7.32.4.14	GetOutputFile	158
	7.32.4.15	GetRunNumber	158
	7.32.4.16	GetTDC	158
	7.32.4.17	GetTDCCollection	158
	7.32.4.18	ReadXML	158
	7.32.4.19	ReadXML	159
	7.32.4.20	SendClear	159
	7.32.4.21	SendPulse	159
	7.32.4.22	SetOutputFile	160
	7.32.4.23	StartPulser	160
	7.32.4.24	StopPulser	160
	7.32.4.25	UseSocket	160
7.32.5	Field Doo	cumentation	160
	7.32.5.1	fBridge	160
	7.32.5.2	fCAENET	160
	7.32.5.3	fCFDCollection	160
	7.32.5.4	fFPGA	161
	7.32.5.5	fHV	161

CONTENTS			XXV
	7.32.5.6	fIsPulserStarted	161
	7.32.5.7	fOnSocket	161
	7.32.5.8	fOutputFiles	161
	7.32.5.9	fSG	161
	7 32 5 10	fTDCCollection	161

# **Module Index**

11	<b>N</b> /I ~	J1	_ ~
1.1	Mo	auı	les

Here is a list of all modules:														
Socket communication objects			 											•

2 Module Index

# **Namespace Index**

#### 2.1 Namespace List

16	ere is a list	of al	I na	ıme	espa	ice	es	W	1tr	1 t	orı	ıei	d	les	SC1	rıŗ	)tı	or	18:	:								
	NIM																											1
	VME .																											13
	VME··TI	CV	1 x 9	00	ncc	ode	2.5																					2

### **Data Structure Index**

#### 3.1 Class Hierarchy

This inheritance list is sorted roughly, but not completely, alphabetically:
VME::BridgeVx718Control
VME::BridgeVx718Status
VME::CAENETControllerV288Status
NIM::HVModuleN470Values::ChannelStatus
file_header_t
FileReader
VME::FPGAUnitV1495Control
VME::GenericBoard < Register, am >
VME::GenericBoard < CAENETControllerV288Register, cvA24_U_DATA > 74
VME::CAENETControllerV288
VME::GenericBoard < CFDV812Register, cvA24_U_DATA >
VME::CFDV812
VME::GenericBoard < CVRegisters, cvA32_U_DATA >
VME::BridgeVx718
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >
VME::FPGAUnitV1495
VME::GenericBoard< IOModuleV262Register, cvA24_U_DATA >
VME::IOModuleV262
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >
VME::TDCV1x90
VME::GlobalOffset
NIM::HVModuleN470
NIM::HVModuleN470ChannelValues
NIM::HVModuleN470Values
Message
HTTPMessage
SocketMessage 110

VME::PCIInterfaceA2818														103	3
Socket														104	4
Client														50	0
VMEReader														152	2
Messenger														9:	5
VME::TDCErrorFlag														110	6
VME::TDCEvent															
VME::TDCMeasurement .														124	4
VME::TDCV1x90Control														144	4
VME::TDCV1x90Status .														14	8
VME::trailead t														15	1

### **Data Structure Index**

#### 4.1 Data Structures

Here are the data structures with brief descriptions:	
VME::BridgeVx718 (Class defining the VME bridge)	25
VME::BridgeVx718Control	
VME::BridgeVx718Status	
VME::CAENETControllerV288 (Handler for a CAEN V288 CAENET con-	
troller)	35
VME::CAENETControllerV288Status	38
VME::CFDV812 (Controller for a CAEN V812 constant fraction discrimi-	
nator )	40
NIM::HVModuleN470Values::ChannelStatus	
Client (Base client object for the socket )	50
file_header_t (Header to the output files )	56
FileReader (Handler for a TDC output file readout )	57
VME::FPGAUnitV1495	
VME::FPGAUnitV1495Control	69
VME::GenericBoard < Register, am >	74
VME::GlobalOffset	77
HTTPMessage (Message to be transmitted through a WebSocket protocol ) .	
NIM::HVModuleN470	80
NIM::HVModuleN470ChannelValues (Single channel monitoring values for	
the HV power supply )	85
NIM::HVModuleN470Values (General monitoring values for the HV power	
supply )	
VME::IOModuleV262	
Message (Base socket message type)	92
Messenger (Base master object for the socket )	95
VME::PCIInterfaceA2818	103
Socket (Base socket object from which clients/master from a socket inherit ) .	104
SocketMessage (Socket-passed message type )	110
VMF::TDCErrorFlag (Error flags handler )	116

VME::TDCEvent (HPTDC event parser )
VME::TDCMeasurement
VME::TDCV1x90
VME::TDCV1x90Control (TDC control register)
VME::TDCV1x90Status (TDC status register)
VME::trailead_t
VMEReader

# **Chapter 5**

# **Module Documentation**

# 5.1 Socket communication objects

# **Data Structures**

• class Client

Base client object for the socket.

• class HTTPMessage

Message to be transmitted through a WebSocket protocol.

• class Messenger

Base master object for the socket.

• class Socket

Base socket object from which clients/master from a socket inherit.

• class SocketMessage

Socket-passed message type.

# Chapter 6

# **Namespace Documentation**

# 6.1 NIM Namespace Reference

#### **Data Structures**

• class HVModuleN470Values

General monitoring values for the HV power supply.

• class HVModuleN470ChannelValues

Single channel monitoring values for the HV power supply.

• class HVModuleN470

#### **Enumerations**

```
• enum HVModuleN470Opcodes {
```

```
kN470GeneralInfo = 0x00, kN470MonStatus = 0x01, kN470OperationalParams = 0x02, kN470V0Value = 0x03, 
kN470I0Value = 0x04, kN470V1Value = 0x05, kN470I1Value = 0x06, kN470TripValue = 0x07, 
kN470RampUpValue = 0x08, kN470RampDownValue = 0x09, kN470ChannelOn = 0x0a, kN470ChannelOff = 0x0b, 
kN470KillAllChannels = 0x0c, kN470ClearAlarm = 0x0d, kN470EnableFrontPanel = 0x0e, kN470DisableFrontPanel = 0x0f, kN470TTLLevel = 0x10, kN470NIMLevel = 0x11 }
```

# **6.1.1** Enumeration Type Documentation

# 6.1.1.1 enum NIM::HVModuleN470Opcodes

#### **Enumerator:**

kN470GeneralInfo

kN470MonStatus

kN470Operational Params

kN470V0Value

kN470I0Value

kN470V1Value

kN470I1Value

kN470TripValue

kN470RampUpValue

kN470RampDownValue

kN470ChannelOn

kN470ChannelOff

kN470 Kill All Channels

kN470ClearAlarm

kN470EnableFrontPanel

kN470DisableFrontPanel

kN470TTLLevel

kN470NIMLevel

# **6.2** VME Namespace Reference

## **Namespaces**

• namespace TDCV1x90Opcodes

#### **Data Structures**

- class BridgeVx718Status
- class BridgeVx718Control
- class BridgeVx718

class defining the VME bridge

- class CAENETControllerV288Status
- class CAENETControllerV288

Handler for a CAEN V288 CAENET controller.

• class CFDV812

Controller for a CAEN V812 constant fraction discriminator.

- class FPGAUnitV1495Control
- class FPGAUnitV1495
- class GenericBoard
- class IOModuleV262
- class PCIInterfaceA2818
- class TDCErrorFlag

Error flags handler.

• class TDCEvent

HPTDC event parser.

- class TDCMeasurement
- struct GlobalOffset
- struct trailead\_t
- class TDCV1x90Status

TDC status register.

• class TDCV1x90Control

TDC control register.

• class TDCV1x90

## **Typedefs**

- typedef std::map< uint32\_t, VME::CFDV812 \* > CFDCollection
   Mapper from physical VME addresses to pointers to CFD objects.
- typedef std::vector< TDCEvent > TDCEventCollection
- typedef std::map< uint32\_t, VME::TDCV1x90 \* > TDCCollection

  Mapper from physical VME addresses to pointers to TDC objects.

#### **Enumerations**

```
enum BridgeType { CAEN_V1718, CAEN_V2718 }
    Compatible bridge types.
• enum CAENETControllerV288Register {
 kV288DataBuffer = 0x00, kV288Status = 0x02, kV288Transmission = 0x04,
 kV288ModuleReset = 0x06,
 kV288IRQVector = 0x08 }
• enum CAENETControllerV288Answer {
 cnSuccess = 0x0000, cnBusy = 0xff00, cnUnrecognizedCode = 0xff01, cnIncor-
 rectValue = 0xff02,
 cnNoData = 0xfffd, cnIncorrectHCC = 0xfffe, cnWrongModuleAddress = 0xffff
enum CFDV812Register {
 kV812ThresholdChannel0 = 0x00, kV812OutputWidthGroup0 = 0x40,
 kV812OutputWidthGroup1 = 0x42, kV812DeadTimeGroup0 = 0x44,
 kV812DeadTimeGroup1 = 0x46,
                                    kV812MajorityThreshold =
                                                               0x48.
 kV812PatternOfInhibit = 0x4a, kV812TestPulse = 0x4c,
 kV812FixedCode = 0xfa, kV812Info0 = 0xfc, kV812Info1 = 0xfe }
enum FPGAUnitV1495Register {
 kV1495ScalerCounter = 0x100c, kV1495DelaySettings =
                                                             0x1010.
 kV1495UserFWRevision = 0x1014, kV1495TDCBoardInterface = 0x1018,
                                       kV1495Control
 kV1495ClockSettings
                            0x101c,
                                                             0x1020.
 kV1495TriggerSettings = 0x1024, kV1495OutputSettings = 0x1028,
 kV1495GeoAddress = 0x8008, kV1495UserFPGAFlashMem = 0x8014,
 kV1495UserFPGAConfig = 0x8016, kV1495ModuleReset = 0x800a,
 kV1495FWRevision = 0x800c, kV1495ConfigurationROM = 0x8100,
 kV1495OUI2 = 0x8124, kV1495OUI1 = 0x8128,
 kV1495OUI0 = 0x812c, kV1495Board2 = 0x8134, kV1495Board1 = 0x8138,
 kV1495Board0 = 0x813c,
 kV1495HWRevision3 = 0x8140, kV1495HWRevision2 = 0x8144,
 kV1495HWRevision1 = 0x8148, kV1495HWRevision0 = 0x814c,
 kV1495SerNum0 = 0x8180, kV1495SerNum1 = 0x8184
```

```
• enum IOModuleV262Register {
 kECLLevelWrite = 0x04, kNIMLevelWrite = 0x06, kNIMPulseWrite = 0x08,
 kNIMPulseRead = 0x0a.
  kIdentifier = 0xfa, kBoardInfo0 = 0xfc, kBoardInfo1 = 0xfe }
• enum AcquisitionMode { CONT_STORAGE, TRIG_MATCH }
     TDC acquisition mode.
• enum DetectionMode { PAIR = 0x0, OTRAILING = 0x1, OLEADING = 0x2,
  TRAILEAD = 0x3 }
• enum trig_conf {
  MATCH_WIN_WIDTH = 0, WIN_OFFSET = 1, EXTRA_SEARCH_WIN_-
  WIDTH = 2, REJECT\_MARGIN = 3,
 TRIG_TIME_SUB = 4 }
• enum trailead_edge_lsb { r800ps = 0, r200ps = 1, r100ps = 2, r25ps = 3 }
• enum micro_handshake { WRITE_OK = 0, READ_OK = 1 }
• enum TDCV1x90Register {
 kOutputBuffer = 0x0000, kControl = 0x1000, kStatus = 0x1002, kInterruptLevel
 = 0x100a,
 kInterruptVector = 0x100c, kGeoAddress = 0x100e, kMCSTBase = 0x1010,
 kMCSTControl = 0x1012,
 kModuleReset = 0x1014, kSoftwareClear = 0x1016, kEventCounter = 0x101c,
 kEventStored = 0x1020,
 kBLTEventNumber = 0x1024, kFirmwareRev = 0x1026, kMicro = 0x102e, kMi-
 croHandshake = 0x1030,
 kEventFIFO = 0x1038, kEventFIFOStoredRegister = 0x103c, kEventFIFOSta-
 tusRegister = 0x103e, kROMOui2 = 0x4024,
  kROMOui1 = 0x4028, kROMOui0 = 0x402c, kROMBoard2 = 0x4034, kROM-
  Board1 = 0x4038,
 kROMBoard0 = 0x403c, kROMRevis3 = 0x4040, kROMRevis2 = 0x4044,
 kROMRevis1 = 0x4048,
 kROMRevis0 = 0x404c, kROMSerNum1 = 0x4080, kROMSerNum0 = 0x4084
```

#### **6.2.1** Typedef Documentation

#### 6.2.1.1 typedef std::map<uint32\_t,VME::CFDV812\*> VME::CFDCollection

Mapper from physical VME addresses to pointers to CFD objects.

## 6.2.1.2 typedef std::map<uint32\_t,VME::TDCV1x90\*> VME::TDCCollection

Mapper from physical VME addresses to pointers to TDC objects.

#### 6.2.1.3 typedef std::vector<TDCEvent> VME::TDCEventCollection

# **6.2.2** Enumeration Type Documentation

## 6.2.2.1 enum VME::AcquisitionMode

TDC acquisition mode.

#### **Author:**

Laurent Forthomme < laurent.forthomme@cern.ch>

#### **Enumerator:**

CONT\_STORAGE
TRIG\_MATCH

## 6.2.2.2 enum VME::BridgeType

Compatible bridge types.

#### **Enumerator:**

*CAEN\_V1718 CAEN\_V2718* 

## 6.2.2.3 enum VME::CAENETControllerV288Answer

## **Enumerator:**

cnSuccess
cnBusy
cnUnrecognizedCode
cnIncorrectValue
cnNoData
cnIncorrectHCC
cnWrongModuleAddress

#### 6.2.2.4 enum VME::CAENETControllerV288Register

#### **Enumerator:**

kV288DataBuffer kV288Status kV288Transmission kV288ModuleReset kV288IRQVector

#### 6.2.2.5 enum VME::CFDV812Register

#### **Enumerator:**

kV812ThresholdChannel0

kV812OutputWidthGroup0

kV812OutputWidthGroup1

kV812DeadTimeGroup0

kV812DeadTimeGroup1

kV812MajorityThreshold

kV812PatternOfInhibit

kV812TestPulse

kV812FixedCode

kV812Info0

kV812Info1

#### 6.2.2.6 enum VME::DetectionMode

#### **Enumerator:**

**PAIR** 

**OTRAILING** 

**OLEADING** 

**TRAILEAD** 

# 6.2.2.7 enum VME::FPGAUnitV1495Register

#### **Enumerator:**

kV1495ScalerCounter

kV1495DelaySettings

kV1495UserFWRevision

kV1495TDCBoardInterface

kV1495ClockSettings

kV1495Control

kV1495TriggerSettings

kV1495OutputSettings

kV1495GeoAddress

kV1495UserFPGAFlashMem

kV1495UserFPGAConfig

kV1495ModuleReset

kV1495FWRevision

kV1495ConfigurationROM

kV14950UI2

kV14950UI1

kV14950UI0

kV1495Board2

kV1495Board1

kV1495Board0

kV1495HWRevision3

kV1495HWRevision2

kV1495HWRevision1

kV1495HWRevision0

kV1495SerNum0

kV1495SerNum1

## 6.2.2.8 enum VME::IOModuleV262Register

#### **Enumerator:**

kECLLevelWrite

kNIMLevelWrite

kNIMPulseWrite

kNIMPulseRead

kIdentifier

kBoardInfo0

kBoardInfo1

# 6.2.2.9 enum VME::micro\_handshake

#### **Enumerator:**

**WRITE\_OK** Is the TDC ready for writing?

**READ\_OK** Is the TDC ready for reading?

## 6.2.2.10 enum VME::TDCV1x90Register

# **Enumerator:**

kOutputBuffer

*kControl* 

**kStatus** 

kInterruptLevel

kInterruptVector

kGeoAddress

*kMCSTBase* 

*kMCSTControl* 

*kModuleReset* 

kSoftwareClear

*kEventCounter* 

*kEventStored* 

kBLTEventNumber

*kFirmwareRev* 

*kMicro* 

*kMicroHandshake* 

*kEventFIFO* 

kEventFIFOStoredRegister

k Event FIFO Status Register

kROMOui2

kROMOui1

kROMOui0

kROMBoard2

kROMBoard1

kROMBoard0

kROMRevis3

kROMRevis2

kROMRevis1

kROMRevis0

kROMSerNum1

kROMSerNum0

# 6.2.2.11 enum VME::trailead\_edge\_lsb

## **Enumerator:**

r800ps

r200ps

r100ps

r25ps

# 6.2.2.12 enum VME::trig\_conf

# **Enumerator:**

MATCH\_WIN\_WIDTH
WIN\_OFFSET
EXTRA\_SEARCH\_WIN\_WIDTH
REJECT\_MARGIN
TRIG\_TIME\_SUB

# 6.3 VME::TDCV1x90Opcodes Namespace Reference

#### **Functions**

- Opcode TRG\_MATCH (0x0000)
- Opcode CONT\_STOR (0x0100)
- Opcode READ ACQ MOD (0x0200)
- Opcode SET\_KEEP\_TOKEN (0x0300)
- Opcode CLEAR\_KEEP\_TOKEN (0x0400)
- Opcode LOAD\_DEF\_CONFIG (0x0500)
- Opcode SAVE\_USER\_CONFIG (0x0600)
- Opcode LOAD\_USER\_CONFIG (0x0700)
- Opcode AUTOLOAD\_USER\_CONF (0x0800)
- Opcode AUTOLOAD\_DEF\_CONFI (0x0900)
- Opcode SET\_WIN\_WIDTH (0x1000)
- Opcode SET\_WIN\_OFFS (0x1100)
- Opcode SET\_SW\_MARGIN (0x1200)
- Opcode SET\_REJ\_MARGIN (0x1300)
- Opcode EN\_SUB\_TRG (0x1400)
- Opcode DIS\_SUB\_TRG (0x1500)
- Opcode READ\_TRG\_CONF (0x1600)
- Opcode SET\_DETECTION (0x2200)
- Opcode READ\_DETECTION (0x2300)
- Opcode SET\_TR\_LEAD\_LSB (0x2400)
- Opcode SET\_PAIR\_RES (0x2500)
- Opcode READ RES (0x2600)
- Opcode SET\_DEAD\_TIME (0x2800)
- Opcode READ\_DEAD\_TIME (0x2900)
- Opcode EN\_HEAD\_TRAILER (0x3000)
- Opcode DIS\_HEAD\_TRAILER (0x3100)
- Opcode READ\_HEAD\_TRAILER (0x3200)
- Opcode SET\_EVENT\_SIZE (0x3300)
- Opcode READ\_EVENT\_SIZE (0x3400)
- Opcode EN\_ERROR\_MARK (0x3500)
- Opcode DIS\_ERROR\_MARK (0x3600)
- Opcode EN\_ERROR\_BYPASS (0x3700)
- Opcode DIS ERROR BYPASS (0x3800)
- Opcode SET\_ERROR\_TYPES (0x3900)
- Opcode READ\_ERROR\_TYPES (0x3a00)
- Opcode SET\_FIFO\_SIZE (0x3b00)
- Opcode READ\_FIFO\_SIZE (0x3c00)
- Opcode EN CHANNEL (0x4000)
- Opcode DIS\_CHANNEL (0x4100)
- Opcode EN\_ALL\_CHANNEL (0x4200)
- Opcode DIS\_ALL\_CHANNEL (0x4300)
- Opcode WRITE\_EN\_PATTERN (0x4400)

- Opcode READ\_EN\_PATTERN (0x4500)
- Opcode WRITE\_EN\_PATTERN32 (0x4600)
- Opcode READ EN PATTERN32 (0x4700)
- Opcode SET\_GLOB\_OFFS (0x5000)
- Opcode READ\_GLOB\_OFFS (0x5100)
- Opcode SET\_ADJUST\_CH (0x5200)
- Opcode READ\_ADJUST\_CH (0x5200)
- Opcode SET\_RC\_ADJ (0x5400)
- Opcode READ\_RC\_ADJ (0x5500)
- Opcode SAVE\_RC\_ADJ (0x5600)
- Opcode READ\_TDC\_ID (0x6000)
- Opcode READ\_MICRO\_REV (0x6100)
- Opcode RESET\_DLL\_PLL (0x6200)
- Opcode WRITE\_SETUP\_REG (0x7000)
- Opcode READ\_SETUP\_REG (0x7100)
- Opcode UPDATE\_SETUP\_REG (0x7200)
- Opcode DEFAULT\_SETUP\_REG (0x7300)
- Opcode READ\_ERROR\_STATUS (0x7400)
- Opcode READ\_DLL\_LOCK (0x7500)
- Opcode READ\_STATUS\_STREAM (0x7600)
- Opcode UPDATE\_SETUP\_TDC (0x7700)
- Opcode WRITE\_EEPROM (0xc000)
- Opcode READ\_EEPROM (0xc100)
- Opcode REV\_DATE\_MICRO\_FW (0xc200)
- Opcode WRITE\_SPARE (0xc300)
- Opcode READ\_SPARE (0xc400)
- Opcode ENABLE\_TEST\_MODE (0xc500)
- Opcode DISABLE\_TEST\_MODE (0xc600)
- Opcode SET\_TDC\_TSET\_OUTPUT (0xc700)
- Opcode SET\_DLL\_CLOCK (0xc800)
- Opcode READ\_SETUP\_SCANPATH (0xc900)

6.3 VME::TDCV1x90Opcodes Namespace Reference	23

	2 1	T- 4.	The state of the s	4 4•
h	4 I	Hinchar	i I Iaciimani	tatian
v	.3.1	runcuvi	Document	ıauvn

- 6.3.1.1 Opcode VME::TDCV1x90Opcodes::AUTOLOAD\_DEF\_CONFI (0x0900)
- 6.3.1.2 Opcode VME::TDCV1x90Opcodes::AUTOLOAD\_USER\_CONF (0x0800)
- 6.3.1.3 Opcode VME::TDCV1x90Opcodes::CLEAR\_KEEP\_TOKEN (0x0400)
- 6.3.1.4 Opcode VME::TDCV1x90Opcodes::CONT\_STOR (0x0100)
- 6.3.1.5 Opcode VME::TDCV1x90Opcodes::DEFAULT\_SETUP\_REG (0x7300)
- 6.3.1.6 Opcode VME::TDCV1x90Opcodes::DIS\_ALL\_CHANNEL (0x4300)
- 6.3.1.7 Opcode VME::TDCV1x90Opcodes::DIS\_CHANNEL (0x4100)
- 6.3.1.8 Opcode VME::TDCV1x90Opcodes::DIS\_ERROR\_BYPASS (0x3800)
- 6.3.1.9 Opcode VME::TDCV1x90Opcodes::DIS\_ERROR\_MARK (0x3600)
- 6.3.1.10 Opcode VME::TDCV1x90Opcodes::DIS\_HEAD\_TRAILER (0x3100)
- 6.3.1.11 Opcode VME::TDCV1x90Opcodes::DIS\_SUB\_TRG (0x1500)
- 6.3.1.12 Opcode VME::TDCV1x90Opcodes::DISABLE\_TEST\_MODE (0xc600)
- 6.3.1.13 Opcode VME::TDCV1x90Opcodes::EN\_ALL\_CHANNEL (0x4200)
- 6.3.1.14 Opcode VME::TDCV1x90Opcodes::EN\_CHANNEL (0x4000)
- 6.3.1.15 Opcode VME::TDCV1x90Opcodes::EN\_ERROR\_BYPASS (0x3700)
- 6.3.1.16 Opcode VME::TDCV1x90Opcodes::EN\_ERROR\_MARK (0x3500)
- 6.3.1.17 Opcode VME::TDCV1x90Opcodes::EN\_HEAD\_TRAILER (0x3000)
- 6.3.1.18 Opcode VME::TDCV1x90Opcodes::EN\_SUB\_TRG (0x1400)
- 6.3.1.19 Opcode VME::TDCV1x90Opcodes::ENABLE\_TEST\_MODE (0xc500)
- 6.3.1.20 Opcode VME::TDCV1x90Opcodes::LOAD\_DEF\_CONFIG (0x0500)
- 6.3.1.21 Opcode VME::TDCV1x90Opcodes::LOAD\_USER\_CONFIG (0x0700)
- 6.3.1.22 Opcode VME::TDCV1x90Opcodes::READ\_ACQ\_MOD (0x0200)
- 6.3.1.23 Opcode VMF::TDCV1x90Qpcodes::RFAD ADJUST CH (0x5200)
- $\mathbf{6.3.1.24} \quad \mathbf{Opcode\ VME::} \mathbf{TDCV1x90Opcodes::} \mathbf{READ\_DEAD\_TIME\ } (0x2900)$
- 6.3.1.25 Opcode VME::TDCV1x90Opcodes::READ\_DETECTION (0x2300)
- 6.3.1.26 Opcode VME::TDCV1x90Opcodes::READ\_DLL\_LOCK (0x7500)
- 6.3.1.27 Opcode VME::TDCV1x90Opcodes::READ\_EEPROM (0xc100)

# **Chapter 7**

# **Data Structure Documentation**

# 7.1 VME::BridgeVx718 Class Reference

```
class defining the VME bridge
```

#include <VME\_BridgeVx718.h>Inheritance diagram for
VME::BridgeVx718:Collaboration diagram for VME::BridgeVx718:

# **Public Types**

```
    enum IRQId {
    IRQ1 = 0x1, IRQ2 = 0x2, IRQ3 = 0x4, IRQ4 = 0x8,
    IRQ5 = 0x10, IRQ6 = 0x20, IRQ7 = 0x40 }
```

## **Public Member Functions**

- BridgeVx718 (const char \*device, BridgeType type) Constructor.
- ~BridgeVx718 ()

Destructor.

• int32\_t GetHandle () const

Bridge's handle value.

- void CheckPCIInterface (const char \*device) const
- void CheckConfiguration () const
- void TestOutputs () const
- void Reset () const

Perform a system reset of the module.

- BridgeVx718Status GetStatus () const
- void SetIRQ (unsigned int irq, bool enable=true)
- void WaitIRQ (unsigned int irq, unsigned long timeout=1000) const
- unsigned int GetIRQStatus () const
- void OutputConf (CVOutputSelect output) const

Set and control the output lines.

- void OutputOn (unsigned short output) const
- void OutputOff (unsigned short output) const
- void InputConf (CVInputSelect input) const

Set and read the input lines.

- void InputRead (CVInputSelect input) const
- void StartPulser (double period, double width, unsigned int num\_pulses=0) const
- void StopPulser () const
- void SinglePulse (unsigned short channel) const

## **Private Attributes**

• bool fHasIRQ

## 7.1.1 Detailed Description

class defining the VME bridge This class initializes the CAEN V1718 VME bridge in order to control the crate.

#### **Author:**

```
Laurent Forthomme < laurent.forthomme@cern.ch>
Bob Velghe <bob.velghe@cern.ch>
```

# Date:

Jun 2010

# 7.1.2 Member Enumeration Documentation

## 7.1.2.1 enum VME::BridgeVx718::IRQId

#### **Enumerator:**

IRQ1

IRQ2

IRQ3

IRQ4

IRQ5

IRQ6

IRQ7

#### 7.1.3 Constructor & Destructor Documentation

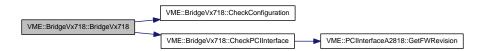
# 7.1.3.1 VME::BridgeVx718::BridgeVx718 (const char \* device, BridgeType type)

Constructor. Bridge class constructor

#### **Parameters:**

- ← *device* Device identifier on the VME crate
- $\leftarrow$  *type* Device type (1718/2718)

Here is the call graph for this function:



## 7.1.3.2 VME::BridgeVx718::~BridgeVx718 ()

Destructor. Bridge class destructor

#### 7.1.4 Member Function Documentation

## 7.1.4.1 void VME::BridgeVx718::CheckConfiguration () const

# 7.1.4.2 void VME::BridgeVx718::CheckPCIInterface (const char \* device) const

Here is the call graph for this function:



## 7.1.4.3 int32\_t VME::BridgeVx718::GetHandle () const [inline]

Bridge's handle value.

#### **Returns:**

Handle value

- 7.1.4.4 unsigned int VME::BridgeVx718::GetIRQStatus () const
- 7.1.4.5 BridgeVx718Status VME::BridgeVx718::GetStatus () const
- 7.1.4.6 void VME::BridgeVx718::InputConf (CVInputSelect input) const

Set and read the input lines.

- 7.1.4.7 void VME::BridgeVx718::InputRead (CVInputSelect input) const
- 7.1.4.8 void VME::BridgeVx718::OutputConf (CVOutputSelect *output*) const

Set and control the output lines.

- 7.1.4.9 void VME::BridgeVx718::OutputOff (unsigned short *output*) const
- 7.1.4.10 void VME::BridgeVx718::OutputOn (unsigned short output) const
- 7.1.4.11 void VME::BridgeVx718::Reset () const

Perform a system reset of the module.

- 7.1.4.12 void VME::BridgeVx718::SetIRQ (unsigned int *irq*, bool *enable* = true)
- 7.1.4.13 void VME::BridgeVx718::SinglePulse (unsigned short channel) const

Here is the call graph for this function:

- 7.1.4.14 void VME::BridgeVx718::StartPulser (double *period*, double *width*, unsigned int *num\_pulses* = 0) const
- 7.1.4.15 void VME::BridgeVx718::StopPulser () const
- 7.1.4.16 void VME::BridgeVx718::TestOutputs () const

Here is the call graph for this function:

- 7.1.4.17 void VME::BridgeVx718::WaitIRQ (unsigned int *irq*, unsigned long *timeout* = 1000) const
- 7.1.5 Field Documentation
- 7.1.5.1 bool VME::BridgeVx718::fHasIRQ [private]

The documentation for this class was generated from the following files:

- include/VME\_BridgeVx718.h
- src/VME\_BridgeVx718.cpp

# 7.2 VME::BridgeVx718Control Class Reference

#include <VME BridgeVx718.h>

#### **Public Member Functions**

- BridgeVx718Control (uint16\_t word)
- virtual ~BridgeVx718Control ()
- bool GetArbiterType () const

Arbiter type.

• bool GetRequesterType () const

 $Requester\ type.$ 

• bool GetReleaseType () const

Release type.

- unsigned int GetBusReqLevel () const
- bool GetInterruptReq () const
- bool GetSysRes () const
- bool GetBusTimeout () const

VME bus timeout.

• bool GetAddressIncrement () const

Address Increment.

#### **Private Attributes**

• uint16\_t fWord

#### 7.2.1 Constructor & Destructor Documentation

- 7.2.1.1 VME::BridgeVx718Control::BridgeVx718Control (uint16\_t word) [inline]
- 7.2.1.2 virtual VME::BridgeVx718Control:: $\sim$ BridgeVx718Control () [inline, virtual]

#### 7.2.2 Member Function Documentation

7.2.2.1 bool VME::BridgeVx718Control::GetAddressIncrement () const [inline]

Address Increment.

#### **Returns:**

true if enabled, else false (FIFO mode)

## 7.2.2.2 bool VME::BridgeVx718Control::GetArbiterType () const [inline]

Arbiter type.

#### **Returns:**

true if "Round Robin", else fixed priority

# 7.2.2.3 unsigned int VME::BridgeVx718Control::GetBusReqLevel () const [inline]

## 7.2.2.4 bool VME::BridgeVx718Control::GetBusTimeout () const [inline]

VME bus timeout.

#### **Returns:**

true if 1400 us, else 50 us

# 7.2.2.5 bool VME::BridgeVx718Control::GetInterruptReq () const [inline]

## 7.2.2.6 bool VME::BridgeVx718Control::GetReleaseType () const [inline]

Release type.

#### **Returns:**

true if release on request, else release when done

# 7.2.2.7 bool VME::BridgeVx718Control::GetRequesterType () const [inline]

Requester type.

#### **Returns:**

true if demand, else fair

# 7.2.2.8 bool VME::BridgeVx718Control::GetSysRes() const [inline]

# 7.2.3 Field Documentation

# 7.2.3.1 uint16\_t VME::BridgeVx718Control::fWord [private]

The documentation for this class was generated from the following file:

• include/VME\_BridgeVx718.h

# 7.3 VME::BridgeVx718Status Class Reference

#include <VME\_BridgeVx718.h>

#### **Public Member Functions**

- BridgeVx718Status (uint16\_t word)
- virtual ~BridgeVx718Status ()
- void Dump () const
- bool GetSystemReset () const
- bool GetSystemControl () const
- bool GetDTACK () const
- bool GetBERR () const
- bool GetDipSwitch (unsigned int sw) const
- bool GetUSBType () const

#### **Private Attributes**

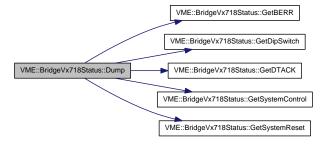
• uint16 t fWord

#### 7.3.1 Constructor & Destructor Documentation

- 7.3.1.1 VME::BridgeVx718Status::BridgeVx718Status (uint16\_t word) [inline]
- 7.3.1.2 virtual VME::BridgeVx718Status::~BridgeVx718Status() [inline, virtual]

## 7.3.2 Member Function Documentation

# 7.3.2.1 void VME::BridgeVx718Status::Dump() const [inline]



- 7.3.2.2 bool VME::BridgeVx718Status::GetBERR() const [inline]
- 7.3.2.3 bool VME::BridgeVx718Status::GetDipSwitch (unsigned int sw) const [inline]
- 7.3.2.4 bool VME::BridgeVx718Status::GetDTACK() const [inline]
- 7.3.2.5 bool VME::BridgeVx718Status::GetSystemControl () const [inline]
- 7.3.2.6 bool VME::BridgeVx718Status::GetSystemReset () const [inline]
- 7.3.2.7 bool VME::BridgeVx718Status::GetUSBType() const [inline]
- 7.3.3 Field Documentation
- 7.3.3.1 uint16\_t VME::BridgeVx718Status::fWord [private]

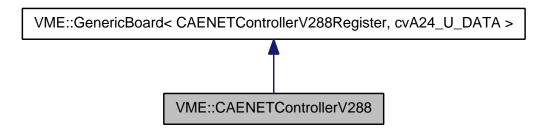
The documentation for this class was generated from the following file:

• include/VME\_BridgeVx718.h

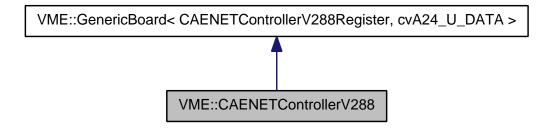
# 7.4 VME::CAENETControllerV288 Class Reference

Handler for a CAEN V288 CAENET controller.

#include <VME\_CAENETControllerV288.h>Inheritance diagram for
VME::CAENETControllerV288:



Collaboration diagram for VME::CAENETControllerV288:



## **Public Member Functions**

- CAENETControllerV288 (int32\_t handle, uint32\_t baseaddr)
- ~CAENETControllerV288 ()
- void Reset () const
- CAENETControllerV288Status GetStatus () const
- void SendBuffer () const

Send the whole buffer through the network.

- std::vector< uint16\_t > FetchBuffer (unsigned int num\_words) const Retrieve the network buffer.
- bool WaitForResponse (CAENETControllerV288Answer \*response, unsigned int max\_trials=-1) const

## **Friends**

• void operator << (const CAENETController V288 &cnt, uint 16\_t word)

Fill the buffer with an additional 16-bit word.

 uint16\_t & operator>> (const CAENETControllerV288 &cnt, uint16\_t &word)

Read back a 16-bit word from the buffer.

## 7.4.1 Detailed Description

Handler for a CAEN V288 CAENET controller.

#### **Author:**

Laurent Forthomme < laurent .forthomme@cern.ch>

#### Date:

23 Jul 2015

## 7.4.2 Constructor & Destructor Documentation

- 7.4.2.1 VME::CAENETControllerV288::CAENETControllerV288 (int32\_t handle, uint32\_t baseaddr)
- 7.4.2.2 VME::CAENETControllerV288::~CAENETControllerV288 ()

#### 7.4.3 Member Function Documentation

7.4.3.1 std::vector< uint16\_t > VME::CAENETControllerV288::FetchBuffer (unsigned int *num\_words* = 1) const

Retrieve the network buffer.

#### 7.4.3.2 CAENETControllerV288Status VME::CAENETControllerV288::GetStatus () const

Here is the call graph for this function:

VME::CAENETControllerV288::GetStatus VME::GenericBoard< CAENETControllerV288Register, cvA24\_U\_DATA >::ReadRegister

#### 7.4.3.3 void VME::CAENETControllerV288::Reset () const

Here is the call graph for this function:



#### 7.4.3.4 void VME::CAENETControllerV288::SendBuffer () const

Send the whole buffer through the network.

Here is the call graph for this function:



# 7.4.3.5 bool VME::CAENETControllerV288::WaitForResponse (CAENETControllerV288Answer \* response, unsigned int max\_trials = -1) const

Here is the call graph for this function:



## 7.4.4 Friends And Related Function Documentation

# 7.4.4.1 void operator << (const CAENETController V288 & cnt, uint16\_t word) [friend]

Fill the buffer with an additional 16-bit word.

# 7.4.4.2 uint16\_t& operator>> (const CAENETControllerV288 & cnt, uint16\_t & word) [friend]

Read back a 16-bit word from the buffer.

The documentation for this class was generated from the following files:

- include/VME\_CAENETControllerV288.h
- src/VME\_CAENETControllerV288.cpp

# 7.5 VME::CAENETControllerV288Status Class Reference

#include <VME\_CAENETControllerV288.h>

# **Public Types**

• enum OperationStatus { Valid = 0x0, Invalid = 0x1 }

## **Public Member Functions**

- CAENETControllerV288Status (uint16\_t word)
- ~CAENETControllerV288Status ()
- OperationStatus GetOperationStatus () const

# **Private Attributes**

• uint16\_t fWord

# 7.5.1 Member Enumeration Documentation

## 7.5.1.1 enum VME::CAENETControllerV288Status::OperationStatus

## **Enumerator:**

Valid

Invalid

# 7.5.2 Constructor & Destructor Documentation

- 7.5.2.1 VME::CAENETControllerV288Status::CAENETControllerV288Status (uint16\_t word) [inline]
- 7.5.2.2 VME::CAENETControllerV288Status::~CAENETControllerV288Status () [inline]

#### 7.5.3 Member Function Documentation

7.5.3.1 OperationStatus

VME::CAENETControllerV288Status::GetOperationStatus () const [inline]

# 7.5.4 Field Documentation

# 7.5.4.1 uint16\_t VME::CAENETControllerV288Status::fWord [private]

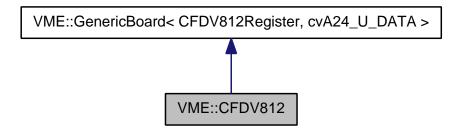
The documentation for this class was generated from the following file:

• include/VME\_CAENETControllerV288.h

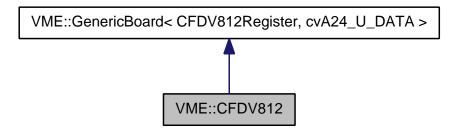
# 7.6 VME::CFDV812 Class Reference

Controller for a CAEN V812 constant fraction discriminator.

#include <VME\_CFDV812.h>Inheritance diagram for VME::CFDV812:



Collaboration diagram for VME::CFDV812:



#### **Public Member Functions**

- CFDV812 (int32\_t bhandle, uint32\_t baseaddr)
- ~CFDV812 ()
- void CheckConfiguration () const
- unsigned short GetFixedCode () const
- unsigned short GetManufacturerId () const
- unsigned short GetModuleType () const
- unsigned short GetModuleVersion () const
- unsigned short GetSerialNumber () const
- void SetPOI (unsigned short poi) const
   Set the pattern of inhibit (list of enabled channels).
- void SetThreshold (unsigned short channel\_id, unsigned short value) const Set the threshold for one single channel, in units of 1 mV.
- void SetOutputWidth (unsigned short group\_id, unsigned short value) const Set the discriminated pulse output width for one group of 8 channels.

• void SetDeadTime (unsigned short group\_id, unsigned short value) const Set the discrimination dead time for one group of 8 channels.

#### **Private Member Functions**

- float OutputWidthCalculator (unsigned short value) const
- float DeadTimeCalculator (unsigned short value) const

## 7.6.1 Detailed Description

Controller for a CAEN V812 constant fraction discriminator.

#### **Author:**

Laurent Forthomme < laurent . forthomme@cern.ch>

#### Date:

22 Jul 2015

## 7.6.2 Constructor & Destructor Documentation

## 7.6.2.1 VME::CFDV812::CFDV812 (int32\_t bhandle, uint32\_t baseaddr)

Here is the call graph for this function:



## 7.6.2.2 VME::CFDV812::~CFDV812() [inline]

## 7.6.3 Member Function Documentation

## 7.6.3.1 void VME::CFDV812::CheckConfiguration () const



# 7.6.3.2 float VME::CFDV812::DeadTimeCalculator (unsigned short value) const [private]

#### 7.6.3.3 unsigned short VME::CFDV812::GetFixedCode () const

Here is the call graph for this function:



#### 7.6.3.4 unsigned short VME::CFDV812::GetManufacturerId () const

Here is the call graph for this function:



## 7.6.3.5 unsigned short VME::CFDV812::GetModuleType () const

Here is the call graph for this function:



## 7.6.3.6 unsigned short VME::CFDV812::GetModuleVersion () const

Here is the call graph for this function:



## 7.6.3.7 unsigned short VME::CFDV812::GetSerialNumber () const



# 7.6.3.8 float VME::CFDV812::OutputWidthCalculator (unsigned short *value*) const [private]

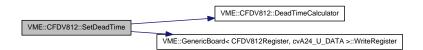
# 7.6.3.9 void VME::CFDV812::SetDeadTime (unsigned short group\_id, unsigned short value) const

Set the discrimination dead time for one group of 8 channels.

#### **Parameters:**

← *group\_id* Group of 8 channels (either 0 for 0-7, or 1 for 8-15)

Here is the call graph for this function:



# 7.6.3.10 void VME::CFDV812::SetOutputWidth (unsigned short *group\_id*, unsigned short *value*) const

Set the discriminated pulse output width for one group of 8 channels.

#### Parameters:

← *group\_id* Group of 8 channels (either 0 for 0-7, or 1 for 8-15)

Here is the call graph for this function:



## 7.6.3.11 void VME::CFDV812::SetPOI (unsigned short poi) const

Set the pattern of inhibit (list of enabled channels).



# 7.6.3.12 void VME::CFDV812::SetThreshold (unsigned short *channel\_id*, unsigned short *value*) const

Set the threshold for one single channel, in units of 1 mV.

Here is the call graph for this function:



The documentation for this class was generated from the following files:

- include/VME\_CFDV812.h
- src/VME\_CFDV812.cpp

# 7.7 NIM::HVModuleN470Values::ChannelStatus Class Reference

#include <NIM\_HVModuleN470.h>

## **Public Types**

• enum SignalStandard { NIM = 0x0, TTL = 0x1 }

#### **Public Member Functions**

- ChannelStatus (unsigned short word)
- ∼ChannelStatus ()
- bool Enabled () const
- bool OVC () const
- bool OVV () const
- bool UNV () const
- bool Trip () const
- bool RampUp () const
- bool RampDown () const
- bool MaxV () const
- bool Polarity () const
- bool Vsel () const
- bool Isel () const
- bool Kill () const
- bool HVEnabled () const
- SignalStandard Standard () const
- bool NonCalibrated () const
- bool Alarm () const
- void Dump () const

#### **Private Attributes**

• unsigned short fWord

#### **Friends**

• std::ostream & operator<< (std::ostream &os, const ChannelStatus &cs)

### 7.7.1 Member Enumeration Documentation

 $7.7.1.1 \quad enum\ NIM:: HV Module N470 Values:: Channel Status:: Signal Standard$ 

**Enumerator:** 

NIM

7.7 NIM::HVModuleN470Values::ChannelStatus Class Reference	47

7	7.2	Comptant	O Dogtany of on	Documentation
1.		Constructor	& Destructor	Documentation

- 7.7.2.1 NIM::HVModuleN470Values::ChannelStatus::ChannelStatus (unsigned short word) [inline]
- 7.7.2.2 NIM::HVModuleN470Values::ChannelStatus::~ChannelStatus () [inline]

#### 7.7.3 Member Function Documentation

- 7.7.3.1 bool NIM::HVModuleN470Values::ChannelStatus::Alarm () const [inline]
- 7.7.3.2 void NIM::HVModuleN470Values::ChannelStatus::Dump () const [inline]
- 7.7.3.3 bool NIM::HVModuleN470Values::ChannelStatus::Enabled () const [inline]
- 7.7.3.4 bool NIM::HVModuleN470Values::ChannelStatus::HVEnabled () const [inline]
- 7.7.3.5 bool NIM::HVModuleN470Values::ChannelStatus::Isel () const [inline]
- 7.7.3.6 bool NIM::HVModuleN470Values::ChannelStatus::Kill () const [inline]
- 7.7.3.7 bool NIM::HVModuleN470Values::ChannelStatus::MaxV () const [inline]
- 7.7.3.8 bool NIM::HVModuleN470Values::ChannelStatus::NonCalibrated () const [inline]
- 7.7.3.9 bool NIM::HVModuleN470Values::ChannelStatus::OVC () const [inline]
- 7.7.3.10 bool NIM::HVModuleN470Values::ChannelStatus::OVV () const [inline]
- 7.7.3.11 bool NIM::HVModuleN470Values::ChannelStatus::Polarity () const [inline]
- 7.7.3.12 bool NIM::HVModuleN470Values::ChannelStatus::RampDown () const [inline]
- 7.7.3.13 bool NIM::HVModuleN470Values::ChannelStatus::RampUp () const [inline]
- 7.7.3.14 SignalStandard

  NIM::HVModuleN470Values::ChannelStatus::Standard () const [inline]
- 7.7.3.15 bool NIM::HVModuleN470Values::ChannelStatus::Trip () const [inline]
- 7.7.3.16 bool NIM::HVModuleN470Values::ChannelStatus::UNV () const [inline]

7.7 NIM::HVModuleN470Values::ChannelStatus Class Reference 49					7 NIM::HVModuleN470Values::ChannelStatus Class Reference		
• include/NIM_HVModuleN470.h							

### 7.8 Client Class Reference

Base client object for the socket.

 $\verb|#include| < \verb|Client.h| > Inheritance diagram for Client: Collaboration diagram for Client:$ 

#### **Public Member Functions**

• Client ()

General void client constructor.

• Client (int port)

Bind a socket client to a given port.

- virtual ∼Client ()
- bool Connect (const SocketType &type=CLIENT)

Bind this client to the socket.

• void Disconnect ()

Unbind this client from the socket.

• void Send (const Message &m) const

Send a message to the master through the socket.

- void Send (const Exception &e) const
- SocketMessage SendAndReceive (const SocketMessage &m, const MessageKey &a) const
- void Receive ()

Receive a socket message from the master.

- SocketMessage Receive (const MessageKey &key)
- virtual void ParseMessage (const SocketMessage &m)

Parse a SocketMessage received from the master.

• virtual SocketType GetType () const

Socket actor type retrieval method.

#### **Private Member Functions**

• void Announce ()

Announce our entry on the socket to its master.

#### **Private Attributes**

- int fClientId
- bool fIsConnected
- SocketType fType

### 7.8.1 Detailed Description

Base client object for the socket. Client object used by the server to send/receive commands from the messenger/broadcaster.

#### **Author:**

Laurent Forthomme < laurent.forthomme@cern.ch>

#### Date:

24 Mar 2015

#### 7.8.2 Constructor & Destructor Documentation

#### 7.8.2.1 Client::Client() [inline]

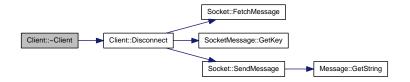
General void client constructor.

#### **7.8.2.2** Client::Client (int *port*)

Bind a socket client to a given port.

### 7.8.2.3 Client::~Client() [virtual]

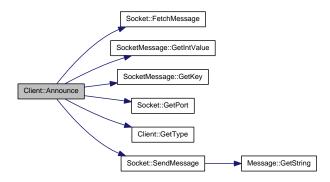
Here is the call graph for this function:



### 7.8.3 Member Function Documentation

## 7.8.3.1 void Client::Announce() [private]

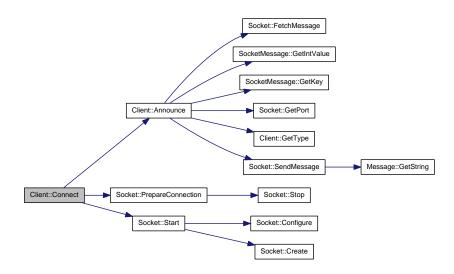
Announce our entry on the socket to its master.



### 7.8.3.2 bool Client::Connect (const SocketType & type = CLIENT)

Bind this client to the socket.

Here is the call graph for this function:



#### 7.8.3.3 void Client::Disconnect ()

Unbind this client from the socket.



### 7.8.3.4 virtual SocketType Client::GetType () const [inline, virtual]

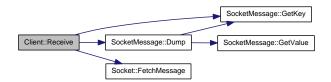
Socket actor type retrieval method.

# 7.8.3.5 virtual void Client::ParseMessage (const SocketMessage & m) [inline, virtual]

Parse a SocketMessage received from the master.

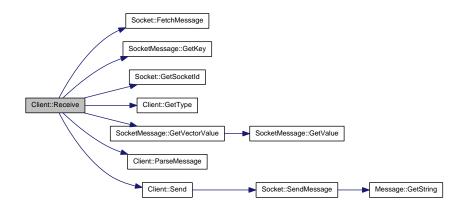
#### 7.8.3.6 SocketMessage Client::Receive (const MessageKey & key)

Here is the call graph for this function:



#### 7.8.3.7 void Client::Receive ()

Receive a socket message from the master.



#### 7.8.3.8 void Client::Send (const Exception & e) const [inline]

Here is the call graph for this function:

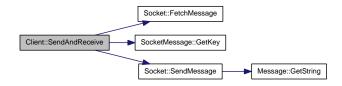


#### 7.8.3.9 void Client::Send (const Message & m) const [inline]

Send a message to the master through the socket.

Here is the call graph for this function:

# 7.8.3.10 SocketMessage Client::SendAndReceive (const SocketMessage & m, const MessageKey & a) const [inline]



# 7.8.4 Field Documentation

- 7.8.4.1 int Client::fClientId [private]
- 7.8.4.2 bool Client::fIsConnected [private]
- 7.8.4.3 SocketType Client::fType [private]

The documentation for this class was generated from the following files:

- include/Client.h
- src/Client.cpp

# 7.9 file\_header\_t Struct Reference

Header to the output files.

```
#include <FileConstants.h>
```

#### **Data Fields**

- uint32\_t magic
- uint32\_t run\_id
- uint32\_t spill\_id
- uint8\_t num\_hptdc
- VME::AcquisitionMode acq\_mode
- VME::DetectionMode det\_mode

#### 7.9.1 Detailed Description

Header to the output files. General header to store in each collected data file for offline readout. It enable any reader to retrieve the run/spill number, as well as the HPTDC configuration during data collection.

#### **Author:**

```
Laurent Forthomme < laurent.forthomme@cern.ch>
```

#### Date:

14 Apr 2015

#### 7.9.2 Field Documentation

- 7.9.2.1 VME::AcquisitionMode file\_header\_t::acq\_mode
- 7.9.2.2 VME::DetectionMode file\_header\_t::det\_mode
- 7.9.2.3 uint32\_t file\_header\_t::magic
- 7.9.2.4 uint8\_t file\_header\_t::num\_hptdc
- 7.9.2.5 uint32\_t file\_header\_t::run\_id
- 7.9.2.6 uint32\_t file\_header\_t::spill\_id

The documentation for this struct was generated from the following file:

• include/FileConstants.h

### 7.10 FileReader Class Reference

Handler for a TDC output file readout.

#include <FileReader.h>Collaboration diagram for FileReader:

#### **Public Member Functions**

- FileReader ()
- FileReader (std::string name)

Class constructor.

- ~FileReader ()
- void Open (std::string name)
- bool IsOpen () const
- void Clear ()
- void Dump () const
- unsigned int GetNumTDCs () const
- unsigned long GetNumEvents () const
- bool GetNextEvent (VME::TDCEvent \*)
- bool GetNextMeasurement (unsigned int channel\_id, VME::TDCMeasurement \*mc)

Fetch the next full measurement on a given channel.

#### **Private Attributes**

- std::ifstream fFile
- file\_header\_t fHeader
- VME::AcquisitionMode fReadoutMode
- time\_t fWriteTime
- unsigned long fNumEvents

### 7.10.1 Detailed Description

Handler for a TDC output file readout.

#### **Author:**

Laurent Forthomme < laurent.forthomme@cern.ch>

#### Date:

Jun 2015

#### 7.10.2 Constructor & Destructor Documentation

7.10.2.1 FileReader::FileReader() [inline]

#### 7.10.2.2 FileReader::FileReader (std::string name)

Class constructor.

#### **Parameters:**

- ← *name* Path to the file to read
- ← ro Data readout mode (continuous storage or trigger matching)

Here is the call graph for this function:



7.10.2.3 FileReader::~FileReader()

#### 7.10.3 Member Function Documentation

- 7.10.3.1 void FileReader::Clear () [inline]
- 7.10.3.2 void FileReader::Dump () const

### 7.10.3.3 bool FileReader::GetNextEvent (VME::TDCEvent \*ev)

Here is the call graph for this function:



# 7.10.3.4 bool FileReader::GetNextMeasurement (unsigned int *channel\_id*, VME::TDCMeasurement \* *mc*)

Fetch the next full measurement on a given channel.

#### **Parameters:**

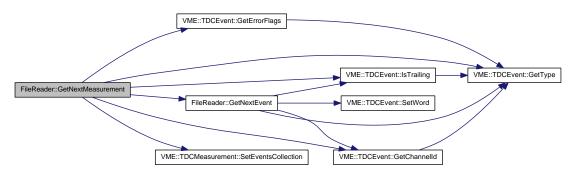
← *channel\_id* Unique identifier of the channel number to retrieve

 $\rightarrow m$  A full measurement with leading, trailing times, ...

#### **Returns:**

A boolean stating the success of retrieval operation

Here is the call graph for this function:



- 7.10.3.5 unsigned long FileReader::GetNumEvents () const [inline]
- 7.10.3.6 unsigned int FileReader::GetNumTDCs () const [inline]
- 7.10.3.7 bool FileReader::IsOpen () const [inline]
- 7.10.3.8 void FileReader::Open (std::string name)
- 7.10.4 Field Documentation
- 7.10.4.1 std::ifstream FileReader::fFile [private]
- 7.10.4.2 file\_header\_t FileReader::fHeader [private]
- 7.10.4.3 unsigned long FileReader::fNumEvents [private]
- 7.10.4.4 VME::AcquisitionMode FileReader::fReadoutMode [private]
- 7.10.4.5 time\_t FileReader::fWriteTime [private]

The documentation for this class was generated from the following files:

- · include/FileReader.h
- src/FileReader.cpp

#### 7.11 VME::FPGAUnitV1495 Class Reference

#include <VME\_FPGAUnitV1495.h>Inheritance diagram for
VME::FPGAUnitV1495:Collaboration diagram for VME::FPGAUnitV1495:

### **Public Types**

• enum TDCBits { kReset = 0x1, kTrigger = 0x2, kClear = 0x4 }

#### **Public Member Functions**

- FPGAUnitV1495 (int32\_t bhandle, uint32\_t baseaddr)
- ~FPGAUnitV1495 ()
- unsigned short GetCAENFirmwareRevision () const
- unsigned short GetUserFirmwareRevision () const
- unsigned int GetHardwareRevision () const
- unsigned short GetSerialNumber () const
- unsigned short GetGeoAddress () const
- void CheckBoardVersion () const
- void ResetFPGA () const
- void DumpFWInformation () const
- void SetTDCBits (unsigned short bits) const

Set a pattern of bits to be sent to all TDCs through the ECL mezzanine.

- void PulseTDCBits (unsigned short bits, unsigned int time\_us=10) const Send a pulse to TDCs' front panel.
- unsigned short GetTDCBits () const
   Retrieve the current bits sent to TDCs' front panel.
- FPGAUnitV1495Control GetControl () const

Retrieve the user-defined control word.

- void SetControl (const FPGAUnitV1495Control &control) const Set the user-defined control word.
- void SetInternalClockPeriod (uint32\_t period) const Set the internal clock period.
- uint32\_t GetInternalClockPeriod () const

Retrieve the internal clock period.

void SetInternalTriggerPeriod (uint32\_t period) const
 Set the internal trigger period.

• uint32\_t GetInternalTriggerPeriod () const

Retrieve the internal trigger period.

- uint32\_t GetOutputPulser () const
- void ClearOutputPulser () const
- void SetOutputPulser (unsigned short id, bool enable=true) const
- void SetOutputPulserPOI (uint32\_t poi) const
- uint32\_t GetOutputDelay () const
- void SetOutputDelay (uint32\_t delay) const
- void StartScaler ()

Start the inner triggers counter.

• void StopScaler ()

Stop the inner triggers counter.

• uint32\_t GetScalerValue () const

Return the inner triggers counter value.

#### **Private Attributes**

· bool fScalerStarted

### 7.11.1 Detailed Description

Handler for the multi-purposes FPGA unit (CAEN V1495)

#### **Author:**

Laurent Forthomme < laurent.forthomme@cern.ch>

#### Date:

25 Jun 2015

#### 7.11.2 Member Enumeration Documentation

### 7.11.2.1 enum VME::FPGAUnitV1495::TDCBits

#### **Enumerator:**

**kReset** 

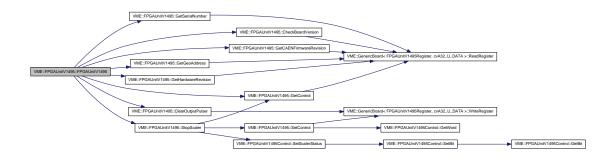
*kTrigger* 

kClear

#### 7.11.3 Constructor & Destructor Documentation

# 7.11.3.1 VME::FPGAUnitV1495::FPGAUnitV1495 (int32\_t bhandle, uint32\_t baseaddr)

Here is the call graph for this function:



#### 7.11.3.2 VME::FPGAUnitV1495::~FPGAUnitV1495()

#### 7.11.4 Member Function Documentation

#### 7.11.4.1 void VME::FPGAUnitV1495::CheckBoardVersion () const

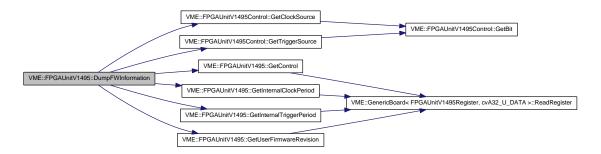
Here is the call graph for this function:

#### 7.11.4.2 void VME::FPGAUnitV1495::ClearOutputPulser () const



#### 7.11.4.3 void VME::FPGAUnitV1495::DumpFWInformation () const

Here is the call graph for this function:



# 7.11.4.4 unsigned short VME::FPGAUnitV1495::GetCAENFirmwareRevision () const

Here is the call graph for this function:



#### 7.11.4.5 FPGAUnitV1495Control VME::FPGAUnitV1495::GetControl () const

Retrieve the user-defined control word.

Here is the call graph for this function:



### 7.11.4.6 unsigned short VME::FPGAUnitV1495::GetGeoAddress () const



#### 7.11.4.7 unsigned int VME::FPGAUnitV1495::GetHardwareRevision() const

Here is the call graph for this function:



#### 7.11.4.8 uint32\_t VME::FPGAUnitV1495::GetInternalClockPeriod () const

Retrieve the internal clock period.

#### **Returns:**

Clock period (in units of 25 ns)

Here is the call graph for this function:



#### 7.11.4.9 uint32\_t VME::FPGAUnitV1495::GetInternalTriggerPeriod () const

Retrieve the internal trigger period.

#### **Returns:**

Trigger period (in units of 50 ns)

Here is the call graph for this function:



#### 7.11.4.10 uint32\_t VME::FPGAUnitV1495::GetOutputDelay () const



#### 7.11.4.11 uint32\_t VME::FPGAUnitV1495::GetOutputPulser () const

Here is the call graph for this function:



#### 7.11.4.12 uint32\_t VME::FPGAUnitV1495::GetScalerValue () const

Return the inner triggers counter value.

Here is the call graph for this function:



#### 7.11.4.13 unsigned short VME::FPGAUnitV1495::GetSerialNumber () const

Here is the call graph for this function:



## 7.11.4.14 unsigned short VME::FPGAUnitV1495::GetTDCBits () const

Retrieve the current bits sent to TDCs' front panel.

#### **Returns:**

A 3-bit word PoI

Here is the call graph for this function:



# 7.11.4.15 unsigned short VME::FPGAUnitV1495::GetUserFirmwareRevision () const



# 7.11.4.16 void VME::FPGAUnitV1495::PulseTDCBits (unsigned short *bits*, unsigned int *time\_us* = 10) const

Send a pulse to TDCs' front panel.

#### **Parameters:**

- $\leftarrow$  *bits* The pattern to send (3 bits)
- ← *time\_us* Pulse width (in us)

Here is the call graph for this function:



#### 7.11.4.17 void VME::FPGAUnitV1495::ResetFPGA () const

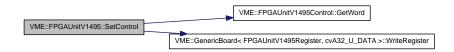
Here is the call graph for this function:



# 7.11.4.18 void VME::FPGAUnitV1495::SetControl (const FPGAUnitV1495Control & control) const

Set the user-defined control word.

Here is the call graph for this function:



# 7.11.4.19 void VME::FPGAUnitV1495::SetInternalClockPeriod (uint32\_t period) const

Set the internal clock period.

#### **Parameters:**

← *period* Clock period (in units of 25 ns)



# 7.11.4.20 void VME::FPGAUnitV1495::SetInternalTriggerPeriod (uint32\_t period) const

Set the internal trigger period.

#### **Parameters:**

← *period* Trigger period (in units of 50 ns)

Here is the call graph for this function:



#### 7.11.4.21 void VME::FPGAUnitV1495::SetOutputDelay (uint32\_t delay) const

Here is the call graph for this function:



# 7.11.4.22 void VME::FPGAUnitV1495::SetOutputPulser (unsigned short id, bool enable = true) const

Here is the call graph for this function:



# 7.11.4.23 void VME::FPGAUnitV1495::SetOutputPulserPOI (uint32\_t poi) const



#### 7.11.4.24 void VME::FPGAUnitV1495::SetTDCBits (unsigned short bits) const

Set a pattern of bits to be sent to all TDCs through the ECL mezzanine.

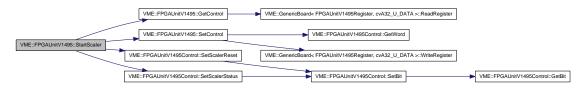
Here is the call graph for this function:



#### 7.11.4.25 void VME::FPGAUnitV1495::StartScaler ()

Start the inner triggers counter.

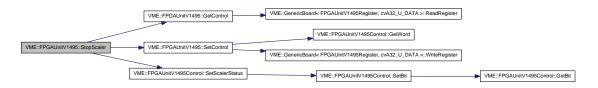
Here is the call graph for this function:



#### 7.11.4.26 void VME::FPGAUnitV1495::StopScaler ()

Stop the inner triggers counter.

Here is the call graph for this function:



#### 7.11.5 Field Documentation

#### 7.11.5.1 bool VME::FPGAUnitV1495::fScalerStarted [private]

The documentation for this class was generated from the following files:

- include/VME\_FPGAUnitV1495.h
- src/VME\_FPGAUnitV1495.cpp

#### 7.12 VME::FPGAUnitV1495Control Class Reference

#include <VME FPGAUnitV1495.h>

### **Public Types**

- enum ClockSource { InternalClock = 0x0, ExternalClock = 0x1 }
- enum TriggerSource { InternalTrigger = 0x0, ExternalTrigger = 0x1 }
- enum SignalSource { InternalSignal = 0x0, ExternalSignal = 0x1 }

#### **Public Member Functions**

- FPGAUnitV1495Control (uint32\_t word)
- virtual ~FPGAUnitV1495Control ()
- void Dump () const
- uint32\_t GetWord () const
- ClockSource GetClockSource () const

Get the clock source.

• void SetClockSource (const ClockSource &cs)

Switch between internal and external clock source.

• TriggerSource GetTriggerSource () const

Get the trigger source.

• void SetTriggerSource (const TriggerSource &cs)

Switch between internal and external trigger source.

- bool GetScalerStatus () const
- void SetScalerStatus (bool start=true)
- void SetScalerReset (bool reset=true)
- SignalSource GetSignalSource (unsigned short map\_id) const
- void SetSignalSource (unsigned short map\_id, const SignalSource &s)

#### **Private Member Functions**

- bool GetBit (unsigned short id) const
- void SetBit (unsigned short id, unsigned short value=0x1)

#### **Private Attributes**

• uint32\_t fWord

### 7.12.1 Detailed Description

User-defined control word to be propagated to the CAEN V1495 board firmware.

#### **Author:**

```
Laurent Forthomme < laurent . forthomme@cern.ch>
```

#### Date:

27 Jun 2015

### 7.12.2 Member Enumeration Documentation

#### 7.12.2.1 enum VME::FPGAUnitV1495Control::ClockSource

#### **Enumerator:**

InternalClock

ExternalClock

#### 7.12.2.2 enum VME::FPGAUnitV1495Control::SignalSource

#### **Enumerator:**

InternalSignal

ExternalSignal

#### 7.12.2.3 enum VME::FPGAUnitV1495Control::TriggerSource

#### **Enumerator:**

InternalTrigger

ExternalTrigger

#### 7.12.3 Constructor & Destructor Documentation

- 7.12.3.1 VME::FPGAUnitV1495Control::FPGAUnitV1495Control (uint32\_t word) [inline]
- 7.12.3.2 virtual VME::FPGAUnitV1495Control::~FPGAUnitV1495Control() [inline, virtual]

#### **7.12.4** Member Function Documentation

- 7.12.4.1 void VME::FPGAUnitV1495Control::Dump()const [inline]
- 7.12.4.2 bool VME::FPGAUnitV1495Control::GetBit (unsigned short id) const [inline, private]
- 7.12.4.3 ClockSource VME::FPGAUnitV1495Control::GetClockSource () const [inline]

Get the clock source.

Here is the call graph for this function:



# 7.12.4.4 bool VME::FPGAUnitV1495Control::GetScalerStatus () const [inline]

Here is the call graph for this function:



# 7.12.4.5 SignalSource VME::FPGAUnitV1495Control::GetSignalSource (unsigned short *map\_id*) const [inline]



# 7.12.4.6 TriggerSource VME::FPGAUnitV1495Control::GetTriggerSource () const [inline]

Get the trigger source.

Here is the call graph for this function:



# 7.12.4.7 uint32\_t VME::FPGAUnitV1495Control::GetWord () const [inline]

# 7.12.4.8 void VME::FPGAUnitV1495Control::SetBit (unsigned short id, unsigned short value = 0x1) [inline, private]

Here is the call graph for this function:



# 7.12.4.9 void VME::FPGAUnitV1495Control::SetClockSource (const ClockSource & cs) [inline]

Switch between internal and external clock source.

Here is the call graph for this function:



# 7.12.4.10 void VME::FPGAUnitV1495Control::SetScalerReset (bool reset = true) [inline]



# 7.12.4.11 void VME::FPGAUnitV1495Control::SetScalerStatus (bool start = true) [inline]

Here is the call graph for this function:



# 7.12.4.12 void VME::FPGAUnitV1495Control::SetSignalSource (unsigned short *map\_id*, const SignalSource & s) [inline]

Here is the call graph for this function:



# 7.12.4.13 void VME::FPGAUnitV1495Control::SetTriggerSource (const TriggerSource & cs) [inline]

Switch between internal and external trigger source.

Here is the call graph for this function:



## 7.12.5 Field Documentation

#### 7.12.5.1 uint32\_t VME::FPGAUnitV1495Control::fWord [private]

The documentation for this class was generated from the following file:

• include/VME\_FPGAUnitV1495.h

# 7.13 VME::GenericBoard < Register, am > Class Template Reference

#include <VME GenericBoard.h>

#### **Public Member Functions**

- GenericBoard (int32\_t bhandle, uint32\_t baseaddr)
- virtual ~GenericBoard ()

#### **Protected Member Functions**

- void WriteRegister (const Register &reg, const uint16\_t &data) const
   Write on register.
- void WriteRegister (const Register &reg, const uint32\_t &data) const
   Write on register.
- void ReadRegister (const Register &reg, uint16\_t \*data) const
   Read on register.
- void ReadRegister (const Register &reg, uint32\_t \*data) const

  \*Read on register.

## **Protected Attributes**

- int32\_t fHandle
- uint32\_t fBaseAddr

template < class Register, CVAddressModifier am > class VME::GenericBoard < Register, am >

#### 7.13.1 Constructor & Destructor Documentation

- 7.13.1.1 template<class Register, CVAddressModifier am>
  VME::GenericBoard< Register, am>::GenericBoard (int32\_t
  bhandle, uint32\_t baseaddr) [inline]
- 7.13.1.2 template<class Register, CVAddressModifier am> virtual VME::GenericBoard< Register, am>::~GenericBoard () [inline, virtual]

#### 7.13.2 Member Function Documentation

7.13.2.1 template < class Register, CVAddressModifier am > void VME::GenericBoard < Register, am >::ReadRegister (const Register & reg, uint32\_t \* data) const [inline, protected]

Read on register. Read a 32-bit word in the register

#### **Parameters:**

- $\leftarrow$  *addr* register
- $\rightarrow$  *data* word
- 7.13.2.2 template < class Register, CVAddressModifier am > void VME::GenericBoard < Register, am >::ReadRegister (const Register & reg, uint16\_t \* data) const [inline, protected]

Read on register. Read a 16-bit word in the register

#### **Parameters:**

- ← addr register
- $\rightarrow$  *data* word
- 7.13.2.3 template<class Register, CVAddressModifier am> void VME::GenericBoard< Register, am>::WriteRegister (const Register & reg, const uint32\_t & data) const [inline, protected]

Write on register. Write a 32-bit word in the register

#### **Parameters:**

- $\leftarrow$  addr register
- $\leftarrow data$  word

7.13.2.4 template<class Register, CVAddressModifier am> void VME::GenericBoard< Register, am>::WriteRegister (const Register & reg, const uint16\_t & data) const [inline, protected]

Write on register. Write a 16-bit word in the register

### **Parameters:**

- $\leftarrow$  addr register
- $\leftarrow$  *data* word

#### 7.13.3 Field Documentation

- 7.13.3.1 template<class Register, CVAddressModifier am> uint32\_t VME::GenericBoard< Register, am>::fBaseAddr [protected]
- 7.13.3.2 template<class Register, CVAddressModifier am> int32\_t VME::GenericBoard< Register, am>::fHandle [protected]

The documentation for this class was generated from the following file:

• include/VME\_GenericBoard.h

## 7.14 VME::GlobalOffset Struct Reference

#include <VME TDCV1x90.h>

### **Data Fields**

- uint16\_t coarse
- uint16\_t fine

#### 7.14.1 Field Documentation

7.14.1.1 uint16\_t VME::GlobalOffset::coarse

7.14.1.2 uint16\_t VME::GlobalOffset::fine

The documentation for this struct was generated from the following file:

• include/VME\_TDCV1x90.h

# 7.15 HTTPMessage Class Reference

Message to be transmitted through a WebSocket protocol.

#include <http://www.hinclude diagram for HTTPMessage:Collaboration diagram for HTTPMessage:

#### **Public Member Functions**

- HTTPMessage (WebSocket \*ws, Message m, MessageAction a)
- HTTPMessage (WebSocket \*ws, const char \*msg, MessageAction a)
- void Decode ()
- void Encode ()
- MessageKey GetKey () const

Placeholder for the MessageKey retrieval method.

• void Dump (std::ostream &os=std::cout) const

#### **Private Attributes**

- WebSocket \* fWS
- std::string fOriginalString

#### 7.15.1 Detailed Description

Message to be transmitted through a WebSocket protocol. Type of message compatible to the transmission through a WebSocket protocol. It enables a direct conversion of standards from any socket message format used elsewhere in this code using the *MessageAction* statement.

#### **Author:**

Laurent Forthomme < laurent.forthomme@cern.ch>

#### Date:

1 Apr 2015

#### 7.15.2 Constructor & Destructor Documentation

7.15.2.1 HTTPMessage::HTTPMessage (WebSocket \* ws, Message m, MessageAction a) [inline]

# 7.15.2.2 HTTPMessage::HTTPMessage (WebSocket \* ws, const char \* msg, MessageAction a) [inline]

Here is the call graph for this function:

#### **7.15.3** Member Function Documentation

- 7.15.3.1 void HTTPMessage::Decode () [inline]
- 7.15.3.2 void HTTPMessage::Dump (std::ostream & os = std::cout) const [inline]

Reimplemented from Message.

- 7.15.3.3 void HTTPMessage::Encode() [inline]
- 7.15.3.4 MessageKey HTTPMessage::GetKey () const [inline]

Placeholder for the MessageKey retrieval method.

Reimplemented from Message.

#### 7.15.4 Field Documentation

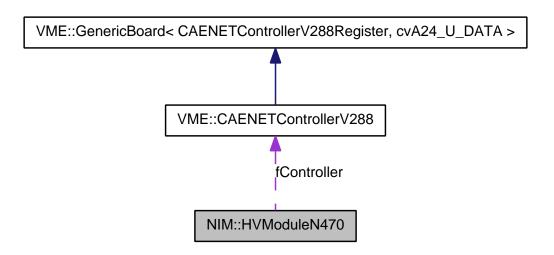
- 7.15.4.1 std::string HTTPMessage::fOriginalString [private]
- 7.15.4.2 WebSocket\* HTTPMessage::fWS [private]

The documentation for this class was generated from the following file:

• include/HTTPMessage.h

#### 7.16 NIM::HVModuleN470 Class Reference

#include <NIM\_HVModuleN470.h>Collaboration diagram for NIM::HVModuleN470:



#### **Public Member Functions**

- HVModuleN470 (uint16\_t addr, VME::CAENETControllerV288 &cont)
- ~HVModuleN470 ()
- std::string GetModuleId () const
- unsigned short GetFWRevision () const
- HVModuleN470Values ReadMonitoringValues () const
- HVModuleN470ChannelValues ReadChannelValues (unsigned short ch\_id) const
- void SetChannelV0 (unsigned short ch\_id, unsigned short v0) const
- void SetChannelIO (unsigned short ch\_id, unsigned short i0) const
- void SetChannelV1 (unsigned short ch\_id, unsigned short v1) const
- void SetChannelI1 (unsigned short ch\_id, unsigned short i1) const

#### **Private Member Functions**

- void ReadRegister (const HVModuleN470Opcodes &reg, std::vector< uint16\_t > \*data, unsigned int num\_words=1) const
   Read in register.
- void WriteRegister (const HVModuleN470Opcodes &reg, const std::vector
   uint16\_t > &data) const

Write on register.

 void WriteRegister (const HVModuleN470Opcodes &reg, const uint16\_t &data) const

Write on register.

#### **Private Attributes**

- VME::CAENETControllerV288 fController
- uint16\_t fAddress

# 7.16.1 Constructor & Destructor Documentation

- 7.16.1.1 NIM::HVModuleN470::HVModuleN470 (uint16\_t addr, VME::CAENETControllerV288 & cont)
- 7.16.1.2 NIM::HVModuleN470::~HVModuleN470() [inline]

#### 7.16.2 Member Function Documentation

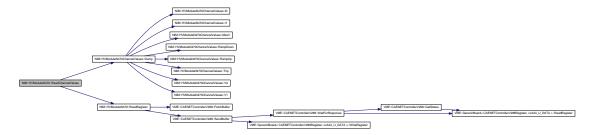
- 7.16.2.1 unsigned short NIM::HVModuleN470::GetFWRevision () const
- 7.16.2.2 std::string NIM::HVModuleN470::GetModuleId () const

Here is the call graph for this function:



# 7.16.2.3 HVModuleN470ChannelValues NIM::HVModuleN470::ReadChannelValues

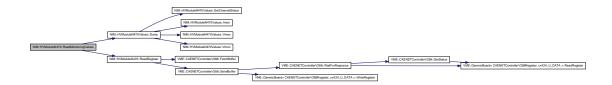
(unsigned short *ch\_id*) const



#### 7.16.2.4 HVModuleN470Values

 $\begin{tabular}{ll} NIM::HVModuleN470::ReadMonitoringValues~() \\ const \end{tabular}$ 

Here is the call graph for this function:



# 7.16.2.5 void NIM::HVModuleN470::ReadRegister (const HVModuleN470Opcodes & reg, std::vector< uint16\_t > \* data, unsigned int num\_words = 1) const [private]

Read in register. Read a vector of 16-bit words in the register

#### **Parameters:**

- $\leftarrow$  addr register
- → *vector* of data words

Here is the call graph for this function:



# 7.16.2.6 void NIM::HVModuleN470::SetChannelI0 (unsigned short $ch\_id$ , unsigned short $i\theta$ ) const



# 7.16.2.7 void NIM::HVModuleN470::SetChannelI1 (unsigned short *ch\_id*, unsigned short *i1*) const

Here is the call graph for this function:



# 7.16.2.8 void NIM::HVModuleN470::SetChannelV0 (unsigned short *ch\_id*, unsigned short *v0*) const

Here is the call graph for this function:



# 7.16.2.9 void NIM::HVModuleN470::SetChannelV1 (unsigned short *ch\_id*, unsigned short *v1*) const

Here is the call graph for this function:



# 7.16.2.10 void NIM::HVModuleN470::WriteRegister (const HVModuleN470Opcodes & reg, const uint16\_t & data) const [private]

Write on register. Write a 16-bit word in the register

#### **Parameters:**

- $\leftarrow$  addr register
- $\rightarrow$  *data* word



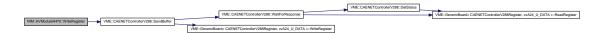
# 7.16.2.11 void NIM::HVModuleN470::WriteRegister (const HVModuleN470Opcodes & reg, const std::vector< uint16\_t > & data) const [private]

Write on register. Write a vector of 16-bit words in the register

#### **Parameters:**

- $\leftarrow$  addr register
- $\rightarrow$  *data* word

Here is the call graph for this function:



### 7.16.3 Field Documentation

7.16.3.1 uint16\_t NIM::HVModuleN470::fAddress [private]

# 7.16.3.2 VME::CAENETControllerV288 NIM::HVModuleN470::fController [private]

The documentation for this class was generated from the following files:

- include/NIM\_HVModuleN470.h
- src/NIM\_HVModuleN470.cpp

# 7.17 NIM::HVModuleN470ChannelValues Class Reference

Single channel monitoring values for the HV power supply.

```
#include <NIM_HVModuleN470.h>
```

#### **Public Member Functions**

- HVModuleN470ChannelValues (unsigned short ch\_id, std::vector< unsigned short > vals)
- ~HVModuleN470ChannelValues ()
- void Dump () const
- unsigned short ChannelStatus () const
- unsigned short Vmon () const
- unsigned short Imon () const
- unsigned short V0 () const
- unsigned short IO () const
- unsigned short V1 () const
- unsigned short I1 () const
- unsigned short Trip () const
- unsigned short RampUp () const
- unsigned short RampDown () const
- unsigned short MaxV () const

#### **Private Attributes**

- unsigned short fChannelId
- std::vector< unsigned short > fValues

### 7.17.1 Detailed Description

Single channel monitoring values for the HV power supply.

#### **Author:**

Laurent Forthomme < laurent.forthomme@cern.ch>

#### Date:

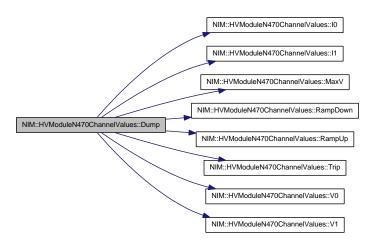
24 Jul 2015

### 7.17.2 Constructor & Destructor Documentation

- 7.17.2.1 NIM::HVModuleN470ChannelValues::HVModuleN470ChannelValues (unsigned short  $ch\_id$ , std::vector< unsigned short > vals) [inline]
- 7.17.2.2 NIM::HVModuleN470ChannelValues:: $\sim$ HVModuleN470ChannelValues () [inline]

#### 7.17.3 Member Function Documentation

- 7.17.3.1 unsigned short NIM::HVModuleN470ChannelValues::ChannelStatus () const [inline]
- 7.17.3.2 void NIM::HVModuleN470ChannelValues::Dump () const [inline]



- 7.17.3.3 unsigned short NIM::HVModuleN470ChannelValues::I0 () const [inline]
- 7.17.3.4 unsigned short NIM::HVModuleN470ChannelValues::I1 () const [inline]
- 7.17.3.5 unsigned short NIM::HVModuleN470ChannelValues::Imon () const [inline]
- 7.17.3.6 unsigned short NIM::HVModuleN470ChannelValues::MaxV () const [inline]
- 7.17.3.7 unsigned short NIM::HVModuleN470ChannelValues::RampDown () const [inline]
- 7.17.3.8 unsigned short NIM::HVModuleN470ChannelValues::RampUp () const [inline]
- 7.17.3.9 unsigned short NIM::HVModuleN470ChannelValues::Trip () const [inline]
- 7.17.3.10 unsigned short NIM::HVModuleN470ChannelValues::V0 () const [inline]
- 7.17.3.11 unsigned short NIM::HVModuleN470ChannelValues::V1 () const [inline]
- 7.17.3.12 unsigned short NIM::HVModuleN470ChannelValues::Vmon () const [inline]

#### 7.17.4 Field Documentation

- 7.17.4.1 unsigned short NIM::HVModuleN470ChannelValues::fChannelId [private]

The documentation for this class was generated from the following file:

• include/NIM\_HVModuleN470.h

# 7.18 NIM::HVModuleN470Values Class Reference

General monitoring values for the HV power supply.

```
#include <NIM_HVModuleN470.h>
```

#### **Data Structures**

• class ChannelStatus

#### **Public Member Functions**

- HVModuleN470Values (std::vector< unsigned short > vals)
- ~HVModuleN470Values ()
- void Dump () const
- unsigned short Vmon () const
- unsigned short Imon () const
- unsigned short Vmax () const
- ChannelStatus GetChannelStatus (unsigned short ch\_id) const

#### **Private Attributes**

• std::vector< unsigned short > fValues

### 7.18.1 Detailed Description

General monitoring values for the HV power supply.

#### **Author:**

Laurent Forthomme < laurent.forthomme@cern.ch>

#### Date:

24 Jul 2015

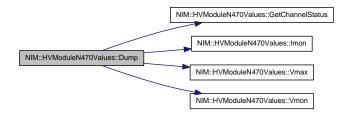
#### 7.18.2 Constructor & Destructor Documentation

- 7.18.2.1 NIM::HVModuleN470Values::HVModuleN470Values (std::vector < unsigned short > vals) [inline]
- 7.18.2.2 NIM::HVModuleN470Values::~HVModuleN470Values () [inline]

#### 7.18.3 Member Function Documentation

#### 7.18.3.1 void NIM::HVModuleN470Values::Dump() const [inline]

Here is the call graph for this function:



- 7.18.3.2 ChannelStatus NIM::HVModuleN470Values::GetChannelStatus (unsigned short *ch\_id*) const [inline]
- 7.18.3.3 unsigned short NIM::HVModuleN470Values::Imon () const [inline]
- 7.18.3.4 unsigned short NIM::HVModuleN470Values::Vmax () const [inline]
- 7.18.3.5 unsigned short NIM::HVModuleN470Values::Vmon () const [inline]

#### 7.18.4 Field Documentation

# 7.18.4.1 std::vector<unsigned short> NIM::HVModuleN470Values::fValues [private]

The documentation for this class was generated from the following file:

• include/NIM\_HVModuleN470.h

# 7.19 VME::IOModuleV262 Class Reference

#include <VME\_IOModuleV262.h>Inheritance diagram for
VME::IOModuleV262:Collaboration diagram for VME::IOModuleV262:

#### **Public Member Functions**

- IOModuleV262 (int32\_t bhandle, uint32\_t baseaddr)
- ∼IOModuleV262 ()
- unsigned short GetSerialNumber () const
- unsigned short GetModuleVersion () const
- unsigned short GetModuleType () const
- unsigned short GetManufacturerId () const
- unsigned short GetIdentifier () const

#### 7.19.1 Constructor & Destructor Documentation

# 7.19.1.1 VME::IOModuleV262::IOModuleV262 (int32\_t bhandle, uint32\_t baseaddr)

Here is the call graph for this function:



#### 7.19.1.2 VME::IOModuleV262::~IOModuleV262() [inline]

#### 7.19.2 Member Function Documentation

#### 7.19.2.1 unsigned short VME::IOModuleV262::GetIdentifier () const



#### 7.19.2.2 unsigned short VME::IOModuleV262::GetManufacturerId () const

Here is the call graph for this function:



#### 7.19.2.3 unsigned short VME::IOModuleV262::GetModuleType () const

Here is the call graph for this function:



#### 7.19.2.4 unsigned short VME::IOModuleV262::GetModuleVersion () const

Here is the call graph for this function:



#### 7.19.2.5 unsigned short VME::IOModuleV262::GetSerialNumber () const

Here is the call graph for this function:



The documentation for this class was generated from the following files:

- include/VME\_IOModuleV262.h
- src/VME\_IOModuleV262.cpp

# 7.20 Message Class Reference

Base socket message type.

#include <Message.h>Inheritance diagram for Message:

#### **Public Member Functions**

• Message ()

Void message constructor.

• Message (const char \*msg)

Construct a message from a string.

• Message (std::string msg)

Construct a message from a string.

- virtual ~Message ()
- MessageKey GetKey () const

Placeholder for the MessageKey retrieval method.

• std::string GetString () const

Retrieve the string carried by this message as a whole.

• bool IsFromWeb () const

Extract from any message its potential arrival from a WebSocket protocol.

• void Dump (std::ostream &os=std::cout) const

#### **Protected Attributes**

• std::string fString

## 7.20.1 Detailed Description

Base socket message type. Base handler for messages to be transmitted through the socket

#### **Author:**

Laurent Forthomme < laurent.forthomme@cern.ch>

#### Date:

6 Apr 2015

#### 7.20.2 Constructor & Destructor Documentation

#### 7.20.2.1 Message::Message() [inline]

Void message constructor.

#### 7.20.2.2 Message::Message (const char \* msg) [inline]

Construct a message from a string.

#### 7.20.2.3 Message::Message (std::string msg) [inline]

Construct a message from a string.

# 7.20.2.4 virtual Message::~Message() [inline, virtual]

#### 7.20.3 Member Function Documentation

# 7.20.3.1 void Message::Dump (std::ostream & os = std::cout) const [inline]

Reimplemented in HTTPMessage, and SocketMessage.

#### 7.20.3.2 MessageKey Message::GetKey () const [inline]

Placeholder for the MessageKey retrieval method.

Reimplemented in HTTPMessage, and SocketMessage.

#### 7.20.3.3 std::string Message::GetString () const [inline]

Retrieve the string carried by this message as a whole.

Reimplemented in SocketMessage.

# 7.20.3.4 bool Message::IsFromWeb() const [inline]

Extract from any message its potential arrival from a WebSocket protocol.

#### 7.20.4 Field Documentation

# 7.20.4.1 std::string Message::fString [protected]

The documentation for this class was generated from the following file:

• include/Message.h

# 7.21 Messenger Class Reference

Base master object for the socket.

#include <Messenger.h>Inheritance diagram for Messenger:Collaboration diagram for Messenger:

#### **Public Member Functions**

• Messenger ()

Build a void master object or socket actor.

• Messenger (int port)

Build a master object to control the socket.

- ∼Messenger ()
- bool Connect ()

Connect the master to the socket.

• void Disconnect ()

Remove the master and destroy the socket.

• void Send (const Message &m, int sid) const

Send any type of message to any client.

 void SendAll (const Socket::SocketType &type, const Message &m) const Send any type of message to all clients of one type.

• void Receive ()

Handle a message reception from a client.

• void Broadcast (const Message &m) const

Emit a message to all clients connected through the socket.

• void StartAcquisition ()

Start the data acquisition.

- void StopAcquisition ()
- SocketType GetType () const

Socket actor type retrieval method.

#### **Private Member Functions**

• void AddClient ()

Add a client to listen to.

• void DisconnectClient (int sid, MessageKey key, bool force=false)

Disconnect a client.

- void SwitchClientType (int sid, Socket::SocketType type)
- void ProcessMessage (SocketMessage m, int sid)

Process a message received from the socket.

### **Private Attributes**

- WebSocket \* fWS
- int fNumAttempts
- pid\_t fPID
- int fStdoutPipe [2]
- int fStderrPipe [2]

# 7.21.1 Detailed Description

Base master object for the socket. Messenger/broadcaster object used by the server to send/receive commands from the clients/listeners.

## **Author:**

Laurent Forthomme < laurent .forthomme@cern.ch>

#### Date:

23 Mar 2015

### 7.21.2 Constructor & Destructor Documentation

#### 7.21.2.1 Messenger::Messenger ()

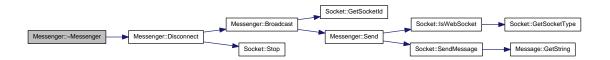
Build a void master object or socket actor.

### 7.21.2.2 Messenger::Messenger (int port)

Build a master object to control the socket.

#### 7.21.2.3 Messenger::~Messenger ()

Here is the call graph for this function:

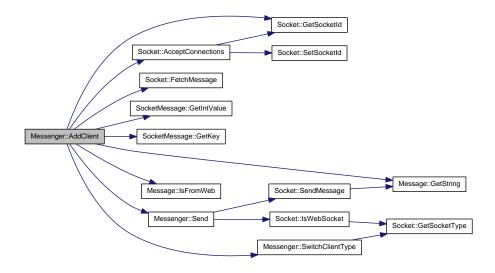


### 7.21.3 Member Function Documentation

### 7.21.3.1 void Messenger::AddClient() [private]

Add a client to listen to. Add one client to the list of socket actors to monitor for message retrieval/submission.

Here is the call graph for this function:



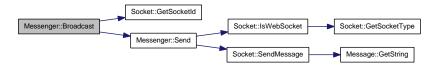
### 7.21.3.2 void Messenger::Broadcast (const Message & m) const

Emit a message to all clients connected through the socket.

#### **Parameters:**

 $\leftarrow$  *m* Message to transmit

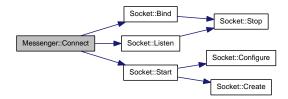
Here is the call graph for this function:



## 7.21.3.3 bool Messenger::Connect ()

Connect the master to the socket. Connect this master to the socket for clients to be able to bind.

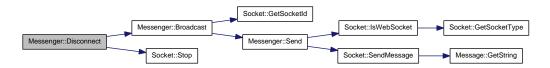
Here is the call graph for this function:



#### 7.21.3.4 void Messenger::Disconnect ()

Remove the master and destroy the socket. Remove this master from the socket, thus disconnecting automatically the clients connected.

Here is the call graph for this function:



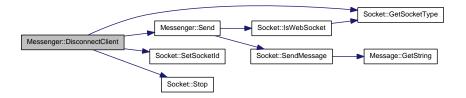
# 7.21.3.5 void Messenger::DisconnectClient (int sid, MessageKey key, bool force = false) [private]

Disconnect a client. Ask to a client to disconnect from this socket.

#### **Parameters:**

- $\leftarrow$  *sid* Unique identifier of the client to disconnect
- ← key Key to the message to transmit for disconnection
- ← force Do we need to force the client out of this socket?

Here is the call graph for this function:



# 7.21.3.6 SocketType Messenger::GetType () const [inline]

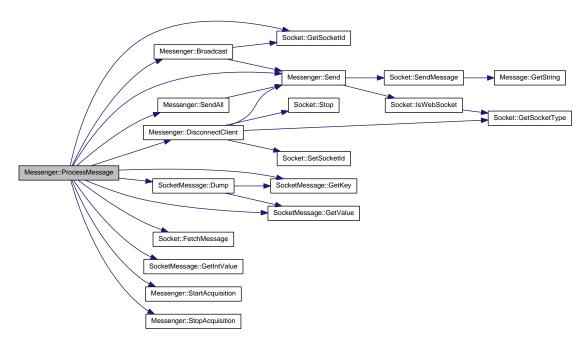
Socket actor type retrieval method.

# 7.21.3.7 void Messenger::ProcessMessage (SocketMessage m, int sid) [private]

Process a message received from the socket.

#### **Parameters:**

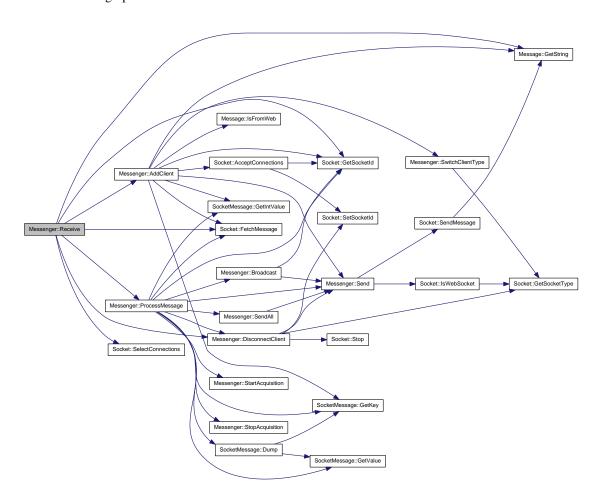
← *Unique* identifier of the client sending the message



#### 7.21.3.8 void Messenger::Receive ()

Handle a message reception from a client.

Here is the call graph for this function:



# 7.21.3.9 void Messenger::Send (const Message & m, int sid) const

Send any type of message to any client.

# **Parameters:**

- $\leftarrow m$  Message to transmit
- $\leftarrow$  *sid* Unique identifier of the client on this socket

Here is the call graph for this function:



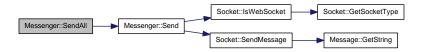
# 7.21.3.10 void Messenger::SendAll (const Socket::SocketType & type, const Message & m) const [inline]

Send any type of message to all clients of one type.

#### **Parameters:**

- ← *type* Client type
- $\leftarrow m$  Message to transmit

Here is the call graph for this function:

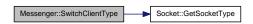


#### 7.21.3.11 void Messenger::StartAcquisition ()

Start the data acquisition.

# 7.21.3.12 void Messenger::StopAcquisition ()

# 7.21.3.13 void Messenger::SwitchClientType (int sid, Socket::SocketType type) [private]



### 7.21.4 Field Documentation

- 7.21.4.1 int Messenger::fNumAttempts [private]
- 7.21.4.2 pid\_t Messenger::fPID [private]
- 7.21.4.3 int Messenger::fStderrPipe[2] [private]
- 7.21.4.4 int Messenger::fStdoutPipe[2] [private]
- 7.21.4.5 WebSocket\* Messenger::fWS [private]

The documentation for this class was generated from the following files:

- include/Messenger.h
- src/Messenger.cpp

# 7.22 VME::PCIInterfaceA2818 Class Reference

#include <VME PCIInterfaceA2818.h>

### **Public Member Functions**

- PCIInterfaceA2818 (const char \*device)
- virtual ~PCIInterfaceA2818 ()
- std::string GetFWRevision () const

#### **Private Attributes**

• int fHandle

#### 7.22.1 Constructor & Destructor Documentation

- 7.22.1.1 VME::PCIInterfaceA2818::PCIInterfaceA2818 (const char \* device) [inline]
- 7.22.1.2 virtual VME::PCIInterfaceA2818::~PCIInterfaceA2818 () [inline, virtual]

### 7.22.2 Member Function Documentation

7.22.2.1 std::string VME::PCIInterfaceA2818::GetFWRevision () const [inline]

#### 7.22.3 Field Documentation

#### 7.22.3.1 int VME::PCIInterfaceA2818::fHandle [private]

The documentation for this class was generated from the following file:

• include/VME\_PCIInterfaceA2818.h

# 7.23 Socket Class Reference

Base socket object from which clients/master from a socket inherit.

#include <Socket.h>Inheritance diagram for Socket:

# **Public Types**

```
    enum SocketType {
        INVALID = -1, MASTER = 0, WEBSOCKET_CLIENT, CLIENT,
        DETECTOR, DQM }
```

Type of actor playing a role on the socket.

• typedef std::set< std::pair< int, SocketType >> SocketCollection

#### **Public Member Functions**

- Socket ()
- Socket (int port)
- virtual ∼Socket ()
- void Stop ()

Terminates the socket and all attached communications.

- void SetPort (int port)
- int GetPort () const

Retrieve the port used for this socket.

• void AcceptConnections (Socket &socket)

Accept connection from a client.

- void SelectConnections ()
- void SetSocketId (int sid)
- int GetSocketId () const
- SocketType GetSocketType (int sid) const
- bool IsWebSocket (int sid) const
- void DumpConnected () const

### **Protected Member Functions**

```
• bool Start ()

Start the socket.
```

• void Bind ()

Bind a name to a socket.

- void PrepareConnection ()
- void Listen (int maxconn)

Listen to incoming messages.

- void SendMessage (Message message, int id=-1) const Send a message on a socket.
- Message FetchMessage (int id=-1) const

Receive a message from a socket.

### **Protected Attributes**

- int fPort
- char fBuffer [MAX\_WORD\_LENGTH]
- SocketCollection fSocketsConnected
- fd\_set fMaster

Master file descriptor list.

• fd\_set fReadFds

Temp file descriptor list for select().

### **Private Member Functions**

• void Create ()

Create an endpoint for communication.

• void Configure ()

Configure the socket object for communication.

#### **Private Attributes**

- int fSocketId
- struct sockaddr\_in fAddress

# 7.23.1 Detailed Description

Base socket object from which clients/master from a socket inherit. General object providing all useful method to connect/bind/send/receive information through system sockets.

#### **Author:**

Laurent Forthomme < laurent.forthomme@cern.ch>

#### Date:

23 Mar 2015

# 7.23.2 Member Typedef Documentation

```
7.23.2.1 typedef std::set< std::pair<int,SocketType> > Socket::SocketCollection
```

#### 7.23.3 Member Enumeration Documentation

#### 7.23.3.1 enum Socket::SocketType

Type of actor playing a role on the socket.

#### **Enumerator:**

**INVALID** 

**MASTER** 

WEBSOCKET\_CLIENT

**CLIENT** 

**DETECTOR** 

DQM

#### 7.23.4 Constructor & Destructor Documentation

```
7.23.4.1 Socket::Socket() [inline]
```

**7.23.4.2** Socket::Socket (int *port*)

7.23.4.3 Socket::~Socket() [virtual]

#### 7.23.5 Member Function Documentation

# 7.23.5.1 void Socket::AcceptConnections (Socket & socket)

Accept connection from a client. Set the socket to accept connections any client transmitting through the socket

#### **Parameters:**

inout] socket Master/client object to enable on the socket

#### 7.23.5.2 void Socket::Bind () [protected]

Bind a name to a socket.

#### **Returns:**

Success of the operation

Here is the call graph for this function:

#### 7.23.5.3 void Socket::Configure () [private]

Configure the socket object for communication.

#### 7.23.5.4 void Socket::Create() [private]

Create an endpoint for communication.

### 7.23.5.5 void Socket::DumpConnected () const

#### 7.23.5.6 Message Socket::FetchMessage (int id = -1) const [protected]

Receive a message from a socket.

#### **Returns:**

Received message as a std::string

### 7.23.5.7 int Socket::GetPort() const [inline]

Retrieve the port used for this socket.

#### 7.23.5.8 int Socket::GetSocketId () const [inline]

#### 7.23.5.9 SocketType Socket::GetSocketType (int sid) const [inline]

### 7.23.5.10 bool Socket::IsWebSocket (int sid) const [inline]

Here is the call graph for this function:

### 7.23.5.11 void Socket::Listen (int maxconn) [protected]

Listen to incoming messages. Set the socket to listen to any message coming from outside

#### 7.23.5.12 void Socket::PrepareConnection() [protected]

Here is the call graph for this function:

#### 7.23.5.13 void Socket::SelectConnections ()

Register all open file descriptors to read their communication through the socket

# 7.23.5.14 void Socket::SendMessage (Message message, int id = -1) const [protected]

Send a message on a socket.

Here is the call graph for this function:

#### 7.23.5.15 void Socket::SetPort (int port) [inline]

#### 7.23.5.16 void Socket::SetSocketId (int sid) [inline]

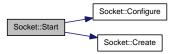
# 7.23.5.17 bool Socket::Start() [protected]

Start the socket. Launch all mandatory operations to set the socket to be used

#### **Returns:**

Success of the operation

Here is the call graph for this function:



#### **7.23.5.18 void Socket::Stop** ()

Terminates the socket and all attached communications.

### 7.23.6 Field Documentation

7.23.6.1 struct sockaddr\_in Socket::fAddress [read, private]

7.23.6.2 char Socket::fBuffer[MAX\_WORD\_LENGTH] [protected]

7.23.6.3 fd\_set Socket::fMaster [protected]

Master file descriptor list.

7.23.6.4 int Socket::fPort [protected]

7.23.6.5 fd\_set Socket::fReadFds [protected]

Temp file descriptor list for select().

7.23.6.6 int Socket::fSocketId [private]

A file descriptor for this socket, if *Create* was performed beforehand.

### 7.23.6.7 SocketCollection Socket::fSocketsConnected [protected]

The documentation for this class was generated from the following files:

- include/Socket.h
- src/Socket.cpp

# 7.24 SocketMessage Class Reference

Socket-passed message type.

#include <SocketMessage.h>Inheritance diagram for SocketMessage:

#### **Public Member Functions**

- SocketMessage ()
- SocketMessage (const Message &msg)
- SocketMessage (const char \*msg\_s)
- SocketMessage (std::string msg\_s)
- SocketMessage (const MessageKey &key)

Construct a socket message out of a key.

- SocketMessage (const MessageKey &key, const char \*value)

  Construct a socket message out of a key and a string-type value.
- SocketMessage (const MessageKey &key, std::string value)

  Construct a socket message out of a key and a string-type value.
- SocketMessage (const MessageKey &key, const int value)

  Construct a socket message out of a key and an integer-type value.
- SocketMessage (const MessageKey &key, const float value)

  Construct a socket message out of a key and a float-type value.
- SocketMessage (const MessageKey &key, const double value)
   Construct a socket message out of a key and a double precision-type value.
- SocketMessage (MessageMap msg\_m)

  Construct a socket message out of a map of key/string-type value.
- ~SocketMessage ()
- void SetKeyValue (const MessageKey &key, const char \*value) String-valued message.
- void SetKeyValue (const MessageKey &key, int int\_value)
   Send an integer-valued message.
- void SetKeyValue (const MessageKey &key, float float\_value) Float-valued message.
- void SetKeyValue (const MessageKey &key, double double\_value)
   Double-valued message.

• std::string GetString () const

Extract the whole key:value message.

• MessageKey GetKey () const

Extract the message's key.

• std::string GetValue () const

Extract the message's string value.

• std::string GetCleanedValue () const

Extract the message's string value (without the trailing endlines).

• int GetIntValue () const

Extract the message's integer value.

• VectorValue GetVectorValue () const

Extract the message's vector of string value.

• void Dump (std::ostream &os=std::cout) const

#### **Private Member Functions**

- MessageMap Object () const
- std::string String () const

#### **Private Attributes**

MessageMap fMessage

# 7.24.1 Detailed Description

Socket-passed message type.

#### **Author:**

Laurent Forthomme < laurent.forthomme@cern.ch>

#### Date:

26 Mar 2015

#### 7.24.2 Constructor & Destructor Documentation

#### 7.24.2.1 SocketMessage::SocketMessage() [inline]

#### 7.24.2.2 SocketMessage::SocketMessage (const Message & msg) [inline]

Here is the call graph for this function:

#### 7.24.2.3 SocketMessage::SocketMessage (const char \* msg\_s) [inline]

Here is the call graph for this function:

#### 7.24.2.4 SocketMessage::SocketMessage (std::string msg\_s) [inline]

Here is the call graph for this function:

# 7.24.2.5 SocketMessage::SocketMessage (const MessageKey & key) [inline]

Construct a socket message out of a key.

Here is the call graph for this function:

# 7.24.2.6 SocketMessage::SocketMessage (const MessageKey & key, const char \* value) [inline]

Construct a socket message out of a key and a string-type value.

Here is the call graph for this function:

# 7.24.2.7 SocketMessage::SocketMessage (const MessageKey & key, std::string value) [inline]

Construct a socket message out of a key and a string-type value.

Here is the call graph for this function:

# 7.24.2.8 SocketMessage::SocketMessage (const MessageKey & key, const int value) [inline]

Construct a socket message out of a key and an integer-type value.

# 7.24.2.9 SocketMessage::SocketMessage (const MessageKey & key, const float value) [inline]

Construct a socket message out of a key and a float-type value.

Here is the call graph for this function:

# 7.24.2.10 SocketMessage::SocketMessage (const MessageKey & key, const double value) [inline]

Construct a socket message out of a key and a double precision-type value.

Here is the call graph for this function:

#### 7.24.2.11 SocketMessage::SocketMessage (MessageMap msg\_m) [inline]

Construct a socket message out of a map of key/string-type value.

#### 7.24.2.12 SocketMessage::~SocketMessage() [inline]

#### 7.24.3 Member Function Documentation

# 7.24.3.1 void SocketMessage::Dump (std::ostream & os = std::cout) const [inline]

Reimplemented from Message.

Here is the call graph for this function:

#### 7.24.3.2 std::string SocketMessage::GetCleanedValue () const [inline]

Extract the message's string value (without the trailing endlines).

#### 7.24.3.3 int SocketMessage::GetIntValue() const [inline]

Extract the message's integer value.

#### 7.24.3.4 MessageKey SocketMessage::GetKey () const [inline]

Extract the message's key.

Reimplemented from Message.

# 7.24.3.5 std::string SocketMessage::GetString() const [inline]

Extract the whole key:value message.

Reimplemented from Message.

#### 7.24.3.6 std::string SocketMessage::GetValue () const [inline]

Extract the message's string value.

#### 7.24.3.7 VectorValue SocketMessage::GetVectorValue () const [inline]

Extract the message's vector of string value.

Here is the call graph for this function:

#### 7.24.3.8 MessageMap SocketMessage::Object() const [inline, private]

# 7.24.3.9 void SocketMessage::SetKeyValue (const MessageKey & key, double double\_value) [inline]

Double-valued message.

Here is the call graph for this function:

# 7.24.3.10 void SocketMessage::SetKeyValue (const MessageKey & key, float float\_value) [inline]

Float-valued message.

Here is the call graph for this function:

# 7.24.3.11 void SocketMessage::SetKeyValue (const MessageKey & key, int int\_value) [inline]

Send an integer-valued message.

Here is the call graph for this function:

# 7.24.3.12 void SocketMessage::SetKeyValue (const MessageKey & key, const char \* value) [inline]

String-valued message.

# 7.24.3.13 std::string SocketMessage::String() const [inline, private]

### 7.24.4 Field Documentation

# 7.24.4.1 MessageMap SocketMessage::fMessage [private]

The documentation for this class was generated from the following file:

• include/SocketMessage.h

# 7.25 VME::TDCErrorFlag Class Reference

#### Error flags handler.

#include <VME\_TDCEvent.h>

#### **Public Member Functions**

- TDCErrorFlag (uint16\_t ef)
- virtual ~TDCErrorFlag ()
- uint16\_t GetWord () const
- void Dump () const
- bool HasReadoutFIFOOverflow (unsigned int group\_id) const

  Check whether hits have been lost from read-out FIFO overflow in a given group.
- bool HasL1BufferOverflow (unsigned int group\_id) const
   Check whether hits have been lost from L1 buffer overflow in a given group.
- bool HasGroupError (unsigned int group\_id) const

  Check whether hits have been lost due to error in a given group.
- bool HasReachedEventSizeLimit () const
   Hits rejected because of programmed event size limit.
- bool HasTriggerFIFOOverflow () const Event lost (trigger FIFO overflow).
- bool HasInternalChipError () const

  Internal fatal chip error has been detected.

#### **Private Attributes**

• uint16\_t fWord

#### **Friends**

• std::ostream & operator<< (std::ostream &os, const TDCErrorFlag &ef)

### 7.25.1 Detailed Description

Error flags handler.

#### **Author:**

Laurent Forthomme < laurent.forthomme@cern.ch>

#### Date:

22 Jun 2015

#### 7.25.2 Constructor & Destructor Documentation

- 7.25.2.1 VME::TDCErrorFlag::TDCErrorFlag (uint16\_t ef) [inline]
- 7.25.2.2 virtual VME::TDCErrorFlag::~TDCErrorFlag() [inline, virtual]

#### 7.25.3 Member Function Documentation

- 7.25.3.1 void VME::TDCErrorFlag::Dump() const [inline]
- 7.25.3.2 uint16\_t VME::TDCErrorFlag::GetWord () const [inline]
- 7.25.3.3 bool VME::TDCErrorFlag::HasGroupError (unsigned int group\_id) const [inline]

Check whether hits have been lost due to error in a given group.

## 7.25.3.4 bool VME::TDCErrorFlag::HasInternalChipError () const [inline]

Internal fatal chip error has been detected.

# 7.25.3.5 bool VME::TDCErrorFlag::HasL1BufferOverflow (unsigned int group\_id) const [inline]

Check whether hits have been lost from L1 buffer overflow in a given group.

## 7.25.3.6 bool VME::TDCErrorFlag::HasReachedEventSizeLimit () const [inline]

Hits rejected because of programmed event size limit.

## 7.25.3.7 bool VME::TDCErrorFlag::HasReadoutFIFOOverflow (unsigned int group\_id) const [inline]

Check whether hits have been lost from read-out FIFO overflow in a given group.

## 7.25.3.8 bool VME::TDCErrorFlag::HasTriggerFIFOOverflow () const [inline]

Event lost (trigger FIFO overflow).

## 7.25.4 Friends And Related Function Documentation

7.25.4.1 std::ostream& operator<< (std::ostream & os, const TDCErrorFlag & ef) [friend]

## 7.25.5 Field Documentation

## 7.25.5.1 uint16\_t VME::TDCErrorFlag::fWord [private]

The documentation for this class was generated from the following file:

• include/VME\_TDCEvent.h

## 7.26 VME::TDCEvent Class Reference

```
HPTDC event parser.
```

```
#include <VME_TDCEvent.h>
```

## **Public Types**

```
• enum EventType {
```

```
TDCMeasurement = 0x0, TDCHeader = 0x1, TDCTrailer = 0x3, TDCError = 0x4,
```

```
GlobalHeader = 0x8, GlobalTrailer = 0x10, ETTT = 0x11, Filler = 0x18 }
```

#### **Public Member Functions**

- TDCEvent ()
- TDCEvent (const TDCEvent &ev)
- TDCEvent (const uint32\_t &word)
- virtual ~TDCEvent ()
- void Dump () const
- void SetWord (const uint32\_t &word)
- uint32\_t GetWord () const
- EventType GetType () const

Type of packet read out from the TDC.

• unsigned int GetTDCId () const

Programmed identifier of master TDC providing the event.

• uint16\_t GetEventId () const

Event identifier from event counter.

• uint16\_t GetWordCount () const

Total number of words in event (including headers and trailers).

- unsigned int GetGeo () const
- unsigned int GetChannelId () const

Channel number for.

• uint32\_t GetEventCount () const

Total number of events.

• uint16\_t GetBunchId () const

Bunch identifier of trigger (or trigger time tag).

• bool IsTrailing () const

Are we dealing with a trailing or a leading measurement?

- uint32\_t GetETTT () const Extended trigger time tag.
- uint32\_t GetTime (bool pair=false) const Edge measurement in programmed time resolution.
- unsigned int GetWidth () const

  Width of pulse in programmed time resolution.
- unsigned int GetStatus () const
- TDCErrorFlag GetErrorFlags () const

Return error flags if an error condition has been detected.

#### **Private Attributes**

• uint32\_t fWord

## 7.26.1 Detailed Description

HPTDC event parser. Object enabling to decipher any measurement/error/debug event returned by the HPTDC chip

#### **Author:**

```
Laurent Forthomme < laurent . forthomme@cern . ch>
```

### Date:

4 May 2015

## 7.26.2 Member Enumeration Documentation

## 7.26.2.1 enum VME::TDCEvent::EventType

#### **Enumerator:**

**TDCM**easurement

**TDCHeader** 

**TDCTrailer** 

**TDCError** 

GlobalHeader

GlobalTrailer

**ETTT** 

Filler

#### 7.26.3 Constructor & Destructor Documentation

- 7.26.3.1 VME::TDCEvent::TDCEvent() [inline]
- 7.26.3.2 VME::TDCEvent::TDCEvent (const TDCEvent & ev) [inline]
- 7.26.3.3 VME::TDCEvent::TDCEvent (const uint32\_t & word) [inline]
- 7.26.3.4 virtual VME::TDCEvent::~TDCEvent() [inline, virtual]

#### 7.26.4 Member Function Documentation

### 7.26.4.1 void VME::TDCEvent::Dump() const [inline]

Here is the call graph for this function:



## 7.26.4.2 uint16\_t VME::TDCEvent::GetBunchId () const [inline]

Bunch identifier of trigger (or trigger time tag).

Here is the call graph for this function:

## 7.26.4.3 unsigned int VME::TDCEvent::GetChannelId () const [inline]

Channel number for.

Here is the call graph for this function:

## 7.26.4.4 TDCErrorFlag VME::TDCEvent::GetErrorFlags () const [inline]

Return error flags if an error condition has been detected.

Here is the call graph for this function:

#### 7.26.4.5 uint32\_t VME::TDCEvent::GetETTT() const [inline]

Extended trigger time tag.

#### 7.26.4.6 uint32\_t VME::TDCEvent::GetEventCount() const [inline]

Total number of events.

Here is the call graph for this function:

#### 7.26.4.7 uint16\_t VME::TDCEvent::GetEventId () const [inline]

Event identifier from event counter.

Here is the call graph for this function:

## 7.26.4.8 unsigned int VME::TDCEvent::GetGeo() const [inline]

Here is the call graph for this function:

#### 7.26.4.9 unsigned int VME::TDCEvent::GetStatus () const [inline]

Here is the call graph for this function:

### 7.26.4.10 unsigned int VME::TDCEvent::GetTDCId () const [inline]

Programmed identifier of master TDC providing the event.

Here is the call graph for this function:

## 7.26.4.11 uint32\_t VME::TDCEvent::GetTime (bool pair = false) const [inline]

Edge measurement in programmed time resolution.

#### **Parameters:**

← *pair* Are we dealing with a pair measurement? (only for leading time word)

Here is the call graph for this function:



### 7.26.4.12 EventType VME::TDCEvent::GetType () const [inline]

Type of packet read out from the TDC.

### 7.26.4.13 unsigned int VME::TDCEvent::GetWidth() const [inline]

Width of pulse in programmed time resolution.

Here is the call graph for this function:

## 7.26.4.14 uint32\_t VME::TDCEvent::GetWord() const [inline]

#### 7.26.4.15 uint16\_t VME::TDCEvent::GetWordCount() const [inline]

Total number of words in event (including headers and trailers).

Here is the call graph for this function:

## 7.26.4.16 bool VME::TDCEvent::IsTrailing() const [inline]

Are we dealing with a trailing or a leading measurement?

Here is the call graph for this function:

## 7.26.4.17 void VME::TDCEvent::SetWord (const uint32\_t & word) [inline]

## 7.26.5 Field Documentation

#### 7.26.5.1 uint32\_t VME::TDCEvent::fWord [private]

The documentation for this class was generated from the following file:

• include/VME\_TDCEvent.h

## 7.27 VME::TDCMeasurement Class Reference

```
#include <VME TDCMeasurement.h>
```

#### **Public Member Functions**

- TDCMeasurement ()
- TDCMeasurement (const std::vector< TDCEvent > &v)
- ~TDCMeasurement ()
- void Dump ()
- void SetEventsCollection (const std::vector< TDCEvent > &v)
- uint32\_t GetLeadingTime (unsigned short event\_id=0)
- uint32\_t GetTrailingTime (unsigned short event\_id=0)
- uint16\_t GetToT (unsigned short event\_id=0)
- uint16\_t GetChannelId (unsigned short event\_id=0)
- uint16\_t GetTDCId ()
- uint16\_t GetEventId ()
- uint16\_t GetBunchId ()
- uint32\_t GetETTT ()
- size\_t NumEvents () const
- size\_t NumErrors () const

## **Private Attributes**

- std::map< TDCEvent::EventType, TDCEvent > fMap
- std::vector< std::pair< TDCEvent, TDCEvent > > fEvents

## 7.27.1 Detailed Description

#### Author:

Laurent Forthomme < laurent.forthomme@cern.ch>

#### Date:

Jun 2015

## 7.27.2 Constructor & Destructor Documentation

## 7.27.2.1 VME::TDCMeasurement::TDCMeasurement() [inline]

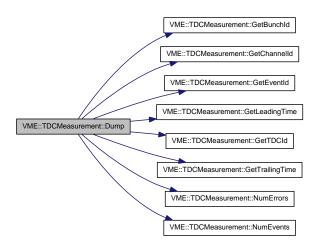
## 7.27.2.2 VME::TDCMeasurement::TDCMeasurement (const std::vector < TDCEvent > & v) [inline]

#### 7.27.2.3 VME::TDCMeasurement::~TDCMeasurement() [inline]

#### 7.27.3 Member Function Documentation

#### 7.27.3.1 void VME::TDCMeasurement::Dump() [inline]

Here is the call graph for this function:



- 7.27.3.2 uint16\_t VME::TDCMeasurement::GetBunchId () [inline]
- 7.27.3.3 uint16\_t VME::TDCMeasurement::GetChannelId (unsigned short event\_id = 0) [inline]
- 7.27.3.4 uint32\_t VME::TDCMeasurement::GetETTT() [inline]
- 7.27.3.5 uint16\_t VME::TDCMeasurement::GetEventId () [inline]
- 7.27.3.6 uint32\_t VME::TDCMeasurement::GetLeadingTime (unsigned short event\_id = 0) [inline]
- 7.27.3.7 uint16\_t VME::TDCMeasurement::GetTDCId() [inline]
- 7.27.3.8 uint16\_t VME::TDCMeasurement::GetToT (unsigned short event\_id = 0) [inline]

- 7.27.3.9 uint32\_t VME::TDCMeasurement::GetTrailingTime (unsigned short event\_id = 0) [inline]
- 7.27.3.10 size\_t VME::TDCMeasurement::NumErrors() const [inline]
- 7.27.3.11 size\_t VME::TDCMeasurement::NumEvents () const [inline]
- 7.27.3.12 void VME::TDCMeasurement::SetEventsCollection (const std::vector< TDCEvent > & v) [inline]

## 7.27.4 Field Documentation

- 7.27.4.1 std::vector< std::pair<TDCEvent,TDCEvent>> VME::TDCMeasurement::fEvents [private]
- 7.27.4.2 std::map<TDCEvent::EventType,TDCEvent> VME::TDCMeasurement::fMap [private]

The documentation for this class was generated from the following file:

• include/VME\_TDCMeasurement.h

## 7.28 VME::TDCV1x90 Class Reference

#include <VME\_TDCV1x90.h>Inheritance diagram for VME::TDCV1x90:Collaboration diagram for VME::TDCV1x90:

## **Public Types**

• enum DLLMode { DLL\_Direct\_LowRes = 0x0, DLL\_PLL\_LowRes = 0x1, DLL\_PLL\_MedRes = 0x2, DLL\_PLL\_HighRes = 0x3 }

#### **Public Member Functions**

- TDCV1x90 (int32\_t bhandle, uint32\_t baseaddr)
- ~TDCV1x90 ()
- void SetVerboseLevel (unsigned short verb=1)
- void SetTestMode (bool en=true) const
- bool GetTestMode () const
- uint32\_t GetModel () const
- uint32\_t GetOUI () const
- uint32 t GetSerialNumber () const
- uint16\_t GetFirmwareRevision () const
- void CheckConfiguration () const
- void EnableChannel (short) const
- void DisableChannel (short) const
- void SetPoI (uint16\_t word1, uint16\_t word2) const
- std::map< unsigned short, bool > GetPoI () const
- void SetLSBTraileadEdge (trailead\_edge\_lsb) const
- void SetAcquisitionMode (const AcquisitionMode &)
- AcquisitionMode GetAcquisitionMode ()
- void SetTriggerMatching ()
- void SetContinuousStorage ()
- void SetDetectionMode (const DetectionMode &detm)
- DetectionMode GetDetectionMode ()
- void SetDLLClock (const DLLMode &dll) const
- DLLMode GetDLLClock () const
- void SetGlobalOffset (const GlobalOffset &) const
- GlobalOffset GetGlobalOffset () const
- void SetRCAdjust (int, uint16\_t) const
- uint16\_t GetRCAdjust (int) const
- uint32\_t GetEventCounter () const

Number of occured triggers.

• uint16\_t GetEventStored () const

Number of events currently stored in the output buffer.

- void SetTDCEncapsulation (bool) const
- bool GetTDCEncapsulation () const
- void SetErrorMarks (bool mode=true)
- bool GetErrorMarks () const
- void SetPairModeResolution (int, int) const
- uint16 t GetResolution () const
- void SetBLTEventNumberRegister (const uint16\_t &) const
- uint16 t GetBLTEventNumberRegister () const
- void SetWindowWidth (const uint16\_t &)
- uint16\_t GetWindowWidth () const
- void SetWindowOffset (const int16\_t &) const
- int16\_t GetWindowOffset () const
- uint16\_t GetTriggerConfiguration (const trig\_conf &) const
- bool SoftwareClear () const
- bool SoftwareReset () const
- bool HardwareReset () const
- void SetETTT (bool ettt=true) const
- bool GetETTT () const
- void SetStatus (const TDCV1x90Status &) const
- TDCV1x90Status GetStatus () const
- void SetControl (const TDCV1x90Control &) const
- TDCV1x90Control GetControl () const
- TDCEventCollection FetchEvents ()
- void SetChannelDeadTime (unsigned short dt) const
- unsigned short GetChannelDeadTime () const
- void SetFIFOSize (const uint16\_t &) const
- uint16\_t GetFIFOSize () const
- void abort ()

### **Private Member Functions**

- bool WaitMicro (const micro\_handshake &mode) const
- void ReadAcquisitionMode ()
- void ReadDetectionMode ()

#### **Private Attributes**

- unsigned short fVerb
- AcquisitionMode fAcquisitionMode
- DetectionMode fDetectionMode
- bool fErrorMarks
- uint16\_t fWindowWidth
- uint32 t \* fBuffer
- uint32\_t nchannels
- bool gEnd
- std::string pair\_lead\_res [8]
- std::string pair\_width\_res [16]

## 7.28.1 Detailed Description

#### **Author:**

Laurent Forthomme < laurent.forthomme@cern.ch>
Bob Velghe < bob.velghe@cern.ch>

#### Date:

Jun 2010 (NA62-Gigatracker) May 2015 (CMS-TOTEM PPS)

## 7.28.2 Member Enumeration Documentation

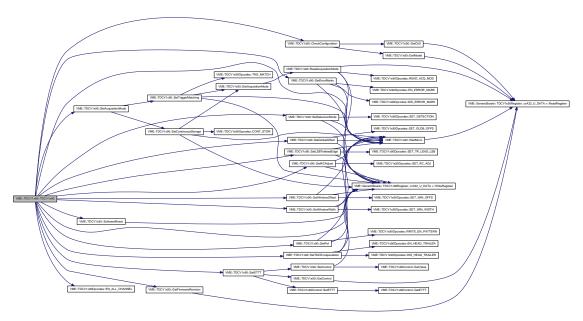
## 7.28.2.1 enum VME::TDCV1x90::DLLMode

#### **Enumerator:**

DLL\_Direct\_LowRes
DLL\_PLL\_LowRes
DLL\_PLL\_MedRes
DLL\_PLL\_HighRes

### 7.28.3 Constructor & Destructor Documentation

## 7.28.3.1 VME::TDCV1x90::TDCV1x90 (int32\_t bhandle, uint32\_t baseaddr)



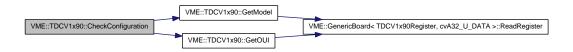
#### 7.28.3.2 VME::TDCV1x90::~TDCV1x90()

## 7.28.4 Member Function Documentation

## 7.28.4.1 void VME::TDCV1x90::abort ()

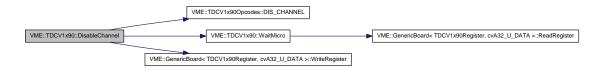
## 7.28.4.2 void VME::TDCV1x90::CheckConfiguration () const

Here is the call graph for this function:



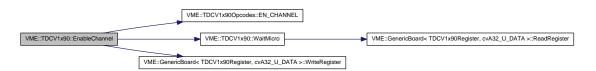
#### 7.28.4.3 void VME::TDCV1x90::DisableChannel (short channel\_id) const

Here is the call graph for this function:



### 7.28.4.4 void VME::TDCV1x90::EnableChannel (short channel\_id) const

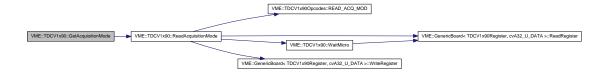
Here is the call graph for this function:



## 7.28.4.5 TDCEventCollection VME::TDCV1x90::FetchEvents ()

## 7.28.4.6 AcquisitionMode VME::TDCV1x90::GetAcquisitionMode () [inline]

Here is the call graph for this function:



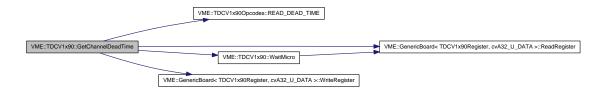
## 7.28.4.7 uint16\_t VME::TDCV1x90::GetBLTEventNumberRegister () const

Here is the call graph for this function:



## 7.28.4.8 unsigned short VME::TDCV1x90::GetChannelDeadTime () const

Here is the call graph for this function:



## 7.28.4.9 TDCV1x90Control VME::TDCV1x90::GetControl () const



## 7.28.4.10 DetectionMode VME::TDCV1x90::GetDetectionMode () [inline]

Here is the call graph for this function:

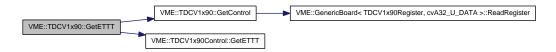


### 7.28.4.11 DLLMode VME::TDCV1x90::GetDLLClock () const

## 7.28.4.12 bool VME::TDCV1x90::GetErrorMarks() const [inline]

## 7.28.4.13 bool VME::TDCV1x90::GetETTT() const [inline]

Here is the call graph for this function:



#### 7.28.4.14 uint32\_t VME::TDCV1x90::GetEventCounter () const

Number of occured triggers. Number of acquired events since the latest module's reset/clear; this counter works in trigger Matching Mode only.

Here is the call graph for this function:



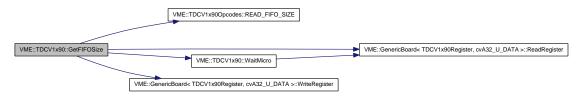
## 7.28.4.15 uint16\_t VME::TDCV1x90::GetEventStored () const

Number of events currently stored in the output buffer.



## $\textbf{7.28.4.16} \quad uint16\_t \ VME::TDCV1x90::GetFIFOSize \ () \ const$

Here is the call graph for this function:



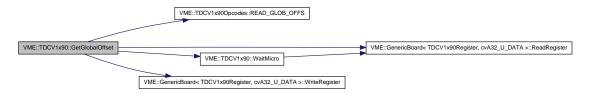
## 7.28.4.17 uint16\_t VME::TDCV1x90::GetFirmwareRevision () const

Here is the call graph for this function:



#### 7.28.4.18 GlobalOffset VME::TDCV1x90::GetGlobalOffset () const

Here is the call graph for this function:

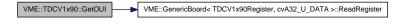


## 7.28.4.19 uint32\_t VME::TDCV1x90::GetModel () const

Here is the call graph for this function:

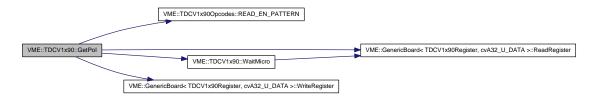


#### 7.28.4.20 uint32\_t VME::TDCV1x90::GetOUI () const



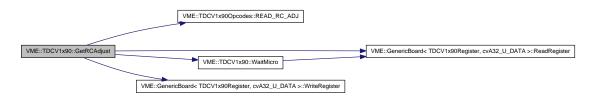
#### 7.28.4.21 std::map< unsigned short, bool > VME::TDCV1x90::GetPoI () const

Here is the call graph for this function:



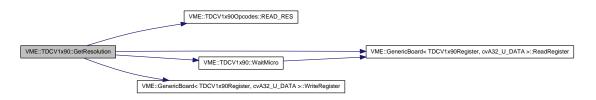
## 7.28.4.22 uint16\_t VME::TDCV1x90::GetRCAdjust (int tdc) const

Here is the call graph for this function:



## 7.28.4.23 uint16\_t VME::TDCV1x90::GetResolution () const

Here is the call graph for this function:



## 7.28.4.24 uint32\_t VME::TDCV1x90::GetSerialNumber () const



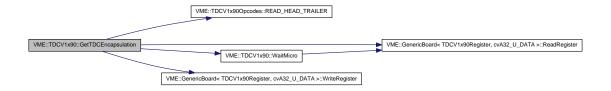
#### 7.28.4.25 TDCV1x90Status VME::TDCV1x90::GetStatus () const

Here is the call graph for this function:



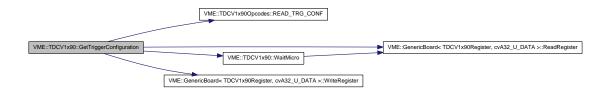
## $\textbf{7.28.4.26} \quad bool~VME::TDCV1x90::GetTDCEncapsulation~()~const$

Here is the call graph for this function:



## 7.28.4.27 bool VME::TDCV1x90::GetTestMode () const

# 7.28.4.28 uint16\_t VME::TDCV1x90::GetTriggerConfiguration (const trig\_conf & type) const



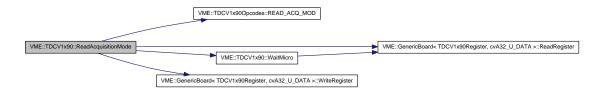
### 7.28.4.29 int16\_t VME::TDCV1x90::GetWindowOffset () const

## 7.28.4.30 uint16\_t VME::TDCV1x90::GetWindowWidth() const [inline]

#### 7.28.4.31 bool VME::TDCV1x90::HardwareReset () const

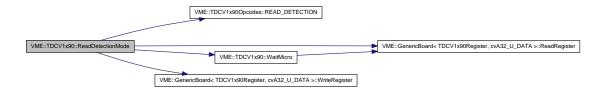
## 7.28.4.32 void VME::TDCV1x90::ReadAcquisitionMode() [private]

Here is the call graph for this function:



## 7.28.4.33 void VME::TDCV1x90::ReadDetectionMode() [private]

Here is the call graph for this function:



# 7.28.4.34 void VME::TDCV1x90::SetAcquisitionMode (const AcquisitionMode & mode)



# 7.28.4.35 void VME::TDCV1x90::SetBLTEventNumberRegister (const uint16\_t & value) const

Here is the call graph for this function:



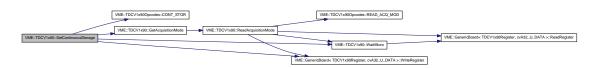
## 7.28.4.36 void VME::TDCV1x90::SetChannelDeadTime (unsigned short dt) const

Here is the call graph for this function:

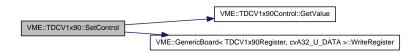


## 7.28.4.37 void VME::TDCV1x90::SetContinuousStorage ()

Here is the call graph for this function:



# 7.28.4.38 void VME::TDCV1x90::SetControl (const TDCV1x90Control & control) const



## 7.28.4.39 void VME::TDCV1x90::SetDetectionMode (const DetectionMode & detm)

Here is the call graph for this function:



#### 7.28.4.40 void VME::TDCV1x90::SetDLLClock (const DLLMode & dll) const

Here is the call graph for this function:



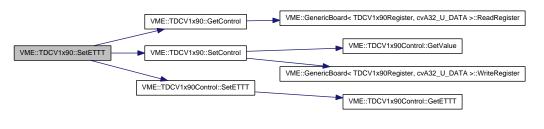
#### 7.28.4.41 void VME::TDCV1x90::SetErrorMarks (bool mode = true)

Here is the call graph for this function:



## 7.28.4.42 void VME::TDCV1x90::SetETTT (bool ettt = true) const [inline]

Here is the call graph for this function:



Generated on Wed Jul 29 19:40:20 2015 for 2015 Test beam Run Control by Doxygen

#### 7.28.4.43 void VME::TDCV1x90::SetFIFOSize (const uint16\_t & size) const

Here is the call graph for this function:



## 7.28.4.44 void VME::TDCV1x90::SetGlobalOffset (const GlobalOffset & offs) const

Here is the call graph for this function:



# 7.28.4.45 void VME::TDCV1x90::SetLSBTraileadEdge (trailead\_edge\_lsb conf) const

Here is the call graph for this function:

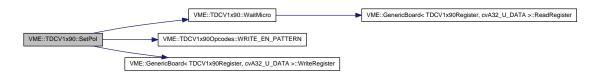


## 7.28.4.46 void VME::TDCV1x90::SetPairModeResolution (int *lead\_time\_res*, int *pulse\_width\_res*) const



## 7.28.4.47 void VME::TDCV1x90::SetPoI (uint16\_t word1, uint16\_t word2) const

Here is the call graph for this function:



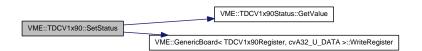
## 7.28.4.48 void VME::TDCV1x90::SetRCAdjust (int tdc, uint16\_t value) const

Here is the call graph for this function:



## 7.28.4.49 void VME::TDCV1x90::SetStatus (const TDCV1x90Status & status) const

Here is the call graph for this function:

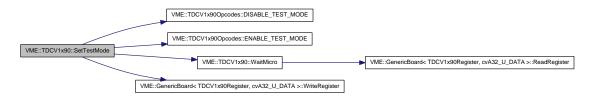


### 7.28.4.50 void VME::TDCV1x90::SetTDCEncapsulation (bool mode) const



#### 7.28.4.51 void VME::TDCV1x90::SetTestMode (bool en = true) const

Here is the call graph for this function:



### 7.28.4.52 void VME::TDCV1x90::SetTriggerMatching ()

Here is the call graph for this function:



# 7.28.4.53 void VME::TDCV1x90::SetVerboseLevel (unsigned short *verb* = 1) [inline]

## 7.28.4.54 void VME::TDCV1x90::SetWindowOffset (const int16\_t & offs) const

Set the offset of the match window with respect to the trigger itself, i.e. the time difference (expressed in clock cycles) between the start of the match window and the trigger time

#### **Parameters:**

← Window offset, in units of clock cycles



#### 7.28.4.55 void VME::TDCV1x90::SetWindowWidth (const uint16\_t & width)

Set the width of the match window (in number of clock cycles)

#### **Parameters:**

← Window width, in units of clock cycles

Here is the call graph for this function:



## 7.28.4.56 bool VME::TDCV1x90::SoftwareClear () const

Here is the call graph for this function:



### 7.28.4.57 bool VME::TDCV1x90::SoftwareReset () const

Here is the call graph for this function:



# 7.28.4.58 bool VME::TDCV1x90::WaitMicro (const micro\_handshake & mode) const [private]



#### 7.28.5 Field Documentation

- 7.28.5.1 AcquisitionMode VME::TDCV1x90::fAcquisitionMode [private]
- 7.28.5.2 uint32\_t\* VME::TDCV1x90::fBuffer [private]
- 7.28.5.3 DetectionMode VME::TDCV1x90::fDetectionMode [private]
- 7.28.5.4 bool VME::TDCV1x90::fErrorMarks [private]
- 7.28.5.5 unsigned short VME::TDCV1x90::fVerb [private]
- 7.28.5.6 uint16\_t VME::TDCV1x90::fWindowWidth [private]
- 7.28.5.7 bool VME::TDCV1x90::gEnd [private]
- 7.28.5.8 uint32\_t VME::TDCV1x90::nchannels [private]
- 7.28.5.9 std::string VME::TDCV1x90::pair\_lead\_res[8] [private]
- 7.28.5.10 std::string VME::TDCV1x90::pair\_width\_res[16] [private]

The documentation for this class was generated from the following files:

- include/VME\_TDCV1x90.h
- src/VME\_TDCV1x90.cpp

## 7.29 VME::TDCV1x90Control Class Reference

#### TDC control register.

#include <VME\_TDCV1x90.h>

#### **Public Member Functions**

- TDCV1x90Control (const uint16\_t &word)
- virtual ~TDCV1x90Control ()
- void Dump () const
- uint16\_t GetValue () const
- bool GetBusError () const
- void SetBusError (bool sw)
- bool GetTermination () const
- void SetTermination (bool sw)
- bool GetSWTermination () const
- void SetSWTermination (bool sw)
- bool GetEmptyEvent () const
- void SetEmptyEvent (bool sw)
- bool GetAlign64 () const
- void SetAlign64 (bool sw)
- bool GetCompensation () const
- void SetCompensation (bool sw)
- bool GetTestFIFO () const
- void SetTestFIFO (bool sw)
- bool GetSRAMCompensation () const
- void SetSRAMCompensation (bool sw)
- bool GetEventFIFO () const
- void SetEventFIFO (bool sw)
- bool GetETTT () const
- void SetETTT (bool sw)
- bool GetMEBAccess () const
- void SetMEBAccess (bool sw)

### **Private Attributes**

• uint16\_t fWord

## 7.29.1 Detailed Description

TDC control register.

#### **Author:**

Laurent Forthomme < laurent .forthomme@cern.ch>

#### Date:

Jun 2015

#### 7.29.2 Constructor & Destructor Documentation

- 7.29.2.1 VME::TDCV1x90Control::TDCV1x90Control (const uint16\_t & word)
  [inline]
- 7.29.2.2 virtual VME::TDCV1x90Control::~TDCV1x90Control() [inline, virtual]

#### 7.29.3 Member Function Documentation

7.29.3.1 void VME::TDCV1x90Control::Dump () const [inline]

Here is the call graph for this function:

- 7.29.3.2 bool VME::TDCV1x90Control::GetAlign64() const [inline]
- 7.29.3.3 bool VME::TDCV1x90Control::GetBusError() const [inline]
- 7.29.3.4 bool VME::TDCV1x90Control::GetCompensation () const [inline]
- 7.29.3.5 bool VME::TDCV1x90Control::GetEmptyEvent() const [inline]
- 7.29.3.6 bool VME::TDCV1x90Control::GetETTT() const [inline]
- 7.29.3.7 bool VME::TDCV1x90Control::GetEventFIFO () const [inline]
- 7.29.3.8 bool VME::TDCV1x90Control::GetMEBAccess () const [inline]
- 7.29.3.9 bool VME::TDCV1x90Control::GetSRAMCompensation () const [inline]
- 7.29.3.10 bool VME::TDCV1x90Control::GetSWTermination () const [inline]
- 7.29.3.11 bool VME::TDCV1x90Control::GetTermination() const [inline]
- 7.29.3.12 bool VME::TDCV1x90Control::GetTestFIFO () const [inline]
- 7.29.3.13 uint16\_t VME::TDCV1x90Control::GetValue() const [inline]
- 7.29.3.14 void VME::TDCV1x90Control::SetAlign64 (bool sw) [inline]

## 7.29.3.15 void VME::TDCV1x90Control::SetBusError (bool sw) [inline]

Here is the call graph for this function:

## 7.29.3.16 void VME::TDCV1x90Control::SetCompensation (bool sw) [inline]

Here is the call graph for this function:

## 7.29.3.17 void VME::TDCV1x90Control::SetEmptyEvent (bool sw) [inline]

Here is the call graph for this function:

#### 7.29.3.18 void VME::TDCV1x90Control::SetETTT (bool sw) [inline]

Here is the call graph for this function:

#### 7.29.3.19 void VME::TDCV1x90Control::SetEventFIFO (bool sw) [inline]

Here is the call graph for this function:

# 7.29.3.20 void VME::TDCV1x90Control::SetMEBAccess (bool sw) [inline]

Here is the call graph for this function:

## 7.29.3.21 void VME::TDCV1x90Control::SetSRAMCompensation (bool sw) [inline]

Here is the call graph for this function:

# 7.29.3.22 void VME::TDCV1x90Control::SetSWTermination (bool sw) [inline]

Here is the call graph for this function:

## 7.29.3.23 void VME::TDCV1x90Control::SetTermination (bool sw) [inline]

## 7.29.3.24 void VME::TDCV1x90Control::SetTestFIFO (bool sw) [inline]

Here is the call graph for this function:

## 7.29.4 Field Documentation

## 7.29.4.1 uint16\_t VME::TDCV1x90Control::fWord [private]

The documentation for this class was generated from the following file:

• include/VME\_TDCV1x90.h

## 7.30 VME::TDCV1x90Status Class Reference

### TDC status register.

```
#include <VME_TDCV1x90.h>
```

## **Public Types**

• enum TDCResolution { R\_800ps = 0x0, R\_200ps = 0x1, R\_100ps = 0x2, R\_25ps = 0x3 }

#### **Public Member Functions**

- TDCV1x90Status (const uint16\_t &word)
- virtual ~TDCV1x90Status ()
- void Dump () const
- uint16\_t GetValue () const
- bool DataReady () const
- bool AlmostFull () const
- bool Full () const
- bool TriggerMatching () const
- bool HeadersEnabled () const
- bool TerminationOn () const
- bool Error (const unsigned int &id) const
- bool Error () const
- bool BusError () const
- bool Purged () const
- TDCResolution Resolution () const
- bool PairMode () const
- bool TriggerLost () const

#### **Private Attributes**

• uint16\_t fWord

## 7.30.1 Detailed Description

TDC status register.

### **Author:**

Laurent Forthomme < laurent .forthomme@cern.ch>

#### Date:

Jun 2015

#### 7.30.2 Member Enumeration Documentation

7.30.2.1 enum VME::TDCV1x90Status::TDCResolution

#### **Enumerator:**

- R\_800ps
- R\_200ps
- R\_100ps
- $R_25ps$

#### 7.30.3 Constructor & Destructor Documentation

- 7.30.3.1 VME::TDCV1x90Status::TDCV1x90Status (const uint16\_t & word) [inline]
- 7.30.3.2 virtual VME::TDCV1x90Status::~TDCV1x90Status() [inline, virtual]

## 7.30.4 Member Function Documentation

- 7.30.4.1 bool VME::TDCV1x90Status::AlmostFull() const [inline]
- 7.30.4.2 bool VME::TDCV1x90Status::BusError() const [inline]
- 7.30.4.3 bool VME::TDCV1x90Status::DataReady () const [inline]
- 7.30.4.4 void VME::TDCV1x90Status::Dump () const [inline]

Here is the call graph for this function:

#### 7.30.4.5 bool VME::TDCV1x90Status::Error() const [inline]

- 7.30.4.6 bool VME::TDCV1x90Status::Error (const unsigned int & id) const [inline]
- 7.30.4.7 bool VME::TDCV1x90Status::Full() const [inline]
- 7.30.4.8 uint16\_t VME::TDCV1x90Status::GetValue() const [inline]
- 7.30.4.9 bool VME::TDCV1x90Status::HeadersEnabled() const [inline]
- 7.30.4.10 bool VME::TDCV1x90Status::PairMode() const [inline]
- 7.30.4.11 bool VME::TDCV1x90Status::Purged () const [inline]
- 7.30.4.12 TDCResolution VME::TDCV1x90Status::Resolution () const [inline]
- 7.30.4.13 bool VME::TDCV1x90Status::TerminationOn() const [inline]
- 7.30.4.14 bool VME::TDCV1x90Status::TriggerLost () const [inline]
- 7.30.4.15 bool VME::TDCV1x90Status::TriggerMatching () const [inline]

#### 7.30.5 Field Documentation

#### 7.30.5.1 uint16\_t VME::TDCV1x90Status::fWord [private]

The documentation for this class was generated from the following file:

• include/VME\_TDCV1x90.h

## 7.31 VME::trailead\_t Struct Reference

#include <VME TDCV1x90.h>

## **Data Fields**

- uint32\_t event\_count
- int total\_hits [16]
- std::multimap< int32\_t, int32\_t > leading
- std::multimap< int32\_t, int32\_t > trailing
- uint32\_t ettt

## 7.31.1 Field Documentation

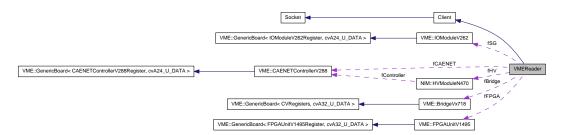
- 7.31.1.1 uint32\_t VME::trailead\_t::ettt
- 7.31.1.2 uint32\_t VME::trailead\_t::event\_count
- 7.31.1.3 std::multimap<int32\_t,int32\_t> VME::trailead\_t::leading
- 7.31.1.4 int VME::trailead\_t::total\_hits[16]
- 7.31.1.5 std::multimap<int32\_t,int32\_t> VME::trailead\_t::trailing

The documentation for this struct was generated from the following file:

• include/VME\_TDCV1x90.h

## 7.32 VMEReader Class Reference

 $\verb|#include| < \verb|VMEReader.h| > Inheritance diagram for VMEReader: Collaboration diagram for VMEReader:$ 



#### **Public Member Functions**

- VMEReader (const char \*device, VME::BridgeType type, bool on\_socket=true)
- virtual ~VMEReader ()
- void ReadXML (const char \*filename)

Load an XML configuration file.

- void ReadXML (std::string filename)
- void AddTDC (uint32\_t address)

Add a TDC to handle.

VME::TDCV1x90 \* GetTDC (uint32\_t address)

Get a TDC on the VME bus Return a pointer to the TDC object, given its physical address on the VME bus.

- size\_t GetNumTDC () const
- VME::TDCCollection GetTDCCollection ()
- void AddIOModule (uint32\_t address)
- VME::IOModuleV262 \* GetIOModule ()
- void AddCFD (uint32 t address)

Add a CFD to handle.

• VME::CFDV812 \* GetCFD (uint32\_t address)

Get a CFD on the VME bus Return a pointer to the CFD object, given its physical address on the VME bus.

- size\_t GetNumCFD () const
- VME::CFDCollection GetCFDCollection ()
- void AddFPGAUnit (uint32\_t address)

Add a multi-purposes FPGA board (CAEN V1495) to the crate controller.

VME::FPGAUnitV1495 \* GetFPGAUnit ()

Return the pointer to the FPGA board connected to this controller (if any; 0 otherwise).

• unsigned int GetRunNumber ()

Ask the socket master a run number.

- void StartPulser (double period, double width, unsigned int num\_pulses=0) Start the bridge's pulse generator [faulty].
- void StopPulser ()

Stop the bridge's pulse generator [faulty].

- void SendPulse (unsigned short output=0) const
   Send a single pulse to the output register/plug connected to TDC boards.
- void SendClear () const

Send a clear signal to both the TDC boards.

- void AddHVModule (uint32\_t vme\_address, uint16\_t nim\_address)
   Add a high voltage module (controlled by a VME-CAENET controller) to the DAQ.
- NIM::HVModuleN470 \* GetHVModule ()

Retrieve the NIM high voltage module.

- void SetOutputFile (uint32\_t tdc\_address, std::string filename)

  Set the path to the output file where the DAQ is to write.
- std::string GetOutputFile (uint32\_t tdc\_address)

  Return the path to the output file the DAQ is currently writing to.
- bool UseSocket () const
- void Abort ()

Abort data collection for all modules on the bus handled by the bridge.

## **Private Types**

• typedef std::map< uint32 t, std::string > OutputFiles

## **Private Attributes**

- VME::BridgeVx718 \* fBridge

  The VME bridge object to handle.
- VME::TDCCollection fTDCCollection

A set of pointers to TDC objects indexed by their physical VME address.

• VME::CFDCollection fCFDCollection

A set of pointers to CFD objects indexed by their physical VME address.

• VME::IOModuleV262 \* fSG

Pointer to the VME input/output module object.

• VME::FPGAUnitV1495 \* fFPGA

Pointer to the VME general purpose FPGA unit object.

• VME::CAENETControllerV288 \* fCAENET

Pointer to the VME CAENET controller.

• NIM::HVModuleN470 \* fHV

Pointer to the NIM high voltage module (passing through the CAENET controller).

• bool fOnSocket

Are we dealing with socket message passing?

• bool fIsPulserStarted

Is the bridge's pulser already started?

• OutputFiles fOutputFiles

## 7.32.1 Detailed Description

VME reader object to fetch events on a HPTDC board

## **Author:**

Laurent Forthomme < laurent . forthomme@cern.ch>

## Date:

4 May 2015

## **7.32.2** Member Typedef Documentation

## 7.32.2.1 typedef std::map<uint32\_t, std::string> VMEReader::OutputFiles [private]

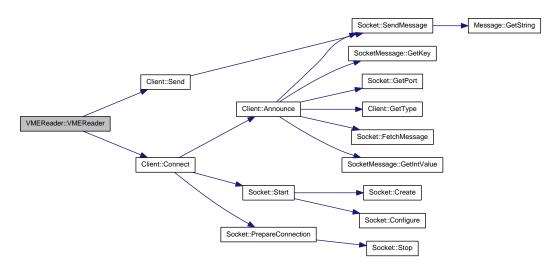
## 7.32.3 Constructor & Destructor Documentation

## 7.32.3.1 VMEReader::VMEReader (const char \* device, VME::BridgeType type, bool on\_socket = true)

#### **Parameters:**

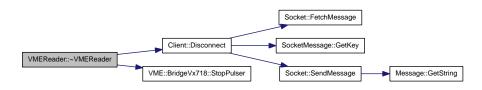
- $\leftarrow$  *device* Path to the device (/dev/xxx)
- ← *type* Bridge model
- ← on\_socket Are we trying to connect through the socket?

Here is the call graph for this function:



## 7.32.3.2 VMEReader::~VMEReader() [virtual]

Here is the call graph for this function:



## 7.32.4 Member Function Documentation

## 7.32.4.1 void VMEReader::Abort ()

Abort data collection for all modules on the bus handled by the bridge.

Here is the call graph for this function:



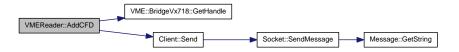
## 7.32.4.2 void VMEReader::AddCFD (uint32\_t address)

Add a CFD to handle.

#### **Parameters:**

← address 32-bit address of the CFD module on the VME bus Create a new CFD handler for the VME bus

Here is the call graph for this function:



## 7.32.4.3 void VMEReader::AddFPGAUnit (uint32\_t address)

Add a multi-purposes FPGA board (CAEN V1495) to the crate controller.

#### **Parameters:**

← address 32-bit address of the TDC module on the VME bus

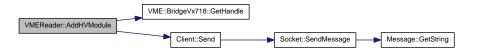
Here is the call graph for this function:



## 7.32.4.4 void VMEReader::AddHVModule (uint32\_t vme\_address, uint16\_t nim\_address)

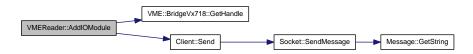
Add a high voltage module (controlled by a VME-CAENET controller) to the DAQ.

Here is the call graph for this function:



## 7.32.4.5 void VMEReader::AddIOModule (uint32\_t address)

Here is the call graph for this function:



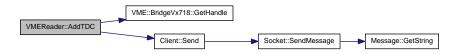
## 7.32.4.6 void VMEReader::AddTDC (uint32\_t address)

Add a TDC to handle.

#### **Parameters:**

← address 32-bit address of the TDC module on the VME bus Create a new TDC handler for the VME bus

Here is the call graph for this function:



## 7.32.4.7 VME::CFDV812\* VMEReader::GetCFD (uint32\_t address) [inline]

Get a CFD on the VME bus Return a pointer to the CFD object, given its physical address on the VME bus.

## 7.32.4.8 VME::CFDCollection VMEReader::GetCFDCollection () [inline]

#### 7.32.4.9 VME::FPGAUnitV1495\* VMEReader::GetFPGAUnit() [inline]

Return the pointer to the FPGA board connected to this controller (if any; 0 otherwise).

#### 7.32.4.10 NIM::HVModuleN470\* VMEReader::GetHVModule() [inline]

Retrieve the NIM high voltage module.

- 7.32.4.11 VME::IOModuleV262\* VMEReader::GetIOModule() [inline]
- 7.32.4.12 size\_t VMEReader::GetNumCFD() const [inline]
- 7.32.4.13 size t VMEReader::GetNumTDC () const [inline]

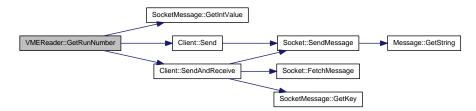
## 7.32.4.14 std::string VMEReader::GetOutputFile (uint32\_t tdc\_address) [inline]

Return the path to the output file the DAQ is currently writing to.

#### 7.32.4.15 unsigned int VMEReader::GetRunNumber ()

Ask the socket master a run number.

Here is the call graph for this function:



## 7.32.4.16 VME::TDCV1x90\* VMEReader::GetTDC (uint32\_t address) [inline]

Get a TDC on the VME bus Return a pointer to the TDC object, given its physical address on the VME bus.

## 7.32.4.17 VME::TDCCollection VMEReader::GetTDCCollection () [inline]

## 7.32.4.18 void VMEReader::ReadXML (std::string filename) [inline]

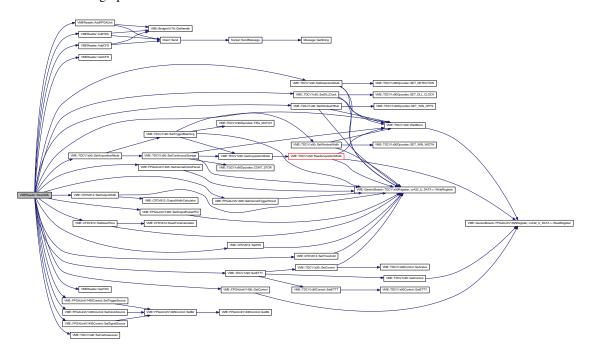
Here is the call graph for this function:



## 7.32.4.19 void VMEReader::ReadXML (const char \* filename)

Load an XML configuration file.

Here is the call graph for this function:



## 7.32.4.20 void VMEReader::SendClear () const [inline]

Send a clear signal to both the TDC boards.

Here is the call graph for this function:



## 7.32.4.21 void VMEReader::SendPulse (unsigned short *output* = 0) const [inline]

Send a single pulse to the output register/plug connected to TDC boards.

Here is the call graph for this function:



## 7.32.4.22 void VMEReader::SetOutputFile (uint32\_t tdc\_address, std::string filename)

Set the path to the output file where the DAQ is to write.

Here is the call graph for this function:



## 7.32.4.23 void VMEReader::StartPulser (double *period*, double *width*, unsigned int *num\_pulses* = 0) [inline]

Start the bridge's pulse generator [faulty].

Here is the call graph for this function:



## 7.32.4.24 void VMEReader::StopPulser() [inline]

Stop the bridge's pulse generator [faulty].

Here is the call graph for this function:

## 7.32.4.25 bool VMEReader::UseSocket () const [inline]

## 7.32.5 Field Documentation

## 7.32.5.1 VME::BridgeVx718\* VMEReader::fBridge [private]

The VME bridge object to handle.

## 7.32.5.2 VME::CAENETControllerV288\* VMEReader::fCAENET [private]

Pointer to the VME CAENET controller.

## 7.32.5.3 VME::CFDCollection VMEReader::fCFDCollection [private]

A set of pointers to CFD objects indexed by their physical VME address.

## 7.32.5.4 VME::FPGAUnitV1495\* VMEReader::fFPGA [private]

Pointer to the VME general purpose FPGA unit object.

## 7.32.5.5 NIM::HVModuleN470\* VMEReader::fHV [private]

Pointer to the NIM high voltage module (passing through the CAENET controller).

## 7.32.5.6 bool VMEReader::fIsPulserStarted [private]

Is the bridge's pulser already started?

## 7.32.5.7 bool VMEReader::fOnSocket [private]

Are we dealing with socket message passing?

## 7.32.5.8 OutputFiles VMEReader::fOutputFiles [private]

Path to the current output files the DAQ is writing to (indexed by the TDC id)

## 7.32.5.9 VME::IOModuleV262\* VMEReader::fSG [private]

Pointer to the VME input/output module object.

## 7.32.5.10 VME::TDCCollection VMEReader::fTDCCollection [private]

A set of pointers to TDC objects indexed by their physical VME address.

The documentation for this class was generated from the following files:

- include/VMEReader.h
- src/VMEReader.cpp

# Index

∼BridgeVx718	~Socket
VME::BridgeVx718, 27	Socket, 106
~BridgeVx718Control	~SocketMessage
VME::BridgeVx718Control, 30	SocketMessage, 113
~BridgeVx718Status	~TDCErrorFlag
VME::BridgeVx718Status, 33	VME::TDCErrorFlag, 117
~CAENETControllerV288	~TDCEvent
VME::CAENETControllerV288, 36	VME::TDCEvent, 121
~CAENETControllerV288Status	~TDCMeasurement
VME::CAENETControllerV288Status	, VME::TDCMeasurement, 124
39	~TDCV1x90
∼CFDV812	VME::TDCV1x90, 129
VME::CFDV812, 41	~TDCV1x90Control
~ChannelStatus	VME::TDCV1x90Control, 145
NIM::HVModuleN470Values::Channe	lSancV1x90Status
48	VME::TDCV1x90Status, 149
~Client	$\sim$ VMEReader
Client, 51	VMEReader, 155
~FPGAUnitV1495	
VME::FPGAUnitV1495, 62	Abort
~FPGAUnitV1495Control	VMEReader, 156
VME::FPGAUnitV1495Control, 71	abort
$\sim$ FileReader	VME::TDCV1x90, 130
FileReader, 58	AcceptConnections
~GenericBoard	Socket, 106
VME::GenericBoard, 75	acq_mode
$\sim$ HVModuleN470	file_header_t, 56
NIM::HVModuleN470, 81	AcquisitionMode
~HVModuleN470ChannelValues	VME, 16
NIM::HVModuleN470ChannelValues,	AddCFD
86	VMEReader, 156
~HVModuleN470Values	AddClient
NIM::HVModuleN470Values, 89	Messenger, 97
~IOModuleV262	AddFPGAUnit
VME::IOModuleV262, 90	VMEReader, 156
$\sim$ Message	AddHVModule
Message, 93	VMEReader, 156
$\sim$ Messenger	AddIOModule
Messenger, 96	VMEReader, 157
~PCIInterfaceA2818	AddTDC
VME::PCIInterfaceA2818, 103	VMEReader, 157

Alarm	NIM::HVModuleN470Values::ChannelStatus,
NIM::HVModuleN470Values::Channe	elStatus, 48
48	CheckBoardVersion
AlmostFull	VME::FPGAUnitV1495, 62
VME::TDCV1x90Status, 149	CheckConfiguration
Announce	VME::BridgeVx718, 27
Client, 51	VME::CFDV812, 41
AUTOLOAD_DEF_CONFI	VME::TDCV1x90, 130
	CheckPCIInterface
VME::TDCV1x90Opcodes, 24	VME::BridgeVx718, 27
AUTOLOAD_USER_CONF	Clear
VME::TDCV1x90Opcodes, 24	
	FileReader, 58
Bind	CLEAR_KEEP_TOKEN
Socket, 106	VME::TDCV1x90Opcodes, 24
BridgeType	ClearOutputPulser
VME, 16	VME::FPGAUnitV1495, 62
BridgeVx718	CLIENT
_	Socket, 106
VME::BridgeVx718, 27	Client, 50
BridgeVx718Control	~Client, 51
VME::BridgeVx718Control, 30	Announce, 51
BridgeVx718Status	Client, 51
VME::BridgeVx718Status, 33	
Broadcast	Connect, 52
Messenger, 97	Disconnect, 52
BusError	fClientId, 55
VME::TDCV1x90Status, 149	fIsConnected, 55
VIVIE IDC VIX90Status, 149	fType, 55
	GetType, 53
CAEN_V1718	ParseMessage, 53
VME, 16	Receive, 53
CAEN_V2718	Send, 54
VME, 16	SendAndReceive, 54
CAENETControllerV288	ClockSource
VME::CAENETControllerV288, 36	
CAENETControllerV288Answer	VME::FPGAUnitV1495Control, 70
VME, 16	cnBusy
	VME, 16
CAENETControllerV288Register	cnIncorrectHCC
VME, 16	VME, 16
CAENETControllerV288Status	cnIncorrectValue
VME::CAENETControllerV288Status	, VME, 16
39	cnNoData
CFDCollection	VME, 16
VME, 15	cnSuccess
CFDV812	VME, 16
VME::CFDV812, 41	cnUnrecognizedCode
CFDV812Register	VME, 16
VME, 16	cnWrongModuleAddress
ChannelStatus	VME, 16
NIM::HVModuleN470ChannelValues,	
86	VME::GlobalOffset, 77

Configure	VME::TDCV1x90, 129
Socket, 107	DLL_PLL_LowRes
Connect	VME::TDCV1x90, 129
Client, 52	DLL_PLL_MedRes
Messenger, 98	VME::TDCV1x90, 129
CONT_STORAGE	DLLMode
VME, 16	VME::TDCV1x90, 129
CONT_STOR	DQM
VME::TDCV1x90Opcodes, 24	Socket, 106
Create	Dump
Socket, 107	FileReader, 58
DataReady	HTTPMessage, 79
VME::TDCV1x90Status, 149	Message, 93
DeadTimeCalculator	NIM::HVModuleN470ChannelValues,
	86
VME::CFDV812, 41	NIM::HVModuleN470Values, 89
Decode	NIM::HVModuleN470Values::ChannelStatus,
HTTPMessage, 79	48
DEFAULT_SETUP_REG	SocketMessage, 113
VME::TDCV1x90Opcodes, 24	VME::BridgeVx718Status, 33
det_mode	VME::FPGAUnitV1495Control, 71
file_header_t, 56	VME::TDCErrorFlag, 117
DetectionMode	VME::TDCEvent, 121
VME, 17	VME::TDCMeasurement, 125
DETECTOR	VME::TDCV1x90Control, 145
Socket, 106	VME::TDCV1x90Status, 149
DIS_ALL_CHANNEL	DumpConnected
VME::TDCV1x90Opcodes, 24	Socket, 107
DIS_CHANNEL	DumpFWInformation
VME::TDCV1x90Opcodes, 24	VME::FPGAUnitV1495, 62
DIS_ERROR_BYPASS	
VME::TDCV1x90Opcodes, 24	EN_ALL_CHANNEL
DIS_ERROR_MARK	VME::TDCV1x90Opcodes, 24
VME::TDCV1x90Opcodes, 24	EN_CHANNEL
DIS_HEAD_TRAILER	VME::TDCV1x90Opcodes, 24
VME::TDCV1x90Opcodes, 24	EN_ERROR_BYPASS
DIS_SUB_TRG	VME::TDCV1x90Opcodes, 24
VME::TDCV1x90Opcodes, 24	EN_ERROR_MARK
DISABLE_TEST_MODE	VME::TDCV1x90Opcodes, 24
VME::TDCV1x90Opcodes, 24	EN_HEAD_TRAILER
DisableChannel	VME::TDCV1x90Opcodes, 24
VME::TDCV1x90, 130	EN_SUB_TRG
Disconnect	VME::TDCV1x90Opcodes, 24
Client, 52	ENABLE_TEST_MODE
Messenger, 98	VME::TDCV1x90Opcodes, 24
DisconnectClient	EnableChannel
Messenger, 98	VME::TDCV1x90, 130
DLL_Direct_LowRes	Enabled
VME::TDCV1x90, 129	NIM::HVModuleN470Values::ChannelStatus,
DLL_PLL_HighRes	48

Encode	FetchEvents
HTTPMessage, 79	VME::TDCV1x90, 130
Error	FetchMessage
VME::TDCV1x90Status, 149	Socket, 107
ETTT	fEvents
VME::TDCEvent, 120	VME::TDCMeasurement, 126
ettt	fFile
VME::trailead_t, 151	FileReader, 59
event_count	fFPGA
VME::trailead_t, 151	VMEReader, 160
EventType	fHandle
VME::TDCEvent, 120	VME::GenericBoard, 76
ExternalClock	VME::PCIInterfaceA2818, 103
VME::FPGAUnitV1495Control, 70	fHasIRQ
ExternalSignal	VME::BridgeVx718, 28
VME::FPGAUnitV1495Control, 70	fHeader
ExternalTrigger	FileReader, 59
VME::FPGAUnitV1495Control, 70	fHV
EXTRA_SEARCH_WIN_WIDTH	VMEReader, 161
VME, 20	file_header_t, 56
	acq_mode, 56
fAcquisitionMode	det_mode, 56
VME::TDCV1x90, 143	magic, 56
fAddress	num_hptdc, 56
NIM::HVModuleN470, 84	run_id, 56
Socket, 109	spill_id, 56
fBaseAddr	FileReader, 57
VME::GenericBoard, 76	~FileReader, 58
fBridge	Clear, 58
VMEReader, 160	Dump, 58
fBuffer	fFile, 59
Socket, 109	fHeader, 59
VME::TDCV1x90, 143	FileReader, 58
fCAENET	fNumEvents, 59
VMEReader, 160	fReadoutMode, 59
fCFDCollection	fWriteTime, 59
VMEReader, 160	GetNextEvent, 58
fChannelId	GetNextMeasurement, 58
NIM::HVModuleN470ChannelValues	GetNumEvents, 59
87	GetNumTDCs, 59
fClientId	IsOpen, 59
Client, 55	Open, 59
fController	Filler
NIM::HVModuleN470, 84	VME::TDCEvent, 120
fDetectionMode	fine
VME::TDCV1x90, 143	VME::GlobalOffset, 77
fErrorMarks	fIsConnected
VME::TDCV1x90, 143	Client, 55
FetchBuffer	fIsPulserStarted
VME::CAENETControllerV288, 36	VMEReader, 161
-,	•

fMap	fValues
VME::TDCMeasurement, 126	NIM::HVModuleN470ChannelValues,
fMaster	87
Socket, 109	NIM::HVModuleN470Values, 89
	fVerb
fMessage SeeketMessage 115	
SocketMessage, 115	VME::TDCV1x90, 143
fNumAttempts	fWindowWidth
Messenger, 102	VME::TDCV1x90, 143
fNumEvents	fWord
FileReader, 59 fOnSocket	NIM::HVModuleN470Values::ChannelStatus,
VMEReader, 161	
,	VME::BridgeVx718Control, 32
fOriginalString	VME::BridgeVx718Status, 34
HTTPMessage, 79	VME::CAENETControllerV288Status,
fOutputFiles	39
VMEReader, 161	VME::FPGAUnitV1495Control, 73
FPGAUnitV1495	VME::TDCErrorFlag, 118
VME::FPGAUnitV1495, 62	VME::TDCEvent, 123
FPGAUnitV1495Control	VME::TDCV1x90Control, 147
VME::FPGAUnitV1495Control, 71	VME::TDCV1x90Status, 150
FPGAUnitV1495Register	fWriteTime
VME, 17	FileReader, 59
fPID	fWS
Messenger, 102	HTTPMessage, 79
fPort	Messenger, 102
Socket, 109	
fReadFds	gEnd
Socket, 109	VME::TDCV1x90, 143
fReadoutMode	GenericBoard
FileReader, 59	VME::GenericBoard, 75
fScalerStarted	GetAcquisitionMode
VME::FPGAUnitV1495, 68	VME::TDCV1x90, 130
fSG	GetAddressIncrement
VMEReader, 161	VME::BridgeVx718Control, 30
fSocketId	GetAlign64
Socket, 109	VME::TDCV1x90Control, 145
fSocketsConnected	GetArbiterType
Socket, 109	VME::BridgeVx718Control, 31
fStderrPipe	GetBERR
Messenger, 102	VME::BridgeVx718Status, 33
fStdoutPipe	GetBit
Messenger, 102	VME::FPGAUnitV1495Control, 71
fString	GetBLTEventNumberRegister
Message, 93	VME::TDCV1x90, 131
fTDCCollection	GetBunchId
VMEReader, 161	VME::TDCEvent, 121
fType	VME::TDCMeasurement, 125
Client, 55	GetBusError
Full	VME::TDCV1x90Control, 145
VME::TDCV1x90Status, 150	GetBusReqLevel

VME::BridgeVx718Control, 31	GetEventId
GetBusTimeout	VME::TDCEvent, 122
VME::BridgeVx718Control, 31	VME::TDCMeasurement, 125
GetCAENFirmwareRevision	GetEventStored
VME::FPGAUnitV1495, 63	VME::TDCV1x90, 132
GetCFD	GetFIFOSize
VMEReader, 157	VME::TDCV1x90, 132
GetCFDCollection	GetFirmwareRevision
VMEReader, 157	VME::TDCV1x90, 133
GetChannelDeadTime	GetFixedCode
VME::TDCV1x90, 131	VME::CFDV812, 42
GetChannelId	GetFPGAUnit
VME::TDCEvent, 121	VMEReader, 157
VME::TDCMeasurement, 125	GetFWRevision
GetChannelStatus	NIM::HVModuleN470, 81
NIM::HVModuleN470Values, 89	VME::PCIInterfaceA2818, 103
GetCleanedValue	GetGeo
SocketMessage, 113	VME::TDCEvent, 122
GetClockSource	GetGeoAddress
VME::FPGAUnitV1495Control, 71	VME::FPGAUnitV1495, 63
GetCompensation	GetGlobalOffset
VME::TDCV1x90Control, 145	VME::TDCV1x90, 133
GetControl	GetHandle
VME::FPGAUnitV1495, 63	VME::BridgeVx718, 27
VME::TDCV1x90, 131	GetHardwareRevision
GetDetectionMode	VME::FPGAUnitV1495, 63
VME::TDCV1x90, 131	GetHVModule
GetDipSwitch	VMEReader, 157
VME::BridgeVx718Status, 34	GetIdentifier
GetDLLClock	VME::IOModuleV262, 90
VME::TDCV1x90, 132	GetInternalClockPeriod
GetDTACK	VME::FPGAUnitV1495, 64
VME::BridgeVx718Status, 34	GetInternalTriggerPeriod
GetEmptyEvent	VME::FPGAUnitV1495, 64
VME::TDCV1x90Control, 145	GetInterruptReq
GetErrorFlags	VME::BridgeVx718Control, 31
VME::TDCEvent, 121	GetIntValue
GetErrorMarks	
	SocketMessage, 113 GetIOModule
VME::TDCV1x90, 132 GetETTT	VMEReader, 158
VME::TDCEvent, 121	
	GetIRQStatus
VME::TDCMeasurement, 125	VME::BridgeVx718, 27
VME::TDCV1x90, 132	GetKey
VME::TDCV1x90Control, 145	HTTPMessage, 79
GetEventCount	Message, 93
VME::TDCEvent, 121	SocketMessage, 113
GetEventCounter	GetLeadingTime
VME::TDCV1x90, 132	VME::TDCMeasurement, 125
GetEventFIFO	GetManufacturerId
VME::TDCV1x90Control, 145	VME::CFDV812, 42

VME::IOModuleV262, 90	GetScalerStatus
GetMEBAccess	VME::FPGAUnitV1495Control, 71
VME::TDCV1x90Control, 145	GetScalerValue
GetModel	VME::FPGAUnitV1495, 65
VME::TDCV1x90, 133	GetSerialNumber
GetModuleId	VME::CFDV812, 42
NIM::HVModuleN470, 81	VME::FPGAUnitV1495, 65
	VME::IOModuleV262, 91
GetModuleType VME::CFDV812, 42	
	VME::TDCV1x90, 134
VME::IOModuleV262, 91	GetSignalSource
GetModuleVersion	VME::FPGAUnitV1495Control, 71
VME::CFDV812, 42	GetSocketId
VME::IOModuleV262, 91	Socket, 107
GetNextEvent	GetSocketType
FileReader, 58	Socket, 107
GetNextMeasurement	GetSRAMCompensation
FileReader, 58	VME::TDCV1x90Control, 145
GetNumCFD	GetStatus
VMEReader, 158	VME::BridgeVx718, 28
GetNumEvents	VME::CAENETControllerV288, 36
FileReader, 59	VME::TDCEvent, 122
GetNumTDC	VME::TDCV1x90, 134
VMEReader, 158	GetString
GetNumTDCs	Message, 93
FileReader, 59	SocketMessage, 113
GetOperationStatus	GetSWTermination
VME::CAENETControllerV288Status	s, VME::TDCV1x90Control, 145
39	GetSysRes
GetOUI	VME::BridgeVx718Control, 31
VME::TDCV1x90, 133	GetSystemControl
GetOutputDelay	VME::BridgeVx718Status, 34
VME::FPGAUnitV1495, 64	GetSystemReset
GetOutputFile	VME::BridgeVx718Status, 34
VMEReader, 158	GetTDC
GetOutputPulser	VMEReader, 158
VME::FPGAUnitV1495, 64	GetTDCBits
GetPoI	VME::FPGAUnitV1495, 65
VME::TDCV1x90, 134	GetTDCCollection
GetPort	VMEReader, 158
Socket, 107	GetTDCEncapsulation
GetRCAdjust	VME::TDCV1x90, 135
VME::TDCV1x90, 134	GetTDCId
GetReleaseType VME::BridgeVx718Control, 31	VME::TDCEvent, 122 VME::TDCMeasurement, 125
e ·	
GetRequesterType	GetTermination
VME::BridgeVx718Control, 31	VME::TDCV1x90Control, 145
GetResolution	GetTestFIFO
VME::TDCV1x90, 134	
	VME::TDCV1x90Control, 145
GetRunNumber VMEReader, 158	

GetTime	VME::TDCErrorFlag, 117
VME::TDCEvent, 122	HasReadoutFIFOOverflow
GetToT	VME::TDCErrorFlag, 117
VME::TDCMeasurement, 125	HasTriggerFIFOOverflow
GetTrailingTime	VME::TDCErrorFlag, 117
VME::TDCMeasurement, 125	HeadersEnabled
GetTriggerConfiguration	VME::TDCV1x90Status, 150
VME::TDCV1x90, 135	HTTPMessage, 78
GetTriggerSource	Decode, 79
VME::FPGAUnitV1495Control, 71	Dump, 79
GetType	Encode, 79
Client, 53	fOriginalString, 79
Messenger, 99	fWS, 79
VME::TDCEvent, 122	GetKey, 79
GetUSBType	HTTPMessage, 78
VME::BridgeVx718Status, 34	HVEnabled
GetUserFirmwareRevision	NIM::HVModuleN470Values::ChannelStatus,
VME::FPGAUnitV1495, 65	48
GetValue	HVModuleN470
SocketMessage, 114	NIM::HVModuleN470, 81
VME::TDCV1x90Control, 145	HVModuleN470ChannelValues
VME::TDCV1x90Status, 150	NIM::HVModuleN470ChannelValues,
GetVectorValue	86
SocketMessage, 114	HVModuleN470Opcodes
GetWidth	NIM, 12
VME::TDCEvent, 122	HVModuleN470Values
GetWindowOffset	NIM::HVModuleN470Values, 89
	Mivi iviodule N4/0 values, 89
VME::TDCV1x90, 135 GetWindowWidth	IO
	NIM::HVModuleN470ChannelValues,
VME::TDCV1x90, 136	86
GetWord	I1
VME::FPGAUnitV1495Control, 72	NIM::HVModuleN470ChannelValues,
VME::TDCErrorFlag, 117	87
VME::TDCEvent, 123	
GetWordCount	Imon
VME::TDCEvent, 123	NIM::HVModuleN470ChannelValues,
GlobalHeader	87
VME::TDCEvent, 120	NIM::HVModuleN470Values, 89
GlobalTrailer	InputConf
VME::TDCEvent, 120	VME::BridgeVx718, 28
	InputRead
HardwareReset	VME::BridgeVx718, 28
VME::TDCV1x90, 136	InternalClock
HasGroupError	VME::FPGAUnitV1495Control, 70
VME::TDCErrorFlag, 117	InternalSignal
HasInternalChipError	VME::FPGAUnitV1495Control, 70
VME::TDCErrorFlag, 117	InternalTrigger
HasL1BufferOverflow	VME::FPGAUnitV1495Control, 70
VME::TDCErrorFlag, 117	INVALID
HasReachedEventSizeLimit	Socket, 106

	TD 60-40
Invalid	VME, 19
VME::CAENETControllerV288Statu	<u> </u>
38	VME, 19
IOModuleV262	kEventFIFOStoredRegister
VME::IOModuleV262, 90	VME, 19
IOModuleV262Register	kEventStored
VME, 18	VME, 19
IRQ1	kFirmwareRev
VME::BridgeVx718, 26	VME, 19
IRQ2	kGeoAddress
VME::BridgeVx718, 26	VME, 19
IRQ3	kIdentifier
VME::BridgeVx718, 26	VME, 18
IRQ4	Kill
VME::BridgeVx718, 26	NIM::HVModuleN470Values::ChannelStatus,
IRQ5	48
VME::BridgeVx718, 26	kInterruptLevel
IRQ6	VME, 19
VME::BridgeVx718, 26	kInterruptVector
IRQ7	VME, 19
VME::BridgeVx718, 26	kMCSTBase
IRQId	VME, 19
VME::BridgeVx718, 26	kMCSTControl
Isel	VME, 19
NIM::HVModuleN470Values::Chann	
48	VME, 19
IsFromWeb	kMicroHandshake
131 10111 1100	
Message 93	
Message, 93	VME, 19
IsOpen	VME, 19 kModuleReset
IsOpen FileReader, 59	VME, 19 kModuleReset VME, 19
IsOpen FileReader, 59 IsTrailing	VME, 19 kModuleReset VME, 19 kN470ChannelOff
IsOpen FileReader, 59 IsTrailing VME::TDCEvent, 123	VME, 19 kModuleReset VME, 19 kN470ChannelOff NIM, 12
IsOpen FileReader, 59 IsTrailing VME::TDCEvent, 123 IsWebSocket	VME, 19 kModuleReset VME, 19 kN470ChannelOff NIM, 12 kN470ChannelOn
IsOpen FileReader, 59 IsTrailing VME::TDCEvent, 123	VME, 19 kModuleReset VME, 19 kN470ChannelOff NIM, 12 kN470ChannelOn NIM, 12
IsOpen FileReader, 59 IsTrailing VME::TDCEvent, 123 IsWebSocket Socket, 107	VME, 19 kModuleReset VME, 19 kN470ChannelOff NIM, 12 kN470ChannelOn NIM, 12 kN470ClearAlarm
IsOpen FileReader, 59 IsTrailing VME::TDCEvent, 123 IsWebSocket Socket, 107 kBLTEventNumber	VME, 19 kModuleReset VME, 19 kN470ChannelOff NIM, 12 kN470ChannelOn NIM, 12 kN470ClearAlarm NIM, 12
IsOpen FileReader, 59 IsTrailing VME::TDCEvent, 123 IsWebSocket Socket, 107  kBLTEventNumber VME, 19	VME, 19 kModuleReset VME, 19 kN470ChannelOff NIM, 12 kN470ChannelOn NIM, 12 kN470ClearAlarm NIM, 12 kN470DisableFrontPanel
IsOpen FileReader, 59 IsTrailing VME::TDCEvent, 123 IsWebSocket Socket, 107  kBLTEventNumber VME, 19 kBoardInfo0	VME, 19 kModuleReset VME, 19 kN470ChannelOff NIM, 12 kN470ChannelOn NIM, 12 kN470ClearAlarm NIM, 12 kN470DisableFrontPanel NIM, 12
IsOpen FileReader, 59 IsTrailing VME::TDCEvent, 123 IsWebSocket Socket, 107  kBLTEventNumber VME, 19 kBoardInfo0 VME, 18	VME, 19 kModuleReset VME, 19 kN470ChannelOff NIM, 12 kN470ChannelOn NIM, 12 kN470ClearAlarm NIM, 12 kN470DisableFrontPanel NIM, 12 kN470DisableFrontPanel
IsOpen FileReader, 59 IsTrailing VME::TDCEvent, 123 IsWebSocket Socket, 107  kBLTEventNumber VME, 19 kBoardInfo0 VME, 18 kBoardInfo1	VME, 19 kModuleReset VME, 19 kN470ChannelOff NIM, 12 kN470ChannelOn NIM, 12 kN470ClearAlarm NIM, 12 kN470DisableFrontPanel NIM, 12 kN470DisableFrontPanel NIM, 12
IsOpen FileReader, 59 IsTrailing VME::TDCEvent, 123 IsWebSocket Socket, 107  kBLTEventNumber VME, 19 kBoardInfo0 VME, 18 kBoardInfo1 VME, 18	VME, 19 kModuleReset VME, 19 kN470ChannelOff NIM, 12 kN470ChannelOn NIM, 12 kN470ClearAlarm NIM, 12 kN470DisableFrontPanel NIM, 12 kN470EnableFrontPanel NIM, 12 kN470EnableFrontPanel NIM, 12 kN470GeneralInfo
IsOpen FileReader, 59 IsTrailing VME::TDCEvent, 123 IsWebSocket Socket, 107  kBLTEventNumber VME, 19 kBoardInfo0 VME, 18 kBoardInfo1 VME, 18 kClear	VME, 19 kModuleReset VME, 19 kN470ChannelOff NIM, 12 kN470ChannelOn NIM, 12 kN470ClearAlarm NIM, 12 kN470DisableFrontPanel NIM, 12 kN470EnableFrontPanel NIM, 12 kN470EnableFrontPanel NIM, 12 kN470GeneralInfo NIM, 12
IsOpen FileReader, 59 IsTrailing VME::TDCEvent, 123 IsWebSocket Socket, 107  kBLTEventNumber VME, 19 kBoardInfo0 VME, 18 kBoardInfo1 VME, 18 kClear VME::FPGAUnitV1495, 61	VME, 19 kModuleReset VME, 19 kN470ChannelOff NIM, 12 kN470ChannelOn NIM, 12 kN470ClearAlarm NIM, 12 kN470DisableFrontPanel NIM, 12 kN470EnableFrontPanel NIM, 12 kN470EnableFrontPanel NIM, 12 kN470GeneralInfo NIM, 12 kN470GeneralInfo NIM, 12 kN470I0Value
IsOpen FileReader, 59 IsTrailing VME::TDCEvent, 123 IsWebSocket Socket, 107  kBLTEventNumber VME, 19 kBoardInfo0 VME, 18 kBoardInfo1 VME, 18 kClear VME::FPGAUnitV1495, 61 kControl	VME, 19 kModuleReset VME, 19 kN470ChannelOff NIM, 12 kN470ChannelOn NIM, 12 kN470ClearAlarm NIM, 12 kN470DisableFrontPanel NIM, 12 kN470EnableFrontPanel NIM, 12 kN470EnableFrontPanel NIM, 12 kN470GeneralInfo NIM, 12 kN470IoValue NIM, 12
IsOpen FileReader, 59 IsTrailing VME::TDCEvent, 123 IsWebSocket Socket, 107  kBLTEventNumber VME, 19 kBoardInfo0 VME, 18 kBoardInfo1 VME, 18 kClear VME::FPGAUnitV1495, 61 kControl VME, 18	VME, 19 kModuleReset VME, 19 kN470ChannelOff NIM, 12 kN470ChannelOn NIM, 12 kN470ClearAlarm NIM, 12 kN470DisableFrontPanel NIM, 12 kN470EnableFrontPanel NIM, 12 kN470EnableFrontPanel NIM, 12 kN470GeneralInfo NIM, 12 kN470IoValue NIM, 12 kN470IoValue NIM, 12 kN470IIValue
IsOpen FileReader, 59 IsTrailing VME::TDCEvent, 123 IsWebSocket Socket, 107  kBLTEventNumber VME, 19 kBoardInfo0 VME, 18 kBoardInfo1 VME, 18 kClear VME::FPGAUnitV1495, 61 kControl VME, 18 kECLLevelWrite	VME, 19 kModuleReset VME, 19 kN470ChannelOff NIM, 12 kN470ChannelOn NIM, 12 kN470ClearAlarm NIM, 12 kN470DisableFrontPanel NIM, 12 kN470EnableFrontPanel NIM, 12 kN470EnableFrontPanel NIM, 12 kN470GeneralInfo NIM, 12 kN470I0Value NIM, 12 kN470I1Value NIM, 12
IsOpen FileReader, 59 IsTrailing VME::TDCEvent, 123 IsWebSocket Socket, 107  kBLTEventNumber VME, 19 kBoardInfo0 VME, 18 kBoardInfo1 VME, 18 kClear VME::FPGAUnitV1495, 61 kControl VME, 18 kECLLevelWrite VME, 18	VME, 19 kModuleReset VME, 19 kN470ChannelOff NIM, 12 kN470ChannelOn NIM, 12 kN470ClearAlarm NIM, 12 kN470DisableFrontPanel NIM, 12 kN470EnableFrontPanel NIM, 12 kN470GeneralInfo NIM, 12 kN470GeneralInfo NIM, 12 kN470I0Value NIM, 12 kN470I1Value NIM, 12 kN470I1Value NIM, 12
IsOpen FileReader, 59 IsTrailing VME::TDCEvent, 123 IsWebSocket Socket, 107  kBLTEventNumber VME, 19 kBoardInfo0 VME, 18 kBoardInfo1 VME, 18 kClear VME::FPGAUnitV1495, 61 kControl VME, 18 kECLLevelWrite VME, 18 kEventCounter	VME, 19 kModuleReset VME, 19 kN470ChannelOff NIM, 12 kN470ChannelOn NIM, 12 kN470ClearAlarm NIM, 12 kN470DisableFrontPanel NIM, 12 kN470EnableFrontPanel NIM, 12 kN470GeneralInfo NIM, 12 kN470IoValue NIM, 12 kN470IValue NIM, 12 kN470IIValue NIM, 12 kN470IIValue NIM, 12 kN470IIValue NIM, 12 kN470IIValue NIM, 12
IsOpen FileReader, 59 IsTrailing VME::TDCEvent, 123 IsWebSocket Socket, 107  kBLTEventNumber VME, 19 kBoardInfo0 VME, 18 kBoardInfo1 VME, 18 kClear VME::FPGAUnitV1495, 61 kControl VME, 18 kECLLevelWrite VME, 18	VME, 19 kModuleReset VME, 19 kN470ChannelOff NIM, 12 kN470ChannelOn NIM, 12 kN470ClearAlarm NIM, 12 kN470DisableFrontPanel NIM, 12 kN470EnableFrontPanel NIM, 12 kN470GeneralInfo NIM, 12 kN470GeneralInfo NIM, 12 kN470I0Value NIM, 12 kN470I1Value NIM, 12 kN470I1Value NIM, 12

kN470NIMLevel	kSoftwareClear
NIM, 12	VME, 19
kN470OperationalParams	kStatus
NIM, 12	VME, 18
kN470RampDownValue	kTrigger
NIM, 12	VME::FPGAUnitV1495, 61
kN470RampUpValue	kV1495Board0
NIM, 12	VME, 18
kN470TripValue	kV1495Board1
NIM, 12	VME, 18
kN470TTLLevel	kV1495Board2
NIM, 12	VME, 18
kN470V0Value	kV1495ClockSettings
NIM, 12	VME, 17
kN470V1Value	kV1495ConfigurationROM
NIM, 12	VME, 18
kNIMLevelWrite	kV1495Control
VME, 18	VME, 17
kNIMPulseRead	kV1495DelaySettings
VME, 18	VME, 17
kNIMPulseWrite	kV1495FWRevision
VME, 18	VME, 17
kOutputBuffer	kV1495GeoAddress
VME, 18	VME, 17
kReset	kV1495HWRevision0
VME::FPGAUnitV1495, 61	VME, 18
kROMBoard0	kV1495HWRevision1
VME, 19	VME, 18
kROMBoard1	kV1495HWRevision2
VME, 19	VME, 18
kROMBoard2	kV1495HWRevision3
VME, 19	VME, 18
kROMOui0	kV1495ModuleReset
VME, 19	VME, 17
kROMOui1	kV1495OUI0
VME, 19	VME, 18
kROMOui2	kV1495OUI1
VME, 19	VME, 18
kROMRevis0	kV1495OUI2
VME, 19	VME, 18
kROMRevis1	kV1495OutputSettings
VME, 19	VME, 17
kROMRevis2	kV1495ScalerCounter
VME, 19	VME, 17
kROMRevis3	kV1495SerNum0
VME, 19	VME, 18
kROMSerNum0	kV1495SerNum1
VME, 19	VME, 18
	kV1495TDCBoardInterface
kROMSerNum1	
VME, 19	VME, 17

kV1495TriggerSettings	magic
VME, 17	file_header_t, 56
kV1495UserFPGAConfig	MASTER
VME, 17	Socket, 106
kV1495UserFPGAFlashMem	MATCH_WIN_WIDTH
VME, 17	VME, 20
kV1495UserFWRevision	MaxV
VME, 17	NIM::HVModuleN470ChannelValues,
kV288DataBuffer	87
VME, 16	NIM::HVModuleN470Values::ChannelStatus,
kV288IRQVector	48
VME, 16	Message, 92
kV288ModuleReset	$\sim$ Message, 93
VME, 16	Dump, 93
kV288Status	fString, 93
VME, 16	GetKey, 93
kV288Transmission	GetString, 93
VME, 16	IsFromWeb, 93
kV812DeadTimeGroup0	Message, 93
VME, 17	Messenger, 95
kV812DeadTimeGroup1	~Messenger, 96
-	AddClient, 97
VME, 17	Broadcast, 97
kV812FixedCode	Connect, 98
VME, 17	Disconnect, 98
kV812Info0	DisconnectClient, 98
VME, 17	fNumAttempts, 102
kV812Info1	fPID, 102
VME, 17	fStderrPipe, 102
kV812MajorityThreshold	fStdoutPipe, 102
VME, 17	fWS, 102
kV812OutputWidthGroup0	GetType, 99
VME, 17	Messenger, 96
kV812OutputWidthGroup1	ProcessMessage, 99
VME, 17	Receive, 99
kV812PatternOfInhibit	Send, 100
VME, 17	SendAll, 101
kV812TestPulse	
VME, 17	StartAcquisition, 101 StopAcquisition, 101
kV812ThresholdChannel0	
VME, 17	SwitchClientType, 101
	micro_handshake
leading	VME, 18
VME::trailead_t, 151	nchannels
Listen	VME::TDCV1x90, 143
Socket, 107	
LOAD_DEF_CONFIG	NIM, 11
	HVModuleN470Opcodes, 12
VME::TDCV1x90Opcodes, 24	kN470ChannelOff, 12
LOAD_USER_CONFIG	kN470ChannelOn, 12
VME::TDCV1x90Opcodes, 24	kN470ClearAlarm, 12

kN470DisableFrontPanel, 12	NIM::HVModuleN470Values, 88
kN470EnableFrontPanel, 12	~HVModuleN470Values, 89
kN470GeneralInfo, 12	Dump, 89
kN470I0Value, 12	fValues, 89
kN470I1Value, 12	GetChannelStatus, 89
kN470KillAllChannels, 12	HVModuleN470Values, 89
kN470MonStatus, 12	Imon, 89
kN470NIMLevel, 12	Vmax, 89
kN470OperationalParams, 12	Vmon, 89
kN470RampDownValue, 12	NIM::HVModuleN470Values::ChannelStatus,
kN470RampUpValue, 12	45
kN470TripValue, 12	~ChannelStatus, 48
kN470TTLLevel, 12	Alarm, 48
kN470V0Value, 12	ChannelStatus, 48
kN470V1Value, 12	Dump, 48
NIM::HVModuleN470Values::Channe	* '
46	fWord, 48
NIM::HVModuleN470, 80	HVEnabled, 48
~HVModuleN470, 81	Isel, 48
fAddress, 84	Kill, 48
fController, 84	MaxV, 48
GetFWRevision, 81	NIM, 46
GetModuleId, 81	NonCalibrated, 48
HVModuleN470, 81	operator<<, 48
ReadChannelValues, 81	OVC, 48
ReadMonitoringValues, 81	OVV, 48
ReadRegister, 82	Polarity, 48
SetChannelIO, 82	RampDown, 48
SetChannelI1, 82	RampUp, 48
SetChannelV0, 83	SignalStandard, 46
SetChannelV1, 83	Standard, 48
WriteRegister, 83	Trip, 48
NIM::HVModuleN470ChannelValues, 85	TTL, 46
~HVModuleN470ChannelValues,	UNV, 48
86	Vsel, 48
ChannelStatus, 86	NonCalibrated
Dump, 86	NIM::HVModuleN470Values::ChannelStatus,
fChannelId, 87	48
fValues, 87	num_hptdc
HVModuleN470ChannelValues, 86	file header t, 56
I0, 86	NumErrors
I1, 87	VME::TDCMeasurement, 126
Imon, 87	NumEvents
MaxV, 87	VME::TDCMeasurement, 126
RampDown, 87	VIVIE I Delvicasurement, 120
RampUp, 87	Object
Trip, 87	SocketMessage, 114
V0, 87	OLEADING
V1, 87	VME, 17
Vmon, 87	Open
, inon, 07	Open

```
FileReader, 59
                                        VME::FPGAUnitV1495, 65
OperationStatus
                                    Purged
    VME::CAENETControllerV288Status,
                                        VME::TDCV1x90Status, 150
                                    r100ps
operator<<
   NIM::HVModuleN470Values::ChannelStatuV,ME, 19
                                    r200ps
                                        VME, 19
    VME::CAENETControllerV288, 37
    VME::TDCErrorFlag, 118
                                    r25ps
                                        VME, 19
operator>>
    VME::CAENETControllerV288, 37
                                    r800ps
                                        VME, 19
OTRAILING
                                    R_100ps
    VME, 17
OutputConf
                                        VME::TDCV1x90Status, 149
                                    R_200ps
    VME::BridgeVx718, 28
                                        VME::TDCV1x90Status, 149
OutputFiles
                                    R_25ps
    VMEReader, 155
                                        VME::TDCV1x90Status, 149
OutputOff
                                    R 800ps
    VME::BridgeVx718, 28
                                        VME::TDCV1x90Status, 149
OutputOn
    VME::BridgeVx718, 28
                                    RampDown
                                        NIM::HVModuleN470ChannelValues,
OutputWidthCalculator
    VME::CFDV812, 42
                                        NIM::HVModuleN470Values::ChannelStatus,
OVC
    NIM::HVModuleN470Values::ChannelStatus,
                                    RampUp
        48
                                        NIM::HVModuleN470ChannelValues,
OVV
    NIM::HVModuleN470Values::ChannelStatus,
                                           87
                                        NIM::HVModuleN470Values::ChannelStatus,
        48
                                    READ_OK
PAIR
    VME, 17
                                        VME, 18
                                    READ ACO MOD
pair_lead_res
    VME::TDCV1x90, 143
                                        VME::TDCV1x90Opcodes, 24
                                    READ_ADJUST_CH
pair_width_res
    VME::TDCV1x90, 143
                                        VME::TDCV1x90Opcodes, 24
PairMode
                                    READ_DEAD_TIME
    VME::TDCV1x90Status, 150
                                        VME::TDCV1x90Opcodes, 24
ParseMessage
                                    READ_DETECTION
    Client, 53
                                        VME::TDCV1x90Opcodes, 24
PCIInterfaceA2818
                                    READ_DLL_LOCK
    VME::PCIInterfaceA2818, 103
                                        VME::TDCV1x90Opcodes, 24
Polarity
                                    READ_EEPROM
    NIM::HVModuleN470Values::ChannelStatuV,ME::TDCV1x90Opcodes, 24
                                    READ_EN_PATTERN
        48
PrepareConnection
                                        VME::TDCV1x90Opcodes, 24
    Socket, 107
                                    READ_EN_PATTERN32
ProcessMessage
                                        VME::TDCV1x90Opcodes, 24
                                    READ_ERROR_STATUS
    Messenger, 99
PulseTDCBits
                                        VME::TDCV1x90Opcodes, 24
```

READ_ERROR_TYPES	VME::TDCV1x90Opcodes, 24
VME::TDCV1x90Opcodes, 24	ResetFPGA
READ_EVENT_SIZE	VME::FPGAUnitV1495, 66
VME::TDCV1x90Opcodes, 24	Resolution
READ_FIFO_SIZE	VME::TDCV1x90Status, 150
VME::TDCV1x90Opcodes, 24	REV_DATE_MICRO_FW
READ_GLOB_OFFS	VME::TDCV1x90Opcodes, 24
VME::TDCV1x90Opcodes, 24	run_id
READ_HEAD_TRAILER	file_header_t, 56
VME::TDCV1x90Opcodes, 24	
READ_MICRO_REV	SAVE_RC_ADJ
VME::TDCV1x90Opcodes, 24	VME::TDCV1x90Opcodes, 24
READ_RC_ADJ	SAVE_USER_CONFIG
VME::TDCV1x90Opcodes, 24	VME::TDCV1x90Opcodes, 24
READ_RES	SelectConnections
VME::TDCV1x90Opcodes, 24	Socket, 108
READ_SETUP_REG	Send
VME::TDCV1x90Opcodes, 24	Client, 54
READ_SETUP_SCANPATH	Messenger, 100
VME::TDCV1x90Opcodes, 24	SendAll
READ_SPARE	Messenger, 101
VME::TDCV1x90Opcodes, 24	SendAndReceive
READ_STATUS_STREAM	Client, 54
VME::TDCV1x90Opcodes, 24	SendBuffer
READ_TDC_ID	VME::CAENETControllerV288, 37
VMETDCV1000124	C - 1C1
VME::TDCV1x90Opcodes, 24	SendClear
READ_TRG_CONF	VMEReader, 159
•	
READ_TRG_CONF	VMEReader, 159
READ_TRG_CONF VME::TDCV1x90Opcodes, 24	VMEReader, 159 SendMessage
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode	VMEReader, 159 SendMessage Socket, 108
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136	VMEReader, 159 SendMessage Socket, 108 SendPulse
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136 ReadChannelValues	VMEReader, 159 SendMessage Socket, 108 SendPulse VMEReader, 159
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136 ReadChannelValues NIM::HVModuleN470, 81	VMEReader, 159 SendMessage Socket, 108 SendPulse VMEReader, 159 SET_ADJUST_CH
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136 ReadChannelValues NIM::HVModuleN470, 81 ReadDetectionMode	VMEReader, 159 SendMessage Socket, 108 SendPulse VMEReader, 159 SET_ADJUST_CH VME::TDCV1x90Opcodes, 24
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136 ReadChannelValues NIM::HVModuleN470, 81 ReadDetectionMode VME::TDCV1x90, 136	VMEReader, 159 SendMessage Socket, 108 SendPulse VMEReader, 159 SET_ADJUST_CH VME::TDCV1x90Opcodes, 24 SET_DEAD_TIME
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136 ReadChannelValues NIM::HVModuleN470, 81 ReadDetectionMode VME::TDCV1x90, 136 ReadMonitoringValues NIM::HVModuleN470, 81	VMEReader, 159 SendMessage Socket, 108 SendPulse VMEReader, 159 SET_ADJUST_CH VME::TDCV1x90Opcodes, 24 SET_DEAD_TIME VME::TDCV1x90Opcodes, 24
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136 ReadChannelValues NIM::HVModuleN470, 81 ReadDetectionMode VME::TDCV1x90, 136 ReadMonitoringValues	VMEReader, 159 SendMessage Socket, 108 SendPulse VMEReader, 159 SET_ADJUST_CH VME::TDCV1x90Opcodes, 24 SET_DEAD_TIME VME::TDCV1x90Opcodes, 24 SET_DETECTION
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136 ReadChannelValues NIM::HVModuleN470, 81 ReadDetectionMode VME::TDCV1x90, 136 ReadMonitoringValues NIM::HVModuleN470, 81 ReadRegister	VMEReader, 159 SendMessage Socket, 108 SendPulse VMEReader, 159 SET_ADJUST_CH VME::TDCV1x90Opcodes, 24 SET_DEAD_TIME VME::TDCV1x90Opcodes, 24 SET_DETECTION VME::TDCV1x90Opcodes, 24
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136 ReadChannelValues NIM::HVModuleN470, 81 ReadDetectionMode VME::TDCV1x90, 136 ReadMonitoringValues NIM::HVModuleN470, 81 ReadRegister NIM::HVModuleN470, 82	VMEReader, 159 SendMessage Socket, 108 SendPulse VMEReader, 159 SET_ADJUST_CH VME::TDCV1x90Opcodes, 24 SET_DEAD_TIME VME::TDCV1x90Opcodes, 24 SET_DETECTION VME::TDCV1x90Opcodes, 24 SET_DLL_CLOCK
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136 ReadChannelValues NIM::HVModuleN470, 81 ReadDetectionMode VME::TDCV1x90, 136 ReadMonitoringValues NIM::HVModuleN470, 81 ReadRegister NIM::HVModuleN470, 82 VME::GenericBoard, 75	VMEReader, 159 SendMessage Socket, 108 SendPulse VMEReader, 159 SET_ADJUST_CH VME::TDCV1x90Opcodes, 24 SET_DEAD_TIME VME::TDCV1x90Opcodes, 24 SET_DETECTION VME::TDCV1x90Opcodes, 24 SET_DLL_CLOCK VME::TDCV1x90Opcodes, 24
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136 ReadChannelValues NIM::HVModuleN470, 81 ReadDetectionMode VME::TDCV1x90, 136 ReadMonitoringValues NIM::HVModuleN470, 81 ReadRegister NIM::HVModuleN470, 82 VME::GenericBoard, 75 ReadXML	VMEReader, 159 SendMessage Socket, 108 SendPulse VMEReader, 159 SET_ADJUST_CH VME::TDCV1x90Opcodes, 24 SET_DEAD_TIME VME::TDCV1x90Opcodes, 24 SET_DETECTION VME::TDCV1x90Opcodes, 24 SET_DLL_CLOCK VME::TDCV1x90Opcodes, 24 SET_ERROR_TYPES
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136 ReadChannelValues NIM::HVModuleN470, 81 ReadDetectionMode VME::TDCV1x90, 136 ReadMonitoringValues NIM::HVModuleN470, 81 ReadRegister NIM::HVModuleN470, 82 VME::GenericBoard, 75 ReadXML VMEReader, 158	VMEReader, 159 SendMessage Socket, 108 SendPulse VMEReader, 159 SET_ADJUST_CH VME::TDCV1x90Opcodes, 24 SET_DEAD_TIME VME::TDCV1x90Opcodes, 24 SET_DETECTION VME::TDCV1x90Opcodes, 24 SET_DLL_CLOCK VME::TDCV1x90Opcodes, 24 SET_ERROR_TYPES VME::TDCV1x90Opcodes, 24
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136 ReadChannelValues NIM::HVModuleN470, 81 ReadDetectionMode VME::TDCV1x90, 136 ReadMonitoringValues NIM::HVModuleN470, 81 ReadRegister NIM::HVModuleN470, 82 VME::GenericBoard, 75 ReadXML VMEReader, 158 Receive	VMEReader, 159 SendMessage Socket, 108 SendPulse VMEReader, 159 SET_ADJUST_CH VME::TDCV1x90Opcodes, 24 SET_DEAD_TIME VME::TDCV1x90Opcodes, 24 SET_DETECTION VME::TDCV1x90Opcodes, 24 SET_DLL_CLOCK VME::TDCV1x90Opcodes, 24 SET_ERROR_TYPES VME::TDCV1x90Opcodes, 24 SET_ERROR_TYPES VME::TDCV1x90Opcodes, 24 SET_EVENT_SIZE
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136 ReadChannelValues NIM::HVModuleN470, 81 ReadDetectionMode VME::TDCV1x90, 136 ReadMonitoringValues NIM::HVModuleN470, 81 ReadRegister NIM::HVModuleN470, 82 VME::GenericBoard, 75 ReadXML VMEReader, 158 Receive Client, 53	VMEReader, 159 SendMessage Socket, 108 SendPulse VMEReader, 159 SET_ADJUST_CH VME::TDCV1x90Opcodes, 24 SET_DEAD_TIME VME::TDCV1x90Opcodes, 24 SET_DETECTION VME::TDCV1x90Opcodes, 24 SET_DLL_CLOCK VME::TDCV1x90Opcodes, 24 SET_ERROR_TYPES VME::TDCV1x90Opcodes, 24 SET_ERROR_TYPES VME::TDCV1x90Opcodes, 24 SET_EVENT_SIZE VME::TDCV1x90Opcodes, 24 SET_FIFO_SIZE
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136 ReadChannelValues NIM::HVModuleN470, 81 ReadDetectionMode VME::TDCV1x90, 136 ReadMonitoringValues NIM::HVModuleN470, 81 ReadRegister NIM::HVModuleN470, 82 VME::GenericBoard, 75 ReadXML VMEReader, 158 Receive Client, 53 Messenger, 99	VMEReader, 159 SendMessage Socket, 108 SendPulse VMEReader, 159 SET_ADJUST_CH VME::TDCV1x90Opcodes, 24 SET_DEAD_TIME VME::TDCV1x90Opcodes, 24 SET_DETECTION VME::TDCV1x90Opcodes, 24 SET_DLL_CLOCK VME::TDCV1x90Opcodes, 24 SET_ERROR_TYPES VME::TDCV1x90Opcodes, 24 SET_ERROR_TYPES VME::TDCV1x90Opcodes, 24 SET_EVENT_SIZE VME::TDCV1x90Opcodes, 24
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136 ReadChannelValues NIM::HVModuleN470, 81 ReadDetectionMode VME::TDCV1x90, 136 ReadMonitoringValues NIM::HVModuleN470, 81 ReadRegister NIM::HVModuleN470, 82 VME::GenericBoard, 75 ReadXML VMEReader, 158 Receive Client, 53 Messenger, 99 REJECT_MARGIN	VMEReader, 159 SendMessage Socket, 108 SendPulse VMEReader, 159 SET_ADJUST_CH VME::TDCV1x90Opcodes, 24 SET_DEAD_TIME VME::TDCV1x90Opcodes, 24 SET_DETECTION VME::TDCV1x90Opcodes, 24 SET_DLL_CLOCK VME::TDCV1x90Opcodes, 24 SET_ERROR_TYPES VME::TDCV1x90Opcodes, 24 SET_EVENT_SIZE VME::TDCV1x90Opcodes, 24 SET_FIFO_SIZE VME::TDCV1x90Opcodes, 24
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136 ReadChannelValues NIM::HVModuleN470, 81 ReadDetectionMode VME::TDCV1x90, 136 ReadMonitoringValues NIM::HVModuleN470, 81 ReadRegister NIM::HVModuleN470, 82 VME::GenericBoard, 75 ReadXML VMEReader, 158 Receive Client, 53 Messenger, 99 REJECT_MARGIN VME, 20	VMEReader, 159 SendMessage Socket, 108 SendPulse VMEReader, 159 SET_ADJUST_CH VME::TDCV1x90Opcodes, 24 SET_DEAD_TIME VME::TDCV1x90Opcodes, 24 SET_DETECTION VME::TDCV1x90Opcodes, 24 SET_DLL_CLOCK VME::TDCV1x90Opcodes, 24 SET_ERROR_TYPES VME::TDCV1x90Opcodes, 24 SET_EVENT_SIZE VME::TDCV1x90Opcodes, 24 SET_FIFO_SIZE VME::TDCV1x90Opcodes, 24 SET_FIFO_SIZE VME::TDCV1x90Opcodes, 24 SET_FIFO_SIZE VME::TDCV1x90Opcodes, 24 SET_GLOB_OFFS
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136 ReadChannelValues NIM::HVModuleN470, 81 ReadDetectionMode VME::TDCV1x90, 136 ReadMonitoringValues NIM::HVModuleN470, 81 ReadRegister NIM::HVModuleN470, 82 VME::GenericBoard, 75 ReadXML VMEReader, 158 Receive Client, 53 Messenger, 99 REJECT_MARGIN VME, 20 Reset	VMEReader, 159 SendMessage Socket, 108 SendPulse VMEReader, 159 SET_ADJUST_CH VME::TDCV1x90Opcodes, 24 SET_DEAD_TIME VME::TDCV1x90Opcodes, 24 SET_DETECTION VME::TDCV1x90Opcodes, 24 SET_DLL_CLOCK VME::TDCV1x90Opcodes, 24 SET_ERROR_TYPES VME::TDCV1x90Opcodes, 24 SET_EVENT_SIZE VME::TDCV1x90Opcodes, 24 SET_FIFO_SIZE VME::TDCV1x90Opcodes, 24 SET_FIFO_SIZE VME::TDCV1x90Opcodes, 24 SET_GLOB_OFFS VME::TDCV1x90Opcodes, 24
READ_TRG_CONF VME::TDCV1x90Opcodes, 24 ReadAcquisitionMode VME::TDCV1x90, 136 ReadChannelValues NIM::HVModuleN470, 81 ReadDetectionMode VME::TDCV1x90, 136 ReadMonitoringValues NIM::HVModuleN470, 81 ReadRegister NIM::HVModuleN470, 82 VME::GenericBoard, 75 ReadXML VMEReader, 158 Receive Client, 53 Messenger, 99 REJECT_MARGIN VME, 20 Reset VME::BridgeVx718, 28	VMEReader, 159 SendMessage Socket, 108 SendPulse VMEReader, 159 SET_ADJUST_CH VME::TDCV1x90Opcodes, 24 SET_DEAD_TIME VME::TDCV1x90Opcodes, 24 SET_DETECTION VME::TDCV1x90Opcodes, 24 SET_DLL_CLOCK VME::TDCV1x90Opcodes, 24 SET_ERROR_TYPES VME::TDCV1x90Opcodes, 24 SET_EVENT_SIZE VME::TDCV1x90Opcodes, 24 SET_FIFO_SIZE VME::TDCV1x90Opcodes, 24 SET_FIFO_SIZE VME::TDCV1x90Opcodes, 24 SET_GLOB_OFFS VME::TDCV1x90Opcodes, 24 SET_GLOB_OFFS VME::TDCV1x90Opcodes, 24 SET_KEEP_TOKEN

VME::TDCV1x90Opcodes, 24	SetEmptyEvent
SET_RC_ADJ	VME::TDCV1x90Control, 146
VME::TDCV1x90Opcodes, 24	SetErrorMarks
SET_REJ_MARGIN	VME::TDCV1x90, 138
VME::TDCV1x90Opcodes, 24	SetETTT VALUE 128
SET_SW_MARGIN	VME::TDCV1x90, 138
VME::TDCV1x90Opcodes, 24 SET_TDC_TSET_OUTPUT	VME::TDCV1x90Control, 146 SetEventFIFO
VME::TDCV1x90Opcodes, 24	VME::TDCV1x90Control, 146
SET_TR_LEAD_LSB	SetEventsCollection
VME::TDCV1x90Opcodes, 24	VME::TDCMeasurement, 126
SET_WIN_OFFS	SetFIFOSize
VME::TDCV1x90Opcodes, 24	VME::TDCV1x90, 139
SET_WIN_WIDTH	SetGlobalOffset
VME::TDCV1x90Opcodes, 24	VME::TDCV1x90, 139
SetAcquisitionMode	SetInternalClockPeriod
VME::TDCV1x90, 136	VME::FPGAUnitV1495, 66
SetAlign64	SetInternalTriggerPeriod
VME::TDCV1x90Control, 145	VME::FPGAUnitV1495, 67
SetBit	SetIRQ
VME::FPGAUnitV1495Control, 72	VME::BridgeVx718, 28
SetBLTEventNumberRegister	SetKeyValue
VME::TDCV1x90, 136	SocketMessage, 114
SetBusError	SetLSBTraileadEdge
VME::TDCV1x90Control, 145	VME::TDCV1x90, 139
SetChannelDeadTime	SetMEBAccess
VME::TDCV1x90, 137	VME::TDCV1x90Control, 146
SetChannelI0	SetOutputDelay
NIM::HVModuleN470, 82	VME::FPGAUnitV1495, 67
SetChannelI1	SetOutputFile
NIM::HVModuleN470, 82	VMEReader, 159
SetChannelV0	SetOutputPulser
NIM::HVModuleN470, 83	VME::FPGAUnitV1495, 67
SetChannelV1	SetOutputPulserPOI
NIM::HVModuleN470, 83	VME::FPGAUnitV1495, 67
SetClockSource	SetOutputWidth
VME::FPGAUnitV1495Control, 72	VME::CFDV812, 43
SetCompensation	SetPairModeResolution
VME::TDCV1x90Control, 146	VME::TDCV1x90, 139
SetContinuousStorage	SetPOI
VME::TDCV1x90, 137	VME::CFDV812, 43
SetControl	SetPoI
VME::FPGAUnitV1495, 66	VME::TDCV1x90, 139
VME::TDCV1x90, 137 SetDeadTime	SetPort 108
	Socket, 108
VME::CFDV812, 43 SetDetectionMode	SetRCAdjust VME::TDCV1x90, 140
VME::TDCV1x90, 137	SetScalerReset
SetDLLClock	VME::FPGAUnitV1495Control, 72
VME: TDCV1x90 138	SetScalerStatus

VME::FPGAUnitV1495Control, 72	DQM, 106
SetSignalSource	DumpConnected, 107
VME::FPGAUnitV1495Control, 73	fAddress, 109
SetSocketId	fBuffer, 109
Socket, 108	FetchMessage, 107
SetSRAMCompensation	fMaster, 109
VME::TDCV1x90Control, 146	fPort, 109
SetStatus	fReadFds, 109
VME::TDCV1x90, 140	fSocketId, 109
SetSWTermination	fSocketsConnected, 109
VME::TDCV1x90Control, 146	GetPort, 107
SetTDCBits	GetSocketId, 107
VME::FPGAUnitV1495, 67	GetSocketType, 107
SetTDCEncapsulation	INVALID, 106
VME::TDCV1x90, 140	IsWebSocket, 107
SetTermination	Listen, 107
VME::TDCV1x90Control, 146	MASTER, 106
SetTestFIFO	PrepareConnection, 107
VME::TDCV1x90Control, 146	SelectConnections, 108
SetTestMode	SendMessage, 108
VME::TDCV1x90, 140	SetPort, 108
SetThreshold	SetSocketId, 108
VME::CFDV812, 43	Socket, 106
SetTriggerMatching	SocketCollection, 106
VME::TDCV1x90, 141	SocketType, 106
SetTriggerSource	Start, 108
VME::FPGAUnitV1495Control, 73	Stop, 108
SetVerboseLevel	WEBSOCKET_CLIENT, 106
VME::TDCV1x90, 141	Socket communication objects, 9
SetWindowOffset	SocketCollection
VME::TDCV1x90, 141	Socket, 106
SetWindowWidth	SocketMessage, 110
VME::TDCV1x90, 141	~SocketMessage, 113
SetWord	Dump, 113
VME::TDCEvent, 123	fMessage, 115
SignalSource	GetCleanedValue, 113
VME::FPGAUnitV1495Control, 70	GetIntValue, 113
SignalStandard	GetKey, 113
NIM::HVModuleN470Values::Chanr	
46	GetValue, 114
SinglePulse	GetVectorValue, 114
VME::BridgeVx718, 28	Object, 114
Socket, 104	SetKeyValue, 114
$\sim$ Socket, 106	SocketMessage, 112, 113
AcceptConnections, 106	String, 114
Bind, 106	SocketType
CLIENT, 106	Socket, 106
Configure, 107	SoftwareClear
Create, 107	VME::TDCV1x90, 142
DETECTOR, 106	SoftwareReset

VME::TDCV1x90, 142	TDCV1x90
spill_id	VME::TDCV1x90, 129
file_header_t, 56	TDCV1x90Control
Standard	VME::TDCV1x90Control, 145
NIM::HVModuleN470Values::Chann	
48	VME, 18
Start	TDCV1x90Status
Socket, 108	VME::TDCV1x90Status, 149
Start Acquisition	TerminationOn
Messenger, 101	VME::TDCV1x90Status, 150
StartPulser	TestOutputs
VME::BridgeVx718, 28	VME::BridgeVx718, 28
VMEReader, 160	total_hits
StartScaler	VME::trailead_t, 151
VME::FPGAUnitV1495, 68	TRAILEAD
Stop	VME, 17
Socket, 108	trailead_edge_lsb
StopAcquisition	VME, 19
Messenger, 101	trailing
StopPulser	VME::trailead_t, 151
VME::BridgeVx718, 28	TRG_MATCH
VMEReader, 160	VME::TDCV1x90Opcodes, 24
StopScaler	TRIG_MATCH
VME::FPGAUnitV1495, 68	VME, 16
String	TRIG_TIME_SUB
SocketMessage, 114	VME, 20
SwitchClientType	trig_conf
Messenger, 101	VME, 19
<i>g</i> , ,	TriggerLost
TDCBits	VME::TDCV1x90Status, 150
VME::FPGAUnitV1495, 61	TriggerMatching
TDCCollection	VME::TDCV1x90Status, 150
VME, 15	TriggerSource
TDCError	VME::FPGAUnitV1495Control, 70
VME::TDCEvent, 120	Trip
TDCErrorFlag	NIM::HVModuleN470ChannelValues,
VME::TDCErrorFlag, 117	87
TDCEvent	NIM::HVModuleN470Values::ChannelStatus,
VME::TDCEvent, 121	48
TDCEventCollection	TTL
VME, 15	NIM::HVModuleN470Values::ChannelStatus,
TDCHeader	46
VME::TDCEvent, 120	
TDCMeasurement	UNV
VME::TDCEvent, 120	NIM::HVModuleN470Values::ChannelStatus,
VME::TDCMeasurement, 124	48
TDCResolution	UPDATE_SETUP_REG
VME::TDCV1x90Status, 149	VME::TDCV1x90Opcodes, 24
TDCTrailer	UPDATE_SETUP_TDC
VME::TDCEvent, 120	VME::TDCV1x90Opcodes, 24

UseSocket	kIdentifier, 18
VMEReader, 160	kInterruptLevel, 19
, , , , , , , , , , , , , , , , , , , ,	kInterrupt Vector, 19
V0	kMCSTBase, 19
NIM::HVModuleN470ChannelVa	
87	kMicro, 19
V1	kMicroHandshake, 19
NIM::HVModuleN470ChannelVa	
87	kNIMLevelWrite, 18
Valid	kNIMPulseRead, 18
VME::CAENETControllerV288S	
38	kOutputBuffer, 18
Vmax	kROMBoard0, 19
NIM::HVModuleN470Values, 89	kROMBoard1, 19
VME, 13	kROMBoard2, 19
AcquisitionMode, 16	kROMOui0, 19
BridgeType, 16	kROMOui1, 19
CAEN_V1718, 16	kROMOui2, 19
CAEN_V2718, 16	kROMRevis0, 19
CAENETControllerV288Answer,	
16	kROMRevis2, 19
CAENETControllerV288Register	
16	kROMSerNum0, 19
CFDCollection, 15	kROMSerNum1, 19
CFDV812Register, 16	kSoftwareClear, 19
cnBusy, 16	kStatus, 18
cnIncorrectHCC, 16	kV1495Board0, 18
cnIncorrectValue, 16	kV1495Board1, 18
cnNoData, 16	kV1495Board2, 18
cnSuccess, 16	kV1495ClockSettings, 17
cnUnrecognizedCode, 16	kV1495ConfigurationROM, 18
cnWrongModuleAddress, 16	kV1495Control, 17
CONT_STORAGE, 16	kV1495DelaySettings, 17
DetectionMode, 17	kV1495FWRevision, 17
EXTRA_SEARCH_WIN_WIDTH	H, kV1495GeoAddress, 17
20	kV1495HWRevision0, 18
FPGAUnitV1495Register, 17	kV1495HWRevision1, 18
IOModuleV262Register, 18	kV1495HWRevision2, 18
kBLTEventNumber, 19	kV1495HWRevision3, 18
kBoardInfo0, 18	kV1495ModuleReset, 17
kBoardInfo1, 18	kV1495OUI0, 18
kControl, 18	kV1495OUI1, 18
kECLLevelWrite, 18	kV1495OUI2, 18
kEventCounter, 19	kV1495OutputSettings, 17
kEventFIFO, 19	kV1495ScalerCounter, 17
kEventFIFOStatusRegister, 19	kV1495SerNum0, 18
kEventFIFOStoredRegister, 19	kV1495SerNum1, 18
kEventStored, 19	kV1495TDCBoardInterface, 17
kFirmwareRev, 19	kV1495TriggerSettings, 17
kGeoAddress, 19	kV1495UserFPGAConfig, 17
	-

kV1495UserFPGAFlashMem, 17	IRQ1, 26
kV1495UserFWRevision, 17	IRQ2, 26
kV288DataBuffer, 16	IRQ3, 26
kV288IRQVector, 16	IRQ4, 26
kV288ModuleReset, 16	IRQ5, 26
kV288Status, 16	IRQ6, 26
kV288Transmission, 16	IRQ7, 26
kV812DeadTimeGroup0, 17	IRQId, 26
kV812DeadTimeGroup1, 17	OutputConf, 28
kV812FixedCode, 17	OutputOff, 28
kV812Info0, 17	OutputOn, 28
kV812Info1, 17	Reset, 28
kV812MajorityThreshold, 17	SetIRQ, 28
kV812OutputWidthGroup0, 17	SinglePulse, 28
kV812OutputWidthGroup1, 17	StartPulser, 28
kV812PatternOfInhibit, 17	StopPulser, 28
kV812TestPulse, 17	TestOutputs, 28
kV812ThresholdChannel0, 17	WaitIRQ, 28
MATCH_WIN_WIDTH, 20	VME::BridgeVx718Control, 30
micro_handshake, 18	~BridgeVx718Control, 30
OLEADING, 17	BridgeVx718Control, 30
OTRAILING, 17	fWord, 32
PAIR, 17	GetAddressIncrement, 30
r100ps, 19	
÷	GetArbiterType, 31
r200ps, 19	GetBusReqLevel, 31
r25ps, 19	GetBusTimeout, 31
r800ps, 19	GetInterruptReq, 31
READ_OK, 18	GetReleaseType, 31
REJECT_MARGIN, 20	GetRequesterType, 31
TDCCollection, 15	GetSysRes, 31
TDCEventCollection, 15	VME::BridgeVx718Status, 33
TDCV1x90Register, 18	~BridgeVx718Status, 33
TRAILEAD, 17	BridgeVx718Status, 33
trailead_edge_lsb, 19	Dump, 33
TRIG_MATCH, 16	fWord, 34
TRIG_TIME_SUB, 20	GetBERR, 33
trig_conf, 19	GetDipSwitch, 34
WIN_OFFSET, 20	GetDTACK, 34
WRITE_OK, 18	GetSystemControl, 34
VME::BridgeVx718, 25	GetSystemReset, 34
$\sim$ BridgeVx718, 27	GetUSBType, 34
BridgeVx718, 27	VME::CAENETControllerV288, 35
CheckConfiguration, 27	~CAENETControllerV288, 36
CheckPCIInterface, 27	CAENETControllerV288, 36
fHasIRQ, 28	FetchBuffer, 36
GetHandle, 27	GetStatus, 36
GetIRQStatus, 27	operator << , 37
GetStatus, 28	operator>>, 37
InputConf, 28	Reset, 36
InputRead, 28	SendBuffer, 37
÷	

WaitForResponse, 37	SetControl, 66
VME::CAENETControllerV288Status,	SetInternalClockPeriod, 66
38	SetInternalTriggerPeriod, 67
~CAENETControllerV288Status,	SetOutputDelay, 67
39	SetOutputPulser, 67
CAENETControllerV288Status, 39	SetOutputPulserPOI, 67
fWord, 39	SetTDCBits, 67
GetOperationStatus, 39	StartScaler, 68
Invalid, 38	StopScaler, 68
OperationStatus, 38	TDCBits, 61
Valid, 38	VME::FPGAUnitV1495Control, 69
VME::CFDV812, 40	~FPGAUnitV1495Control, 71
~CFDV812, 41	ClockSource, 70
CFDV812, 41	Dump, 71
CheckConfiguration, 41	ExternalClock, 70
DeadTimeCalculator, 41	ExternalSignal, 70
GetFixedCode, 42	ExternalTrigger, 70
GetManufacturerId, 42	FPGAUnitV1495Control, 71
GetModuleType, 42	fWord, 73
GetModuleVersion, 42	GetBit, 71
GetSerialNumber, 42	GetClockSource, 71
OutputWidthCalculator, 42	GetScalerStatus, 71
SetDeadTime, 43	GetSignalSource, 71
SetOutputWidth, 43	GetTriggerSource, 71
SetPOI, 43	GetWord, 72
SetThreshold, 43	InternalClock, 70
VME::FPGAUnitV1495, 60	InternalSignal, 70
~FPGAUnitV1495, 62	InternalTrigger, 70
CheckBoardVersion, 62	SetBit, 72
ClearOutputPulser, 62	SetClockSource, 72
DumpFWInformation, 62	SetScalerReset, 72
FPGAUnitV1495, 62	SetScalerStatus, 72
fScalerStarted, 68	SetSignalSource, 73
GetCAENFirmwareRevision, 63	SetTriggerSource, 73
GetControl, 63	SignalSource, 70
GetGeoAddress, 63	TriggerSource, 70
GetHardwareRevision, 63	VME::GenericBoard, 74
GetInternalClockPeriod, 64	~GenericBoard, 75
GetInternalTriggerPeriod, 64	fBaseAddr, 76
GetOutputDelay, 64	fHandle, 76
GetOutputPulser, 64	GenericBoard, 75
GetScalerValue, 65	ReadRegister, 75
GetSerialNumber, 65	WriteRegister, 75
GetTDCBits, 65	VME::GlobalOffset, 77
GetUserFirmwareRevision, 65	coarse, 77
kClear, 61	fine, 77
kReset, 61	VME::IOModuleV262, 90
kTrigger, 61	~IOModuleV262, 90
PulseTDCBits, 65	GetIdentifier, 90
ResetFPGA, 66	GetManufacturerId, 90

GetModuleType, 91	TDCMeasurement, 120
GetModuleVersion, 91	TDCTrailer, 120
GetSerialNumber, 91	VME::TDCMeasurement, 124
IOModuleV262, 90	~TDCMeasurement, 124
VME::PCIInterfaceA2818, 103	Dump, 125
~PCIInterfaceA2818, 103	fEvents, 126
fHandle, 103	fMap, 126
GetFWRevision, 103	GetBunchId, 125
PCIInterfaceA2818, 103	GetChannelId, 125
VME::TDCErrorFlag, 116	GetETTT, 125
~TDCErrorFlag, 117	GetEventId, 125
Dump, 117	GetLeadingTime, 125
fWord, 118	GetTDCId, 125
GetWord, 117	GetToT, 125
HasGroupError, 117	GetTrailingTime, 125
HasInternalChipError, 117	NumErrors, 126
HasL1BufferOverflow, 117	NumEvents, 126
HasReachedEventSizeLimit, 117	SetEventsCollection, 126
HasReadoutFIFOOverflow, 117	TDCMeasurement, 124
HasTriggerFIFOOverflow, 117	VME::TDCV1x90, 127
operator <<, 118	∼TDCV1x90, 129
TDCErrorFlag, 117	abort, 130
VME::TDCEvent, 119	CheckConfiguration, 130
$\sim$ TDCEvent, 121	DisableChannel, 130
Dump, 121	DLL_Direct_LowRes, 129
ETTT, 120	DLL_PLL_HighRes, 129
EventType, 120	DLL_PLL_LowRes, 129
Filler, 120	DLL_PLL_MedRes, 129
fWord, 123	DLLMode, 129
GetBunchId, 121	EnableChannel, 130
GetChannelId, 121	fAcquisitionMode, 143
GetErrorFlags, 121	fBuffer, 143
GetETTT, 121	fDetectionMode, 143
GetEventCount, 121	fErrorMarks, 143
GetEventId, 122	FetchEvents, 130
GetGeo, 122	fVerb, 143
GetStatus, 122	fWindowWidth, 143
GetTDCId, 122	gEnd, 143
GetTime, 122	GetAcquisitionMode, 130
GetType, 122	GetBLTEventNumberRegister, 131
GetWidth, 122	GetChannelDeadTime, 131
GetWord, 123	GetControl, 131
GetWordCount, 123	GetDetectionMode, 131
GlobalHeader, 120	GetDLLClock, 132
GlobalTrailer, 120	GetErrorMarks, 132
IsTrailing, 123	GetETTT, 132
SetWord, 123	GetEventCounter, 132
TDCError, 120	GetEventStored, 132
TDCEvent, 121	GetFIFOSize, 132
TDCHeader, 120	GetFirmwareRevision, 133
<i>,</i>	,

GetGlobalOffset, 133	GetBusError, 145
GetModel, 133	GetCompensation, 145
GetOUI, 133	GetEmptyEvent, 145
GetPoI, 134	GetETTT, 145
GetRCAdjust, 134	GetEventFIFO, 145
GetResolution, 134	GetMEBAccess, 145
GetSerialNumber, 134	GetSRAMCompensation, 145
GetStatus, 134	GetSWTermination, 145
GetTDCEncapsulation, 135	GetTermination, 145
GetTestMode, 135	GetTestFIFO, 145
GetTriggerConfiguration, 135	GetValue, 145
GetWindowOffset, 135	SetAlign64, 145
GetWindowWidth, 136	SetBusError, 145
HardwareReset, 136	SetCompensation, 146
nchannels, 143	SetEmptyEvent, 146
pair_lead_res, 143	SetETTT, 146
pair_width_res, 143	SetEventFIFO, 146
ReadAcquisitionMode, 136	SetMEBAccess, 146
ReadDetectionMode, 136	SetSRAMCompensation, 146
SetAcquisitionMode, 136	SetSWTermination, 146
SetBLTEventNumberRegister, 136	SetTermination, 146
SetChannelDeadTime, 137	SetTestFIFO, 146
SetContinuousStorage, 137	TDCV1x90Control, 145
SetControl, 137	VME::TDCV1x90Opcodes, 21
SetDetectionMode, 137	AUTOLOAD_DEF_CONFI, 24
SetDLLClock, 138	AUTOLOAD_USER_CONF, 24
SetErrorMarks, 138	CLEAR_KEEP_TOKEN, 24
SetETTT, 138	CONT_STOR, 24
SetFIFOSize, 139	DEFAULT_SETUP_REG, 24
SetGlobalOffset, 139	DIS_ALL_CHANNEL, 24
SetLSBTraileadEdge, 139	DIS_CHANNEL, 24
SetPairModeResolution, 139	DIS_ERROR_BYPASS, 24
SetPoI, 139	DIS_ERROR_MARK, 24
SetRCAdjust, 140	DIS_HEAD_TRAILER, 24
SetStatus, 140	DIS_SUB_TRG, 24
SetTDCEncapsulation, 140	DISABLE_TEST_MODE, 24
SetTestMode, 140	EN_ALL_CHANNEL, 24
SetTriggerMatching, 141	EN_CHANNEL, 24
SetVerboseLevel, 141	EN_ERROR_BYPASS, 24
SetWindowOffset, 141	EN_ERROR_MARK, 24
SetWindowWidth, 141	EN_HEAD_TRAILER, 24
SoftwareClear, 142	EN_SUB_TRG, 24
SoftwareReset, 142	ENABLE_TEST_MODE, 24
TDCV1x90, 129	LOAD_DEF_CONFIG, 24
WaitMicro, 142	LOAD_USER_CONFIG, 24
VME::TDCV1x90Control, 144	READ_ACQ_MOD, 24
~TDCV1x90Control, 145	READ_ADJUST_CH, 24
Dump, 145	READ_DEAD_TIME, 24
fWord, 147	READ_DETECTION, 24
GetAlign64, 145	READ_DLL_LOCK, 24
<b>5</b>	= <b>-</b> '

READ_EEPROM, 24	BusError, 149
READ_EN_PATTERN, 24	DataReady, 149
READ_EN_PATTERN32, 24	Dump, 149
READ_ERROR_STATUS, 24	Error, 149
READ_ERROR_TYPES, 24	Full, 150
READ_EVENT_SIZE, 24	fWord, 150
READ_FIFO_SIZE, 24	GetValue, 150
READ_GLOB_OFFS, 24	HeadersEnabled, 150
READ_HEAD_TRAILER, 24	PairMode, 150
READ_MICRO_REV, 24	Purged, 150
READ_RC_ADJ, 24	R_100ps, 149
READ_RES, 24	R_200ps, 149
READ_SETUP_REG, 24	R_25ps, 149
READ_SETUP_SCANPATH, 24	R_800ps, 149
READ_SPARE, 24	Resolution, 150
READ_STATUS_STREAM, 24	
	TDCResolution, 149
READ_TDC_ID, 24	TDCV1x90Status, 149
READ_TRG_CONF, 24	TerminationOn, 150
RESET_DLL_PLL, 24	TriggerLost, 150
REV_DATE_MICRO_FW, 24	TriggerMatching, 150
SAVE_RC_ADJ, 24	VME::trailead_t, 151
SAVE_USER_CONFIG, 24	ettt, 151
SET_ADJUST_CH, 24	event_count, 151
SET_DEAD_TIME, 24	leading, 151
SET_DETECTION, 24	total_hits, 151
SET_DLL_CLOCK, 24	trailing, 151
SET_ERROR_TYPES, 24	VMEReader, 152
SET_EVENT_SIZE, 24	$\sim$ VMEReader, 155
SET_FIFO_SIZE, 24	Abort, 156
SET_GLOB_OFFS, 24	AddCFD, 156
SET_KEEP_TOKEN, 24	AddFPGAUnit, 156
SET_PAIR_RES, 24	AddHVModule, 156
SET_RC_ADJ, 24	AddIOModule, 157
SET_REJ_MARGIN, 24	AddTDC, 157
SET_SW_MARGIN, 24	fBridge, 160
SET_TDC_TSET_OUTPUT, 24	fCAENET, 160
SET_TR_LEAD_LSB, 24	fCFDCollection, 160
SET_WIN_OFFS, 24	fFPGA, 160
SET_WIN_WIDTH, 24	fHV, 161
TRG MATCH, 24	fIsPulserStarted, 161
UPDATE_SETUP_REG, 24	fOnSocket, 161
UPDATE_SETUP_TDC, 24	fOutputFiles, 161
WRITE EEPROM, 24	fSG, 161
<del>-</del>	
WRITE_EN_PATTERN, 24	fTDCCollection, 161
WRITE_EN_PATTERN32, 24	GetCFDCollection 157
WRITE_SETUP_REG, 24 WRITE_SPARE, 24	GetCFDCollection, 157
W/RITH SP/LRH 7/1	CAPPOATI 'A 157
	GetFPGAUnit, 157
VME::TDCV1x90Status, 148	GetHVModule, 157

```
GetNumTDC, 158
   GetOutputFile, 158
   GetRunNumber, 158
   GetTDC, 158
    GetTDCCollection, 158
    OutputFiles, 155
    ReadXML, 158
    SendClear, 159
    SendPulse, 159
    SetOutputFile, 159
    StartPulser, 160
    StopPulser, 160
    UseSocket, 160
    VMEReader, 155
Vmon
   NIM::HVModuleN470ChannelValues,
   NIM::HVModuleN470Values, 89
Vsel
   NIM::HVModuleN470Values::ChannelStatus,
WaitForResponse
    VME::CAENETControllerV288, 37
WaitIRQ
    VME::BridgeVx718, 28
WaitMicro
    VME::TDCV1x90, 142
WEBSOCKET_CLIENT
    Socket, 106
WIN OFFSET
    VME, 20
WRITE_OK
    VME, 18
WRITE_EEPROM
    VME::TDCV1x90Opcodes, 24
WRITE_EN_PATTERN
    VME::TDCV1x90Opcodes, 24
WRITE_EN_PATTERN32
    VME::TDCV1x90Opcodes, 24
WRITE_SETUP_REG
    VME::TDCV1x90Opcodes, 24
WRITE SPARE
    VME::TDCV1x90Opcodes, 24
WriteRegister
   NIM::HVModuleN470, 83
    VME::GenericBoard, 75
```