2015 Test beam Run Control

Generated by Doxygen 1.6.1

Wed Jul 29 19:40:20 2015

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Data Structures

- class Client

  Base client object for the socket.

- class HTTPMessage

  Message to be transmitted through a WebSocket protocol.

- class Messenger

  Base master object for the socket.

- class Socket

  Base socket object from which clients/master from a socket inherit.

- class SocketMessage

  Socket-passed message type.

Chapter 6

Namespace Documentation

6.1 NIM Namespace Reference

Data Structures

- class HVModuleN470Values

  General monitoring values for the HV power supply.

- class HVModuleN470ChannelValues

  Single channel monitoring values for the HV power supply.

- class HVModuleN470

Enumerations

- enum HVModuleN470Opcodes {

  kN470GeneralInfo = 0x00, kN470MonStatus = 0x01, kN470OperationalParams = 0x02, kN470V0Value = 0x03,

  kN470I0Value = 0x04, kN470V1Value = 0x05, kN470I1Value = 0x06, kN470TripValue = 0x07,

  kN470RampUpValue = 0x08, kN470RampDownValue = 0x09, kN470ChannelOn = 0x0a, kN470ChannelOff = 0x0b,

  kN470KillAllChannels = 0x0c, kN470ClearAlarm = 0x0d, kN470EnableFrontPanel = 0x0e, kN470DisableFrontPanel = 0x0f,

  kN470TTLLevel = 0x10, kN470NIMLevel = 0x11 }

6.1.1 Enumeration Type Documentation

6.1.1.1 enum NIM::HVModuleN470Opcodes

Enumerator:

kN470GeneralInfo

kN470MonStatus

kN470OperationalParams

kN470V0Value

kN470I0Value

kN470V1Value

kN470I1Value

kN470TripValue

kN470RampUpValue

kN470RampDownValue

kN470ChannelOn

kN470ChannelOff

kN470KillAllChannels

kN470ClearAlarm

kN470EnableFrontPanel

kN470DisableFrontPanel

kN470TTLLevel

kN470NIMLevel

6.2 VME Namespace Reference

Namespaces

- namespace TDCV1x90Opcodes

Data Structures

- class BridgeVx718Status
- class BridgeVx718Control
- class BridgeVx718

  class defining the VME bridge

- class CAENETControllerV288Status
- class CAENETControllerV288

  Handler for a CAEN V288 CAENET controller.

- class CFDV812

  Controller for a CAEN V812 constant fraction discriminator.

- class FPGAUnitV1495Control
- class FPGAUnitV1495
- class GenericBoard
- class IOModuleV262
- class PCIInterfaceA2818
- class TDCErrorFlag

  Error flags handler.

- class TDCEvent

  HPTDC event parser.

- class TDCMeasurement
- struct GlobalOffset
- struct trailead_t
- class TDCV1x90Status

  TDC status register.

- class TDCV1x90Control

  TDC control register.

- class TDCV1x90

Typedefs

- typedef std::map< uint32_t, VME::CFDV812 ∗ > CFDCollection

  Mapper from physical VME addresses to pointers to CFD objects.

- typedef std::vector< TDCEvent > TDCEventCollection
- typedef std::map< uint32_t, VME::TDCV1x90 ∗ > TDCCollection

  Mapper from physical VME addresses to pointers to TDC objects.

Enumerations

- enum BridgeType { CAEN_V1718, CAEN_V2718 }

  Compatible bridge types.

- enum CAENETControllerV288Register {

  kV288DataBuffer = 0x00, kV288Status = 0x02, kV288Transmission = 0x04, kV288ModuleReset = 0x06,

  kV288IRQVector = 0x08 }
- enum CAENETControllerV288Answer {

  cnSuccess = 0x0000, cnBusy = 0xff00, cnUnrecognizedCode = 0xff01, cnIncorrectValue = 0xff02,

  cnNoData = 0xfffd, cnIncorrectHCC = 0xfffe, cnWrongModuleAddress = 0xffff }
- enum CFDV812Register {

  kV812ThresholdChannel0 = 0x00, kV812OutputWidthGroup0 = 0x40, kV812OutputWidthGroup1 = 0x42, kV812DeadTimeGroup0 = 0x44,

  kV812DeadTimeGroup1 = 0x46, kV812MajorityThreshold = 0x48, kV812PatternOfInhibit = 0x4a, kV812TestPulse = 0x4c,

  kV812FixedCode = 0xfa, kV812Info0 = 0xfc, kV812Info1 = 0xfe }
- enum FPGAUnitV1495Register {

  kV1495ScalerCounter = 0x100c, kV1495DelaySettings = 0x1010, kV1495UserFWRevision = 0x1014, kV1495TDCBoardInterface = 0x1018,

  kV1495ClockSettings = 0x101c, kV1495Control = 0x1020, kV1495TriggerSettings = 0x1024, kV1495OutputSettings = 0x1028,

  kV1495GeoAddress = 0x8008, kV1495UserFPGAFlashMem = 0x8014, kV1495UserFPGAConfig = 0x8016, kV1495ModuleReset = 0x800a,

  kV1495FWRevision = 0x800c, kV1495ConfigurationROM = 0x8100, kV1495OUI2 = 0x8124, kV1495OUI1 = 0x8128,

  kV1495OUI0 = 0x812c, kV1495Board2 = 0x8134, kV1495Board1 = 0x8138, kV1495Board0 = 0x813c,

  kV1495HWRevision3 = 0x8140, kV1495HWRevision2 = 0x8144, kV1495HWRevision1 = 0x8148, kV1495HWRevision0 = 0x814c,

  kV1495SerNum0 = 0x8180, kV1495SerNum1 = 0x8184 }

- enum IOModuleV262Register {

  kECLLevelWrite = 0x04, kNIMLevelWrite = 0x06, kNIMPulseWrite = 0x08, kNIMPulseRead = 0x0a,

  kIdentifier = 0xfa, kBoardInfo0 = 0xfc, kBoardInfo1 = 0xfe }
- enum AcquisitionMode { CONT_STORAGE, TRIG_MATCH }

  TDC acquisition mode.

- enum DetectionMode { PAIR = 0x0, OTRAILING = 0x1, OLEADING = 0x2, TRAILEAD = 0x3 }
- enum trig_conf {

  MATCH_WIN_WIDTH = 0, WIN_OFFSET = 1, EXTRA_SEARCH_WIN_-WIDTH = 2, REJECT_MARGIN = 3,

  TRIG_TIME_SUB = 4 }
- enum trailead_edge_lsb { r800ps = 0, r200ps = 1, r100ps = 2, r25ps = 3 }
- enum micro_handshake { WRITE_OK = 0, READ_OK = 1 }
- enum TDCV1x90Register {

  kOutputBuffer = 0x0000, kControl = 0x1000, kStatus = 0x1002, kInterruptLevel = 0x100a,

  kInterruptVector = 0x100c, kGeoAddress = 0x100e, kMCSTBase = 0x1010, kMCSTControl = 0x1012,

  kModuleReset = 0x1014, kSoftwareClear = 0x1016, kEventCounter = 0x101c, kEventStored = 0x1020,

  kBLTEventNumber = 0x1024, kFirmwareRev = 0x1026, kMicro = 0x102e, kMi-croHandshake = 0x1030,

  kEventFIFO = 0x1038, kEventFIFOStoredRegister = 0x103c, kEventFIFOSta-tusRegister = 0x103e, kROMOui2 = 0x4024,

  kROMOui1 = 0x4028, kROMOui0 = 0x402c, kROMBoard2 = 0x4034, kROM-Board1 = 0x4038,

  kROMBoard0 = 0x403c, kROMRevis3 = 0x4040, kROMRevis2 = 0x4044, kROMRevis1 = 0x4048,

  kROMRevis0 = 0x404c, kROMSerNum1 = 0x4080, kROMSerNum0 = 0x4084 }

6.2.1 Typedef Documentation

6.2.1.1 typedef std::map<uint32_t,VME::CFDV812∗> VME::CFDCollection

Mapper from physical VME addresses to pointers to CFD objects.

6.2.1.2 typedef std::map<uint32_t,VME::TDCV1x90∗> VME::TDCCollection

Mapper from physical VME addresses to pointers to TDC objects.

6.2.1.3 typedef std::vector<TDCEvent> VME::TDCEventCollection

6.2.2 Enumeration Type Documentation

6.2.2.1 enum VME::AcquisitionMode

TDC acquisition mode.

Author:

Laurent Forthomme <laurent.forthomme@cern.ch>

Enumerator:

CONT_STORAGE
TRIG_MATCH

6.2.2.2 enum VME::BridgeType

Compatible bridge types.

Enumerator:

CAEN_V1718
CAEN_V2718

6.2.2.3 enum VME::CAENETControllerV288Answer

Enumerator:

cnSuccess
cnBusy
cnUnrecognizedCode
cnIncorrectValue
cnNoData
cnIncorrectHCC
cnWrongModuleAddress

6.2.2.4 enum VME::CAENETControllerV288Register

Enumerator:

kV288DataBuffer
kV288Status
kV288Transmission
kV288ModuleReset
kV288IRQVector

6.2.2.5 enum VME::CFDV812Register

Enumerator:

kV812ThresholdChannel0

kV812OutputWidthGroup0

kV812OutputWidthGroup1

kV812DeadTimeGroup0

kV812DeadTimeGroup1

kV812MajorityThreshold

kV812PatternOfInhibit

kV812TestPulse

kV812FixedCode

kV812Info0

kV812Info1

6.2.2.6 enum VME::DetectionMode

Enumerator:

PAIR

OTRAILING

OLEADING

TRAILEAD

6.2.2.7 enum VME::FPGAUnitV1495Register

Enumerator:

kV1495ScalerCounter

kV1495DelaySettings

kV1495UserFWRevision

kV1495TDCBoardInterface

kV1495ClockSettings

kV1495Control

kV1495TriggerSettings

kV1495OutputSettings

kV1495GeoAddress

kV1495UserFPGAFlashMem

kV1495UserFPGAConfig

kV1495ModuleReset

kV1495FWRevision

kV1495ConfigurationROM

kV1495OUI2

kV1495OUI1

kV1495OUI0

kV1495Board2

kV1495Board1

kV1495Board0

kV1495HWRevision3

kV1495HWRevision2

kV1495HWRevision1

kV1495HWRevision0

kV1495SerNum0

kV1495SerNum1

6.2.2.8 enum VME::IOModuleV262Register

Enumerator:

kECLLevelWrite

kNIMLevelWrite

kNIMPulseWrite

kNIMPulseRead

kIdentifier

kBoardInfo0

kBoardInfo1

6.2.2.9 enum VME::micro_handshake

Enumerator:

WRITE_OK	Is the TDC ready for writing?
READ_OK	Is the TDC ready for reading?

6.2.2.10 enum VME::TDCV1x90Register

Enumerator:

kOutputBuffer

kControl

kStatus

kInterruptLevel

kInterruptVector

kGeoAddress

kMCSTBase

kMCSTControl

kModuleReset

kSoftwareClear

kEventCounter

kEventStored

kBLTEventNumber

kFirmwareRev

kMicro

kMicroHandshake

kEventFIFO

kEventFIFOStoredRegister

kEventFIFOStatusRegister

kROMOui2

kROMOui1

kROMOui0

kROMBoard2

kROMBoard1

kROMBoard0

kROMRevis3

kROMRevis2

kROMRevis1

kROMRevis0

kROMSerNum1

kROMSerNum0

6.2.2.11 enum VME::trailead_edge_lsb

Enumerator:

r800ps

r200ps

r100ps

r25ps

6.2.2.12 enum VME::trig_conf

Enumerator:

MATCH_WIN_WIDTH

WIN_OFFSET

EXTRA_SEARCH_WIN_WIDTH

REJECT_MARGIN

TRIG_TIME_SUB

6.3 VME::TDCV1x90Opcodes Namespace Reference

Functions

- Opcode TRG_MATCH (0x0000)
- Opcode CONT_STOR (0x0100)
- Opcode READ_ACQ_MOD (0x0200)
- Opcode SET_KEEP_TOKEN (0x0300)
- Opcode CLEAR_KEEP_TOKEN (0x0400)
- Opcode LOAD_DEF_CONFIG (0x0500)
- Opcode SAVE_USER_CONFIG (0x0600)
- Opcode LOAD_USER_CONFIG (0x0700)
- Opcode AUTOLOAD_USER_CONF (0x0800)
- Opcode AUTOLOAD_DEF_CONFI (0x0900)
- Opcode SET_WIN_WIDTH (0x1000)
- Opcode SET_WIN_OFFS (0x1100)
- Opcode SET_SW_MARGIN (0x1200)
- Opcode SET_REJ_MARGIN (0x1300)
- Opcode EN_SUB_TRG (0x1400)
- Opcode DIS_SUB_TRG (0x1500)
- Opcode READ_TRG_CONF (0x1600)
- Opcode SET_DETECTION (0x2200)
- Opcode READ_DETECTION (0x2300)
- Opcode SET_TR_LEAD_LSB (0x2400)
- Opcode SET_PAIR_RES (0x2500)
- Opcode READ_RES (0x2600)
- Opcode SET_DEAD_TIME (0x2800)
- Opcode READ_DEAD_TIME (0x2900)
- Opcode EN_HEAD_TRAILER (0x3000)
- Opcode DIS_HEAD_TRAILER (0x3100)
- Opcode READ_HEAD_TRAILER (0x3200)
- Opcode SET_EVENT_SIZE (0x3300)
- Opcode READ_EVENT_SIZE (0x3400)
- Opcode EN_ERROR_MARK (0x3500)
- Opcode DIS_ERROR_MARK (0x3600)
- Opcode EN_ERROR_BYPASS (0x3700)
- Opcode DIS_ERROR_BYPASS (0x3800)
- Opcode SET_ERROR_TYPES (0x3900)
- Opcode READ_ERROR_TYPES (0x3a00)
- Opcode SET_FIFO_SIZE (0x3b00)
- Opcode READ_FIFO_SIZE (0x3c00)
- Opcode EN_CHANNEL (0x4000)
- Opcode DIS_CHANNEL (0x4100)
- Opcode EN_ALL_CHANNEL (0x4200)
- Opcode DIS_ALL_CHANNEL (0x4300)
- Opcode WRITE_EN_PATTERN (0x4400)

- Opcode READ_EN_PATTERN (0x4500)
- Opcode WRITE_EN_PATTERN32 (0x4600)
- Opcode READ_EN_PATTERN32 (0x4700)
- Opcode SET_GLOB_OFFS (0x5000)
- Opcode READ_GLOB_OFFS (0x5100)
- Opcode SET_ADJUST_CH (0x5200)
- Opcode READ_ADJUST_CH (0x5200)
- Opcode SET_RC_ADJ (0x5400)
- Opcode READ_RC_ADJ (0x5500)
- Opcode SAVE_RC_ADJ (0x5600)
- Opcode READ_TDC_ID (0x6000)
- Opcode READ_MICRO_REV (0x6100)
- Opcode RESET_DLL_PLL (0x6200)
- Opcode WRITE_SETUP_REG (0x7000)
- Opcode READ_SETUP_REG (0x7100)
- Opcode UPDATE_SETUP_REG (0x7200)
- Opcode DEFAULT_SETUP_REG (0x7300)
- Opcode READ_ERROR_STATUS (0x7400)
- Opcode READ_DLL_LOCK (0x7500)
- Opcode READ_STATUS_STREAM (0x7600)
- Opcode UPDATE_SETUP_TDC (0x7700)
- Opcode WRITE_EEPROM (0xc000)
- Opcode READ_EEPROM (0xc100)
- Opcode REV_DATE_MICRO_FW (0xc200)
- Opcode WRITE_SPARE (0xc300)
- Opcode READ_SPARE (0xc400)
- Opcode ENABLE_TEST_MODE (0xc500)
- Opcode DISABLE_TEST_MODE (0xc600)
- Opcode SET_TDC_TSET_OUTPUT (0xc700)
- Opcode SET_DLL_CLOCK (0xc800)
- Opcode READ_SETUP_SCANPATH (0xc900)

6.3.1 Function Documentation

6.3.1.1 Opcode VME::TDCV1x90Opcodes::AUTOLOAD_DEF_CONFI (0x0900)

6.3.1.2 Opcode VME::TDCV1x90Opcodes::AUTOLOAD_USER_CONF (0x0800)

6.3.1.3 Opcode VME::TDCV1x90Opcodes::CLEAR_KEEP_TOKEN (0x0400)

6.3.1.4 Opcode VME::TDCV1x90Opcodes::CONT_STOR (0x0100)

6.3.1.5 Opcode VME::TDCV1x90Opcodes::DEFAULT_SETUP_REG (0x7300)

6.3.1.6 Opcode VME::TDCV1x90Opcodes::DIS_ALL_CHANNEL (0x4300)

6.3.1.7 Opcode VME::TDCV1x90Opcodes::DIS_CHANNEL (0x4100)

6.3.1.8 Opcode VME::TDCV1x90Opcodes::DIS_ERROR_BYPASS (0x3800)

6.3.1.9 Opcode VME::TDCV1x90Opcodes::DIS_ERROR_MARK (0x3600)

6.3.1.10 Opcode VME::TDCV1x90Opcodes::DIS_HEAD_TRAILER (0x3100)

6.3.1.11 Opcode VME::TDCV1x90Opcodes::DIS_SUB_TRG (0x1500)

6.3.1.12 Opcode VME::TDCV1x90Opcodes::DISABLE_TEST_MODE (0xc600)

6.3.1.13 Opcode VME::TDCV1x90Opcodes::EN_ALL_CHANNEL (0x4200)

6.3.1.14 Opcode VME::TDCV1x90Opcodes::EN_CHANNEL (0x4000)

6.3.1.15 Opcode VME::TDCV1x90Opcodes::EN_ERROR_BYPASS (0x3700)

6.3.1.16 Opcode VME::TDCV1x90Opcodes::EN_ERROR_MARK (0x3500)

6.3.1.17 Opcode VME::TDCV1x90Opcodes::EN_HEAD_TRAILER (0x3000)

6.3.1.18 Opcode VME::TDCV1x90Opcodes::EN_SUB_TRG (0x1400)

6.3.1.19 Opcode VME::TDCV1x90Opcodes::ENABLE_TEST_MODE (0xc500)

6.3.1.20 Opcode VME::TDCV1x90Opcodes::LOAD_DEF_CONFIG (0x0500)

6.3.1.21 Opcode VME::TDCV1x90Opcodes::LOAD_USER_CONFIG (0x0700)

6.3.1.22 Opcode VME::TDCV1x90Opcodes::READ_ACQ_MOD (0x0200)

6.3.1.23 Opcode VME::TDCV1x90Opcodes::READ_ADJUST_CH (0x5200)

6.3.1.24 Opcode VME::TDCV1x90Opcodes::READ_DEAD_TIME (0x2900)

6.3.1.25 Opcode VME::TDCV1x90Opcodes::READ_DETECTION (0x2300)

6.3.1.26 Opcode VME::TDCV1x90Opcodes::READ_DLL_LOCK (0x7500)

6.3.1.27 Opcode VME::TDCV1x90Opcodes::READ_EEPROM (0xc100)

Chapter 7

Data Structure Documentation

7.1 VME::BridgeVx718 Class Reference

class defining the VME bridge

`#include <VME_BridgeVx718.h>`Inheritance diagram for VME::BridgeVx718:Collaboration diagram for VME::BridgeVx718:

Public Types

- enum IRQId {
  IRQ1 = 0x1, IRQ2 = 0x2, IRQ3 = 0x4, IRQ4 = 0x8,
  IRQ5 = 0x10, IRQ6 = 0x20, IRQ7 = 0x40 }

Public Member Functions

- BridgeVx718 (const char ∗device, BridgeType type)

  Constructor.
- ∼BridgeVx718 ()

  Destructor.
- int32_t GetHandle () const

  Bridge's handle value.
- void CheckPCIInterface (const char ∗device) const
- void CheckConfiguration () const
- void TestOutputs () const
- void Reset () const

  Perform a system reset of the module.

- BridgeVx718Status GetStatus () const
- void SetIRQ (unsigned int irq, bool enable=true)
- void WaitIRQ (unsigned int irq, unsigned long timeout=1000) const
- unsigned int GetIRQStatus () const
- void OutputConf (CVOutputSelect output) const

    Set and control the output lines.

- void OutputOn (unsigned short output) const
- void OutputOff (unsigned short output) const
- void InputConf (CVInputSelect input) const

    Set and read the input lines.

- void InputRead (CVInputSelect input) const
- void StartPulser (double period, double width, unsigned int num_pulses=0) const
- void StopPulser () const
- void SinglePulse (unsigned short channel) const

Private Attributes

- bool fHasIRQ

7.1.1 Detailed Description

class defining the VME bridge This class initializes the CAEN V1718 VME bridge in order to control the crate.

Author:

Laurent Forthomme <laurent.forthomme@cern.ch>
Bob Velghe <bob.velghe@cern.ch>

Date:

Jun 2010

7.1.2 Member Enumeration Documentation

7.1.2.1 enum VME::BridgeVx718::IRQId

Enumerator:

IRQ1
IRQ2
IRQ3
IRQ4
IRQ5
IRQ6
IRQ7

7.1.3 Constructor & Destructor Documentation

7.1.3.1 VME::BridgeVx718::BridgeVx718 (const char ∗ *device*, BridgeType *type*)

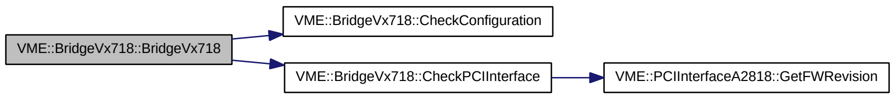
Constructor. Bridge class constructor

Parameters:

← ***device*** Device identifier on the VME crate

← ***type*** Device type (1718/2718)

Here is the call graph for this function:


VME::BridgeVx718::BridgeVx718
VME::BridgeVx718::CheckConfiguration
VME::BridgeVx718::CheckPCIInterface
VME::PCIInterfaceA2818::GetFWRevision

7.1.3.2 VME::BridgeVx718::∼BridgeVx718 ()

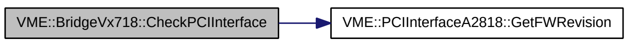
Destructor. Bridge class destructor

7.1.4 Member Function Documentation

7.1.4.1 void VME::BridgeVx718::CheckConfiguration () const

7.1.4.2 void VME::BridgeVx718::CheckPCIInterface (const char ∗ *device*) const

Here is the call graph for this function:


VME::BridgeVx718::CheckPCIInterface
VME::PCIInterfaceA2818::GetFWRevision

7.1.4.3 int32_t VME::BridgeVx718::GetHandle () const `[inline]`

Bridge's handle value.

Returns:

Handle value

7.1.4.4 unsigned int VME::BridgeVx718::GetIRQStatus () const

7.1.4.5 BridgeVx718Status VME::BridgeVx718::GetStatus () const

7.1.4.6 void VME::BridgeVx718::InputConf (CVInputSelect *input*) const

Set and read the input lines.

7.1.4.7 void VME::BridgeVx718::InputRead (CVInputSelect *input*) const

7.1.4.8 void VME::BridgeVx718::OutputConf (CVOutputSelect *output*) const

Set and control the output lines.

7.1.4.9 void VME::BridgeVx718::OutputOff (unsigned short *output*) const

7.1.4.10 void VME::BridgeVx718::OutputOn (unsigned short *output*) const

7.1.4.11 void VME::BridgeVx718::Reset () const

Perform a system reset of the module.

7.1.4.12 void VME::BridgeVx718::SetIRQ (unsigned int *irq*, bool *enable* = `true`)

7.1.4.13 void VME::BridgeVx718::SinglePulse (unsigned short *channel*) const

Here is the call graph for this function:

7.1.4.14 void VME::BridgeVx718::StartPulser (double *period*, double *width*, unsigned int *num_pulses* = `0`) const

7.1.4.15 void VME::BridgeVx718::StopPulser () const

7.1.4.16 void VME::BridgeVx718::TestOutputs () const

Here is the call graph for this function:

7.1.4.17 void VME::BridgeVx718::WaitIRQ (unsigned int *irq*, unsigned long *timeout* = `1000`) const

7.1.5 Field Documentation

7.1.5.1 bool VME::BridgeVx718::fHasIRQ `[private]`

The documentation for this class was generated from the following files:

- include/VME_BridgeVx718.h
- src/VME_BridgeVx718.cpp

7.2 VME::BridgeVx718Control Class Reference

`#include <VME_BridgeVx718.h>`

Public Member Functions

- BridgeVx718Control (uint16_t word)
- virtual ∼BridgeVx718Control ()
- bool GetArbiterType () const

  Arbiter type.
- bool GetRequesterType () const

  Requester type.
- bool GetReleaseType () const

  Release type.
- unsigned int GetBusReqLevel () const
- bool GetInterruptReq () const
- bool GetSysRes () const
- bool GetBusTimeout () const

  VME bus timeout.
- bool GetAddressIncrement () const

  Address Increment.

Private Attributes

- uint16_t fWord

7.2.1 Constructor & Destructor Documentation

7.2.1.1 VME::BridgeVx718Control::BridgeVx718Control (uint16_t *word*) `[inline]`

7.2.1.2 virtual VME::BridgeVx718Control::∼BridgeVx718Control () `[inline, virtual]`

7.2.2 Member Function Documentation

7.2.2.1 bool VME::BridgeVx718Control::GetAddressIncrement () const `[inline]`

Address Increment.

Returns:

true if enabled, else false (FIFO mode)

7.2.2.2 bool VME::BridgeVx718Control::GetArbiterType () const `[inline]`

Arbiter type.

Returns:

true if "Round Robin", else fixed priority

7.2.2.3 unsigned int VME::BridgeVx718Control::GetBusReqLevel () const `[inline]`

7.2.2.4 bool VME::BridgeVx718Control::GetBusTimeout () const `[inline]`

VME bus timeout.

Returns:

true if 1400 us, else 50 us

7.2.2.5 bool VME::BridgeVx718Control::GetInterruptReq () const `[inline]`

7.2.2.6 bool VME::BridgeVx718Control::GetReleaseType () const `[inline]`

Release type.

Returns:

true if release on request, else release when done

7.2.2.7 bool VME::BridgeVx718Control::GetRequesterType () const `[inline]`

Requester type.

Returns:

true if demand, else fair

7.2.2.8 bool VME::BridgeVx718Control::GetSysRes () const `[inline]`

7.2.3 Field Documentation

7.2.3.1 uint16_t VME::BridgeVx718Control::fWord `[private]`

The documentation for this class was generated from the following file:

- include/VME_BridgeVx718.h

7.3 VME::BridgeVx718Status Class Reference

```
#include <VME_BridgeVx718.h>
```

Public Member Functions

- BridgeVx718Status (uint16_t word)
- virtual ∼BridgeVx718Status ()
- void Dump () const
- bool GetSystemReset () const
- bool GetSystemControl () const
- bool GetDTACK () const
- bool GetBERR () const
- bool GetDipSwitch (unsigned int sw) const
- bool GetUSBType () const

Private Attributes

- uint16_t fWord

7.3.1 Constructor & Destructor Documentation

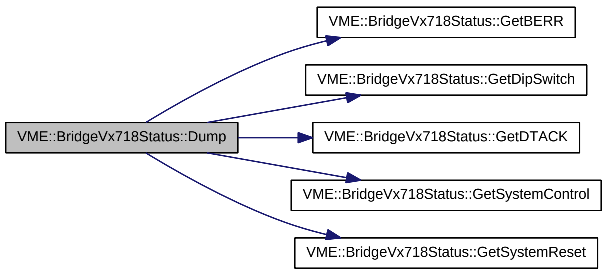
7.3.1.1 VME::BridgeVx718Status::BridgeVx718Status (uint16_t *word*) `[inline]`

7.3.1.2 virtual VME::BridgeVx718Status::∼BridgeVx718Status () `[inline, virtual]`

7.3.2 Member Function Documentation

7.3.2.1 void VME::BridgeVx718Status::Dump () const `[inline]`

Here is the call graph for this function:


VME::BridgeVx718Status::GetBERR
VME::BridgeVx718Status::GetDipSwitch
VME::BridgeVx718Status::Dump
VME::BridgeVx718Status::GetDTACK
VME::BridgeVx718Status::GetSystemControl
VME::BridgeVx718Status::GetSystemReset

7.3.2.2 bool VME::BridgeVx718Status::GetBERR () const `[inline]`

7.3.2.3 bool VME::BridgeVx718Status::GetDipSwitch (unsigned int *sw*) const `[inline]`

7.3.2.4 bool VME::BridgeVx718Status::GetDTACK () const `[inline]`

7.3.2.5 bool VME::BridgeVx718Status::GetSystemControl () const `[inline]`

7.3.2.6 bool VME::BridgeVx718Status::GetSystemReset () const `[inline]`

7.3.2.7 bool VME::BridgeVx718Status::GetUSBType () const `[inline]`

7.3.3 Field Documentation

7.3.3.1 uint16_t VME::BridgeVx718Status::fWord `[private]`

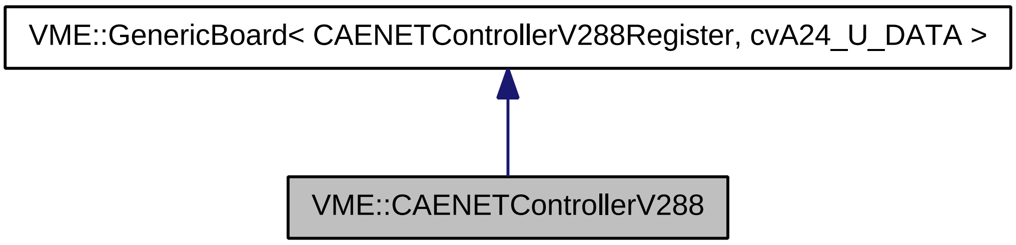
The documentation for this class was generated from the following file:

- include/VME_BridgeVx718.h

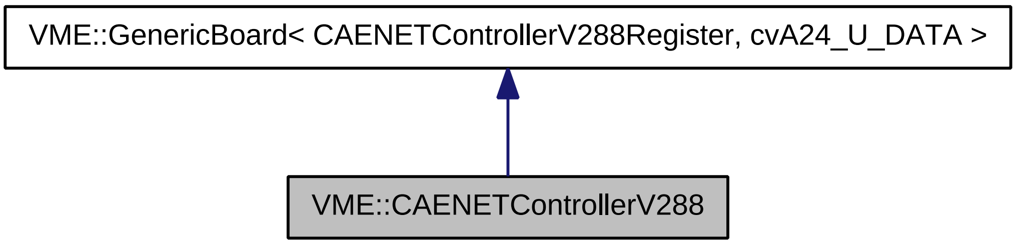
7.4 VME::CAENETControllerV288 Class Reference

Handler for a CAEN V288 CAENET controller.

`#include <VME_CAENETControllerV288.h>`Inheritance diagram for VME::CAENETControllerV288:


VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >
VME::CAENETControllerV288

Collaboration diagram for VME::CAENETControllerV288:


VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >
VME::CAENETControllerV288

Public Member Functions

- CAENETControllerV288 (int32_t handle, uint32_t baseaddr)
- ∼CAENETControllerV288 ()
- void Reset () const
- CAENETControllerV288Status GetStatus () const
- void SendBuffer () const

  Send the whole buffer through the network.

- std::vector< uint16_t > FetchBuffer (unsigned int num_words) const

  Retrieve the network buffer.

- bool WaitForResponse (CAENETControllerV288Answer ∗response, unsigned int max_trials=-1) const

Friends

- void operator<< (const CAENETControllerV288 &cnt, uint16_t word)

Fill the buffer with an additional 16-bit word.

- uint16_t & operator>> (const CAENETControllerV288 &cnt, uint16_t &word)

  Read back a 16-bit word from the buffer.

7.4.1 Detailed Description

Handler for a CAEN V288 CAENET controller.

Author:

Laurent Forthomme <laurent.forthomme@cern.ch>

Date:

23 Jul 2015

7.4.2 Constructor & Destructor Documentation

7.4.2.1 VME::CAENETControllerV288::CAENETControllerV288 (int32_t *handle*, uint32_t *baseaddr*)

7.4.2.2 VME::CAENETControllerV288::~CAENETControllerV288 ()

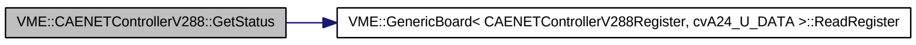
7.4.3 Member Function Documentation

7.4.3.1 std::vector< uint16_t > VME::CAENETControllerV288::FetchBuffer (unsigned int *num_words* = 1) const

Retrieve the network buffer.

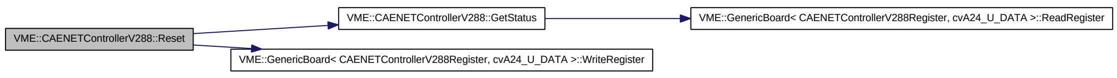
7.4.3.2 CAENETControllerV288Status VME::CAENETControllerV288::GetStatus () const

Here is the call graph for this function:


VME::CAENETControllerV288::GetStatus
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::ReadRegister

7.4.3.3 void VME::CAENETControllerV288::Reset () const

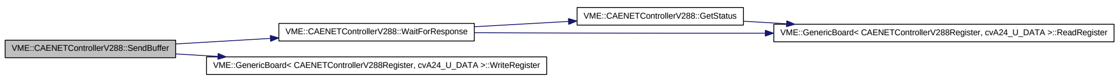
Here is the call graph for this function:


VME::CAENETControllerV288::Reset
VME::CAENETControllerV288::GetStatus
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::ReadRegister
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::WriteRegister

7.4.3.4 void VME::CAENETControllerV288::SendBuffer () const

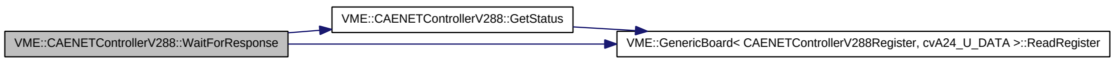
Send the whole buffer through the network.

Here is the call graph for this function:


VME::CAENETControllerV288::SendBuffer
VME::CAENETControllerV288::WaitForResponse
VME::CAENETControllerV288::GetStatus
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::ReadRegister
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::WriteRegister

7.4.3.5 bool VME::CAENETControllerV288::WaitForResponse (CAENETControllerV288Answer ∗ *response*, unsigned int *max_trials* = `-1`) const

Here is the call graph for this function:


VME::CAENETControllerV288::WaitForResponse
VME::CAENETControllerV288::GetStatus
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::ReadRegister

7.4.4 Friends And Related Function Documentation

7.4.4.1 void operator<< (const CAENETControllerV288 & *cnt*, uint16_t *word*) `[friend]`

Fill the buffer with an additional 16-bit word.

7.4.4.2 uint16_t& operator>> (const CAENETControllerV288 & *cnt*, uint16_t & *word*) `[friend]`

Read back a 16-bit word from the buffer.

The documentation for this class was generated from the following files:

- include/VME_CAENETControllerV288.h
- src/VME_CAENETControllerV288.cpp

7.5 VME::CAENETControllerV288Status Class Reference

```
#include <VME_CAENETControllerV288.h>
```

Public Types

- enum OperationStatus { Valid = 0x0, Invalid = 0x1 }

Public Member Functions

- CAENETControllerV288Status (uint16_t word)
- ~CAENETControllerV288Status ()
- OperationStatus GetOperationStatus () const

Private Attributes

- uint16_t fWord

7.5.1 Member Enumeration Documentation

7.5.1.1 enum VME::CAENETControllerV288Status::OperationStatus

Enumerator:

Valid

Invalid

7.5.2 Constructor & Destructor Documentation

7.5.2.1 VME::CAENETControllerV288Status::CAENETControllerV288Status (uint16_t *word*) `[inline]`

7.5.2.2 VME::CAENETControllerV288Status::∼CAENETControllerV288Status () `[inline]`

7.5.3 Member Function Documentation

7.5.3.1 OperationStatus VME::CAENETControllerV288Status::GetOperationStatus () const `[inline]`

7.5.4 Field Documentation

7.5.4.1 uint16_t VME::CAENETControllerV288Status::fWord `[private]`

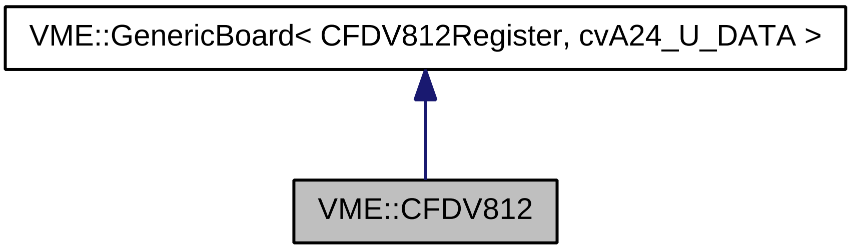
The documentation for this class was generated from the following file:

- include/VME_CAENETControllerV288.h

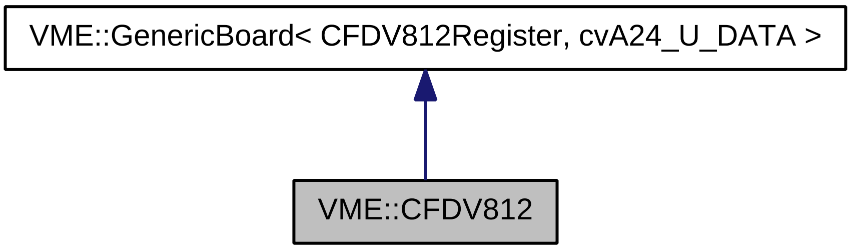
7.6 VME::CFDV812 Class Reference

Controller for a CAEN V812 constant fraction discriminator.

`#include <VME_CFDV812.h>`Inheritance diagram for VME::CFDV812:



Collaboration diagram for VME::CFDV812:



Public Member Functions

- CFDV812 (int32_t bhandle, uint32_t baseaddr)
- ~CFDV812 ()
- void CheckConfiguration () const
- unsigned short GetFixedCode () const
- unsigned short GetManufacturerId () const
- unsigned short GetModuleType () const
- unsigned short GetModuleVersion () const
- unsigned short GetSerialNumber () const
- void SetPOI (unsigned short poi) const

  Set the pattern of inhibit (list of enabled channels).
- void SetThreshold (unsigned short channel_id, unsigned short value) const

  Set the threshold for one single channel, in units of 1 mV.
- void SetOutputWidth (unsigned short group_id, unsigned short value) const

  Set the discriminated pulse output width for one group of 8 channels.

- void SetDeadTime (unsigned short group_id, unsigned short value) const

  Set the discrimination dead time for one group of 8 channels.

Private Member Functions

- float OutputWidthCalculator (unsigned short value) const
- float DeadTimeCalculator (unsigned short value) const

7.6.1 Detailed Description

Controller for a CAEN V812 constant fraction discriminator.

Author:

Laurent Forthomme <`laurent.forthomme@cern.ch`>

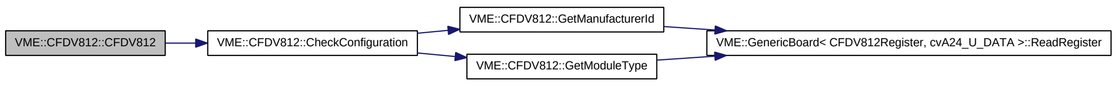
Date:

22 Jul 2015

7.6.2 Constructor & Destructor Documentation

7.6.2.1 VME::CFDV812::CFDV812 (int32_t *bhandle*, uint32_t *baseaddr*)

Here is the call graph for this function:

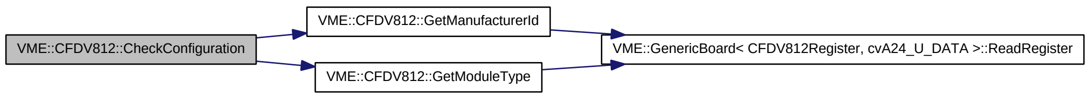

VME::CFDV812::CFDV812
VME::CFDV812::CheckConfiguration
VME::CFDV812::GetManufacturerId
VME::CFDV812::GetModuleType
VME::GenericBoard< CFDV812Register, cvA24_U_DATA >::ReadRegister

7.6.2.2 VME::CFDV812::∼CFDV812 () `[inline]`

7.6.3 Member Function Documentation

7.6.3.1 void VME::CFDV812::CheckConfiguration () const

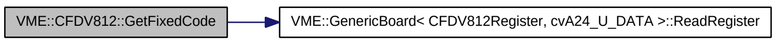
Here is the call graph for this function:


VME::CFDV812::CheckConfiguration
VME::CFDV812::GetManufacturerId
VME::CFDV812::GetModuleType
VME::GenericBoard< CFDV812Register, cvA24_U_DATA >::ReadRegister

7.6.3.2 float VME::CFDV812::DeadTimeCalculator (unsigned short *value*) const `[private]`

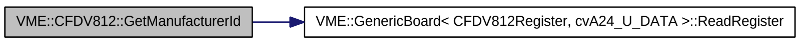
7.6.3.3 unsigned short VME::CFDV812::GetFixedCode () const

Here is the call graph for this function:


VME::CFDV812::GetFixedCode
VME::GenericBoard< CFDV812Register, cvA24_U_DATA >::ReadRegister

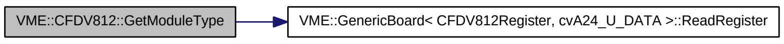
7.6.3.4 unsigned short VME::CFDV812::GetManufacturerId () const

Here is the call graph for this function:


VME::CFDV812::GetManufacturerId
VME::GenericBoard< CFDV812Register, cvA24_U_DATA >::ReadRegister

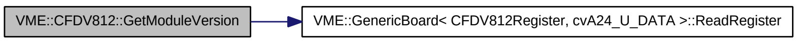
7.6.3.5 unsigned short VME::CFDV812::GetModuleType () const

Here is the call graph for this function:


VME::CFDV812::GetModuleType
VME::GenericBoard< CFDV812Register, cvA24_U_DATA >::ReadRegister

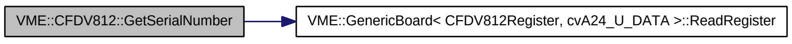
7.6.3.6 unsigned short VME::CFDV812::GetModuleVersion () const

Here is the call graph for this function:


VME::CFDV812::GetModuleVersion
VME::GenericBoard< CFDV812Register, cvA24_U_DATA >::ReadRegister

7.6.3.7 unsigned short VME::CFDV812::GetSerialNumber () const

Here is the call graph for this function:


VME::CFDV812::GetSerialNumber
VME::GenericBoard< CFDV812Register, cvA24_U_DATA >::ReadRegister

7.6.3.8 float VME::CFDV812::OutputWidthCalculator (unsigned short *value*) const `[private]`

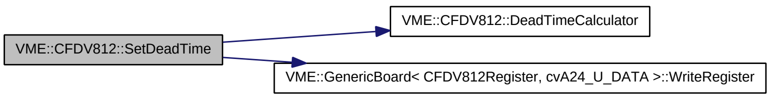
7.6.3.9 void VME::CFDV812::SetDeadTime (unsigned short *group_id*, unsigned short *value*) const

Set the discrimination dead time for one group of 8 channels.

Parameters:

← ***group_id*** Group of 8 channels (either 0 for 0-7, or 1 for 8-15)

Here is the call graph for this function:


VME::CFDV812::SetDeadTime
VME::CFDV812::DeadTimeCalculator
VME::GenericBoard< CFDV812Register, cvA24_U_DATA >::WriteRegister

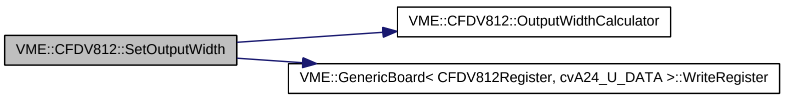
7.6.3.10 void VME::CFDV812::SetOutputWidth (unsigned short *group_id*, unsigned short *value*) const

Set the discriminated pulse output width for one group of 8 channels.

Parameters:

← ***group_id*** Group of 8 channels (either 0 for 0-7, or 1 for 8-15)

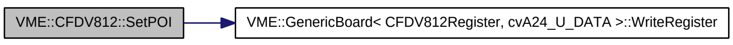
Here is the call graph for this function:


VME::CFDV812::SetOutputWidth
VME::CFDV812::OutputWidthCalculator
VME::GenericBoard< CFDV812Register, cvA24_U_DATA >::WriteRegister

7.6.3.11 void VME::CFDV812::SetPOI (unsigned short *poi*) const

Set the pattern of inhibit (list of enabled channels).

Here is the call graph for this function:


VME::CFDV812::SetPOI
VME::GenericBoard< CFDV812Register, cvA24_U_DATA >::WriteRegister

7.6.3.12 void VME::CFDV812::SetThreshold (unsigned short *channel_id*, unsigned short *value*) const

Set the threshold for one single channel, in units of 1 mV.

Here is the call graph for this function:

VME::CFDV812::SetThreshold
VME::GenericBoard< CFDV812Register, cvA24_U_DATA >::WriteRegister

The documentation for this class was generated from the following files:

- include/VME_CFDV812.h
- src/VME_CFDV812.cpp

7.7 NIM::HVModuleN470Values::ChannelStatus Class Reference

```
#include <NIM_HVModuleN470.h>
```

Public Types

- enum SignalStandard { NIM = 0x0, TTL = 0x1 }

Public Member Functions

- ChannelStatus (unsigned short word)
- ∼ChannelStatus ()
- bool Enabled () const
- bool OVC () const
- bool OVV () const
- bool UNV () const
- bool Trip () const
- bool RampUp () const
- bool RampDown () const
- bool MaxV () const
- bool Polarity () const
- bool Vsel () const
- bool Isel () const
- bool Kill () const
- bool HVEnabled () const
- SignalStandard Standard () const
- bool NonCalibrated () const
- bool Alarm () const
- void Dump () const

Private Attributes

- unsigned short fWord

Friends

- std::ostream & operator<< (std::ostream &os, const ChannelStatus &cs)

7.7.1 Member Enumeration Documentation

7.7.1.1 enum NIM::HVModuleN470Values::ChannelStatus::SignalStandard

Enumerator:

NIM

TTL

7.7.2 Constructor & Destructor Documentation

7.7.2.1 NIM::HVModuleN470Values::ChannelStatus::ChannelStatus (unsigned short *word*) `[inline]`

7.7.2.2 NIM::HVModuleN470Values::ChannelStatus::∼ChannelStatus () `[inline]`

7.7.3 Member Function Documentation

7.7.3.1 bool NIM::HVModuleN470Values::ChannelStatus::Alarm () const `[inline]`

7.7.3.2 void NIM::HVModuleN470Values::ChannelStatus::Dump () const `[inline]`

7.7.3.3 bool NIM::HVModuleN470Values::ChannelStatus::Enabled () const `[inline]`

7.7.3.4 bool NIM::HVModuleN470Values::ChannelStatus::HVEnabled () const `[inline]`

7.7.3.5 bool NIM::HVModuleN470Values::ChannelStatus::Isel () const `[inline]`

7.7.3.6 bool NIM::HVModuleN470Values::ChannelStatus::Kill () const `[inline]`

7.7.3.7 bool NIM::HVModuleN470Values::ChannelStatus::MaxV () const `[inline]`

7.7.3.8 bool NIM::HVModuleN470Values::ChannelStatus::NonCalibrated () const `[inline]`

7.7.3.9 bool NIM::HVModuleN470Values::ChannelStatus::OVC () const `[inline]`

7.7.3.10 bool NIM::HVModuleN470Values::ChannelStatus::OVV () const `[inline]`

7.7.3.11 bool NIM::HVModuleN470Values::ChannelStatus::Polarity () const `[inline]`

7.7.3.12 bool NIM::HVModuleN470Values::ChannelStatus::RampDown () const `[inline]`

7.7.3.13 bool NIM::HVModuleN470Values::ChannelStatus::RampUp () const `[inline]`

7.7.3.14 SignalStandard NIM::HVModuleN470Values::ChannelStatus::Standard () const `[inline]`

7.7.3.15 bool NIM::HVModuleN470Values::ChannelStatus::Trip () const `[inline]`

7.7.3.16 bool NIM::HVModuleN470Values::ChannelStatus::UNV () const `[inline]`

- include/NIM_HVModuleN470.h

7.8 Client Class Reference

Base client object for the socket.

`#include <Client.h>`Inheritance diagram for Client:Collaboration diagram for Client:

Public Member Functions

- Client ()

  General void client constructor.

- Client (int port)

  Bind a socket client to a given port.

- virtual ∼Client ()
- bool Connect (const SocketType &type=CLIENT)

  Bind this client to the socket.

- void Disconnect ()

  Unbind this client from the socket.

- void Send (const Message &m) const

  Send a message to the master through the socket.

- void Send (const Exception &e) const
- SocketMessage SendAndReceive (const SocketMessage &m, const MessageKey &a) const
- void Receive ()

  Receive a socket message from the master.

- SocketMessage Receive (const MessageKey &key)
- virtual void ParseMessage (const SocketMessage &m)

  Parse a SocketMessage received from the master.

- virtual SocketType GetType () const

  Socket actor type retrieval method.

Private Member Functions

- void Announce ()

  Announce our entry on the socket to its master.

Private Attributes

- int fClientId
- bool fIsConnected
- SocketType fType

7.8.1 Detailed Description

Base client object for the socket. Client object used by the server to send/receive commands from the messenger/broadcaster.

Author:

Laurent Forthomme <laurent.forthomme@cern.ch>

Date:

24 Mar 2015

7.8.2 Constructor & Destructor Documentation

7.8.2.1 Client::Client () `[inline]`

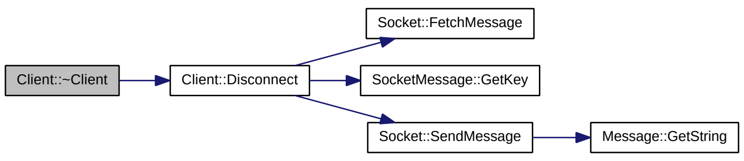
General void client constructor.

7.8.2.2 Client::Client (int *port*)

Bind a socket client to a given port.

7.8.2.3 Client::∼Client () `[virtual]`

Here is the call graph for this function:

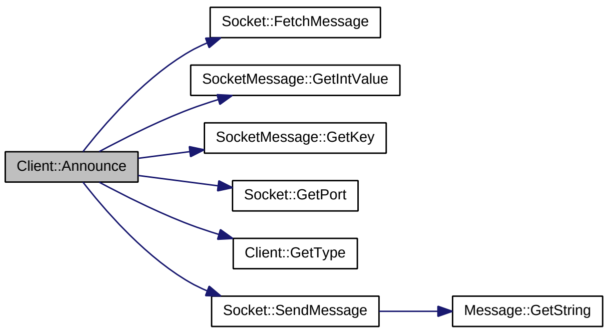


7.8.3 Member Function Documentation

7.8.3.1 void Client::Announce () `[private]`

Announce our entry on the socket to its master.

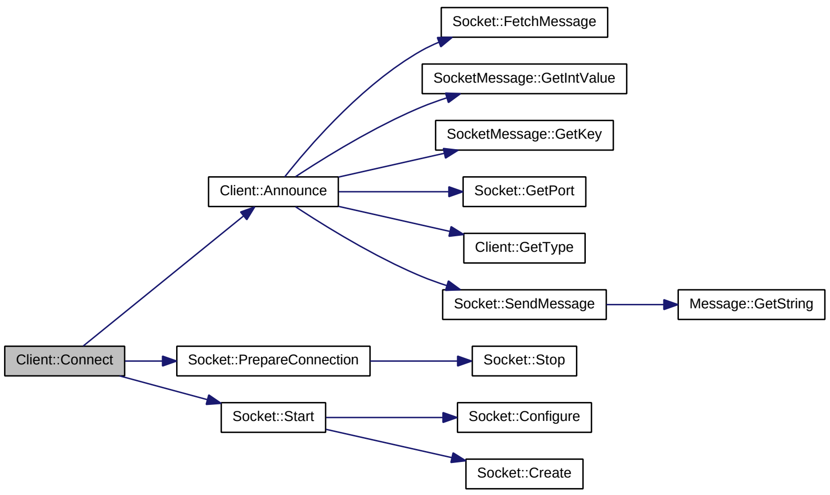
Here is the call graph for this function:



7.8.3.2 bool Client::Connect (const SocketType & *type* = `CLIENT`)

Bind this client to the socket.

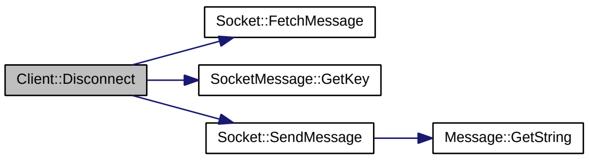
Here is the call graph for this function:



7.8.3.3 void Client::Disconnect ()

Unbind this client from the socket.

Here is the call graph for this function:



7.8.3.4 virtual SocketType Client::GetType () const `[inline, virtual]`

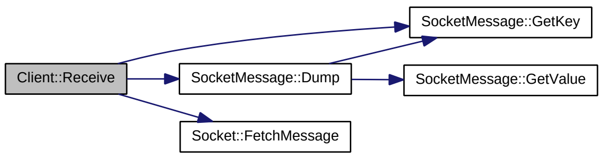
Socket actor type retrieval method.

7.8.3.5 virtual void Client::ParseMessage (const SocketMessage & *m*) `[inline, virtual]`

Parse a SocketMessage received from the master.

7.8.3.6 SocketMessage Client::Receive (const MessageKey & *key*)

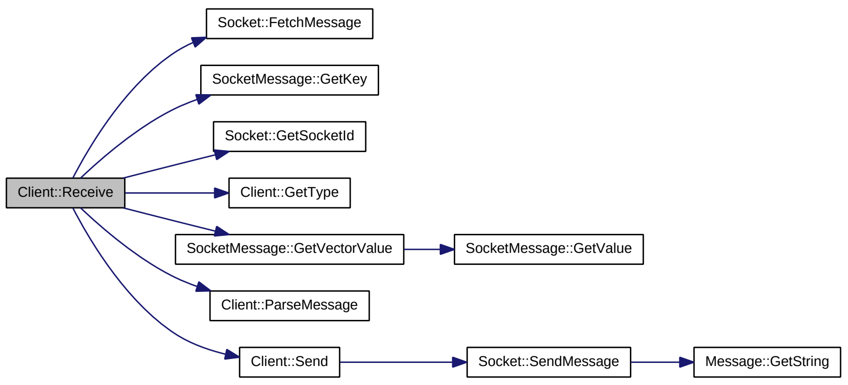
Here is the call graph for this function:



7.8.3.7 void Client::Receive ()

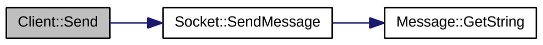
Receive a socket message from the master.

Here is the call graph for this function:



7.8.3.8 void Client::Send (const Exception & *e*) const `[inline]`

Here is the call graph for this function:



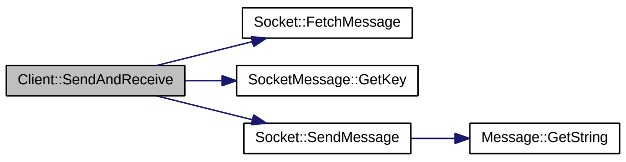
7.8.3.9 void Client::Send (const Message & *m*) const `[inline]`

Send a message to the master through the socket.

Here is the call graph for this function:

7.8.3.10 SocketMessage Client::SendAndReceive (const SocketMessage & *m*, const MessageKey & *a*) const `[inline]`

Here is the call graph for this function:



7.8.4 Field Documentation

7.8.4.1 int Client::fClientId `[private]`

7.8.4.2 bool Client::fIsConnected `[private]`

7.8.4.3 SocketType Client::fType `[private]`

The documentation for this class was generated from the following files:

- include/Client.h
- src/Client.cpp

7.9 file_header_t Struct Reference

Header to the output files.

```
#include <FileConstants.h>
```

Data Fields

- uint32_t magic
- uint32_t run_id
- uint32_t spill_id
- uint8_t num_hptdc
- VME::AcquisitionMode acq_mode
- VME::DetectionMode det_mode

7.9.1 Detailed Description

Header to the output files. General header to store in each collected data file for offline readout. It enable any reader to retrieve the run/spill number, as well as the HPTDC configuration during data collection.

Author:

Laurent Forthomme <laurent.forthomme@cern.ch>

Date:

14 Apr 2015

7.9.2 Field Documentation

7.9.2.1 VME::AcquisitionMode file_header_t::acq_mode

7.9.2.2 VME::DetectionMode file_header_t::det_mode

7.9.2.3 uint32_t file_header_t::magic

7.9.2.4 uint8_t file_header_t::num_hptdc

7.9.2.5 uint32_t file_header_t::run_id

7.9.2.6 uint32_t file_header_t::spill_id

The documentation for this struct was generated from the following file:

- include/FileConstants.h

7.10 FileReader Class Reference

Handler for a TDC output file readout.

`#include <FileReader.h>`Collaboration diagram for FileReader:

Public Member Functions

- FileReader ()
- FileReader (std::string name)

    Class constructor.

- ~FileReader ()
- void Open (std::string name)
- bool IsOpen () const
- void Clear ()
- void Dump () const
- unsigned int GetNumTDCs () const
- unsigned long GetNumEvents () const
- bool GetNextEvent (VME::TDCEvent ∗)
- bool GetNextMeasurement (unsigned int channel_id, VME::TDCMeasurement ∗mc)

    Fetch the next full measurement on a given channel.

Private Attributes

- std::ifstream fFile
- file_header_t fHeader
- VME::AcquisitionMode fReadoutMode
- time_t fWriteTime
- unsigned long fNumEvents

7.10.1 Detailed Description

Handler for a TDC output file readout.

Author:

Laurent Forthomme <`laurent.forthomme@cern.ch`>

Date:

Jun 2015

7.10.2 Constructor & Destructor Documentation

7.10.2.1 FileReader::FileReader () `[inline]`

7.10.2.2 FileReader::FileReader (std::string *name*)

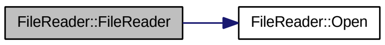
Class constructor.

Parameters:

← ***name*** Path to the file to read

← ***ro*** Data readout mode (continuous storage or trigger matching)

Here is the call graph for this function:



7.10.2.3 FileReader::∼FileReader ()

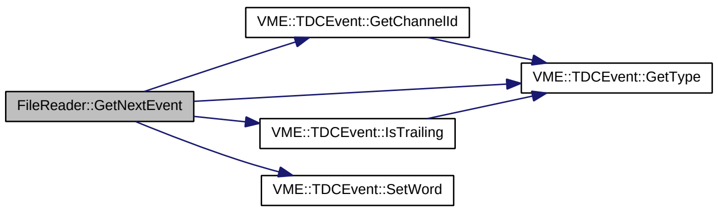
7.10.3 Member Function Documentation

7.10.3.1 void FileReader::Clear () `[inline]`

7.10.3.2 void FileReader::Dump () const

7.10.3.3 bool FileReader::GetNextEvent (VME::TDCEvent ∗ *ev*)

Here is the call graph for this function:



7.10.3.4 bool FileReader::GetNextMeasurement (unsigned int *channel_id*, VME::TDCMeasurement ∗ *mc*)

Fetch the next full measurement on a given channel.

Parameters:

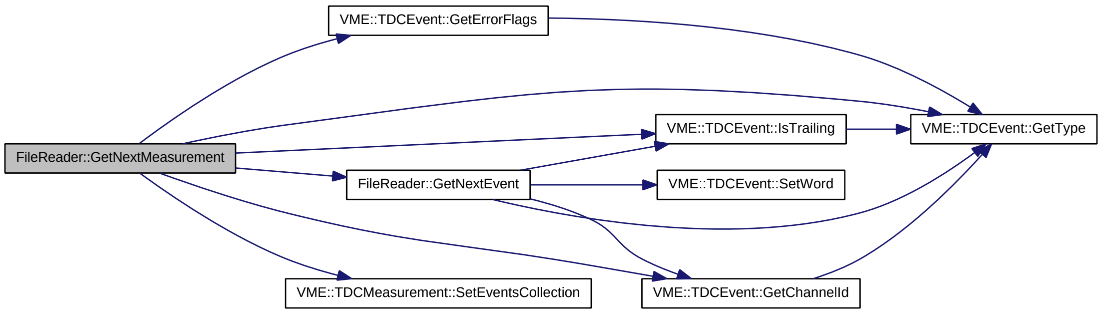
← ***channel_id*** Unique identifier of the channel number to retrieve

→ ***m*** A full measurement with leading, trailing times, ...

Returns:

A boolean stating the success of retrieval operation

Here is the call graph for this function:


VME::TDCEvent::GetErrorFlags
VME::TDCEvent::IsTrailing
VME::TDCEvent::GetType
FileReader::GetNextMeasurement
FileReader::GetNextEvent
VME::TDCEvent::SetWord
VME::TDCMeasurement::SetEventsCollection
VME::TDCEvent::GetChannelId

7.10.3.5 unsigned long FileReader::GetNumEvents () const `[inline]`

7.10.3.6 unsigned int FileReader::GetNumTDCs () const `[inline]`

7.10.3.7 bool FileReader::IsOpen () const `[inline]`

7.10.3.8 void FileReader::Open (std::string *name*)

7.10.4 Field Documentation

7.10.4.1 std::ifstream FileReader::fFile `[private]`

7.10.4.2 file_header_t FileReader::fHeader `[private]`

7.10.4.3 unsigned long FileReader::fNumEvents `[private]`

7.10.4.4 VME::AcquisitionMode FileReader::fReadoutMode `[private]`

7.10.4.5 time_t FileReader::fWriteTime `[private]`

The documentation for this class was generated from the following files:

- include/FileReader.h
- src/FileReader.cpp

7.11 VME::FPGAUnitV1495 Class Reference

`#include <VME_FPGAUnitV1495.h>`Inheritance diagram for VME::FPGAUnitV1495:Collaboration diagram for VME::FPGAUnitV1495:

Public Types

- enum TDCBits { kReset = 0x1, kTrigger = 0x2, kClear = 0x4 }

Public Member Functions

- FPGAUnitV1495 (int32_t bhandle, uint32_t baseaddr)
- ~FPGAUnitV1495 ()
- unsigned short GetCAENFirmwareRevision () const
- unsigned short GetUserFirmwareRevision () const
- unsigned int GetHardwareRevision () const
- unsigned short GetSerialNumber () const
- unsigned short GetGeoAddress () const
- void CheckBoardVersion () const
- void ResetFPGA () const
- void DumpFWInformation () const
- void SetTDCBits (unsigned short bits) const

  Set a pattern of bits to be sent to all TDCs through the ECL mezzanine.

- void PulseTDCBits (unsigned short bits, unsigned int time_us=10) const

  Send a pulse to TDCs' front panel.

- unsigned short GetTDCBits () const

  Retrieve the current bits sent to TDCs' front panel.

- FPGAUnitV1495Control GetControl () const

  Retrieve the user-defined control word.

- void SetControl (const FPGAUnitV1495Control &control) const

  Set the user-defined control word.

- void SetInternalClockPeriod (uint32_t period) const

  Set the internal clock period.

- uint32_t GetInternalClockPeriod () const

  Retrieve the internal clock period.

- void SetInternalTriggerPeriod (uint32_t period) const

  Set the internal trigger period.

- uint32_t GetInternalTriggerPeriod () const

  Retrieve the internal trigger period.

- uint32_t GetOutputPulser () const
- void ClearOutputPulser () const
- void SetOutputPulser (unsigned short id, bool enable=true) const
- void SetOutputPulserPOI (uint32_t poi) const
- uint32_t GetOutputDelay () const
- void SetOutputDelay (uint32_t delay) const
- void StartScaler ()

  Start the inner triggers counter.

- void StopScaler ()

  Stop the inner triggers counter.

- uint32_t GetScalerValue () const

  Return the inner triggers counter value.

Private Attributes

- bool fScalerStarted

7.11.1 Detailed Description

Handler for the multi-purposes FPGA unit (CAEN V1495)

Author:

Laurent Forthomme <laurent.forthomme@cern.ch>

Date:

25 Jun 2015

7.11.2 Member Enumeration Documentation

7.11.2.1 enum VME::FPGAUnitV1495::TDCBits

Enumerator:

kReset

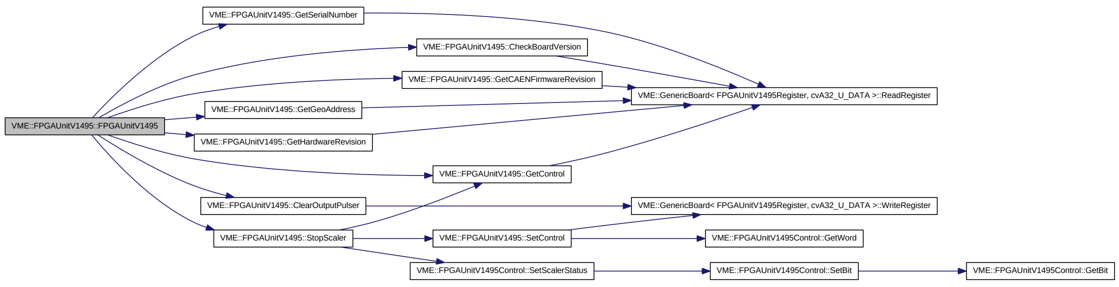
kTrigger

kClear

7.11.3 Constructor & Destructor Documentation

7.11.3.1 VME::FPGAUnitV1495::FPGAUnitV1495 (int32_t *bhandle*, uint32_t *baseaddr*)

Here is the call graph for this function:


VME::FPGAUnitV1495::FPGAUnitV1495
VME::FPGAUnitV1495::GetSerialNumber
VME::FPGAUnitV1495::CheckBoardVersion
VME::FPGAUnitV1495::GetCAENFirmwareRevision
VME::FPGAUnitV1495::GetGeoAddress
VME::FPGAUnitV1495::GetHardwareRevision
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister
VME::FPGAUnitV1495::GetControl
VME::FPGAUnitV1495::ClearOutputPulser
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::WriteRegister
VME::FPGAUnitV1495::StopScaler
VME::FPGAUnitV1495::SetControl
VME::FPGAUnitV1495Control::GetWord
VME::FPGAUnitV1495Control::SetScalerStatus
VME::FPGAUnitV1495Control::SetBit
VME::FPGAUnitV1495Control::GetBit

7.11.3.2 VME::FPGAUnitV1495::∼FPGAUnitV1495 ()

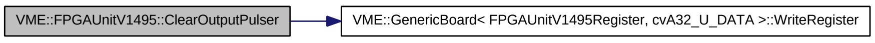
7.11.4 Member Function Documentation

7.11.4.1 void VME::FPGAUnitV1495::CheckBoardVersion () const

Here is the call graph for this function:

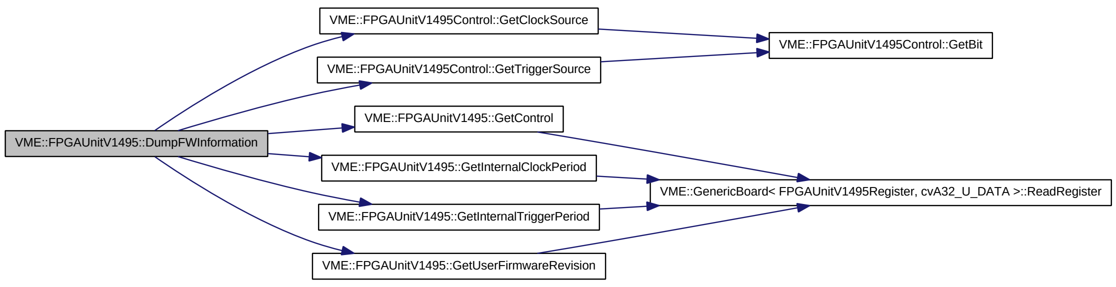
7.11.4.2 void VME::FPGAUnitV1495::ClearOutputPulser () const

Here is the call graph for this function:


VME::FPGAUnitV1495::ClearOutputPulser
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::WriteRegister

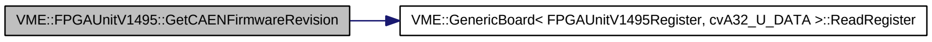
7.11.4.3 void VME::FPGAUnitV1495::DumpFWInformation () const

Here is the call graph for this function:


VME::FPGAUnitV1495::DumpFWInformation
VME::FPGAUnitV1495Control::GetClockSource
VME::FPGAUnitV1495Control::GetTriggerSource
VME::FPGAUnitV1495::GetControl
VME::FPGAUnitV1495::GetInternalClockPeriod
VME::FPGAUnitV1495::GetInternalTriggerPeriod
VME::FPGAUnitV1495::GetUserFirmwareRevision
VME::FPGAUnitV1495Control::GetBit
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister

7.11.4.4 unsigned short VME::FPGAUnitV1495::GetCAENFirmwareRevision () const

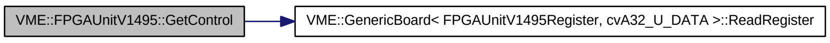
Here is the call graph for this function:


VME::FPGAUnitV1495::GetCAENFirmwareRevision
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister

7.11.4.5 FPGAUnitV1495Control VME::FPGAUnitV1495::GetControl () const

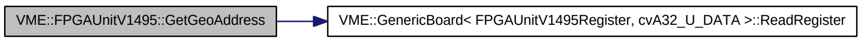
Retrieve the user-defined control word.

Here is the call graph for this function:


VME::FPGAUnitV1495::GetControl
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister

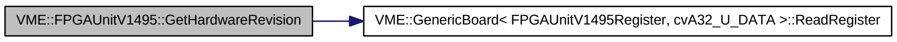
7.11.4.6 unsigned short VME::FPGAUnitV1495::GetGeoAddress () const

Here is the call graph for this function:


VME::FPGAUnitV1495::GetGeoAddress
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister

7.11.4.7 unsigned int VME::FPGAUnitV1495::GetHardwareRevision () const

Here is the call graph for this function:


VME::FPGAUnitV1495::GetHardwareRevision
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister

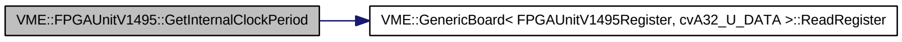
7.11.4.8 uint32_t VME::FPGAUnitV1495::GetInternalClockPeriod () const

Retrieve the internal clock period.

Returns:

Clock period (in units of 25 ns)

Here is the call graph for this function:


VME::FPGAUnitV1495::GetInternalClockPeriod
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister

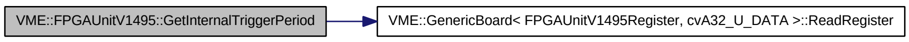
7.11.4.9 uint32_t VME::FPGAUnitV1495::GetInternalTriggerPeriod () const

Retrieve the internal trigger period.

Returns:

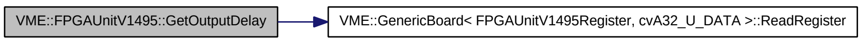
Trigger period (in units of 50 ns)

Here is the call graph for this function:


VME::FPGAUnitV1495::GetInternalTriggerPeriod
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister

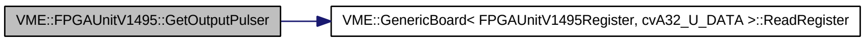
7.11.4.10 uint32_t VME::FPGAUnitV1495::GetOutputDelay () const

Here is the call graph for this function:


VME::FPGAUnitV1495::GetOutputDelay
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister

7.11.4.11 uint32_t VME::FPGAUnitV1495::GetOutputPulser () const

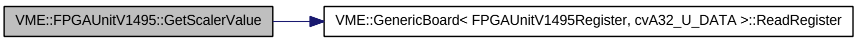
Here is the call graph for this function:


VME::FPGAUnitV1495::GetOutputPulser
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister

7.11.4.12 uint32_t VME::FPGAUnitV1495::GetScalerValue () const

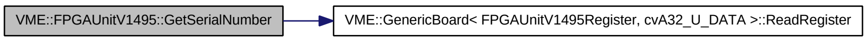
Return the inner triggers counter value.

Here is the call graph for this function:


VME::FPGAUnitV1495::GetScalerValue
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister

7.11.4.13 unsigned short VME::FPGAUnitV1495::GetSerialNumber () const

Here is the call graph for this function:


VME::FPGAUnitV1495::GetSerialNumber
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister

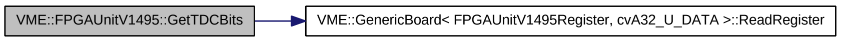
7.11.4.14 unsigned short VME::FPGAUnitV1495::GetTDCBits () const

Retrieve the current bits sent to TDCs' front panel.

Returns:

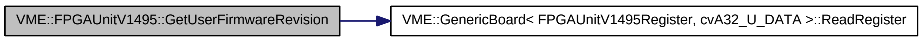
A 3-bit word PoI

Here is the call graph for this function:


VME::FPGAUnitV1495::GetTDCBits
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister

7.11.4.15 unsigned short VME::FPGAUnitV1495::GetUserFirmwareRevision () const

Here is the call graph for this function:


VME::FPGAUnitV1495::GetUserFirmwareRevision
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister

7.11.4.16 void VME::FPGAUnitV1495::PulseTDCBits (unsigned short *bits*, unsigned int *time_us* = `10`) const

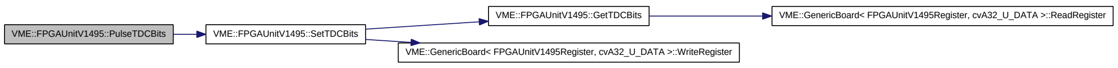
Send a pulse to TDCs' front panel.

Parameters:

← ***bits*** The pattern to send (3 bits)

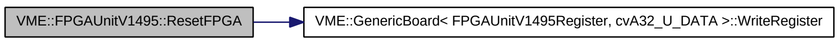
← ***time_us*** Pulse width (in us)

Here is the call graph for this function:


VME::FPGAUnitV1495::PulseTDCBits
VME::FPGAUnitV1495::SetTDCBits
VME::FPGAUnitV1495::GetTDCBits
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::WriteRegister

7.11.4.17 void VME::FPGAUnitV1495::ResetFPGA () const

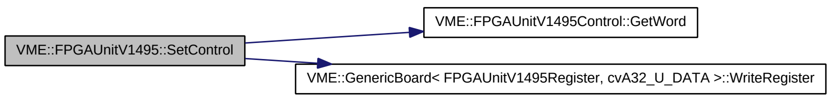
Here is the call graph for this function:


VME::FPGAUnitV1495::ResetFPGA
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::WriteRegister

7.11.4.18 void VME::FPGAUnitV1495::SetControl (const FPGAUnitV1495Control & *control*) const

Set the user-defined control word.

Here is the call graph for this function:


VME::FPGAUnitV1495::SetControl
VME::FPGAUnitV1495Control::GetWord
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::WriteRegister

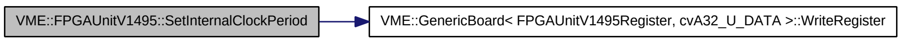
7.11.4.19 void VME::FPGAUnitV1495::SetInternalClockPeriod (uint32_t *period*) const

Set the internal clock period.

Parameters:

← ***period*** Clock period (in units of 25 ns)

Here is the call graph for this function:


VME::FPGAUnitV1495::SetInternalClockPeriod
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::WriteRegister

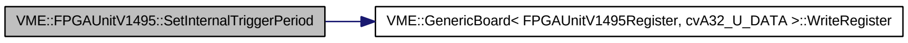
7.11.4.20 void VME::FPGAUnitV1495::SetInternalTriggerPeriod (uint32_t *period*) const

Set the internal trigger period.

Parameters:

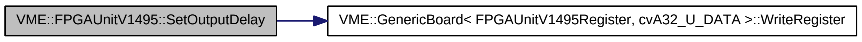
← ***period*** Trigger period (in units of 50 ns)

Here is the call graph for this function:


VME::FPGAUnitV1495::SetInternalTriggerPeriod
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::WriteRegister

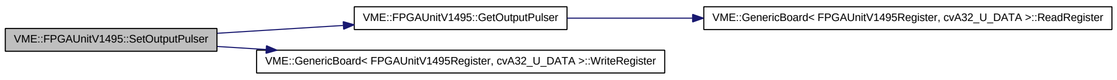
7.11.4.21 void VME::FPGAUnitV1495::SetOutputDelay (uint32_t *delay*) const

Here is the call graph for this function:


VME::FPGAUnitV1495::SetOutputDelay
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::WriteRegister

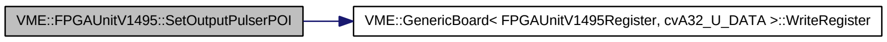
7.11.4.22 void VME::FPGAUnitV1495::SetOutputPulser (unsigned short *id*, bool *enable* = `true`) const

Here is the call graph for this function:


VME::FPGAUnitV1495::SetOutputPulser
VME::FPGAUnitV1495::GetOutputPulser
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::WriteRegister

7.11.4.23 void VME::FPGAUnitV1495::SetOutputPulserPOI (uint32_t *poi*) const

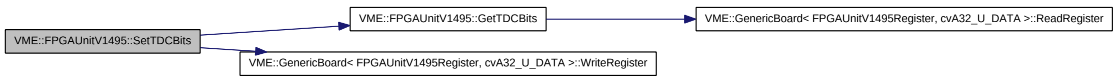
Here is the call graph for this function:


VME::FPGAUnitV1495::SetOutputPulserPOI
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::WriteRegister

7.11.4.24 void VME::FPGAUnitV1495::SetTDCBits (unsigned short *bits*) const

Set a pattern of bits to be sent to all TDCs through the ECL mezzanine.

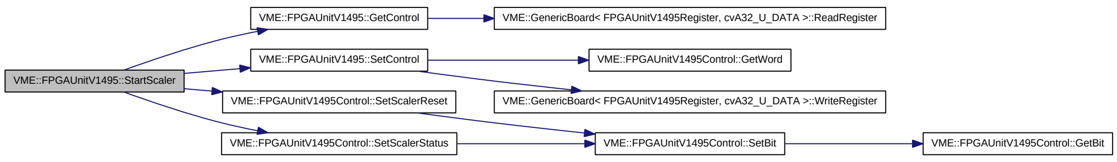
Here is the call graph for this function:


VME::FPGAUnitV1495::SetTDCBits
VME::FPGAUnitV1495::GetTDCBits
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::WriteRegister

7.11.4.25 void VME::FPGAUnitV1495::StartScaler ()

Start the inner triggers counter.

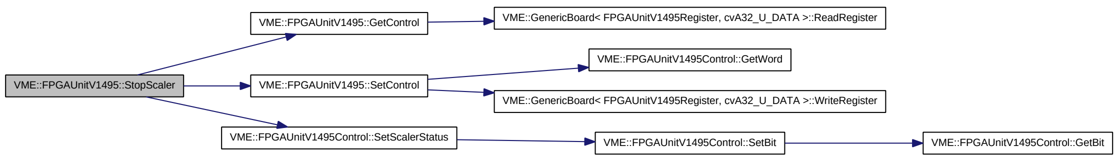
Here is the call graph for this function:


VME::FPGAUnitV1495::StartScaler
VME::FPGAUnitV1495::GetControl
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister
VME::FPGAUnitV1495::SetControl
VME::FPGAUnitV1495Control::GetWord
VME::FPGAUnitV1495Control::SetScalerReset
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::WriteRegister
VME::FPGAUnitV1495Control::SetScalerStatus
VME::FPGAUnitV1495Control::SetBit
VME::FPGAUnitV1495Control::GetBit

7.11.4.26 void VME::FPGAUnitV1495::StopScaler ()

Stop the inner triggers counter.

Here is the call graph for this function:


VME::FPGAUnitV1495::StopScaler
VME::FPGAUnitV1495::GetControl
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister
VME::FPGAUnitV1495Control::GetWord
VME::FPGAUnitV1495::SetControl
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::WriteRegister
VME::FPGAUnitV1495Control::SetScalerStatus
VME::FPGAUnitV1495Control::SetBit
VME::FPGAUnitV1495Control::GetBit

7.11.5 Field Documentation

7.11.5.1 bool VME::FPGAUnitV1495::fScalerStarted `[private]`

The documentation for this class was generated from the following files:

- include/VME_FPGAUnitV1495.h
- src/VME_FPGAUnitV1495.cpp

7.12 VME::FPGAUnitV1495Control Class Reference

`#include <VME_FPGAUnitV1495.h>`

Public Types

- enum ClockSource { InternalClock = 0x0, ExternalClock = 0x1 }
- enum TriggerSource { InternalTrigger = 0x0, ExternalTrigger = 0x1 }
- enum SignalSource { InternalSignal = 0x0, ExternalSignal = 0x1 }

Public Member Functions

- FPGAUnitV1495Control (uint32_t word)
- virtual ∼FPGAUnitV1495Control ()
- void Dump () const
- uint32_t GetWord () const
- ClockSource GetClockSource () const

  Get the clock source.
- void SetClockSource (const ClockSource &cs)

  Switch between internal and external clock source.
- TriggerSource GetTriggerSource () const

  Get the trigger source.
- void SetTriggerSource (const TriggerSource &cs)

  Switch between internal and external trigger source.
- bool GetScalerStatus () const
- void SetScalerStatus (bool start=true)
- void SetScalerReset (bool reset=true)
- SignalSource GetSignalSource (unsigned short map_id) const
- void SetSignalSource (unsigned short map_id, const SignalSource &s)

Private Member Functions

- bool GetBit (unsigned short id) const
- void SetBit (unsigned short id, unsigned short value=0x1)

Private Attributes

- uint32_t fWord

7.12.1 Detailed Description

User-defined control word to be propagated to the CAEN V1495 board firmware.

Author:

Laurent Forthomme <`laurent.forthomme@cern.ch`>

Date:

27 Jun 2015

7.12.2 Member Enumeration Documentation

7.12.2.1 enum VME::FPGAUnitV1495Control::ClockSource

Enumerator:

InternalClock

ExternalClock

7.12.2.2 enum VME::FPGAUnitV1495Control::SignalSource

Enumerator:

InternalSignal

ExternalSignal

7.12.2.3 enum VME::FPGAUnitV1495Control::TriggerSource

Enumerator:

InternalTrigger

ExternalTrigger

7.12.3 Constructor & Destructor Documentation

7.12.3.1 VME::FPGAUnitV1495Control::FPGAUnitV1495Control (uint32_t *word*) `[inline]`

7.12.3.2 virtual VME::FPGAUnitV1495Control::∼FPGAUnitV1495Control () `[inline, virtual]`

7.12.4 Member Function Documentation

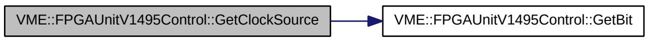
7.12.4.1 void VME::FPGAUnitV1495Control::Dump () const `[inline]`

7.12.4.2 bool VME::FPGAUnitV1495Control::GetBit (unsigned short *id*) const `[inline, private]`

7.12.4.3 ClockSource VME::FPGAUnitV1495Control::GetClockSource () const `[inline]`

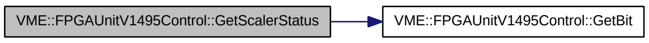
Get the clock source.

Here is the call graph for this function:


VME::FPGAUnitV1495Control::GetClockSource
VME::FPGAUnitV1495Control::GetBit

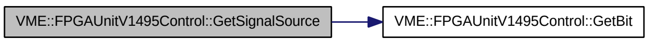
7.12.4.4 bool VME::FPGAUnitV1495Control::GetScalerStatus () const `[inline]`

Here is the call graph for this function:


VME::FPGAUnitV1495Control::GetScalerStatus
VME::FPGAUnitV1495Control::GetBit

7.12.4.5 SignalSource VME::FPGAUnitV1495Control::GetSignalSource (unsigned short *map_id*) const `[inline]`

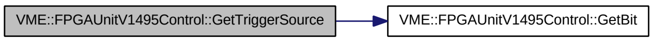
Here is the call graph for this function:


VME::FPGAUnitV1495Control::GetSignalSource
VME::FPGAUnitV1495Control::GetBit

7.12.4.6 TriggerSource VME::FPGAUnitV1495Control::GetTriggerSource () const `[inline]`

Get the trigger source.

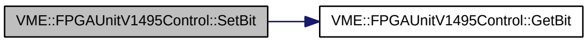
Here is the call graph for this function:


VME::FPGAUnitV1495Control::GetTriggerSource
VME::FPGAUnitV1495Control::GetBit

7.12.4.7 uint32_t VME::FPGAUnitV1495Control::GetWord () const `[inline]`

7.12.4.8 void VME::FPGAUnitV1495Control::SetBit (unsigned short *id*, unsigned short *value* = `0x1`) `[inline, private]`

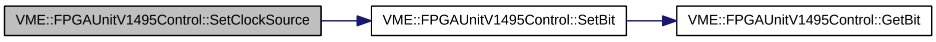
Here is the call graph for this function:


VME::FPGAUnitV1495Control::SetBit
VME::FPGAUnitV1495Control::GetBit

7.12.4.9 void VME::FPGAUnitV1495Control::SetClockSource (const ClockSource & *cs*) `[inline]`

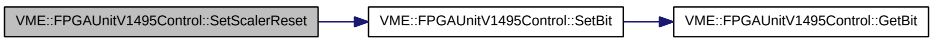
Switch between internal and external clock source.

Here is the call graph for this function:


VME::FPGAUnitV1495Control::SetClockSource
VME::FPGAUnitV1495Control::SetBit
VME::FPGAUnitV1495Control::GetBit

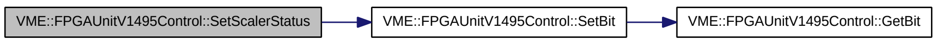
7.12.4.10 void VME::FPGAUnitV1495Control::SetScalerReset (bool *reset* = `true`) `[inline]`

Here is the call graph for this function:


VME::FPGAUnitV1495Control::SetScalerReset
VME::FPGAUnitV1495Control::SetBit
VME::FPGAUnitV1495Control::GetBit

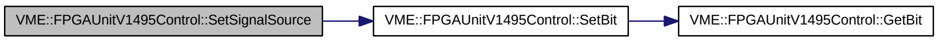
7.12.4.11 void VME::FPGAUnitV1495Control::SetScalerStatus (bool *start* = `true`) `[inline]`

Here is the call graph for this function:


VME::FPGAUnitV1495Control::SetScalerStatus
VME::FPGAUnitV1495Control::SetBit
VME::FPGAUnitV1495Control::GetBit

7.12.4.12 void VME::FPGAUnitV1495Control::SetSignalSource (unsigned short *map_id*, const SignalSource & *s*) `[inline]`

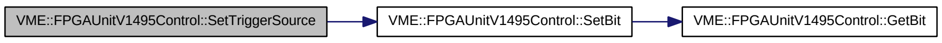
Here is the call graph for this function:


VME::FPGAUnitV1495Control::SetSignalSource
VME::FPGAUnitV1495Control::SetBit
VME::FPGAUnitV1495Control::GetBit

7.12.4.13 void VME::FPGAUnitV1495Control::SetTriggerSource (const TriggerSource & *cs*) `[inline]`

Switch between internal and external trigger source.

Here is the call graph for this function:


VME::FPGAUnitV1495Control::SetTriggerSource
VME::FPGAUnitV1495Control::SetBit
VME::FPGAUnitV1495Control::GetBit

7.12.5 Field Documentation

7.12.5.1 uint32_t VME::FPGAUnitV1495Control::fWord `[private]`

The documentation for this class was generated from the following file:

- include/VME_FPGAUnitV1495.h

7.13 VME::GenericBoard< Register, am > Class Template Reference

`#include <VME_GenericBoard.h>`

Public Member Functions

- GenericBoard (int32_t bhandle, uint32_t baseaddr)
- virtual ~GenericBoard ()

Protected Member Functions

- void WriteRegister (const Register &reg, const uint16_t &data) const

  Write on register.

- void WriteRegister (const Register &reg, const uint32_t &data) const

  Write on register.

- void ReadRegister (const Register &reg, uint16_t ∗data) const

  Read on register.

- void ReadRegister (const Register &reg, uint32_t ∗data) const

  Read on register.

Protected Attributes

- int32_t fHandle
- uint32_t fBaseAddr

template<class Register, CVAddressModifier am> class VME::GenericBoard< Register, am >

7.13.1 Constructor & Destructor Documentation

7.13.1.1 template<class Register, CVAddressModifier am> VME::GenericBoard< Register, am >::GenericBoard (int32_t *bhandle*, uint32_t *baseaddr*) `[inline]`

7.13.1.2 template<class Register, CVAddressModifier am> virtual VME::GenericBoard< Register, am >::∼GenericBoard () `[inline, virtual]`

7.13.2 Member Function Documentation

7.13.2.1 template<class Register, CVAddressModifier am> void VME::GenericBoard< Register, am >::ReadRegister (const Register & *reg*, uint32_t ∗ *data*) const `[inline, protected]`

Read on register. Read a 32-bit word in the register

Parameters:

← ***addr*** register

→ ***data*** word

7.13.2.2 template<class Register, CVAddressModifier am> void VME::GenericBoard< Register, am >::ReadRegister (const Register & *reg*, uint16_t ∗ *data*) const `[inline, protected]`

Read on register. Read a 16-bit word in the register

Parameters:

← ***addr*** register

→ ***data*** word

7.13.2.3 template<class Register, CVAddressModifier am> void VME::GenericBoard< Register, am >::WriteRegister (const Register & *reg*, const uint32_t & *data*) const `[inline, protected]`

Write on register. Write a 32-bit word in the register

Parameters:

← ***addr*** register

← ***data*** word

7.13.2.4 template<class Register, CVAddressModifier am> void VME::GenericBoard< Register, am >::WriteRegister (const Register & *reg*, const uint16_t & *data*) const `[inline, protected]`

Write on register. Write a 16-bit word in the register

Parameters:

← ***addr*** register

← ***data*** word

7.13.3 Field Documentation

7.13.3.1 template<class Register, CVAddressModifier am> uint32_t VME::GenericBoard< Register, am >::fBaseAddr `[protected]`

7.13.3.2 template<class Register, CVAddressModifier am> int32_t VME::GenericBoard< Register, am >::fHandle `[protected]`

The documentation for this class was generated from the following file:

- include/VME_GenericBoard.h

7.14 VME::GlobalOffset Struct Reference

```
#include <VME_TDCV1x90.h>
```

Data Fields

- uint16_t coarse
- uint16_t fine

7.14.1 Field Documentation

7.14.1.1 uint16_t VME::GlobalOffset::coarse

7.14.1.2 uint16_t VME::GlobalOffset::fine

The documentation for this struct was generated from the following file:

- include/VME_TDCV1x90.h

7.15 HTTPMessage Class Reference

Message to be transmitted through a WebSocket protocol.

`#include <HTTPMessage.h>`Inheritance diagram for HTTPMessage:Collaboration diagram for HTTPMessage:

Public Member Functions

- HTTPMessage (WebSocket ∗ws, Message m, MessageAction a)
- HTTPMessage (WebSocket ∗ws, const char ∗msg, MessageAction a)
- void Decode ()
- void Encode ()
- MessageKey GetKey () const

  Placeholder for the MessageKey retrieval method.
- void Dump (std::ostream &os=std::cout) const

Private Attributes

- WebSocket ∗ fWS
- std::string fOriginalString

7.15.1 Detailed Description

Message to be transmitted through a WebSocket protocol. Type of message compatible to the transmission through a WebSocket protocol. It enables a direct conversion of standards from any socket message format used elsewhere in this code using the *MessageAction* statement.

Author:

Laurent Forthomme <`laurent.forthomme@cern.ch`>

Date:

1 Apr 2015

7.15.2 Constructor & Destructor Documentation

7.15.2.1 HTTPMessage::HTTPMessage (WebSocket ∗ *ws*, Message *m*, MessageAction *a*) `[inline]`

Here is the call graph for this function:

7.15.2.2 HTTPMessage::HTTPMessage (WebSocket ∗ *ws*, const char ∗ *msg*, MessageAction *a*) `[inline]`

Here is the call graph for this function:

7.15.3 Member Function Documentation

7.15.3.1 void HTTPMessage::Decode () `[inline]`

7.15.3.2 void HTTPMessage::Dump (std::ostream & *os* = `std::cout`) const `[inline]`

Reimplemented from Message.

7.15.3.3 void HTTPMessage::Encode () `[inline]`

7.15.3.4 MessageKey HTTPMessage::GetKey () const `[inline]`

Placeholder for the MessageKey retrieval method.

Reimplemented from Message.

7.15.4 Field Documentation

7.15.4.1 std::string HTTPMessage::fOriginalString `[private]`

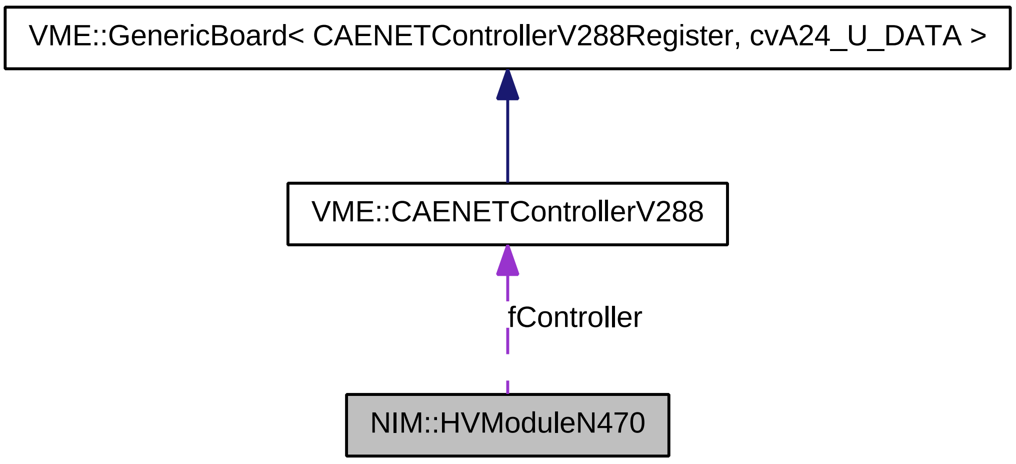
7.15.4.2 WebSocket∗ HTTPMessage::fWS `[private]`

The documentation for this class was generated from the following file:

- include/HTTPMessage.h

7.16 NIM::HVModuleN470 Class Reference

`#include <NIM_HVModuleN470.h>`Collaboration diagram for NIM::HVModuleN470:


VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >
VME::CAENETControllerV288
fController
NIM::HVModuleN470

Public Member Functions

- HVModuleN470 (uint16_t addr, VME::CAENETControllerV288 &cont)
- ∼HVModuleN470 ()
- std::string GetModuleId () const
- unsigned short GetFWRevision () const
- HVModuleN470Values ReadMonitoringValues () const
- HVModuleN470ChannelValues ReadChannelValues (unsigned short ch_id) const
- void SetChannelV0 (unsigned short ch_id, unsigned short v0) const
- void SetChannelI0 (unsigned short ch_id, unsigned short i0) const
- void SetChannelV1 (unsigned short ch_id, unsigned short v1) const
- void SetChannelI1 (unsigned short ch_id, unsigned short i1) const

Private Member Functions

- void ReadRegister (const HVModuleN470Opcodes &reg, std::vector< uint16_t > ∗data, unsigned int num_words=1) const

  Read in register.

- void WriteRegister (const HVModuleN470Opcodes &reg, const std::vector< uint16_t > &data) const

  Write on register.

- void WriteRegister (const HVModuleN470Opcodes &reg, const uint16_t &data) const

  Write on register.

Private Attributes

- VME::CAENETControllerV288 fController
- uint16_t fAddress

7.16.1 Constructor & Destructor Documentation

7.16.1.1 NIM::HVModuleN470::HVModuleN470 (uint16_t *addr*, VME::CAENETControllerV288 & *cont*)

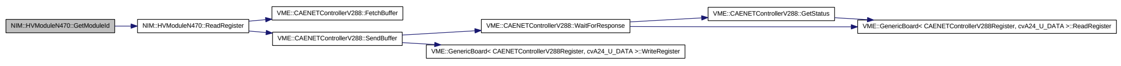
7.16.1.2 NIM::HVModuleN470::∼HVModuleN470 () `[inline]`

7.16.2 Member Function Documentation

7.16.2.1 unsigned short NIM::HVModuleN470::GetFWRevision () const

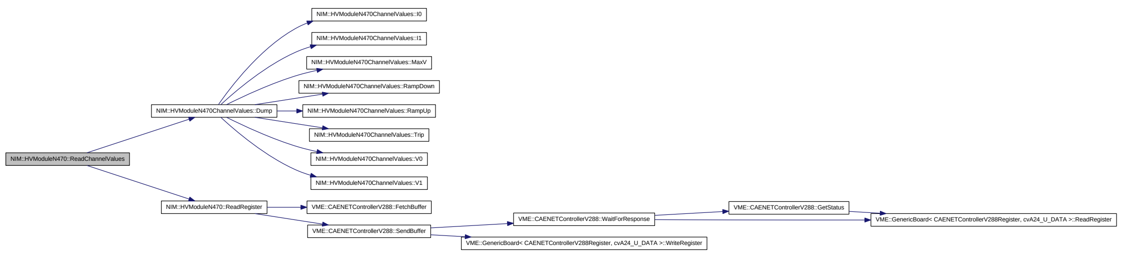
7.16.2.2 std::string NIM::HVModuleN470::GetModuleId () const

Here is the call graph for this function:


NIM::HVModuleN470::GetModuleId
NIM::HVModuleN470::ReadRegister
VME::CAENETControllerV288::FetchBuffer
VME::CAENETControllerV288::SendBuffer
VME::CAENETControllerV288::WaitForResponse
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::WriteRegister
VME::CAENETControllerV288::GetStatus
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::ReadRegister

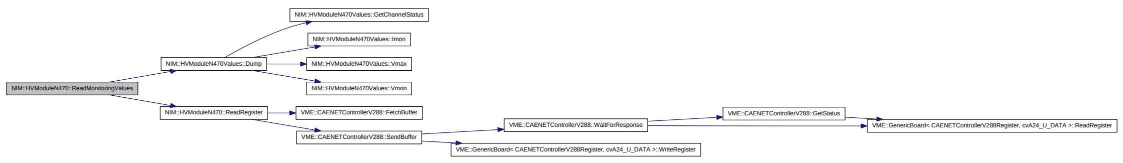
7.16.2.3 HVModuleN470ChannelValues NIM::HVModuleN470::ReadChannelValues (unsigned short *ch_id*) const

Here is the call graph for this function:


NIM::HVModuleN470ChannelValues::I0
NIM::HVModuleN470ChannelValues::I1
NIM::HVModuleN470ChannelValues::MaxV
NIM::HVModuleN470ChannelValues::RampDown
NIM::HVModuleN470ChannelValues::Dump
NIM::HVModuleN470ChannelValues::RampUp
NIM::HVModuleN470ChannelValues::Trip
NIM::HVModuleN470::ReadChannelValues
NIM::HVModuleN470ChannelValues::V0
NIM::HVModuleN470ChannelValues::V1
NIM::HVModuleN470::ReadRegister
VME::CAENETControllerV288::FetchBuffer
VME::CAENETControllerV288::SendBuffer
VME::CAENETControllerV288::WaitForResponse
VME::CAENETControllerV288::GetStatus
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::ReadRegister
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::WriteRegister

7.16.2.4 HVModuleN470Values NIM::HVModuleN470::ReadMonitoringValues () const

Here is the call graph for this function:


NIM::HVModuleN470::ReadMonitoringValues
NIM::HVModuleN470Values::Dump
NIM::HVModuleN470Values::GetChannelStatus
NIM::HVModuleN470Values::Imon
NIM::HVModuleN470Values::Vmax
NIM::HVModuleN470Values::Vmon
NIM::HVModuleN470::ReadRegister
VME::CAENETControllerV288::FetchBuffer
VME::CAENETControllerV288::SendBuffer
VME::CAENETControllerV288::WaitForResponse
VME::CAENETControllerV288::GetStatus
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::ReadRegister
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::WriteRegister

7.16.2.5 void NIM::HVModuleN470::ReadRegister (const HVModuleN470Opcodes & *reg*, std::vector< uint16_t > ∗ *data*, unsigned int *num_words* = 1) const `[private]`

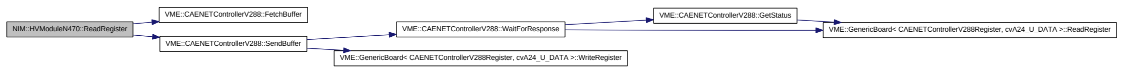
Read in register. Read a vector of 16-bit words in the register

Parameters:

← ***addr*** register

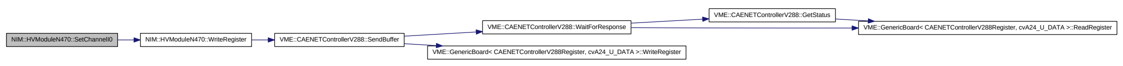
→ ***vector*** of data words

Here is the call graph for this function:


NIM::HVModuleN470::ReadRegister
VME::CAENETControllerV288::FetchBuffer
VME::CAENETControllerV288::SendBuffer
VME::CAENETControllerV288::WaitForResponse
VME::CAENETControllerV288::GetStatus
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::ReadRegister
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::WriteRegister

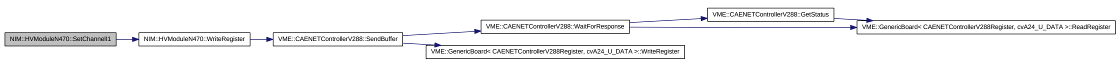
7.16.2.6 void NIM::HVModuleN470::SetChannelI0 (unsigned short *ch_id*, unsigned short *i0*) const

Here is the call graph for this function:


NIM::HVModuleN470::SetChannelI0
NIM::HVModuleN470::WriteRegister
VME::CAENETControllerV288::SendBuffer
VME::CAENETControllerV288::WaitForResponse
VME::CAENETControllerV288::GetStatus
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::ReadRegister
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::WriteRegister

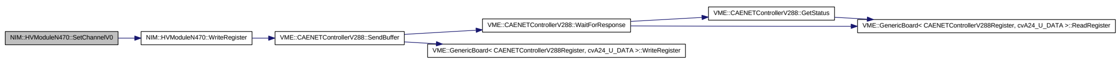
7.16.2.7 void NIM::HVModuleN470::SetChannelI1 (unsigned short *ch_id*, unsigned short *i1*) const

Here is the call graph for this function:


NIM::HVModuleN470::SetChannelI1
NIM::HVModuleN470::WriteRegister
VME::CAENETControllerV288::SendBuffer
VME::CAENETControllerV288::WaitForResponse
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::WriteRegister
VME::CAENETControllerV288::GetStatus
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::ReadRegister

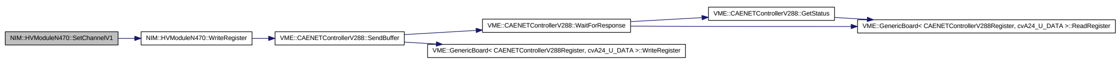
7.16.2.8 void NIM::HVModuleN470::SetChannelV0 (unsigned short *ch_id*, unsigned short *v0*) const

Here is the call graph for this function:


NIM::HVModuleN470::SetChannelV0
NIM::HVModuleN470::WriteRegister
VME::CAENETControllerV288::SendBuffer
VME::CAENETControllerV288::WaitForResponse
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::WriteRegister
VME::CAENETControllerV288::GetStatus
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::ReadRegister

7.16.2.9 void NIM::HVModuleN470::SetChannelV1 (unsigned short *ch_id*, unsigned short *v1*) const

Here is the call graph for this function:


NIM::HVModuleN470::SetChannelV1
NIM::HVModuleN470::WriteRegister
VME::CAENETControllerV288::SendBuffer
VME::CAENETControllerV288::WaitForResponse
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::WriteRegister
VME::CAENETControllerV288::GetStatus
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::ReadRegister

7.16.2.10 void NIM::HVModuleN470::WriteRegister (const HVModuleN470Opcodes & *reg*, const uint16_t & *data*) const `[private]`

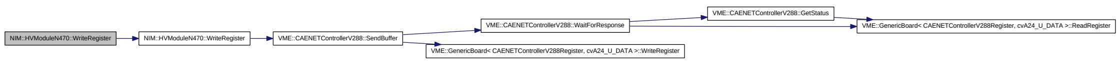
Write on register. Write a 16-bit word in the register

Parameters:

← ***addr*** register

→ ***data*** word

Here is the call graph for this function:


NIM::HVModuleN470::WriteRegister
NIM::HVModuleN470::WriteRegister
VME::CAENETControllerV288::SendBuffer
VME::CAENETControllerV288::WaitForResponse
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::WriteRegister
VME::CAENETControllerV288::GetStatus
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::ReadRegister

7.16.2.11 void NIM::HVModuleN470::WriteRegister (const HVModuleN470Opcodes & *reg*, const std::vector< uint16_t > & *data*) const `[private]`

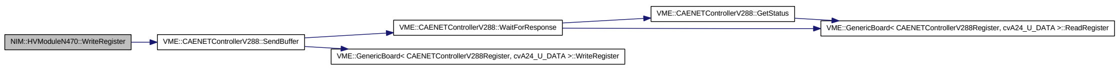
Write on register. Write a vector of 16-bit words in the register

Parameters:

← ***addr*** register

→ ***data*** word

Here is the call graph for this function:


NIM::HVModuleN470::WriteRegister
VME::CAENETControllerV288::SendBuffer
VME::CAENETControllerV288::WaitForResponse
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::WriteRegister
VME::CAENETControllerV288::GetStatus
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >::ReadRegister

7.16.3 Field Documentation

7.16.3.1 uint16_t NIM::HVModuleN470::fAddress `[private]`

7.16.3.2 VME::CAENETControllerV288 NIM::HVModuleN470::fController `[private]`

The documentation for this class was generated from the following files:

- include/NIM_HVModuleN470.h
- src/NIM_HVModuleN470.cpp

7.17 NIM::HVModuleN470ChannelValues Class Reference

Single channel monitoring values for the HV power supply.

`#include <NIM_HVModuleN470.h>`

Public Member Functions

- HVModuleN470ChannelValues (unsigned short ch_id, std::vector< unsigned short > vals)
- ~HVModuleN470ChannelValues ()
- void Dump () const
- unsigned short ChannelStatus () const
- unsigned short Vmon () const
- unsigned short Imon () const
- unsigned short V0 () const
- unsigned short I0 () const
- unsigned short V1 () const
- unsigned short I1 () const
- unsigned short Trip () const
- unsigned short RampUp () const
- unsigned short RampDown () const
- unsigned short MaxV () const

Private Attributes

- unsigned short fChannelId
- std::vector< unsigned short > fValues

7.17.1 Detailed Description

Single channel monitoring values for the HV power supply.

Author:

Laurent Forthomme <laurent.forthomme@cern.ch>

Date:

24 Jul 2015

7.17.2 Constructor & Destructor Documentation

7.17.2.1 NIM::HVModuleN470ChannelValues::HVModuleN470ChannelValues (unsigned short *ch_id*, std::vector< unsigned short > *vals*) `[inline]`

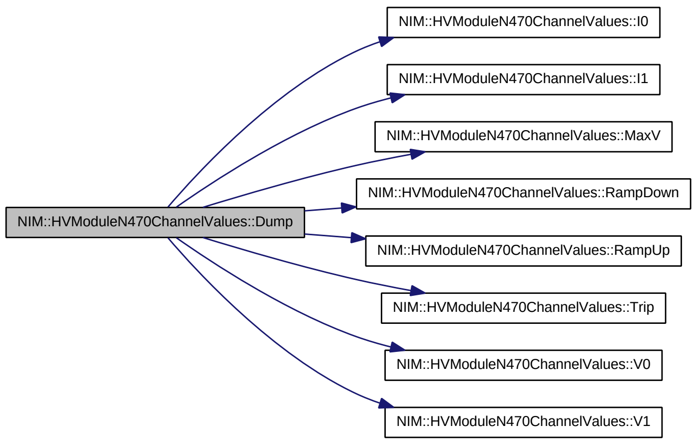
7.17.2.2 NIM::HVModuleN470ChannelValues::∼HVModuleN470ChannelValues () `[inline]`

7.17.3 Member Function Documentation

7.17.3.1 unsigned short NIM::HVModuleN470ChannelValues::ChannelStatus () const `[inline]`

7.17.3.2 void NIM::HVModuleN470ChannelValues::Dump () const `[inline]`

Here is the call graph for this function:


NIM::HVModuleN470ChannelValues::Dump
NIM::HVModuleN470ChannelValues::I0
NIM::HVModuleN470ChannelValues::I1
NIM::HVModuleN470ChannelValues::MaxV
NIM::HVModuleN470ChannelValues::RampDown
NIM::HVModuleN470ChannelValues::RampUp
NIM::HVModuleN470ChannelValues::Trip
NIM::HVModuleN470ChannelValues::V0
NIM::HVModuleN470ChannelValues::V1

7.17.3.3 unsigned short NIM::HVModuleN470ChannelValues::I0 () const `[inline]`

7.17.3.4 unsigned short NIM::HVModuleN470ChannelValues::I1 () const `[inline]`

7.17.3.5 unsigned short NIM::HVModuleN470ChannelValues::Imon () const `[inline]`

7.17.3.6 unsigned short NIM::HVModuleN470ChannelValues::MaxV () const `[inline]`

7.17.3.7 unsigned short NIM::HVModuleN470ChannelValues::RampDown () const `[inline]`

7.17.3.8 unsigned short NIM::HVModuleN470ChannelValues::RampUp () const `[inline]`

7.17.3.9 unsigned short NIM::HVModuleN470ChannelValues::Trip () const `[inline]`

7.17.3.10 unsigned short NIM::HVModuleN470ChannelValues::V0 () const `[inline]`

7.17.3.11 unsigned short NIM::HVModuleN470ChannelValues::V1 () const `[inline]`

7.17.3.12 unsigned short NIM::HVModuleN470ChannelValues::Vmon () const `[inline]`

7.17.4 Field Documentation

7.17.4.1 unsigned short NIM::HVModuleN470ChannelValues::fChannelId `[private]`

7.17.4.2 std::vector<unsigned short> NIM::HVModuleN470ChannelValues::fValues `[private]`

The documentation for this class was generated from the following file:

- include/NIM_HVModuleN470.h

7.18 NIM::HVModuleN470Values Class Reference

General monitoring values for the HV power supply.

`#include <NIM_HVModuleN470.h>`

Data Structures

- class ChannelStatus

Public Member Functions

- HVModuleN470Values (std::vector< unsigned short > vals)
- ~HVModuleN470Values ()
- void Dump () const
- unsigned short Vmon () const
- unsigned short Imon () const
- unsigned short Vmax () const
- ChannelStatus GetChannelStatus (unsigned short ch_id) const

Private Attributes

- std::vector< unsigned short > fValues

7.18.1 Detailed Description

General monitoring values for the HV power supply.

Author:

Laurent Forthomme <`laurent.forthomme@cern.ch`>

Date:

24 Jul 2015

7.18.2 Constructor & Destructor Documentation

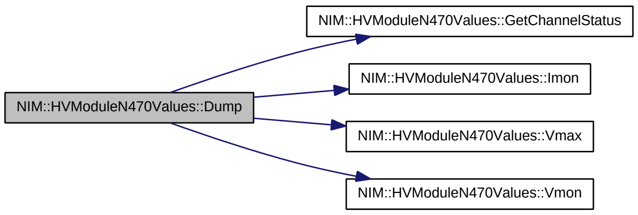
7.18.2.1 NIM::HVModuleN470Values::HVModuleN470Values (std::vector< unsigned short > *vals*) `[inline]`

7.18.2.2 NIM::HVModuleN470Values::∼HVModuleN470Values () `[inline]`

7.18.3 Member Function Documentation

7.18.3.1 void NIM::HVModuleN470Values::Dump () const `[inline]`

Here is the call graph for this function:


NIM::HVModuleN470Values::Dump
NIM::HVModuleN470Values::GetChannelStatus
NIM::HVModuleN470Values::Imon
NIM::HVModuleN470Values::Vmax
NIM::HVModuleN470Values::Vmon

7.18.3.2 ChannelStatus NIM::HVModuleN470Values::GetChannelStatus (unsigned short *ch_id*) const `[inline]`

7.18.3.3 unsigned short NIM::HVModuleN470Values::Imon () const `[inline]`

7.18.3.4 unsigned short NIM::HVModuleN470Values::Vmax () const `[inline]`

7.18.3.5 unsigned short NIM::HVModuleN470Values::Vmon () const `[inline]`

7.18.4 Field Documentation

7.18.4.1 std::vector<unsigned short> NIM::HVModuleN470Values::fValues `[private]`

The documentation for this class was generated from the following file:

- include/NIM_HVModuleN470.h

7.19 VME::IOModuleV262 Class Reference

`#include <VME_IOModuleV262.h>`Inheritance diagram for VME::IOModuleV262:Collaboration diagram for VME::IOModuleV262:

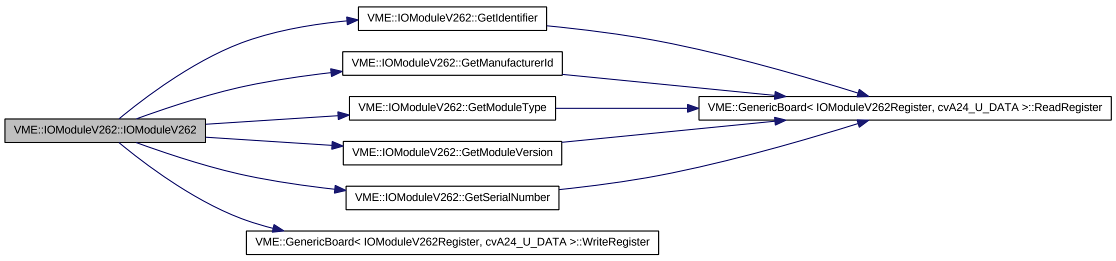
Public Member Functions

- IOModuleV262 (int32_t bhandle, uint32_t baseaddr)
- ∼IOModuleV262 ()
- unsigned short GetSerialNumber () const
- unsigned short GetModuleVersion () const
- unsigned short GetModuleType () const
- unsigned short GetManufacturerId () const
- unsigned short GetIdentifier () const

7.19.1 Constructor & Destructor Documentation

7.19.1.1 VME::IOModuleV262::IOModuleV262 (int32_t *bhandle*, uint32_t *baseaddr*)

Here is the call graph for this function:

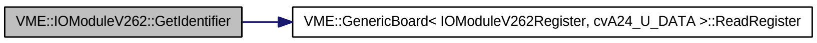

VME::IOModuleV262::IOModuleV262
VME::IOModuleV262::GetIdentifier
VME::IOModuleV262::GetManufacturerId
VME::IOModuleV262::GetModuleType
VME::IOModuleV262::GetModuleVersion
VME::IOModuleV262::GetSerialNumber
VME::GenericBoard< IOModuleV262Register, cvA24_U_DATA >::WriteRegister
VME::GenericBoard< IOModuleV262Register, cvA24_U_DATA >::ReadRegister

7.19.1.2 VME::IOModuleV262::∼IOModuleV262 () `[inline]`

7.19.2 Member Function Documentation

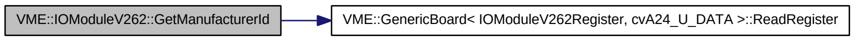
7.19.2.1 unsigned short VME::IOModuleV262::GetIdentifier () const

Here is the call graph for this function:


VME::IOModuleV262::GetIdentifier
VME::GenericBoard< IOModuleV262Register, cvA24_U_DATA >::ReadRegister

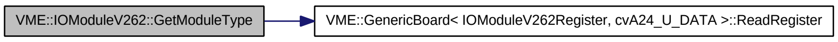
7.19.2.2 unsigned short VME::IOModuleV262::GetManufacturerId () const

Here is the call graph for this function:


VME::IOModuleV262::GetManufacturerId
VME::GenericBoard< IOModuleV262Register, cvA24_U_DATA >::ReadRegister

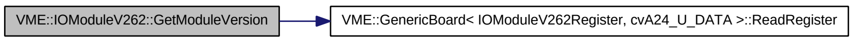
7.19.2.3 unsigned short VME::IOModuleV262::GetModuleType () const

Here is the call graph for this function:


VME::IOModuleV262::GetModuleType
VME::GenericBoard< IOModuleV262Register, cvA24_U_DATA >::ReadRegister

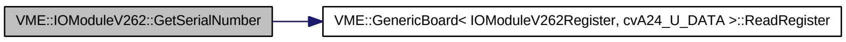
7.19.2.4 unsigned short VME::IOModuleV262::GetModuleVersion () const

Here is the call graph for this function:


VME::IOModuleV262::GetModuleVersion
VME::GenericBoard< IOModuleV262Register, cvA24_U_DATA >::ReadRegister

7.19.2.5 unsigned short VME::IOModuleV262::GetSerialNumber () const

Here is the call graph for this function:


VME::IOModuleV262::GetSerialNumber
VME::GenericBoard< IOModuleV262Register, cvA24_U_DATA >::ReadRegister

The documentation for this class was generated from the following files:

- include/VME_IOModuleV262.h
- src/VME_IOModuleV262.cpp

7.20 Message Class Reference

Base socket message type.

`#include <Message.h>`Inheritance diagram for Message:

Public Member Functions

- Message ()

  Void message constructor.

- Message (const char ∗msg)

  Construct a message from a string.

- Message (std::string msg)

  Construct a message from a string.

- virtual ∼Message ()
- MessageKey GetKey () const

  Placeholder for the MessageKey retrieval method.

- std::string GetString () const

  Retrieve the string carried by this message as a whole.

- bool IsFromWeb () const

  Extract from any message its potential arrival from a WebSocket protocol.

- void Dump (std::ostream &os=std::cout) const

Protected Attributes

- std::string fString

7.20.1 Detailed Description

Base socket message type. Base handler for messages to be transmitted through the socket

Author:

Laurent Forthomme <`laurent.forthomme@cern.ch`>

Date:

6 Apr 2015

7.20.2 Constructor & Destructor Documentation

7.20.2.1 Message::Message () `[inline]`

Void message constructor.

7.20.2.2 Message::Message (const char ∗ *msg*) `[inline]`

Construct a message from a string.

7.20.2.3 Message::Message (std::string *msg*) `[inline]`

Construct a message from a string.

7.20.2.4 virtual Message::∼Message () `[inline, virtual]`

7.20.3 Member Function Documentation

7.20.3.1 void Message::Dump (std::ostream & *os* = `std::cout`) const `[inline]`

Reimplemented in HTTPMessage, and SocketMessage.

7.20.3.2 MessageKey Message::GetKey () const `[inline]`

Placeholder for the MessageKey retrieval method.

Reimplemented in HTTPMessage, and SocketMessage.

7.20.3.3 std::string Message::GetString () const `[inline]`

Retrieve the string carried by this message as a whole.

Reimplemented in SocketMessage.

7.20.3.4 bool Message::IsFromWeb () const `[inline]`

Extract from any message its potential arrival from a WebSocket protocol.

7.20.4 Field Documentation

7.20.4.1 std::string Message::fString `[protected]`

The documentation for this class was generated from the following file:

- include/Message.h

7.21 Messenger Class Reference

Base master object for the socket.

`#include <Messenger.h>`Inheritance diagram for Messenger:Collaboration diagram for Messenger:

Public Member Functions

- Messenger ()

  Build a void master object or socket actor.

- Messenger (int port)

  Build a master object to control the socket.

- ~Messenger ()
- bool Connect ()

  Connect the master to the socket.

- void Disconnect ()

  Remove the master and destroy the socket.

- void Send (const Message &m, int sid) const

  Send any type of message to any client.

- void SendAll (const Socket::SocketType &type, const Message &m) const

  Send any type of message to all clients of one type.

- void Receive ()

  Handle a message reception from a client.

- void Broadcast (const Message &m) const

  Emit a message to all clients connected through the socket.

- void StartAcquisition ()

  Start the data acquisition.

- void StopAcquisition ()
- SocketType GetType () const

  Socket actor type retrieval method.

Private Member Functions

- void AddClient ()

  Add a client to listen to.

- void DisconnectClient (int sid, MessageKey key, bool force=false)

  Disconnect a client.

- void SwitchClientType (int sid, Socket::SocketType type)
- void ProcessMessage (SocketMessage m, int sid)

  Process a message received from the socket.

Private Attributes

- WebSocket ∗ fWS
- int fNumAttempts
- pid_t fPID
- int fStdoutPipe [2]
- int fStderrPipe [2]

7.21.1 Detailed Description

Base master object for the socket. Messenger/broadcaster object used by the server to send/receive commands from the clients/listeners.

Author:

Laurent Forthomme <`laurent.forthomme@cern.ch`>

Date:

23 Mar 2015

7.21.2 Constructor & Destructor Documentation

7.21.2.1 Messenger::Messenger ()

Build a void master object or socket actor.

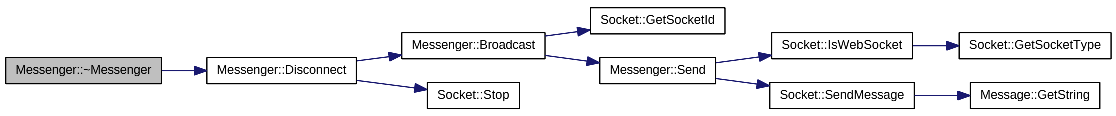
7.21.2.2 Messenger::Messenger (int *port*)

Build a master object to control the socket.

Here is the call graph for this function:

7.21.2.3 Messenger::∼Messenger ()

Here is the call graph for this function:

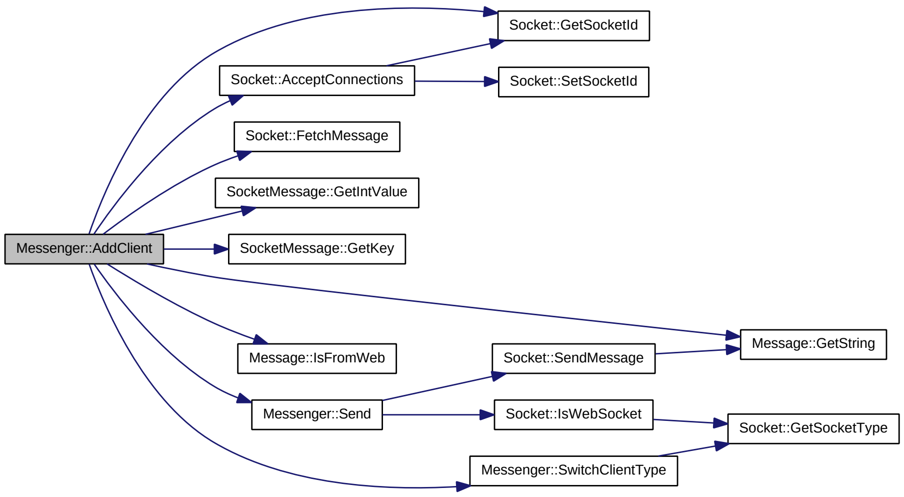


7.21.3 Member Function Documentation

7.21.3.1 void Messenger::AddClient () `[private]`

Add a client to listen to. Add one client to the list of socket actors to monitor for message retrieval/submission.

Here is the call graph for this function:



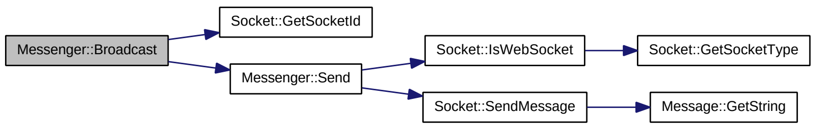
7.21.3.2 void Messenger::Broadcast (const Message & *m*) const

Emit a message to all clients connected through the socket.

Parameters:

← ***m*** Message to transmit

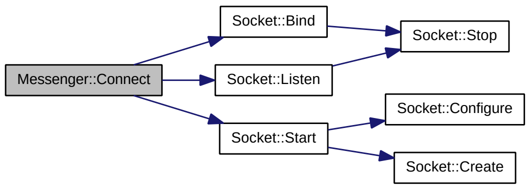
Here is the call graph for this function:



7.21.3.3 bool Messenger::Connect ()

Connect the master to the socket. Connect this master to the socket for clients to be able to bind.

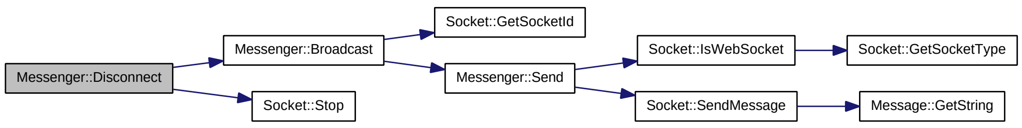
Here is the call graph for this function:



7.21.3.4 void Messenger::Disconnect ()

Remove the master and destroy the socket. Remove this master from the socket, thus disconnecting automatically the clients connected.

Here is the call graph for this function:



7.21.3.5 void Messenger::DisconnectClient (int *sid*, MessageKey *key*, bool *force* = `false`) `[private]`

Disconnect a client. Ask to a client to disconnect from this socket.

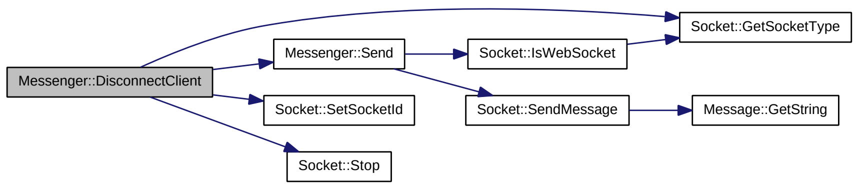
Parameters:

← ***sid*** Unique identifier of the client to disconnect

← ***key*** Key to the message to transmit for disconnection

← ***force*** Do we need to force the client out of this socket ?

Here is the call graph for this function:



7.21.3.6 SocketType Messenger::GetType () const `[inline]`

Socket actor type retrieval method.

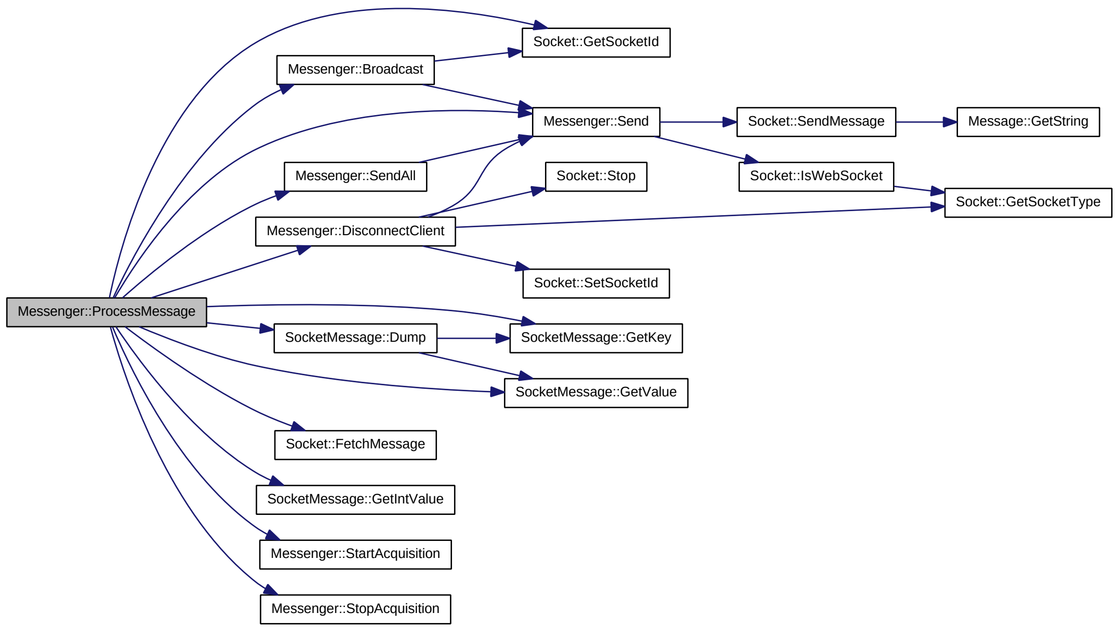
7.21.3.7 void Messenger::ProcessMessage (SocketMessage *m*, int *sid*) `[private]`

Process a message received from the socket.

Parameters:

← ***Unique*** identifier of the client sending the message

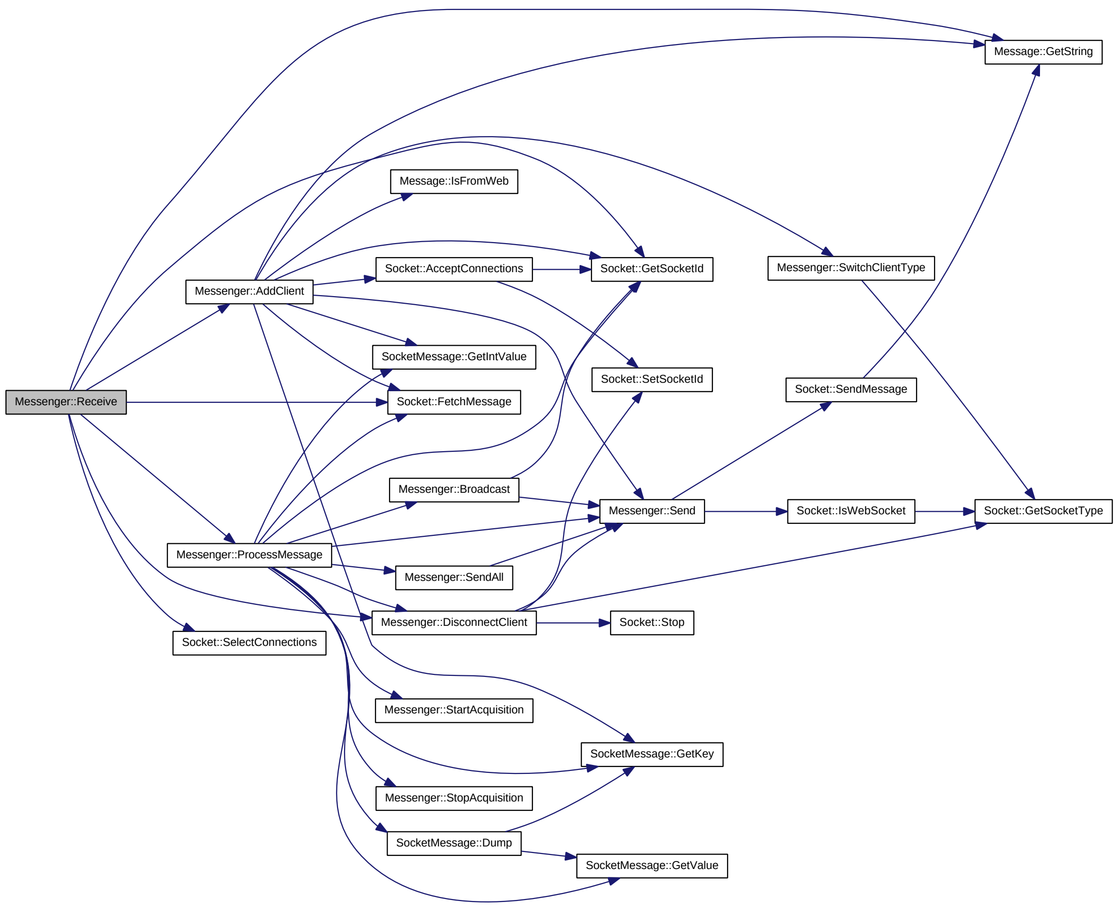
Here is the call graph for this function:



7.21.3.8 void Messenger::Receive ()

Handle a message reception from a client.

Here is the call graph for this function:



7.21.3.9 void Messenger::Send (const Message & *m*, int *sid*) const

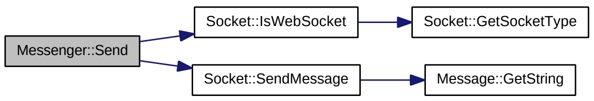
Send any type of message to any client.

Parameters:

← ***m*** Message to transmit

← ***sid*** Unique identifier of the client on this socket

Here is the call graph for this function:



7.21.3.10 void Messenger::SendAll (const Socket::SocketType & *type*, const Message & *m*) const `[inline]`

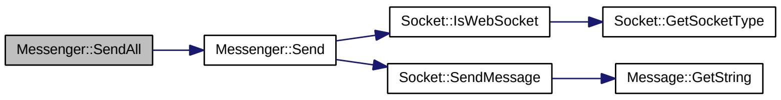
Send any type of message to all clients of one type.

Parameters:

← ***type*** Client type

← ***m*** Message to transmit

Here is the call graph for this function:



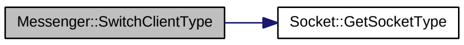
7.21.3.11 void Messenger::StartAcquisition ()

Start the data acquisition.

7.21.3.12 void Messenger::StopAcquisition ()

7.21.3.13 void Messenger::SwitchClientType (int *sid*, Socket::SocketType *type*) `[private]`

Here is the call graph for this function:



7.21.4 Field Documentation

7.21.4.1 int Messenger::fNumAttempts `[private]`

7.21.4.2 pid_t Messenger::fPID `[private]`

7.21.4.3 int Messenger::fStderrPipe[2] `[private]`

7.21.4.4 int Messenger::fStdoutPipe[2] `[private]`

7.21.4.5 WebSocket∗ Messenger::fWS `[private]`

The documentation for this class was generated from the following files:

- include/Messenger.h
- src/Messenger.cpp

7.22 VME::PCIInterfaceA2818 Class Reference

`#include <VME_PCIInterfaceA2818.h>`

Public Member Functions

- PCIInterfaceA2818 (const char ∗device)
- virtual ∼PCIInterfaceA2818 ()
- std::string GetFWRevision () const

Private Attributes

- int fHandle

7.22.1 Constructor & Destructor Documentation

7.22.1.1 VME::PCIInterfaceA2818::PCIInterfaceA2818 (const char ∗ *device*) `[inline]`

7.22.1.2 virtual VME::PCIInterfaceA2818::∼PCIInterfaceA2818 () `[inline, virtual]`

7.22.2 Member Function Documentation

7.22.2.1 std::string VME::PCIInterfaceA2818::GetFWRevision () const `[inline]`

7.22.3 Field Documentation

7.22.3.1 int VME::PCIInterfaceA2818::fHandle `[private]`

The documentation for this class was generated from the following file:

- include/VME_PCIInterfaceA2818.h

7.23 Socket Class Reference

Base socket object from which clients/master from a socket inherit.

`#include <Socket.h>`Inheritance diagram for Socket:

Public Types

- enum SocketType {
  INVALID = -1, MASTER = 0, WEBSOCKET_CLIENT, CLIENT,
  DETECTOR, DQM }

  Type of actor playing a role on the socket.

- typedef std::set< std::pair< int, SocketType > > SocketCollection

Public Member Functions

- Socket ()
- Socket (int port)
- virtual ~Socket ()
- void Stop ()

  Terminates the socket and all attached communications.

- void SetPort (int port)
- int GetPort () const

  Retrieve the port used for this socket.

- void AcceptConnections (Socket &socket)

  Accept connection from a client.

- void SelectConnections ()
- void SetSocketId (int sid)
- int GetSocketId () const
- SocketType GetSocketType (int sid) const
- bool IsWebSocket (int sid) const
- void DumpConnected () const

Protected Member Functions

- bool Start ()

  Start the socket.

- void Bind ()

  Bind a name to a socket.

- void PrepareConnection ()
- void Listen (int maxconn)

  Listen to incoming messages.

- void SendMessage (Message message, int id=-1) const

  Send a message on a socket.

- Message FetchMessage (int id=-1) const

  Receive a message from a socket.

Protected Attributes

- int fPort
- char fBuffer [MAX_WORD_LENGTH]
- SocketCollection fSocketsConnected
- fd_set fMaster

  Master file descriptor list.

- fd_set fReadFds

  Temp file descriptor list for select().

Private Member Functions

- void Create ()

  Create an endpoint for communication.

- void Configure ()

  Configure the socket object for communication.

Private Attributes

- int fSocketId
- struct sockaddr_in fAddress

7.23.1 Detailed Description

Base socket object from which clients/master from a socket inherit. General object providing all useful method to connect/bind/send/receive information through system sockets.

Author:

Laurent Forthomme <`laurent.forthomme@cern.ch`>

Date:

23 Mar 2015

7.23.2 Member Typedef Documentation

7.23.2.1 typedef std::set< std::pair<int,SocketType> > Socket::SocketCollection

7.23.3 Member Enumeration Documentation

7.23.3.1 enum Socket::SocketType

Type of actor playing a role on the socket.

Enumerator:

INVALID

MASTER

WEBSOCKET_CLIENT

CLIENT

DETECTOR

DQM

7.23.4 Constructor & Destructor Documentation

7.23.4.1 Socket::Socket () `[inline]`

7.23.4.2 Socket::Socket (int *port*)

7.23.4.3 Socket::∼Socket () `[virtual]`

7.23.5 Member Function Documentation

7.23.5.1 void Socket::AcceptConnections (Socket & *socket*)

Accept connection from a client. Set the socket to accept connections any client transmitting through the socket

Parameters:

inout] socket Master/client object to enable on the socket

Here is the call graph for this function:

7.23.5.2 void Socket::Bind () `[protected]`

Bind a name to a socket.

Returns:

Success of the operation

Here is the call graph for this function:

7.23.5.3 void Socket::Configure () `[private]`

Configure the socket object for communication.

7.23.5.4 void Socket::Create () `[private]`

Create an endpoint for communication.

7.23.5.5 void Socket::DumpConnected () const

7.23.5.6 Message Socket::FetchMessage (int *id* = `-1`) const `[protected]`

Receive a message from a socket.

Returns:

Received message as a std::string

7.23.5.7 int Socket::GetPort () const `[inline]`

Retrieve the port used for this socket.

7.23.5.8 int Socket::GetSocketId () const `[inline]`

7.23.5.9 SocketType Socket::GetSocketType (int *sid*) const `[inline]`

7.23.5.10 bool Socket::IsWebSocket (int *sid*) const `[inline]`

Here is the call graph for this function:

7.23.5.11 void Socket::Listen (int *maxconn*) `[protected]`

Listen to incoming messages. Set the socket to listen to any message coming from outside

Here is the call graph for this function:

7.23.5.12 void Socket::PrepareConnection () `[protected]`

Here is the call graph for this function:

7.23.5.13 void Socket::SelectConnections ()

Register all open file descriptors to read their communication through the socket

7.23.5.14 void Socket::SendMessage (Message *message*, int *id* = `-1`) const `[protected]`

Send a message on a socket.

Here is the call graph for this function:

7.23.5.15 void Socket::SetPort (int *port*) `[inline]`

7.23.5.16 void Socket::SetSocketId (int *sid*) `[inline]`

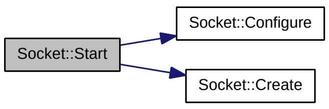
7.23.5.17 bool Socket::Start () `[protected]`

Start the socket. Launch all mandatory operations to set the socket to be used

Returns:

Success of the operation

Here is the call graph for this function:



7.23.5.18 void Socket::Stop ()

Terminates the socket and all attached communications.

7.23.6 Field Documentation

7.23.6.1 struct sockaddr_in Socket::fAddress `[read, private]`

7.23.6.2 char Socket::fBuffer[MAX_WORD_LENGTH] `[protected]`

7.23.6.3 fd_set Socket::fMaster `[protected]`

Master file descriptor list.

7.23.6.4 int Socket::fPort `[protected]`

7.23.6.5 fd_set Socket::fReadFds `[protected]`

Temp file descriptor list for select().

7.23.6.6 int Socket::fSocketId `[private]`

A file descriptor for this socket, if *Create* was performed beforehand.

7.23.6.7 SocketCollection Socket::fSocketsConnected `[protected]`

The documentation for this class was generated from the following files:

- include/Socket.h
- src/Socket.cpp

7.24 SocketMessage Class Reference

Socket-passed message type.

`#include <SocketMessage.h>`Inheritance diagram for SocketMessage:Collaboration diagram for SocketMessage:

Public Member Functions

- SocketMessage ()
- SocketMessage (const Message &msg)
- SocketMessage (const char ∗msg_s)
- SocketMessage (std::string msg_s)
- SocketMessage (const MessageKey &key)

  Construct a socket message out of a key.

- SocketMessage (const MessageKey &key, const char ∗value)

  Construct a socket message out of a key and a string-type value.

- SocketMessage (const MessageKey &key, std::string value)

  Construct a socket message out of a key and a string-type value.

- SocketMessage (const MessageKey &key, const int value)

  Construct a socket message out of a key and an integer-type value.

- SocketMessage (const MessageKey &key, const float value)

  Construct a socket message out of a key and a float-type value.

- SocketMessage (const MessageKey &key, const double value)

  Construct a socket message out of a key and a double precision-type value.

- SocketMessage (MessageMap msg_m)

  Construct a socket message out of a map of key/string-type value.

- ∼SocketMessage ()
- void SetKeyValue (const MessageKey &key, const char ∗value)

  String-valued message.

- void SetKeyValue (const MessageKey &key, int int_value)

  Send an integer-valued message.

- void SetKeyValue (const MessageKey &key, float float_value)

  Float-valued message.

- void SetKeyValue (const MessageKey &key, double double_value)

  Double-valued message.

- std::string GetString () const

  Extract the whole key:value message.

- MessageKey GetKey () const

  Extract the message's key.

- std::string GetValue () const

  Extract the message's string value.

- std::string GetCleanedValue () const

  Extract the message's string value (without the trailing endlines).

- int GetIntValue () const

  Extract the message's integer value.

- VectorValue GetVectorValue () const

  Extract the message's vector of string value.

- void Dump (std::ostream &os=std::cout) const

Private Member Functions

- MessageMap Object () const
- std::string String () const

Private Attributes

- MessageMap fMessage

7.24.1 Detailed Description

Socket-passed message type.

Author:

Laurent Forthomme <`laurent.forthomme@cern.ch`>

Date:

26 Mar 2015

7.24.2 Constructor & Destructor Documentation

7.24.2.1 SocketMessage::SocketMessage () `[inline]`

7.24.2.2 SocketMessage::SocketMessage (const Message & *msg*) `[inline]`

Here is the call graph for this function:

7.24.2.3 SocketMessage::SocketMessage (const char ∗ *msg_s*) `[inline]`

Here is the call graph for this function:

7.24.2.4 SocketMessage::SocketMessage (std::string *msg_s*) `[inline]`

Here is the call graph for this function:

7.24.2.5 SocketMessage::SocketMessage (const MessageKey & *key*) `[inline]`

Construct a socket message out of a key.

Here is the call graph for this function:

7.24.2.6 SocketMessage::SocketMessage (const MessageKey & *key*, const char ∗ *value*) `[inline]`

Construct a socket message out of a key and a string-type value.

Here is the call graph for this function:

7.24.2.7 SocketMessage::SocketMessage (const MessageKey & *key*, std::string *value*) `[inline]`

Construct a socket message out of a key and a string-type value.

Here is the call graph for this function:

7.24.2.8 SocketMessage::SocketMessage (const MessageKey & *key*, const int *value*) `[inline]`

Construct a socket message out of a key and an integer-type value.

Here is the call graph for this function:

7.24.2.9 SocketMessage::SocketMessage (const MessageKey & *key*, const float *value*) `[inline]`

Construct a socket message out of a key and a float-type value.

Here is the call graph for this function:

7.24.2.10 SocketMessage::SocketMessage (const MessageKey & *key*, const double *value*) `[inline]`

Construct a socket message out of a key and a double precision-type value.

Here is the call graph for this function:

7.24.2.11 SocketMessage::SocketMessage (MessageMap *msg_m*) `[inline]`

Construct a socket message out of a map of key/string-type value.

7.24.2.12 SocketMessage::∼SocketMessage () `[inline]`

7.24.3 Member Function Documentation

7.24.3.1 void SocketMessage::Dump (std::ostream & *os* = `std::cout`) const `[inline]`

Reimplemented from Message.

Here is the call graph for this function:

7.24.3.2 std::string SocketMessage::GetCleanedValue () const `[inline]`

Extract the message's string value (without the trailing endlines).

7.24.3.3 int SocketMessage::GetIntValue () const `[inline]`

Extract the message's integer value.

7.24.3.4 MessageKey SocketMessage::GetKey () const `[inline]`

Extract the message's key.

Reimplemented from Message.

7.24.3.5 std::string SocketMessage::GetString () const `[inline]`

Extract the whole key:value message.

Reimplemented from Message.

7.24.3.6 std::string SocketMessage::GetValue () const `[inline]`

Extract the message's string value.

7.24.3.7 VectorValue SocketMessage::GetVectorValue () const `[inline]`

Extract the message's vector of string value.

Here is the call graph for this function:

7.24.3.8 MessageMap SocketMessage::Object () const `[inline, private]`

7.24.3.9 void SocketMessage::SetKeyValue (const MessageKey & *key*, double *double_value*) `[inline]`

Double-valued message.

Here is the call graph for this function:

7.24.3.10 void SocketMessage::SetKeyValue (const MessageKey & *key*, float *float_value*) `[inline]`

Float-valued message.

Here is the call graph for this function:

7.24.3.11 void SocketMessage::SetKeyValue (const MessageKey & *key*, int *int_value*) `[inline]`

Send an integer-valued message.

Here is the call graph for this function:

7.24.3.12 void SocketMessage::SetKeyValue (const MessageKey & *key*, const char ∗ *value*) `[inline]`

String-valued message.

Here is the call graph for this function:

7.24.3.13 std::string SocketMessage::String () const `[inline, private]`

7.24.4 Field Documentation

7.24.4.1 MessageMap SocketMessage::fMessage `[private]`

The documentation for this class was generated from the following file:

- include/SocketMessage.h

7.25 VME::TDCErrorFlag Class Reference

Error flags handler.

`#include <VME_TDCEvent.h>`

Public Member Functions

- TDCErrorFlag (uint16_t ef)
- virtual ∼TDCErrorFlag ()
- uint16_t GetWord () const
- void Dump () const
- bool HasReadoutFIFOOverflow (unsigned int group_id) const

  Check whether hits have been lost from read-out FIFO overflow in a given group.
- bool HasL1BufferOverflow (unsigned int group_id) const

  Check whether hits have been lost from L1 buffer overflow in a given group.
- bool HasGroupError (unsigned int group_id) const

  Check whether hits have been lost due to error in a given group.
- bool HasReachedEventSizeLimit () const

  Hits rejected because of programmed event size limit.
- bool HasTriggerFIFOOverflow () const

  Event lost (trigger FIFO overflow).
- bool HasInternalChipError () const

  Internal fatal chip error has been detected.

Private Attributes

- uint16_t fWord

Friends

- std::ostream & operator<< (std::ostream &os, const TDCErrorFlag &ef)

7.25.1 Detailed Description

Error flags handler.

Author:

Laurent Forthomme <`laurent.forthomme@cern.ch`>

Date:

22 Jun 2015

7.25.2 Constructor & Destructor Documentation

7.25.2.1 VME::TDCErrorFlag::TDCErrorFlag (uint16_t *ef*) `[inline]`

7.25.2.2 virtual VME::TDCErrorFlag::∼TDCErrorFlag () `[inline, virtual]`

7.25.3 Member Function Documentation

7.25.3.1 void VME::TDCErrorFlag::Dump () const `[inline]`

7.25.3.2 uint16_t VME::TDCErrorFlag::GetWord () const `[inline]`

7.25.3.3 bool VME::TDCErrorFlag::HasGroupError (unsigned int *group_id*) const `[inline]`

Check whether hits have been lost due to error in a given group.

7.25.3.4 bool VME::TDCErrorFlag::HasInternalChipError () const `[inline]`

Internal fatal chip error has been detected.

7.25.3.5 bool VME::TDCErrorFlag::HasL1BufferOverflow (unsigned int *group_id*) const `[inline]`

Check whether hits have been lost from L1 buffer overflow in a given group.

7.25.3.6 bool VME::TDCErrorFlag::HasReachedEventSizeLimit () const `[inline]`

Hits rejected because of programmed event size limit.

7.25.3.7 bool VME::TDCErrorFlag::HasReadoutFIFOOverflow (unsigned int *group_id*) const `[inline]`

Check whether hits have been lost from read-out FIFO overflow in a given group.

7.25.3.8 bool VME::TDCErrorFlag::HasTriggerFIFOOverflow () const `[inline]`

Event lost (trigger FIFO overflow).

7.25.4 Friends And Related Function Documentation

7.25.4.1 std::ostream& operator<< (std::ostream & *os*, const TDCErrorFlag & *ef*) `[friend]`

7.25.5 Field Documentation

7.25.5.1 uint16_t VME::TDCErrorFlag::fWord `[private]`

The documentation for this class was generated from the following file:

- include/VME_TDCEvent.h

7.26 VME::TDCEvent Class Reference

HPTDC event parser.

```
#include <VME_TDCEvent.h>
```

Public Types

- enum EventType {
  TDCMeasurement = 0x0, TDCHeader = 0x1, TDCTrailer = 0x3, TDCError = 0x4,
  GlobalHeader = 0x8, GlobalTrailer = 0x10, ETTT = 0x11, Filler = 0x18 }

Public Member Functions

- TDCEvent ()
- TDCEvent (const TDCEvent &ev)
- TDCEvent (const uint32_t &word)
- virtual ∼TDCEvent ()
- void Dump () const
- void SetWord (const uint32_t &word)
- uint32_t GetWord () const
- EventType GetType () const

  Type of packet read out from the TDC.
- unsigned int GetTDCId () const

  Programmed identifier of master TDC providing the event.
- uint16_t GetEventId () const

  Event identifier from event counter.
- uint16_t GetWordCount () const

  Total number of words in event (including headers and trailers).
- unsigned int GetGeo () const
- unsigned int GetChannelId () const

  Channel number for.
- uint32_t GetEventCount () const

  Total number of events.
- uint16_t GetBunchId () const

  Bunch identifier of trigger (or trigger time tag).
- bool IsTrailing () const

Are we dealing with a trailing or a leading measurement?

- uint32_t GetETTT () const

  Extended trigger time tag.

- uint32_t GetTime (bool pair=false) const

  Edge measurement in programmed time resolution.

- unsigned int GetWidth () const

  Width of pulse in programmed time resolution.

- unsigned int GetStatus () const
- TDCErrorFlag GetErrorFlags () const

  Return error flags if an error condition has been detected.

Private Attributes

- uint32_t fWord

7.26.1 Detailed Description

HPTDC event parser. Object enabling to decipher any measurement/error/debug event returned by the HPTDC chip

Author:

Laurent Forthomme <`laurent.forthomme@cern.ch`>

Date:

4 May 2015

7.26.2 Member Enumeration Documentation

7.26.2.1 enum VME::TDCEvent::EventType

Enumerator:

TDCMeasurement

TDCHeader

TDCTrailer

TDCError

GlobalHeader

GlobalTrailer

ETTT

Filler

7.26.3 Constructor & Destructor Documentation

7.26.3.1 VME::TDCEvent::TDCEvent () `[inline]`

7.26.3.2 VME::TDCEvent::TDCEvent (const TDCEvent & *ev*) `[inline]`

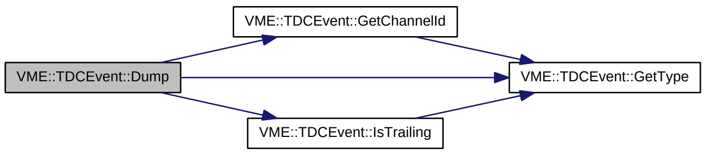
7.26.3.3 VME::TDCEvent::TDCEvent (const uint32_t & *word*) `[inline]`

7.26.3.4 virtual VME::TDCEvent::∼TDCEvent () `[inline, virtual]`

7.26.4 Member Function Documentation

7.26.4.1 void VME::TDCEvent::Dump () const `[inline]`

Here is the call graph for this function:



7.26.4.2 uint16_t VME::TDCEvent::GetBunchId () const `[inline]`

Bunch identifier of trigger (or trigger time tag).

Here is the call graph for this function:

7.26.4.3 unsigned int VME::TDCEvent::GetChannelId () const `[inline]`

Channel number for.

Here is the call graph for this function:

7.26.4.4 TDCErrorFlag VME::TDCEvent::GetErrorFlags () const `[inline]`

Return error flags if an error condition has been detected.

Here is the call graph for this function:

7.26.4.5 uint32_t VME::TDCEvent::GetETTT () const `[inline]`

Extended trigger time tag.

Here is the call graph for this function:

7.26.4.6 uint32_t VME::TDCEvent::GetEventCount () const `[inline]`

Total number of events.

Here is the call graph for this function:

7.26.4.7 uint16_t VME::TDCEvent::GetEventId () const `[inline]`

Event identifier from event counter.

Here is the call graph for this function:

7.26.4.8 unsigned int VME::TDCEvent::GetGeo () const `[inline]`

Here is the call graph for this function:

7.26.4.9 unsigned int VME::TDCEvent::GetStatus () const `[inline]`

Here is the call graph for this function:

7.26.4.10 unsigned int VME::TDCEvent::GetTDCId () const `[inline]`

Programmed identifier of master TDC providing the event.

Here is the call graph for this function:

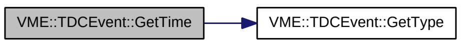
7.26.4.11 uint32_t VME::TDCEvent::GetTime (bool *pair* = `false`) const `[inline]`

Edge measurement in programmed time resolution.

Parameters:

← ***pair*** Are we dealing with a pair measurement? (only for leading time word)

Here is the call graph for this function:



7.26.4.12 EventType VME::TDCEvent::GetType () const `[inline]`

Type of packet read out from the TDC.

7.26.4.13 unsigned int VME::TDCEvent::GetWidth () const `[inline]`

Width of pulse in programmed time resolution.

Here is the call graph for this function:

7.26.4.14 uint32_t VME::TDCEvent::GetWord () const `[inline]`

7.26.4.15 uint16_t VME::TDCEvent::GetWordCount () const `[inline]`

Total number of words in event (including headers and trailers).

Here is the call graph for this function:

7.26.4.16 bool VME::TDCEvent::IsTrailing () const `[inline]`

Are we dealing with a trailing or a leading measurement?

Here is the call graph for this function:

7.26.4.17 void VME::TDCEvent::SetWord (const uint32_t & *word*) `[inline]`

7.26.5 Field Documentation

7.26.5.1 uint32_t VME::TDCEvent::fWord `[private]`

The documentation for this class was generated from the following file:

- include/VME_TDCEvent.h

7.27 VME::TDCMeasurement Class Reference

`#include <VME_TDCMeasurement.h>`

Public Member Functions

- TDCMeasurement ()
- TDCMeasurement (const std::vector< TDCEvent > &v)
- ~TDCMeasurement ()
- void Dump ()
- void SetEventsCollection (const std::vector< TDCEvent > &v)
- uint32_t GetLeadingTime (unsigned short event_id=0)
- uint32_t GetTrailingTime (unsigned short event_id=0)
- uint16_t GetToT (unsigned short event_id=0)
- uint16_t GetChannelId (unsigned short event_id=0)
- uint16_t GetTDCId ()
- uint16_t GetEventId ()
- uint16_t GetBunchId ()
- uint32_t GetETTT ()
- size_t NumEvents () const
- size_t NumErrors () const

Private Attributes

- std::map< TDCEvent::EventType, TDCEvent > fMap
- std::vector< std::pair< TDCEvent, TDCEvent > > fEvents

7.27.1 Detailed Description

Author:

Laurent Forthomme <`laurent.forthomme@cern.ch`>

Date:

Jun 2015

7.27.2 Constructor & Destructor Documentation

7.27.2.1 VME::TDCMeasurement::TDCMeasurement () `[inline]`

7.27.2.2 VME::TDCMeasurement::TDCMeasurement (const std::vector< TDCEvent > & *v*) `[inline]`

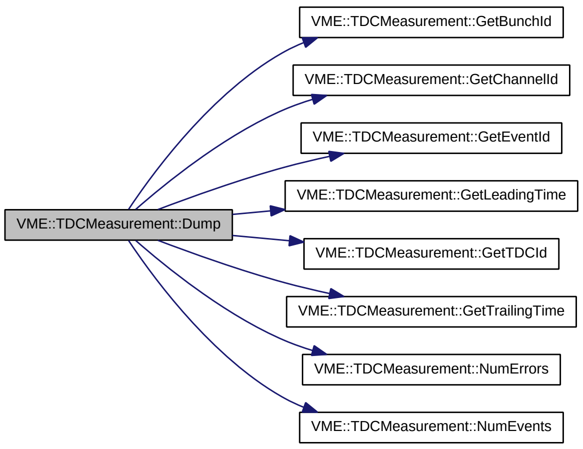
Here is the call graph for this function:

7.27.2.3 VME::TDCMeasurement::∼TDCMeasurement () `[inline]`

7.27.3 Member Function Documentation

7.27.3.1 void VME::TDCMeasurement::Dump () `[inline]`

Here is the call graph for this function:


VME::TDCMeasurement::Dump
VME::TDCMeasurement::GetBunchId
VME::TDCMeasurement::GetChannelId
VME::TDCMeasurement::GetEventId
VME::TDCMeasurement::GetLeadingTime
VME::TDCMeasurement::GetTDCId
VME::TDCMeasurement::GetTrailingTime
VME::TDCMeasurement::NumErrors
VME::TDCMeasurement::NumEvents

7.27.3.2 uint16_t VME::TDCMeasurement::GetBunchId () `[inline]`

7.27.3.3 uint16_t VME::TDCMeasurement::GetChannelId (unsigned short *event_id* = `0`) `[inline]`

7.27.3.4 uint32_t VME::TDCMeasurement::GetETTT () `[inline]`

7.27.3.5 uint16_t VME::TDCMeasurement::GetEventId () `[inline]`

7.27.3.6 uint32_t VME::TDCMeasurement::GetLeadingTime (unsigned short *event_id* = `0`) `[inline]`

7.27.3.7 uint16_t VME::TDCMeasurement::GetTDCId () `[inline]`

7.27.3.8 uint16_t VME::TDCMeasurement::GetToT (unsigned short *event_id* = `0`) `[inline]`

Here is the call graph for this function:

7.27.3.9 uint32_t VME::TDCMeasurement::GetTrailingTime (unsigned short *event_id* = 0) `[inline]`

7.27.3.10 size_t VME::TDCMeasurement::NumErrors () const `[inline]`

7.27.3.11 size_t VME::TDCMeasurement::NumEvents () const `[inline]`

7.27.3.12 void VME::TDCMeasurement::SetEventsCollection (const std::vector< TDCEvent > & *v*) `[inline]`

7.27.4 Field Documentation

7.27.4.1 std::vector< std::pair<TDCEvent,TDCEvent> > VME::TDCMeasurement::fEvents `[private]`

7.27.4.2 std::map<TDCEvent::EventType,TDCEvent> VME::TDCMeasurement::fMap `[private]`

The documentation for this class was generated from the following file:

- include/VME_TDCMeasurement.h

7.28 VME::TDCV1x90 Class Reference

`#include <VME_TDCV1x90.h>`Inheritance diagram for VME::TDCV1x90:Collaboration diagram for VME::TDCV1x90:

Public Types

- enum DLLMode { DLL_Direct_LowRes = 0x0, DLL_PLL_LowRes = 0x1, DLL_PLL_MedRes = 0x2, DLL_PLL_HighRes = 0x3 }

Public Member Functions

- TDCV1x90 (int32_t bhandle, uint32_t baseaddr)
- ∼TDCV1x90 ()
- void SetVerboseLevel (unsigned short verb=1)
- void SetTestMode (bool en=true) const
- bool GetTestMode () const
- uint32_t GetModel () const
- uint32_t GetOUI () const
- uint32_t GetSerialNumber () const
- uint16_t GetFirmwareRevision () const
- void CheckConfiguration () const
- void EnableChannel (short) const
- void DisableChannel (short) const
- void SetPoI (uint16_t word1, uint16_t word2) const
- std::map< unsigned short, bool > GetPoI () const
- void SetLSBTraileadEdge (trailead_edge_lsb) const
- void SetAcquisitionMode (const AcquisitionMode &)
- AcquisitionMode GetAcquisitionMode ()
- void SetTriggerMatching ()
- void SetContinuousStorage ()
- void SetDetectionMode (const DetectionMode &detm)
- DetectionMode GetDetectionMode ()
- void SetDLLClock (const DLLMode &dll) const
- DLLMode GetDLLClock () const
- void SetGlobalOffset (const GlobalOffset &) const
- GlobalOffset GetGlobalOffset () const
- void SetRCAdjust (int, uint16_t) const
- uint16_t GetRCAdjust (int) const
- uint32_t GetEventCounter () const

  Number of occured triggers.

- uint16_t GetEventStored () const

  Number of events currently stored in the output buffer.

- void SetTDCEncapsulation (bool) const
- bool GetTDCEncapsulation () const
- void SetErrorMarks (bool mode=true)
- bool GetErrorMarks () const
- void SetPairModeResolution (int, int) const
- uint16_t GetResolution () const
- void SetBLTEventNumberRegister (const uint16_t &) const
- uint16_t GetBLTEventNumberRegister () const
- void SetWindowWidth (const uint16_t &)
- uint16_t GetWindowWidth () const
- void SetWindowOffset (const int16_t &) const
- int16_t GetWindowOffset () const
- uint16_t GetTriggerConfiguration (const trig_conf &) const
- bool SoftwareClear () const
- bool SoftwareReset () const
- bool HardwareReset () const
- void SetETTT (bool ettt=true) const
- bool GetETTT () const
- void SetStatus (const TDCV1x90Status &) const
- TDCV1x90Status GetStatus () const
- void SetControl (const TDCV1x90Control &) const
- TDCV1x90Control GetControl () const
- TDCEventCollection FetchEvents ()
- void SetChannelDeadTime (unsigned short dt) const
- unsigned short GetChannelDeadTime () const
- void SetFIFOSize (const uint16_t &) const
- uint16_t GetFIFOSize () const
- void abort ()

Private Member Functions

- bool WaitMicro (const micro_handshake &mode) const
- void ReadAcquisitionMode ()
- void ReadDetectionMode ()

Private Attributes

- unsigned short fVerb
- AcquisitionMode fAcquisitionMode
- DetectionMode fDetectionMode
- bool fErrorMarks
- uint16_t fWindowWidth
- uint32_t ∗ fBuffer
- uint32_t nchannels
- bool gEnd
- std::string pair_lead_res [8]
- std::string pair_width_res [16]

7.28.1 Detailed Description

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Date:

Jun 2010 (NA62-Gigatracker)
May 2015 (CMS-TOTEM PPS)

7.28.2 Member Enumeration Documentation

7.28.2.1 enum VME::TDCV1x90::DLLMode

Enumerator:

DLL_Direct_LowRes

DLL_PLL_LowRes

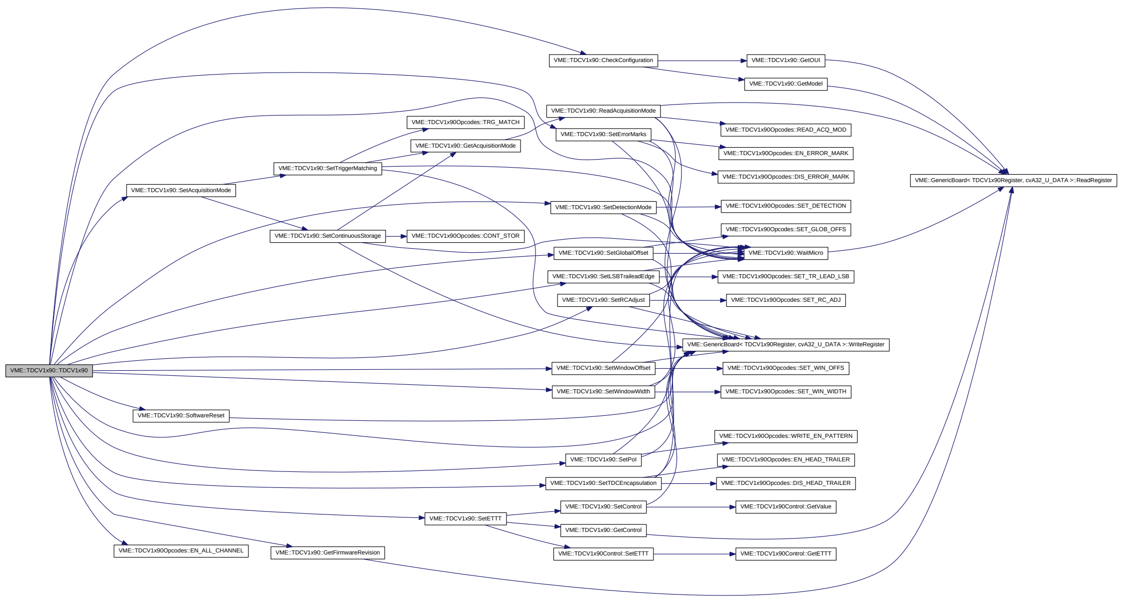
DLL_PLL_MedRes

DLL_PLL_HighRes

7.28.3 Constructor & Destructor Documentation

7.28.3.1 VME::TDCV1x90::TDCV1x90 (int32_t *bhandle*, uint32_t *baseaddr*)

Here is the call graph for this function:


VME::TDCV1x90::CheckConfiguration
VME::TDCV1x90::GetOUI
VME::TDCV1x90::GetModel
VME::TDCV1x90::ReadAcquisitionMode
VME::TDCV1x90Opcodes::TRG_MATCH
VME::TDCV1x90::SetErrorMarks
VME::TDCV1x90Opcodes::READ_ACQ_MOD
VME::TDCV1x90::GetAcquisitionMode
VME::TDCV1x90Opcodes::EN_ERROR_MARK
VME::TDCV1x90::SetTriggerMatching
VME::TDCV1x90Opcodes::DIS_ERROR_MARK
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::TDCV1x90::SetAcquisitionMode
VME::TDCV1x90::SetDetectionMode
VME::TDCV1x90Opcodes::SET_DETECTION
VME::TDCV1x90Opcodes::SET_GLOB_OFFS
VME::TDCV1x90::SetContinuousStorage
VME::TDCV1x90Opcodes::CONT_STOR
VME::TDCV1x90::SetGlobalOffset
VME::TDCV1x90::WaitMicro
VME::TDCV1x90::SetLSBTraileadEdge
VME::TDCV1x90Opcodes::SET_TR_LEAD_LSB
VME::TDCV1x90::SetRCAdjust
VME::TDCV1x90Opcodes::SET_RC_ADJ
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister
VME::TDCV1x90::TDCV1x90
VME::TDCV1x90::SetWindowOffset
VME::TDCV1x90Opcodes::SET_WIN_OFFS
VME::TDCV1x90::SetWindowWidth
VME::TDCV1x90Opcodes::SET_WIN_WIDTH
VME::TDCV1x90::SoftwareReset
VME::TDCV1x90Opcodes::WRITE_EN_PATTERN
VME::TDCV1x90::SetPol
VME::TDCV1x90Opcodes::EN_HEAD_TRAILER
VME::TDCV1x90::SetTDCEncapsulation
VME::TDCV1x90Opcodes::DIS_HEAD_TRAILER
VME::TDCV1x90::SetControl
VME::TDCV1x90Control::GetValue
VME::TDCV1x90::SetETTT
VME::TDCV1x90::GetControl
VME::TDCV1x90Opcodes::EN_ALL_CHANNEL
VME::TDCV1x90::GetFirmwareRevision
VME::TDCV1x90Control::SetETTT
VME::TDCV1x90Control::GetETTT

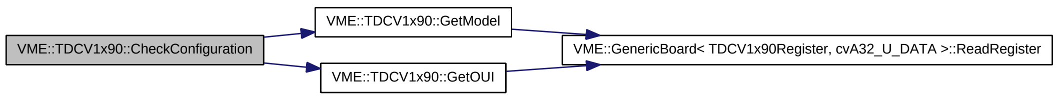
7.28.3.2 VME::TDCV1x90::∼TDCV1x90 ()

7.28.4 Member Function Documentation

7.28.4.1 void VME::TDCV1x90::abort ()

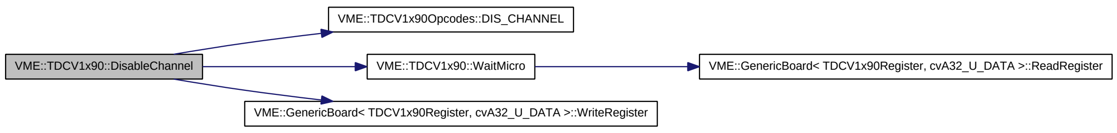
7.28.4.2 void VME::TDCV1x90::CheckConfiguration () const

Here is the call graph for this function:


VME::TDCV1x90::CheckConfiguration
VME::TDCV1x90::GetModel
VME::TDCV1x90::GetOUI
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister

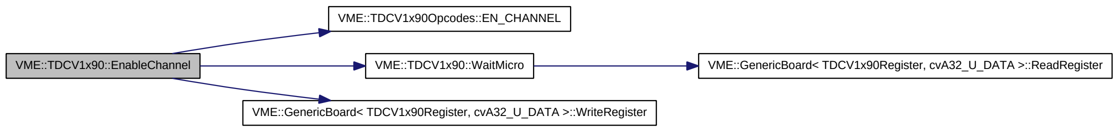
7.28.4.3 void VME::TDCV1x90::DisableChannel (short *channel_id*) const

Here is the call graph for this function:


VME::TDCV1x90::DisableChannel
VME::TDCV1x90Opcodes::DIS_CHANNEL
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister

7.28.4.4 void VME::TDCV1x90::EnableChannel (short *channel_id*) const

Here is the call graph for this function:

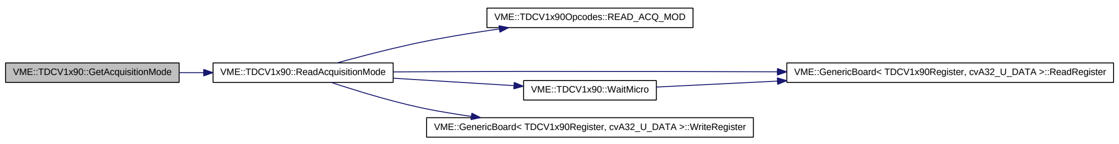

VME::TDCV1x90::EnableChannel
VME::TDCV1x90Opcodes::EN_CHANNEL
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister

7.28.4.5 TDCEventCollection VME::TDCV1x90::FetchEvents ()

Here is the call graph for this function:

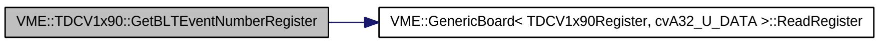
7.28.4.6 AcquisitionMode VME::TDCV1x90::GetAcquisitionMode () [inline]

Here is the call graph for this function:


VME::TDCV1x90::GetAcquisitionMode
VME::TDCV1x90::ReadAcquisitionMode
VME::TDCV1x90Opcodes::READ_ACQ_MOD
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

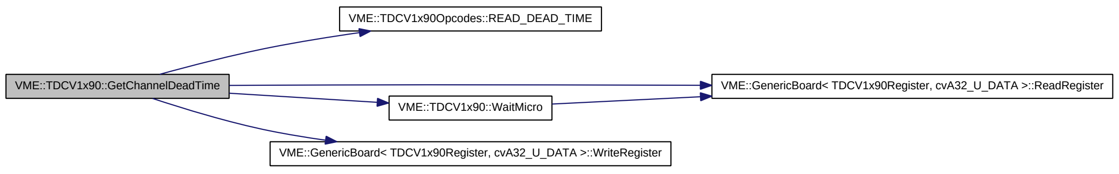
7.28.4.7 uint16_t VME::TDCV1x90::GetBLTEventNumberRegister () const

Here is the call graph for this function:


VME::TDCV1x90::GetBLTEventNumberRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister

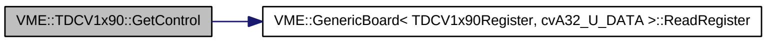
7.28.4.8 unsigned short VME::TDCV1x90::GetChannelDeadTime () const

Here is the call graph for this function:


VME::TDCV1x90::GetChannelDeadTime
VME::TDCV1x90Opcodes::READ_DEAD_TIME
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

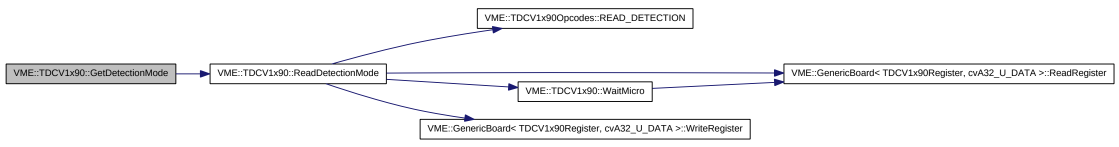
7.28.4.9 TDCV1x90Control VME::TDCV1x90::GetControl () const

Here is the call graph for this function:


VME::TDCV1x90::GetControl
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister

7.28.4.10 DetectionMode VME::TDCV1x90::GetDetectionMode () `[inline]`

Here is the call graph for this function:

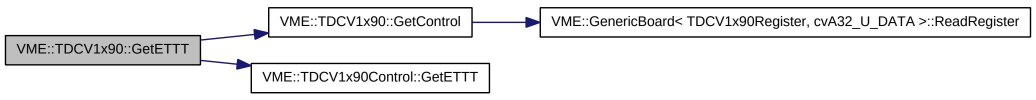

VME::TDCV1x90::GetDetectionMode
VME::TDCV1x90::ReadDetectionMode
VME::TDCV1x90Opcodes::READ_DETECTION
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

7.28.4.11 DLLMode VME::TDCV1x90::GetDLLClock () const

7.28.4.12 bool VME::TDCV1x90::GetErrorMarks () const `[inline]`

7.28.4.13 bool VME::TDCV1x90::GetETTT () const `[inline]`

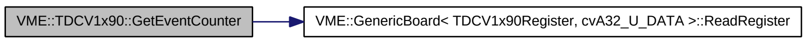
Here is the call graph for this function:


VME::TDCV1x90::GetETTT
VME::TDCV1x90::GetControl
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::TDCV1x90Control::GetETTT

7.28.4.14 uint32_t VME::TDCV1x90::GetEventCounter () const

Number of occured triggers. Number of acquired events since the latest module's reset/clear; this counter works in trigger Matching Mode only.

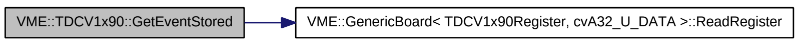
Here is the call graph for this function:


VME::TDCV1x90::GetEventCounter
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister

7.28.4.15 uint16_t VME::TDCV1x90::GetEventStored () const

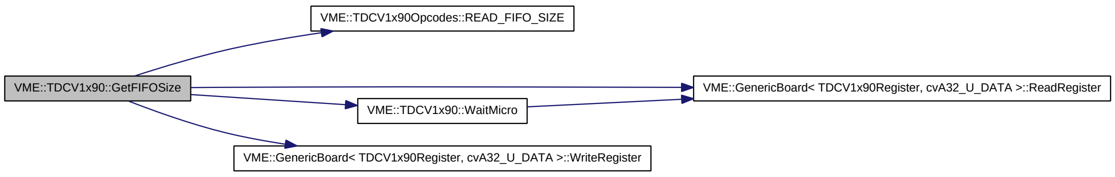
Number of events currently stored in the output buffer.

Here is the call graph for this function:


VME::TDCV1x90::GetEventStored
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister

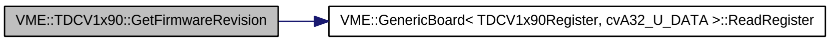
7.28.4.16 uint16_t VME::TDCV1x90::GetFIFOSize () const

Here is the call graph for this function:


VME::TDCV1x90::GetFIFOSize
VME::TDCV1x90Opcodes::READ_FIFO_SIZE
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

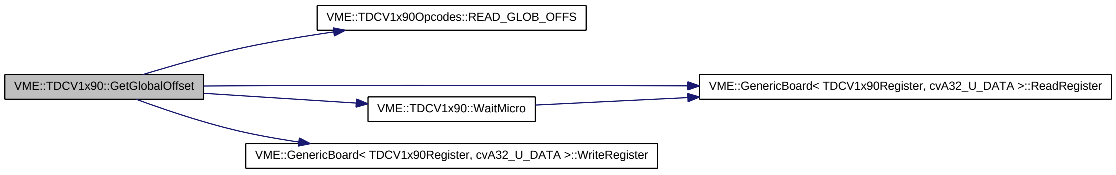
7.28.4.17 uint16_t VME::TDCV1x90::GetFirmwareRevision () const

Here is the call graph for this function:


VME::TDCV1x90::GetFirmwareRevision
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister

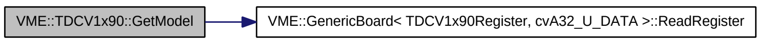
7.28.4.18 GlobalOffset VME::TDCV1x90::GetGlobalOffset () const

Here is the call graph for this function:


VME::TDCV1x90::GetGlobalOffset
VME::TDCV1x90Opcodes::READ_GLOB_OFFS
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

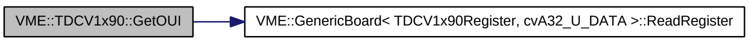
7.28.4.19 uint32_t VME::TDCV1x90::GetModel () const

Here is the call graph for this function:


VME::TDCV1x90::GetModel
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister

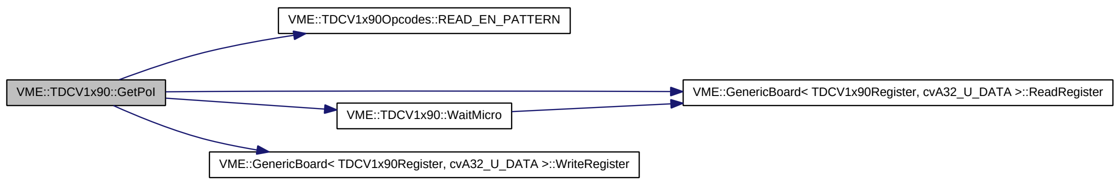
7.28.4.20 uint32_t VME::TDCV1x90::GetOUI () const

Here is the call graph for this function:


VME::TDCV1x90::GetOUI
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister

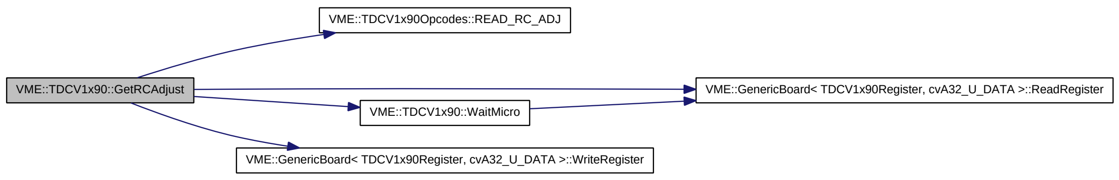
7.28.4.21 std::map< unsigned short, bool > VME::TDCV1x90::GetPoI () const

Here is the call graph for this function:


VME::TDCV1x90Opcodes::READ_EN_PATTERN
VME::TDCV1x90::GetPoI
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

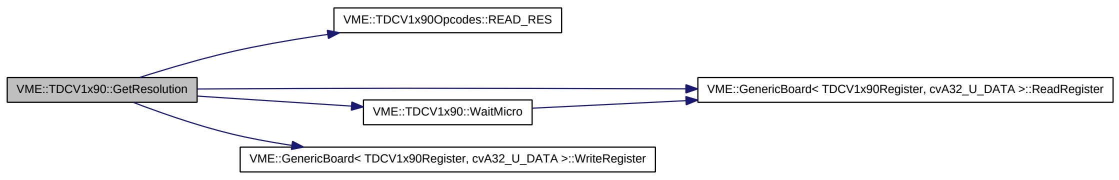
7.28.4.22 uint16_t VME::TDCV1x90::GetRCAdjust (int *tdc*) const

Here is the call graph for this function:


VME::TDCV1x90Opcodes::READ_RC_ADJ
VME::TDCV1x90::GetRCAdjust
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

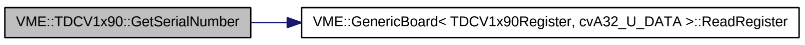
7.28.4.23 uint16_t VME::TDCV1x90::GetResolution () const

Here is the call graph for this function:


VME::TDCV1x90Opcodes::READ_RES
VME::TDCV1x90::GetResolution
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

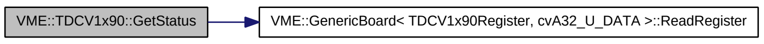
7.28.4.24 uint32_t VME::TDCV1x90::GetSerialNumber () const

Here is the call graph for this function:


VME::TDCV1x90::GetSerialNumber
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister

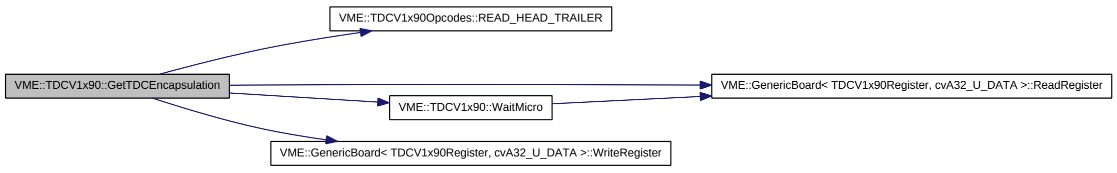
7.28.4.25 TDCV1x90Status VME::TDCV1x90::GetStatus () const

Here is the call graph for this function:


VME::TDCV1x90::GetStatus
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister

7.28.4.26 bool VME::TDCV1x90::GetTDCEncapsulation () const

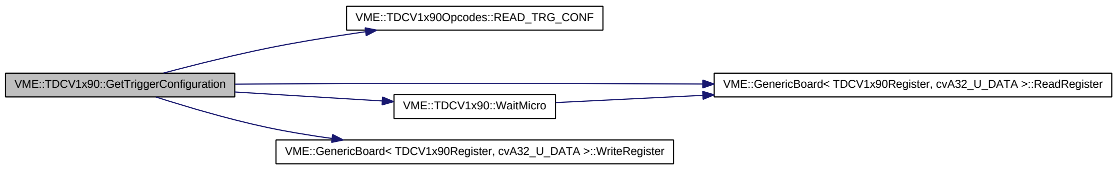
Here is the call graph for this function:


VME::TDCV1x90Opcodes::READ_HEAD_TRAILER
VME::TDCV1x90::GetTDCEncapsulation
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

7.28.4.27 bool VME::TDCV1x90::GetTestMode () const

7.28.4.28 uint16_t VME::TDCV1x90::GetTriggerConfiguration (const trig_conf & *type*) const

Here is the call graph for this function:


VME::TDCV1x90Opcodes::READ_TRG_CONF
VME::TDCV1x90::GetTriggerConfiguration
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

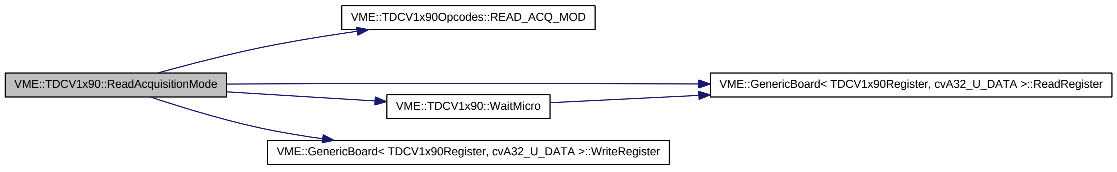
7.28.4.29 int16_t VME::TDCV1x90::GetWindowOffset () const

7.28.4.30 uint16_t VME::TDCV1x90::GetWindowWidth () const [inline]

7.28.4.31 bool VME::TDCV1x90::HardwareReset () const

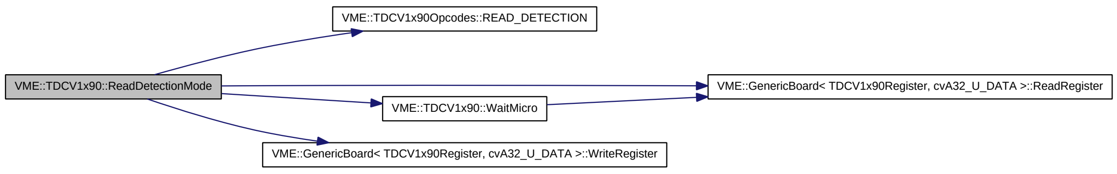
7.28.4.32 void VME::TDCV1x90::ReadAcquisitionMode () [private]

Here is the call graph for this function:


VME::TDCV1x90::ReadAcquisitionMode
VME::TDCV1x90Opcodes::READ_ACQ_MOD
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

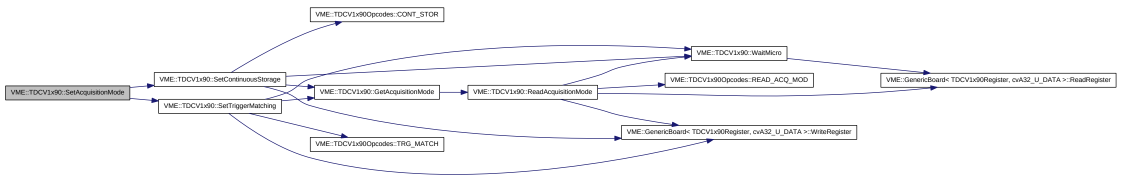
7.28.4.33 void VME::TDCV1x90::ReadDetectionMode () [private]

Here is the call graph for this function:


VME::TDCV1x90::ReadDetectionMode
VME::TDCV1x90Opcodes::READ_DETECTION
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

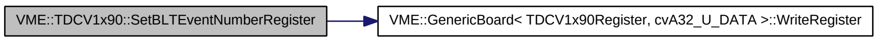
7.28.4.34 void VME::TDCV1x90::SetAcquisitionMode (const AcquisitionMode & *mode*)

Here is the call graph for this function:


VME::TDCV1x90::SetAcquisitionMode
VME::TDCV1x90::SetContinuousStorage
VME::TDCV1x90::SetTriggerMatching
VME::TDCV1x90Opcodes::CONT_STOR
VME::TDCV1x90::GetAcquisitionMode
VME::TDCV1x90Opcodes::TRG_MATCH
VME::TDCV1x90::ReadAcquisitionMode
VME::TDCV1x90::WaitMicro
VME::TDCV1x90Opcodes::READ_ACQ_MOD
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister

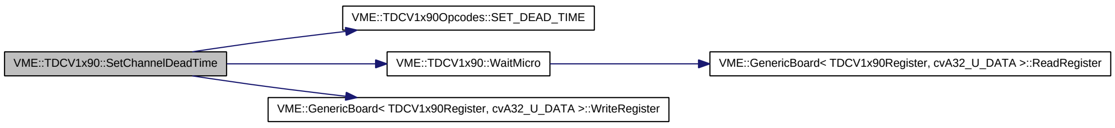
7.28.4.35 void VME::TDCV1x90::SetBLTEventNumberRegister (const uint16_t & *value*) const

Here is the call graph for this function:


VME::TDCV1x90::SetBLTEventNumberRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

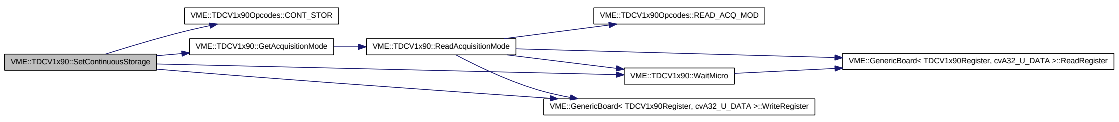
7.28.4.36 void VME::TDCV1x90::SetChannelDeadTime (unsigned short *dt*) const

Here is the call graph for this function:


VME::TDCV1x90::SetChannelDeadTime
VME::TDCV1x90Opcodes::SET_DEAD_TIME
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

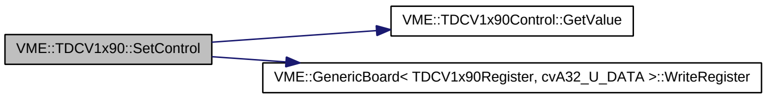
7.28.4.37 void VME::TDCV1x90::SetContinuousStorage ()

Here is the call graph for this function:


VME::TDCV1x90::SetContinuousStorage
VME::TDCV1x90Opcodes::CONT_STOR
VME::TDCV1x90::GetAcquisitionMode
VME::TDCV1x90::ReadAcquisitionMode
VME::TDCV1x90Opcodes::READ_ACQ_MOD
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

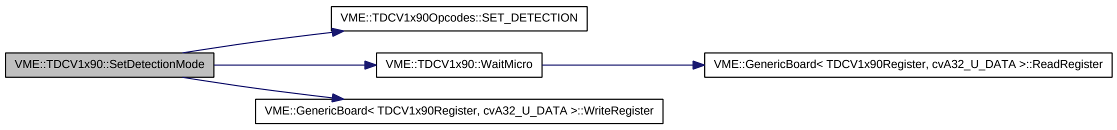
7.28.4.38 void VME::TDCV1x90::SetControl (const TDCV1x90Control & *control*) const

Here is the call graph for this function:


VME::TDCV1x90::SetControl
VME::TDCV1x90Control::GetValue
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

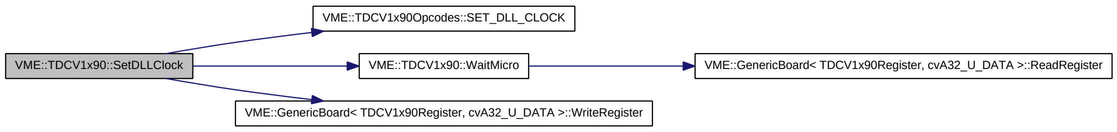
7.28.4.39 void VME::TDCV1x90::SetDetectionMode (const DetectionMode & *detm*)

Here is the call graph for this function:


VME::TDCV1x90::SetDetectionMode
VME::TDCV1x90Opcodes::SET_DETECTION
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

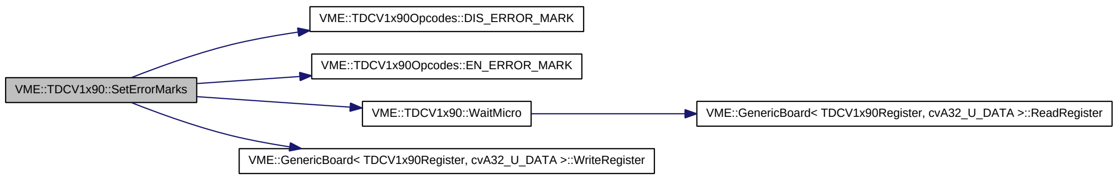
7.28.4.40 void VME::TDCV1x90::SetDLLClock (const DLLMode & *dll*) const

Here is the call graph for this function:


VME::TDCV1x90::SetDLLClock
VME::TDCV1x90Opcodes::SET_DLL_CLOCK
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

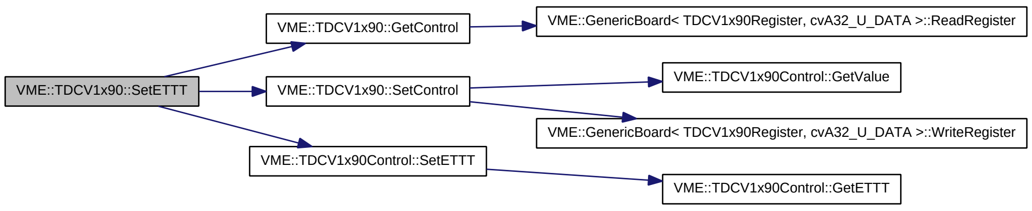
7.28.4.41 void VME::TDCV1x90::SetErrorMarks (bool *mode* = `true`)

Here is the call graph for this function:


VME::TDCV1x90::SetErrorMarks
VME::TDCV1x90Opcodes::DIS_ERROR_MARK
VME::TDCV1x90Opcodes::EN_ERROR_MARK
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

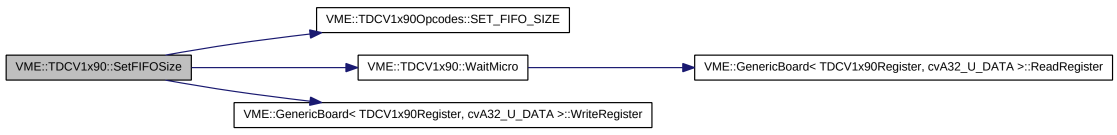
7.28.4.42 void VME::TDCV1x90::SetETTT (bool *ettt* = `true`) const `[inline]`

Here is the call graph for this function:


VME::TDCV1x90::SetETTT
VME::TDCV1x90::GetControl
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::TDCV1x90::SetControl
VME::TDCV1x90Control::GetValue
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister
VME::TDCV1x90Control::SetETTT
VME::TDCV1x90Control::GetETTT

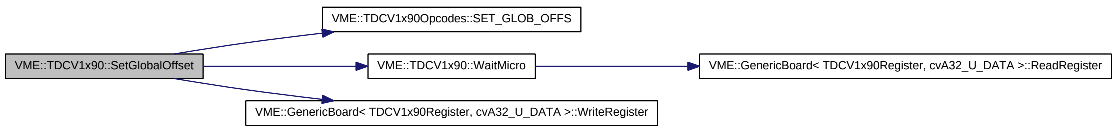
7.28.4.43 void VME::TDCV1x90::SetFIFOSize (const uint16_t & *size*) const

Here is the call graph for this function:


VME::TDCV1x90::SetFIFOSize
VME::TDCV1x90Opcodes::SET_FIFO_SIZE
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

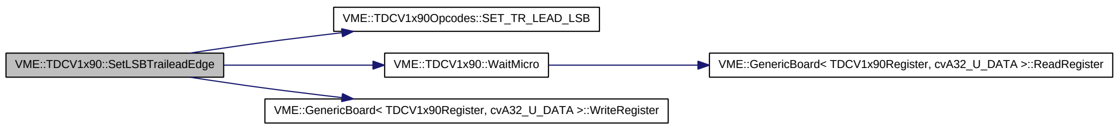
7.28.4.44 void VME::TDCV1x90::SetGlobalOffset (const GlobalOffset & *offs*) const

Here is the call graph for this function:


VME::TDCV1x90::SetGlobalOffset
VME::TDCV1x90Opcodes::SET_GLOB_OFFS
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

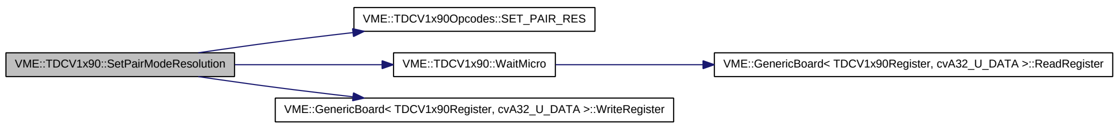
7.28.4.45 void VME::TDCV1x90::SetLSBTraileadEdge (trailead_edge_lsb *conf*) const

Here is the call graph for this function:


VME::TDCV1x90::SetLSBTraileadEdge
VME::TDCV1x90Opcodes::SET_TR_LEAD_LSB
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

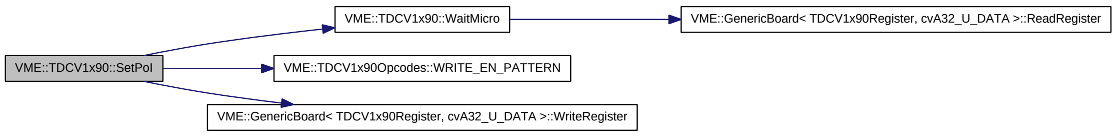
7.28.4.46 void VME::TDCV1x90::SetPairModeResolution (int *lead_time_res*, int *pulse_width_res*) const

Here is the call graph for this function:


VME::TDCV1x90::SetPairModeResolution
VME::TDCV1x90Opcodes::SET_PAIR_RES
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

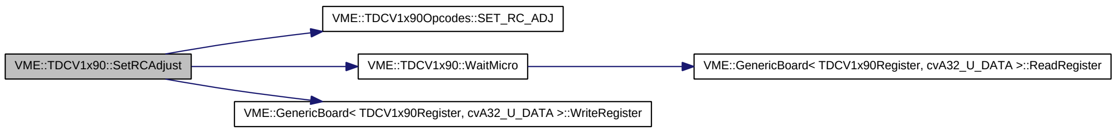
7.28.4.47 void VME::TDCV1x90::SetPoI (uint16_t *word1*, uint16_t *word2*) const

Here is the call graph for this function:


VME::TDCV1x90::SetPoI
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::TDCV1x90Opcodes::WRITE_EN_PATTERN
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

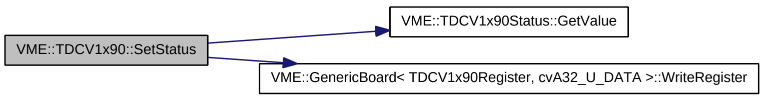
7.28.4.48 void VME::TDCV1x90::SetRCAdjust (int *tdc*, uint16_t *value*) const

Here is the call graph for this function:


VME::TDCV1x90::SetRCAdjust
VME::TDCV1x90Opcodes::SET_RC_ADJ
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

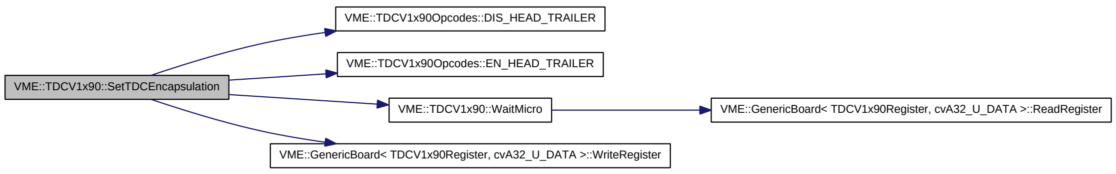
7.28.4.49 void VME::TDCV1x90::SetStatus (const TDCV1x90Status & *status*) const

Here is the call graph for this function:


VME::TDCV1x90::SetStatus
VME::TDCV1x90Status::GetValue
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

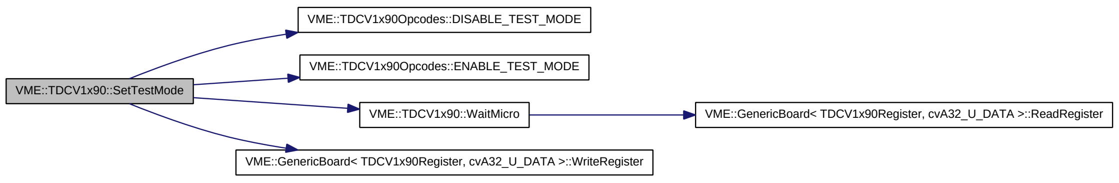
7.28.4.50 void VME::TDCV1x90::SetTDCEncapsulation (bool *mode*) const

Here is the call graph for this function:


VME::TDCV1x90::SetTDCEncapsulation
VME::TDCV1x90Opcodes::DIS_HEAD_TRAILER
VME::TDCV1x90Opcodes::EN_HEAD_TRAILER
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

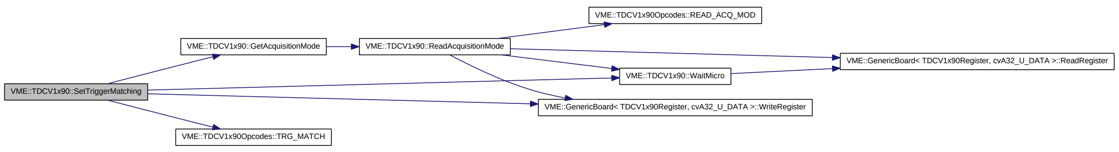
7.28.4.51 void VME::TDCV1x90::SetTestMode (bool *en* = `true`) const

Here is the call graph for this function:


VME::TDCV1x90::SetTestMode
VME::TDCV1x90Opcodes::DISABLE_TEST_MODE
VME::TDCV1x90Opcodes::ENABLE_TEST_MODE
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

7.28.4.52 void VME::TDCV1x90::SetTriggerMatching ()

Here is the call graph for this function:


VME::TDCV1x90::SetTriggerMatching
VME::TDCV1x90::GetAcquisitionMode
VME::TDCV1x90::ReadAcquisitionMode
VME::TDCV1x90Opcodes::READ_ACQ_MOD
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister
VME::TDCV1x90Opcodes::TRG_MATCH

7.28.4.53 void VME::TDCV1x90::SetVerboseLevel (unsigned short *verb* = `1`) `[inline]`

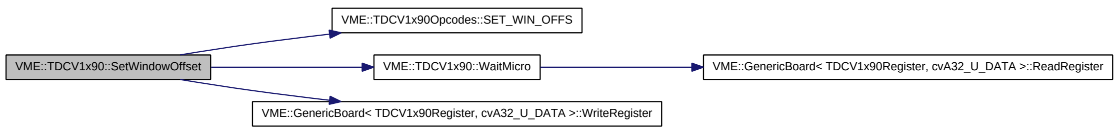
7.28.4.54 void VME::TDCV1x90::SetWindowOffset (const int16_t & *offs*) const

Set the offset of the match window with respect to the trigger itself, i.e. the time difference (expressed in clock cycles) between the start of the match window and the trigger time

Parameters:

← ***Window*** offset, in units of clock cycles

Here is the call graph for this function:


VME::TDCV1x90::SetWindowOffset
VME::TDCV1x90Opcodes::SET_WIN_OFFS
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

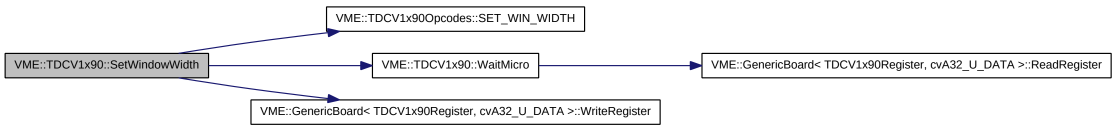
7.28.4.55 void VME::TDCV1x90::SetWindowWidth (const uint16_t & *width*)

Set the width of the match window (in number of clock cycles)

Parameters:

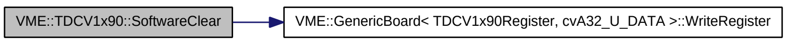
← ***Window*** width, in units of clock cycles

Here is the call graph for this function:


VME::TDCV1x90::SetWindowWidth
VME::TDCV1x90Opcodes::SET_WIN_WIDTH
VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

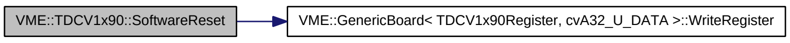
7.28.4.56 bool VME::TDCV1x90::SoftwareClear () const

Here is the call graph for this function:


VME::TDCV1x90::SoftwareClear
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

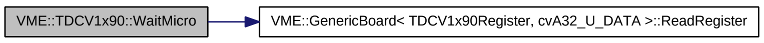
7.28.4.57 bool VME::TDCV1x90::SoftwareReset () const

Here is the call graph for this function:


VME::TDCV1x90::SoftwareReset
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister

7.28.4.58 bool VME::TDCV1x90::WaitMicro (const micro_handshake & *mode*) const `[private]`

Here is the call graph for this function:


VME::TDCV1x90::WaitMicro
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::ReadRegister

7.28.5 Field Documentation

7.28.5.1 AcquisitionMode VME::TDCV1x90::fAcquisitionMode `[private]`

7.28.5.2 uint32_t* VME::TDCV1x90::fBuffer `[private]`

7.28.5.3 DetectionMode VME::TDCV1x90::fDetectionMode `[private]`

7.28.5.4 bool VME::TDCV1x90::fErrorMarks `[private]`

7.28.5.5 unsigned short VME::TDCV1x90::fVerb `[private]`

7.28.5.6 uint16_t VME::TDCV1x90::fWindowWidth `[private]`

7.28.5.7 bool VME::TDCV1x90::gEnd `[private]`

7.28.5.8 uint32_t VME::TDCV1x90::nchannels `[private]`

7.28.5.9 std::string VME::TDCV1x90::pair_lead_res[8] `[private]`

7.28.5.10 std::string VME::TDCV1x90::pair_width_res[16] `[private]`

The documentation for this class was generated from the following files:

- include/VME_TDCV1x90.h
- src/VME_TDCV1x90.cpp

7.29 VME::TDCV1x90Control Class Reference

TDC control register.

`#include <VME_TDCV1x90.h>`

Public Member Functions

- TDCV1x90Control (const uint16_t &word)
- virtual ∼TDCV1x90Control ()
- void Dump () const
- uint16_t GetValue () const
- bool GetBusError () const
- void SetBusError (bool sw)
- bool GetTermination () const
- void SetTermination (bool sw)
- bool GetSWTermination () const
- void SetSWTermination (bool sw)
- bool GetEmptyEvent () const
- void SetEmptyEvent (bool sw)
- bool GetAlign64 () const
- void SetAlign64 (bool sw)
- bool GetCompensation () const
- void SetCompensation (bool sw)
- bool GetTestFIFO () const
- void SetTestFIFO (bool sw)
- bool GetSRAMCompensation () const
- void SetSRAMCompensation (bool sw)
- bool GetEventFIFO () const
- void SetEventFIFO (bool sw)
- bool GetETTT () const
- void SetETTT (bool sw)
- bool GetMEBAccess () const
- void SetMEBAccess (bool sw)

Private Attributes

- uint16_t fWord

7.29.1 Detailed Description

TDC control register.

Author:

Laurent Forthomme <`laurent.forthomme@cern.ch`>

Date:

Jun 2015

7.29.2 Constructor & Destructor Documentation

7.29.2.1 VME::TDCV1x90Control::TDCV1x90Control (const uint16_t & *word*) `[inline]`

7.29.2.2 virtual VME::TDCV1x90Control::∼TDCV1x90Control () `[inline, virtual]`

7.29.3 Member Function Documentation

7.29.3.1 void VME::TDCV1x90Control::Dump () const `[inline]`

Here is the call graph for this function:

7.29.3.2 bool VME::TDCV1x90Control::GetAlign64 () const `[inline]`

7.29.3.3 bool VME::TDCV1x90Control::GetBusError () const `[inline]`

7.29.3.4 bool VME::TDCV1x90Control::GetCompensation () const `[inline]`

7.29.3.5 bool VME::TDCV1x90Control::GetEmptyEvent () const `[inline]`

7.29.3.6 bool VME::TDCV1x90Control::GetETTT () const `[inline]`

7.29.3.7 bool VME::TDCV1x90Control::GetEventFIFO () const `[inline]`

7.29.3.8 bool VME::TDCV1x90Control::GetMEBAccess () const `[inline]`

7.29.3.9 bool VME::TDCV1x90Control::GetSRAMCompensation () const `[inline]`

7.29.3.10 bool VME::TDCV1x90Control::GetSWTermination () const `[inline]`

7.29.3.11 bool VME::TDCV1x90Control::GetTermination () const `[inline]`

7.29.3.12 bool VME::TDCV1x90Control::GetTestFIFO () const `[inline]`

7.29.3.13 uint16_t VME::TDCV1x90Control::GetValue () const `[inline]`

7.29.3.14 void VME::TDCV1x90Control::SetAlign64 (bool *sw*) `[inline]`

Here is the call graph for this function:

7.29.3.15 void VME::TDCV1x90Control::SetBusError (bool *sw*) `[inline]`

Here is the call graph for this function:

7.29.3.16 void VME::TDCV1x90Control::SetCompensation (bool *sw*) `[inline]`

Here is the call graph for this function:

7.29.3.17 void VME::TDCV1x90Control::SetEmptyEvent (bool *sw*) `[inline]`

Here is the call graph for this function:

7.29.3.18 void VME::TDCV1x90Control::SetETTT (bool *sw*) `[inline]`

Here is the call graph for this function:

7.29.3.19 void VME::TDCV1x90Control::SetEventFIFO (bool *sw*) `[inline]`

Here is the call graph for this function:

7.29.3.20 void VME::TDCV1x90Control::SetMEBAccess (bool *sw*) `[inline]`

Here is the call graph for this function:

7.29.3.21 void VME::TDCV1x90Control::SetSRAMCompensation (bool *sw*) `[inline]`

Here is the call graph for this function:

7.29.3.22 void VME::TDCV1x90Control::SetSWTermination (bool *sw*) `[inline]`

Here is the call graph for this function:

7.29.3.23 void VME::TDCV1x90Control::SetTermination (bool *sw*) `[inline]`

Here is the call graph for this function:

7.29.3.24 void VME::TDCV1x90Control::SetTestFIFO (bool *sw*) `[inline]`

Here is the call graph for this function:

7.29.4 Field Documentation

7.29.4.1 uint16_t VME::TDCV1x90Control::fWord `[private]`

The documentation for this class was generated from the following file:

- include/VME_TDCV1x90.h

7.30 VME::TDCV1x90Status Class Reference

TDC status register.

`#include <VME_TDCV1x90.h>`

Public Types

- enum TDCResolution { R_800ps = 0x0, R_200ps = 0x1, R_100ps = 0x2, R_-25ps = 0x3 }

Public Member Functions

- TDCV1x90Status (const uint16_t &word)
- virtual ~TDCV1x90Status ()
- void Dump () const
- uint16_t GetValue () const
- bool DataReady () const
- bool AlmostFull () const
- bool Full () const
- bool TriggerMatching () const
- bool HeadersEnabled () const
- bool TerminationOn () const
- bool Error (const unsigned int &id) const
- bool Error () const
- bool BusError () const
- bool Purged () const
- TDCResolution Resolution () const
- bool PairMode () const
- bool TriggerLost () const

Private Attributes

- uint16_t fWord

7.30.1 Detailed Description

TDC status register.

Author:

Laurent Forthomme <`laurent.forthomme@cern.ch`>

Date:

Jun 2015

7.30.2 Member Enumeration Documentation

7.30.2.1 enum VME::TDCV1x90Status::TDCResolution

Enumerator:

R_800ps

R_200ps

R_100ps

R_25ps

7.30.3 Constructor & Destructor Documentation

7.30.3.1 VME::TDCV1x90Status::TDCV1x90Status (const uint16_t & *word*) `[inline]`

7.30.3.2 virtual VME::TDCV1x90Status::∼TDCV1x90Status () `[inline, virtual]`

7.30.4 Member Function Documentation

7.30.4.1 bool VME::TDCV1x90Status::AlmostFull () const `[inline]`

7.30.4.2 bool VME::TDCV1x90Status::BusError () const `[inline]`

7.30.4.3 bool VME::TDCV1x90Status::DataReady () const `[inline]`

7.30.4.4 void VME::TDCV1x90Status::Dump () const `[inline]`

Here is the call graph for this function:

7.30.4.5 bool VME::TDCV1x90Status::Error () const `[inline]`

Here is the call graph for this function:

7.30.4.6 bool VME::TDCV1x90Status::Error (const unsigned int & *id*) const `[inline]`

7.30.4.7 bool VME::TDCV1x90Status::Full () const `[inline]`

7.30.4.8 uint16_t VME::TDCV1x90Status::GetValue () const `[inline]`

7.30.4.9 bool VME::TDCV1x90Status::HeadersEnabled () const `[inline]`

7.30.4.10 bool VME::TDCV1x90Status::PairMode () const `[inline]`

7.30.4.11 bool VME::TDCV1x90Status::Purged () const `[inline]`

7.30.4.12 TDCResolution VME::TDCV1x90Status::Resolution () const `[inline]`

7.30.4.13 bool VME::TDCV1x90Status::TerminationOn () const `[inline]`

7.30.4.14 bool VME::TDCV1x90Status::TriggerLost () const `[inline]`

7.30.4.15 bool VME::TDCV1x90Status::TriggerMatching () const `[inline]`

7.30.5 Field Documentation

7.30.5.1 uint16_t VME::TDCV1x90Status::fWord `[private]`

The documentation for this class was generated from the following file:

- include/VME_TDCV1x90.h

7.31 VME::trailead_t Struct Reference

```
#include <VME_TDCV1x90.h>
```

Data Fields

- uint32_t event_count
- int total_hits [16]
- std::multimap< int32_t, int32_t > leading
- std::multimap< int32_t, int32_t > trailing
- uint32_t ettt

7.31.1 Field Documentation

7.31.1.1 uint32_t VME::trailead_t::ettt

7.31.1.2 uint32_t VME::trailead_t::event_count

7.31.1.3 std::multimap<int32_t,int32_t> VME::trailead_t::leading

7.31.1.4 int VME::trailead_t::total_hits[16]

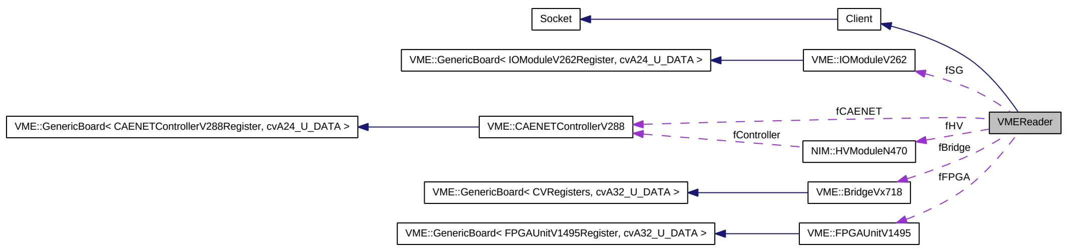
7.31.1.5 std::multimap<int32_t,int32_t> VME::trailead_t::trailing

The documentation for this struct was generated from the following file:

- include/VME_TDCV1x90.h

7.32 VMEReader Class Reference

`#include <VMEReader.h>`Inheritance diagram for VMEReader:Collaboration diagram for VMEReader:


Socket
Client
VME::GenericBoard< IOModuleV262Register, cvA24_U_DATA >
VME::IOModuleV262
fSG
VME::GenericBoard< CAENETControllerV288Register, cvA24_U_DATA >
VME::CAENETControllerV288
fCAENET
fController
fHV
VMEReader
NIM::HVModuleN470
fBridge
fFPGA
VME::GenericBoard< CVRegisters, cvA32_U_DATA >
VME::BridgeVx718
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >
VME::FPGAUnitV1495

Public Member Functions

- VMEReader (const char ∗device, VME::BridgeType type, bool on_socket=true)
- virtual ∼VMEReader ()
- void ReadXML (const char ∗filename)

  Load an XML configuration file.

- void ReadXML (std::string filename)
- void AddTDC (uint32_t address)

  Add a TDC to handle.

- VME::TDCV1x90 ∗ GetTDC (uint32_t address)

  Get a TDC on the VME bus Return a pointer to the TDC object, given its physical address on the VME bus.

- size_t GetNumTDC () const
- VME::TDCCollection GetTDCCollection ()
- void AddIOModule (uint32_t address)
- VME::IOModuleV262 ∗ GetIOModule ()
- void AddCFD (uint32_t address)

  Add a CFD to handle.

- VME::CFDV812 ∗ GetCFD (uint32_t address)

  Get a CFD on the VME bus Return a pointer to the CFD object, given its physical address on the VME bus.

- size_t GetNumCFD () const
- VME::CFDCollection GetCFDCollection ()
- void AddFPGAUnit (uint32_t address)

  Add a multi-purposes FPGA board (CAEN V1495) to the crate controller.

- VME::FPGAUnitV1495 ∗ GetFPGAUnit ()

Return the pointer to the FPGA board connected to this controller (if any ; 0 otherwise).

- unsigned int GetRunNumber ()

  Ask the socket master a run number.

- void StartPulser (double period, double width, unsigned int num_pulses=0)

  Start the bridge's pulse generator [faulty].

- void StopPulser ()

  Stop the bridge's pulse generator [faulty].

- void SendPulse (unsigned short output=0) const

  Send a single pulse to the output register/plug connected to TDC boards.

- void SendClear () const

  Send a clear signal to both the TDC boards.

- void AddHVModule (uint32_t vme_address, uint16_t nim_address)

  Add a high voltage module (controlled by a VME-CAENET controller) to the DAQ.

- NIM::HVModuleN470 ∗ GetHVModule ()

  Retrieve the NIM high voltage module.

- void SetOutputFile (uint32_t tdc_address, std::string filename)

  Set the path to the output file where the DAQ is to write.

- std::string GetOutputFile (uint32_t tdc_address)

  Return the path to the output file the DAQ is currently writing to.

- bool UseSocket () const
- void Abort ()

  Abort data collection for all modules on the bus handled by the bridge.

Private Types

- typedef std::map< uint32_t, std::string > OutputFiles

Private Attributes

- VME::BridgeVx718 ∗ fBridge

  The VME bridge object to handle.

- VME::TDCCollection fTDCCollection

A set of pointers to TDC objects indexed by their physical VME address.

- VME::CFDCollection fCFDCollection

  A set of pointers to CFD objects indexed by their physical VME address.

- VME::IOModuleV262 ∗ fSG

  Pointer to the VME input/output module object.

- VME::FPGAUnitV1495 ∗ fFPGA

  Pointer to the VME general purpose FPGA unit object.

- VME::CAENETControllerV288 ∗ fCAENET

  Pointer to the VME CAENET controller.

- NIM::HVModuleN470 ∗ fHV

  Pointer to the NIM high voltage module (passing through the CAENET controller).

- bool fOnSocket

  Are we dealing with socket message passing?

- bool fIsPulserStarted

  Is the bridge's pulser already started?

- OutputFiles fOutputFiles

7.32.1 Detailed Description

VME reader object to fetch events on a HPTDC board

Author:

Laurent Forthomme <laurent.forthomme@cern.ch>

Date:

4 May 2015

7.32.2 Member Typedef Documentation

7.32.2.1 typedef std::map<uint32_t, std::string> VMEReader::OutputFiles `[private]`

7.32.3 Constructor & Destructor Documentation

7.32.3.1 VMEReader::VMEReader (const char ∗ *device*, VME::BridgeType *type*, bool *on_socket* = `true`)

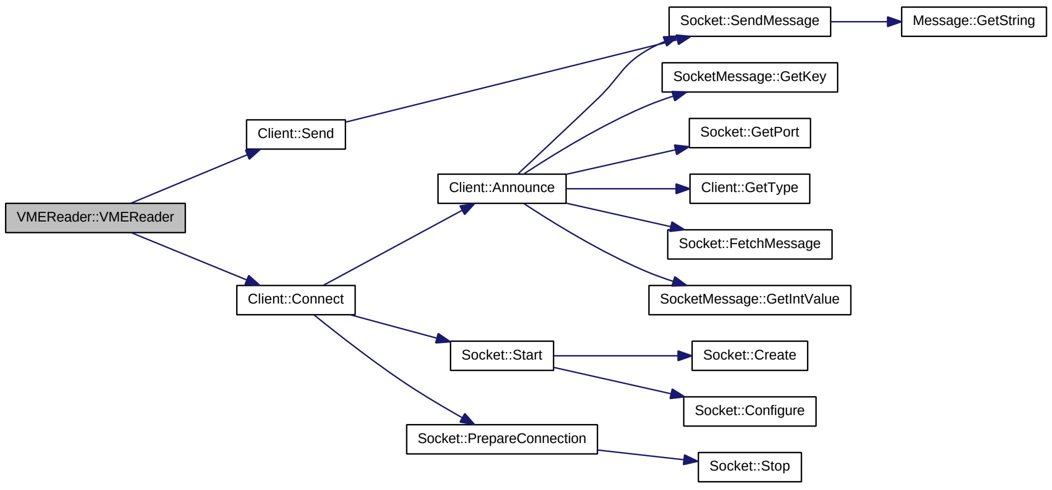
Parameters:

← ***device*** Path to the device (/dev/xxx)

← ***type*** Bridge model

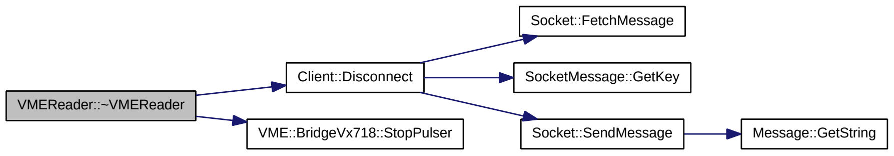
← ***on_socket*** Are we trying to connect through the socket?

Here is the call graph for this function:



7.32.3.2 VMEReader::∼VMEReader () `[virtual]`

Here is the call graph for this function:

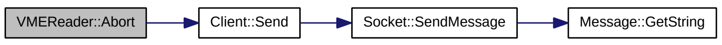


7.32.4 Member Function Documentation

7.32.4.1 void VMEReader::Abort ()

Abort data collection for all modules on the bus handled by the bridge.

Here is the call graph for this function:



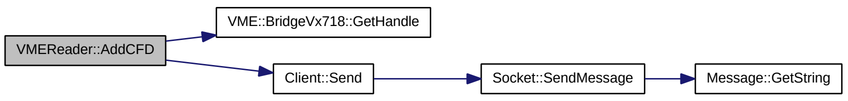
7.32.4.2 void VMEReader::AddCFD (uint32_t *address*)

Add a CFD to handle.

Parameters:

← ***address*** 32-bit address of the CFD module on the VME bus Create a new CFD handler for the VME bus

Here is the call graph for this function:



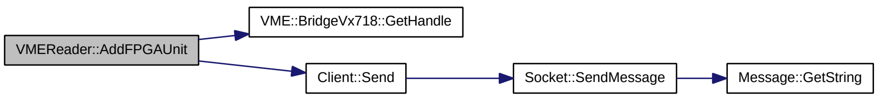
7.32.4.3 void VMEReader::AddFPGAUnit (uint32_t *address*)

Add a multi-purposes FPGA board (CAEN V1495) to the crate controller.

Parameters:

← ***address*** 32-bit address of the TDC module on the VME bus

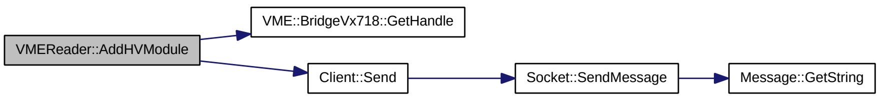
Here is the call graph for this function:



7.32.4.4 void VMEReader::AddHVModule (uint32_t *vme_address*, uint16_t *nim_address*)

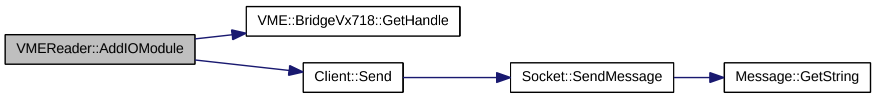
Add a high voltage module (controlled by a VME-CAENET controller) to the DAQ.

Here is the call graph for this function:



7.32.4.5 void VMEReader::AddIOModule (uint32_t *address*)

Here is the call graph for this function:



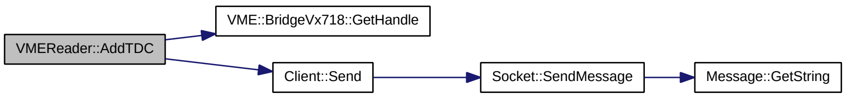
7.32.4.6 void VMEReader::AddTDC (uint32_t *address*)

Add a TDC to handle.

Parameters:

← ***address*** 32-bit address of the TDC module on the VME bus Create a new TDC handler for the VME bus

Here is the call graph for this function:



7.32.4.7 VME::CFDV812∗ VMEReader::GetCFD (uint32_t *address*) `[inline]`

Get a CFD on the VME bus Return a pointer to the CFD object, given its physical address on the VME bus.

7.32.4.8 VME::CFDCollection VMEReader::GetCFDCollection () `[inline]`

7.32.4.9 VME::FPGAUnitV1495∗ VMEReader::GetFPGAUnit () `[inline]`

Return the pointer to the FPGA board connected to this controller (if any ; 0 otherwise).

7.32.4.10 NIM::HVModuleN470∗ VMEReader::GetHVModule () [inline]

Retrieve the NIM high voltage module.

7.32.4.11 VME::IOModuleV262∗ VMEReader::GetIOModule () [inline]

7.32.4.12 size_t VMEReader::GetNumCFD () const [inline]

7.32.4.13 size_t VMEReader::GetNumTDC () const [inline]

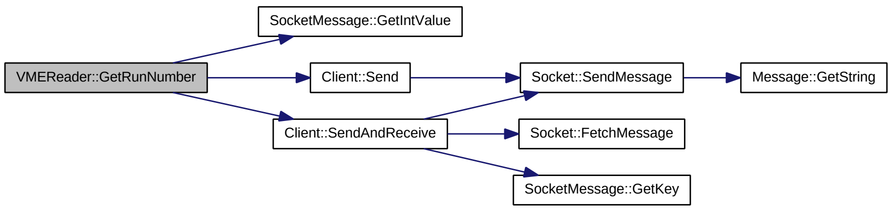
7.32.4.14 std::string VMEReader::GetOutputFile (uint32_t *tdc_address*) [inline]

Return the path to the output file the DAQ is currently writing to.

7.32.4.15 unsigned int VMEReader::GetRunNumber ()

Ask the socket master a run number.

Here is the call graph for this function:



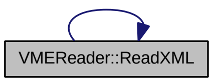
7.32.4.16 VME::TDCV1x90∗ VMEReader::GetTDC (uint32_t *address*) [inline]

Get a TDC on the VME bus Return a pointer to the TDC object, given its physical address on the VME bus.

7.32.4.17 VME::TDCCollection VMEReader::GetTDCCollection () [inline]

7.32.4.18 void VMEReader::ReadXML (std::string *filename*) [inline]

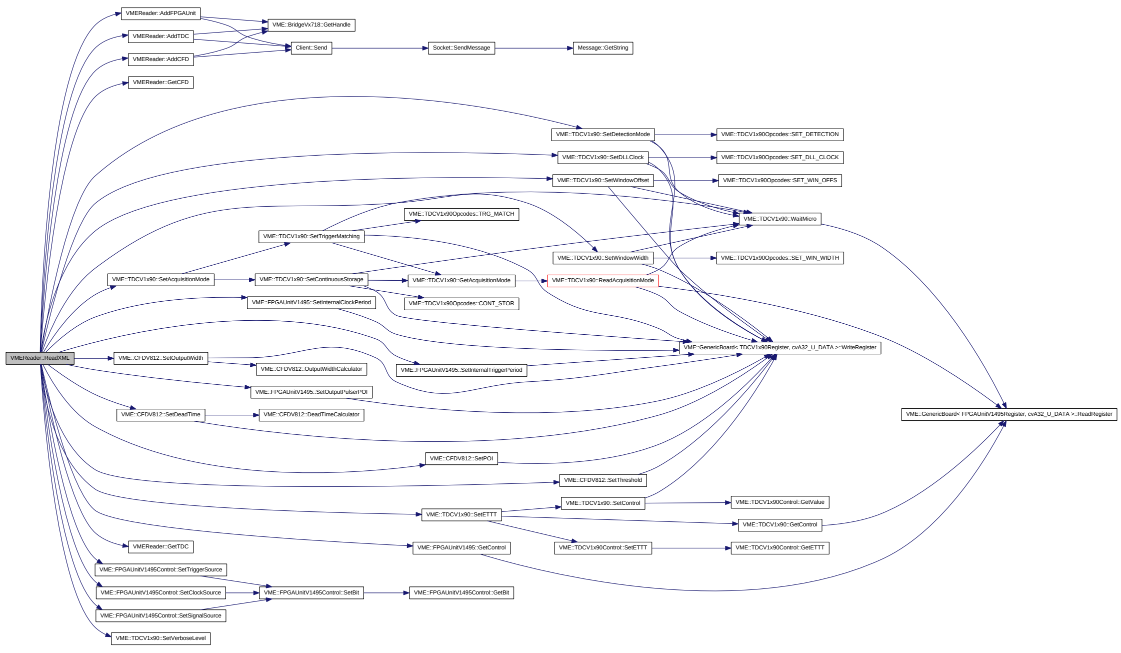
Here is the call graph for this function:



7.32.4.19 void VMEReader::ReadXML (const char * *filename*)

Load an XML configuration file.

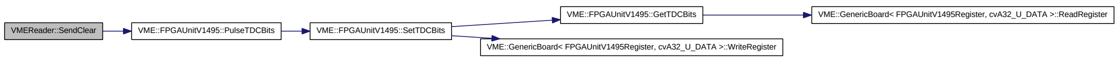
Here is the call graph for this function:


VMEReader::ReadXML
VMEReader::AddFPGAUnit
VMEReader::AddTDC
VMEReader::AddCFD
VMEReader::GetCFD
VME::BridgeVx718::GetHandle
Client::Send
Socket::SendMessage
Message::GetString
VME::TDCV1x90::SetDetectionMode
VME::TDCV1x90::SetDLLClock
VME::TDCV1x90::SetWindowOffset
VME::TDCV1x90Opcodes::SET_DETECTION
VME::TDCV1x90Opcodes::SET_DLL_CLOCK
VME::TDCV1x90Opcodes::SET_WIN_OFFS
VME::TDCV1x90Opcodes::TRG_MATCH
VME::TDCV1x90::WaitMicro
VME::TDCV1x90::SetTriggerMatching
VME::TDCV1x90::SetWindowWidth
VME::TDCV1x90Opcodes::SET_WIN_WIDTH
VME::TDCV1x90::SetAcquisitionMode
VME::TDCV1x90::SetContinuousStorage
VME::TDCV1x90::GetAcquisitionMode
VME::TDCV1x90::ReadAcquisitionMode
VME::FPGAUnitV1495::SetInternalClockPeriod
VME::TDCV1x90Opcodes::CONT_STOR
VME::GenericBoard< TDCV1x90Register, cvA32_U_DATA >::WriteRegister
VME::CFDV812::SetOutputWidth
VME::CFDV812::OutputWidthCalculator
VME::FPGAUnitV1495::SetInternalTriggerPeriod
VME::FPGAUnitV1495::SetOutputPulserPOI
VME::CFDV812::SetDeadTime
VME::CFDV812::DeadTimeCalculator
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister
VME::CFDV812::SetPOI
VME::CFDV812::SetThreshold
VME::TDCV1x90::SetControl
VME::TDCV1x90Control::GetValue
VME::TDCV1x90::SetETTT
VME::TDCV1x90::GetControl
VMEReader::GetTDC
VME::FPGAUnitV1495::GetControl
VME::TDCV1x90Control::SetETTT
VME::TDCV1x90Control::GetETTT
VME::FPGAUnitV1495Control::SetTriggerSource
VME::FPGAUnitV1495Control::SetClockSource
VME::FPGAUnitV1495Control::SetBit
VME::FPGAUnitV1495Control::GetBit
VME::FPGAUnitV1495Control::SetSignalSource
VME::TDCV1x90::SetVerboseLevel

7.32.4.20 void VMEReader::SendClear () const `[inline]`

Send a clear signal to both the TDC boards.

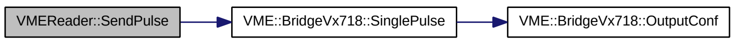
Here is the call graph for this function:


VMEReader::SendClear
VME::FPGAUnitV1495::PulseTDCBits
VME::FPGAUnitV1495::SetTDCBits
VME::FPGAUnitV1495::GetTDCBits
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::ReadRegister
VME::GenericBoard< FPGAUnitV1495Register, cvA32_U_DATA >::WriteRegister

7.32.4.21 void VMEReader::SendPulse (unsigned short *output* = 0) const `[inline]`

Send a single pulse to the output register/plug connected to TDC boards.

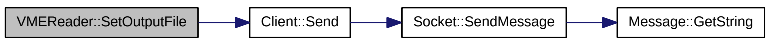
Here is the call graph for this function:



7.32.4.22 void VMEReader::SetOutputFile (uint32_t *tdc_address*, std::string *filename*)

Set the path to the output file where the DAQ is to write.

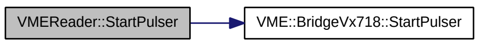
Here is the call graph for this function:



7.32.4.23 void VMEReader::StartPulser (double *period*, double *width*, unsigned int *num_pulses* = 0) `[inline]`

Start the bridge's pulse generator [faulty].

Here is the call graph for this function:



7.32.4.24 void VMEReader::StopPulser () `[inline]`

Stop the bridge's pulse generator [faulty].

Here is the call graph for this function:

7.32.4.25 bool VMEReader::UseSocket () const `[inline]`

7.32.5 Field Documentation

7.32.5.1 VME::BridgeVx718* VMEReader::fBridge `[private]`

The VME bridge object to handle.

7.32.5.2 VME::CAENETControllerV288* VMEReader::fCAENET `[private]`

Pointer to the VME CAENET controller.

7.32.5.3 VME::CFDCollection VMEReader::fCFDCollection `[private]`

A set of pointers to CFD objects indexed by their physical VME address.

7.32.5.4 VME::FPGAUnitV1495∗ VMEReader::fFPGA `[private]`

Pointer to the VME general purpose FPGA unit object.

7.32.5.5 NIM::HVModuleN470∗ VMEReader::fHV `[private]`

Pointer to the NIM high voltage module (passing through the CAENET controller).

7.32.5.6 bool VMEReader::fIsPulserStarted `[private]`

Is the bridge's pulser already started?

7.32.5.7 bool VMEReader::fOnSocket `[private]`

Are we dealing with socket message passing?

7.32.5.8 OutputFiles VMEReader::fOutputFiles `[private]`

Path to the current output files the DAQ is writing to (indexed by the TDC id)

7.32.5.9 VME::IOModuleV262∗ VMEReader::fSG `[private]`

Pointer to the VME input/output module object.

7.32.5.10 VME::TDCCollection VMEReader::fTDCCollection `[private]`

A set of pointers to TDC objects indexed by their physical VME address.

The documentation for this class was generated from the following files:

- include/VMEReader.h
- src/VMEReader.cpp

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