

Alireza Alivand

Curriculum Vitae

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Education

2017–2022 **Urmia University** B.Sc., Electrical Engineering.
Major GPA : 3.85/4

Interests

- Integrated Circuit
- Vlsi/Vlsi Interconnect
- RF - Integrated Circuit
- Silicon Photonics
- Computer Architecture
- Low-power system Design
- MPSoC
- Hardware Acceleration

Research Experience at Urmia University

Spring 2022 **Research Assistant**,
Working under supervision of Prof. A, Fathi, **ISSCC Reviewer**.
Fall 2022 **Research Assistant**,
Working under supervision of Prof. A, Fathi, **Low-power system Design**.
Fall 2022 **Research Assistant**,
Working under supervision of Prof. A, Fathi, **Vlsi Interconnect**.
Summer 2021 **Intern**,
Working under supervision of Prof.S,kashef, **ISSCC Reviewer**.
Spring 2021 **Research Assistant**,
Working under supervision of Prof. M, Mojarad, **MPSoC**.(BSc Project)

Teaching Experience

Teaching Assistant at Urmia University

Spring 2022 **Pulse and Digital Circuits**, Dr.M.Ghasemi.
Holding tutorial classes
Spring 2022 **RF Communication Circuits**, Dr.M.Mojarad.
Holding tutorial classes, training circuit design software(ADS)
Spring 2021 **Computer Architecture**, Dr.A.Fathi.(2 Groups)
Holding tutorial classes
Spring 2021 **Electromagnetic**, Dr.Mohammadpour.
Holding tutorial classes
Spring 2021 **Cmos Integrated Circuits**, Dr.M.Mojarad.(Graduate Course)
Holding tutorial classes, training circuit design software(Hspice-Pspice)
Spring 2021 **Electromagnetic**, Dr. S.Mohammadpour.
Holding tutorial classes
2021 **Analog Circuits**, Dr.A.Fathi.(2 semesters)(3 Groups)
Holding tutorial classes, designing homework, training circuit design software(Hspice, Pspice)
Spring 2021 **Filter Synthesis and Design**, Dr.M.karamirad

Fall 2021 *Holding tutorial classes*
Electronics Principles, Dr.M.Mojarad.(2 Groups)
Holding tutorial classes, training circuit design software(Hspice, Pspice,Proteus)

Spring 2022 *Lab Assistant at Urmia University*
RF Communication Circuits Lab, Dr.S.Hajizadeh.
Training circuit design software(Hspice, Pspice, Proteus)
Spring 2021 **Computer Architecture Lab**, Dr.S.Hajizadeh.
Verilog Instructor
Spring 2021 **Analog Circuits Lab**, Dr.S.Hajizadeh.
Training circuit design software(Hspice,Pspice, Proteus)

Honors and Awards

2017 Ranked within the top 2% among more than 300,000 participants in the national university entrance exam.
2017 Received national undergraduate full scholarship, from Urmia University.

Computer Skills

Programming Languages

Verilog, System Verilog, VHDL, Python, X86 and Mips assembly, Perl, Shell, Java, C/C++, P(Java, Python, C++), Parallel Programming (CUDA), CSS, HTML

Related Softwares

Matlab, Xilinx ISE, Xilinx Vivadeo, Modelsim, Arduino Studio

Design & Simulation Software

ADS, Hspice, Pspice, Proetus, Cadence Virtuoso, Microwind

Database

SQL, MySQL

Version Control

Git

Operating Systems

Windows, Mac, Ubuntu

Documentary Software

Microsft Office(Word, Excel, Powerpoint)

Platform

Arduino, FPGA

Selected Courses

University Courses

Summer 2022 **Microprocessor I**, Dr. M. Nikzad, 4/4.
Fall 2021 **Pulse and Digital Circuits Lab**, Dr. F. Abbasi, 4/4.
Spring 2020 **Pulse and Digital Circuits**, Dr. M. Bashirpour, 4/4.
Spring 2020 **RF Communication Circuits**, Dr. M. Mojarad, 4/4.
Spring 2020 **RF Communication Circuits Lab**, Dr. H. Nabizadeh, 4/4.
Spring 2020 **FPGA & ASIC Design Course**, Dr. M. Mousazadeh, 3/4.
Spring 2020 **FPGA & ASIC Design Lab Course**, Dr. O. Asiaban, 4/4.
Spring 2020 **Power Electronics**, Dr. D. Nazarpour, 4/4.
Fall 2020 **Cmos Integrated Circuits (Graduate Course)**, Dr. M. Mousazadeh, 4/4.
Fall 2020 **Computer Architecture**, Dr. A. Fathi, 4/4.
Fall 2020 **Computer Architecture Lab**, Dr. F. Foroumandi, 4/4.
Fall 2020 **Filter Synthesis and Design**, Dr. A. Fathi, 4/4.
Fall 2020 **Analog Electronics**, Dr. M. Mousazadeh, 3/4.
Fall 2020 **Analog Electronics Lab**, Dr. K. Mousavi kia, 4/4.
Fall 2020 **Filter Synthesis and Design**, Dr. A. Fathi, 4/4.
Fall 2019 **Analog Circuits**, Dr. A. Fathi, 4/4.

Fall 2019 **Electronics Principles**, Dr. M. Mojarad, 4/4.

Audited/Self-Studied Courses

Spring 2022 **Vlsi**, Dr.M.Momtazpour.
Spring 2022 **Advanced Solid-state Devices**, Dr. R. Faez.
Fall 2021 **GPU**(Self-Studied)
Fall 2021 **Microprocessor II**, Dr. E. Sanaei.
Winter 2021 **Operating System**(Self-studied)
Winter 2021 **Data Structure**(Self-Studied)
Winter 2021 **Algorithms Design**(Self-Studied)
Fall 2021 **Vlsi Interconnect**, Dr.R.sarvari.
Fall 2021 **Computer Interface Circuit**, Dr. M. Movahedian.
Fall 2021 **Low Power Design**, Dr.A. Ejlali.
Fall 2021 **Electronics Digital**, Dr. M. Aminian.
Summer 2021 **Cmos II**(Self-Studied)
Spring 2021 **MPSoC**(Self-Studied)
Winter 2021 **Network**, Mr. A. Kazemi.
Summer 2020 **Computer Architecture**, Dr. M. Goudarzi.

Projects

Course Projects at Urmia University

Fall 2020 **Layout & Simulation of Inverter in 45nm by Cadance Virtuoso(With DRC & LVS)**
Cmos Integrated Circuits Course(Graduate Course).
Spring 2020 **Receiver System Using Blocks By ADS**, RF Communication Circuits Course.
Spring 2020 **Vending Machine By Verilog**, *FPGA & ASIC Design* Course.
Fall 2019 **Design and simulation of 741 Amplifier on transistor level**, Analog Circuits Course.

Non-Course Projects

Summer 2022 **Rent a Bike Online.**
Implementation of Rent a Bike Online in Java.
Semmer 2017 **Snake Game.**
Implementation of Snake game in C.