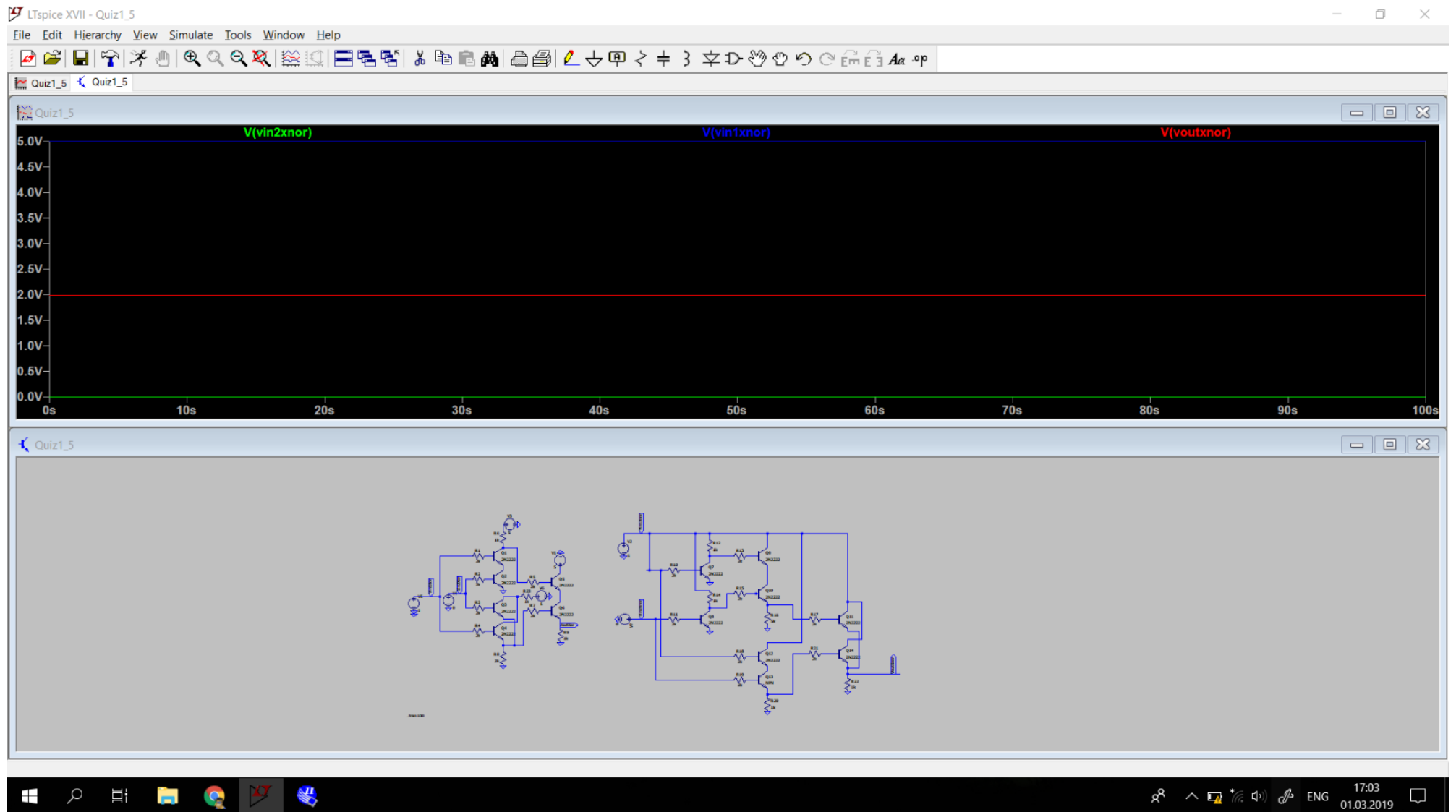
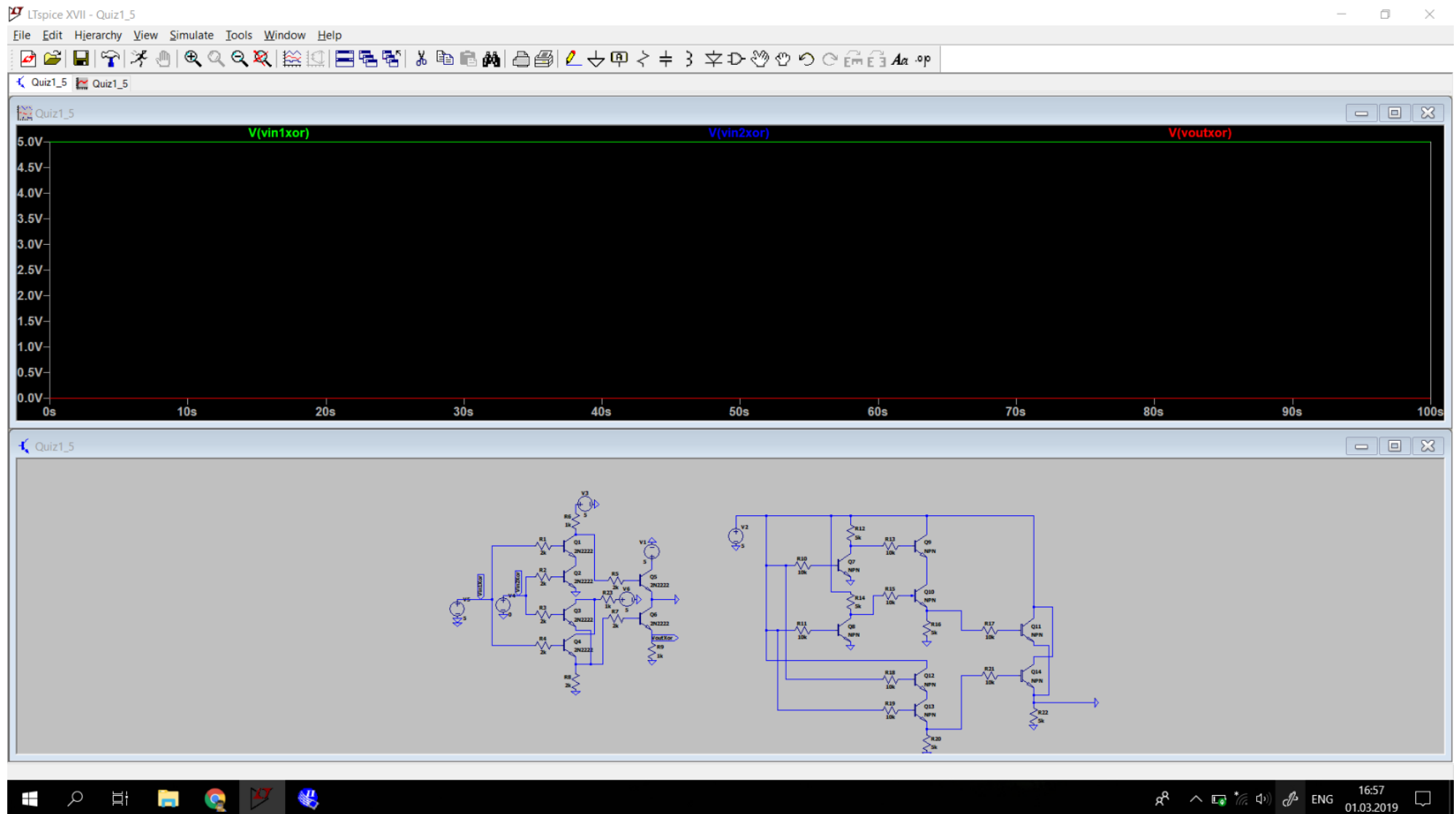


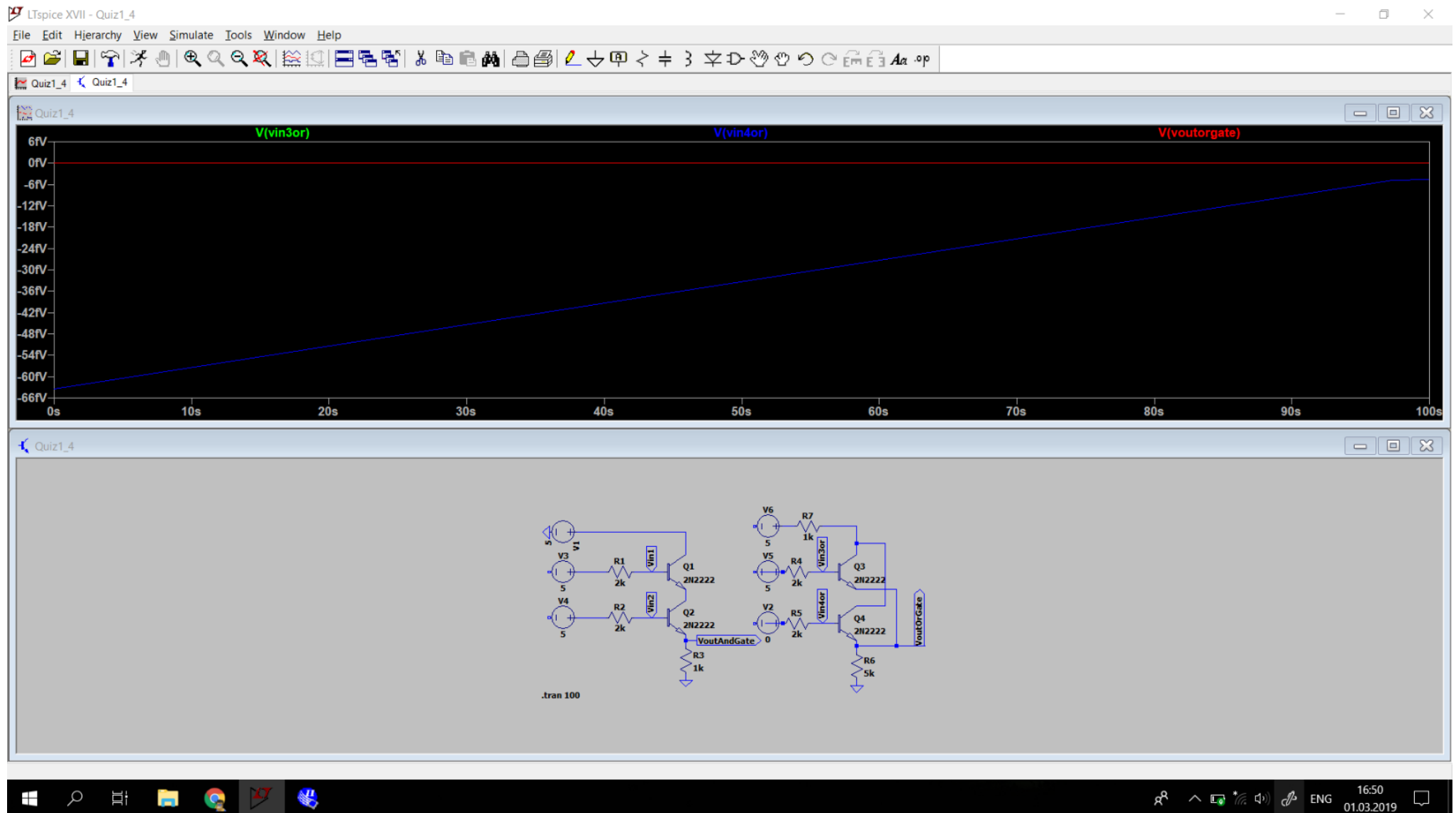
# XNOR Gate



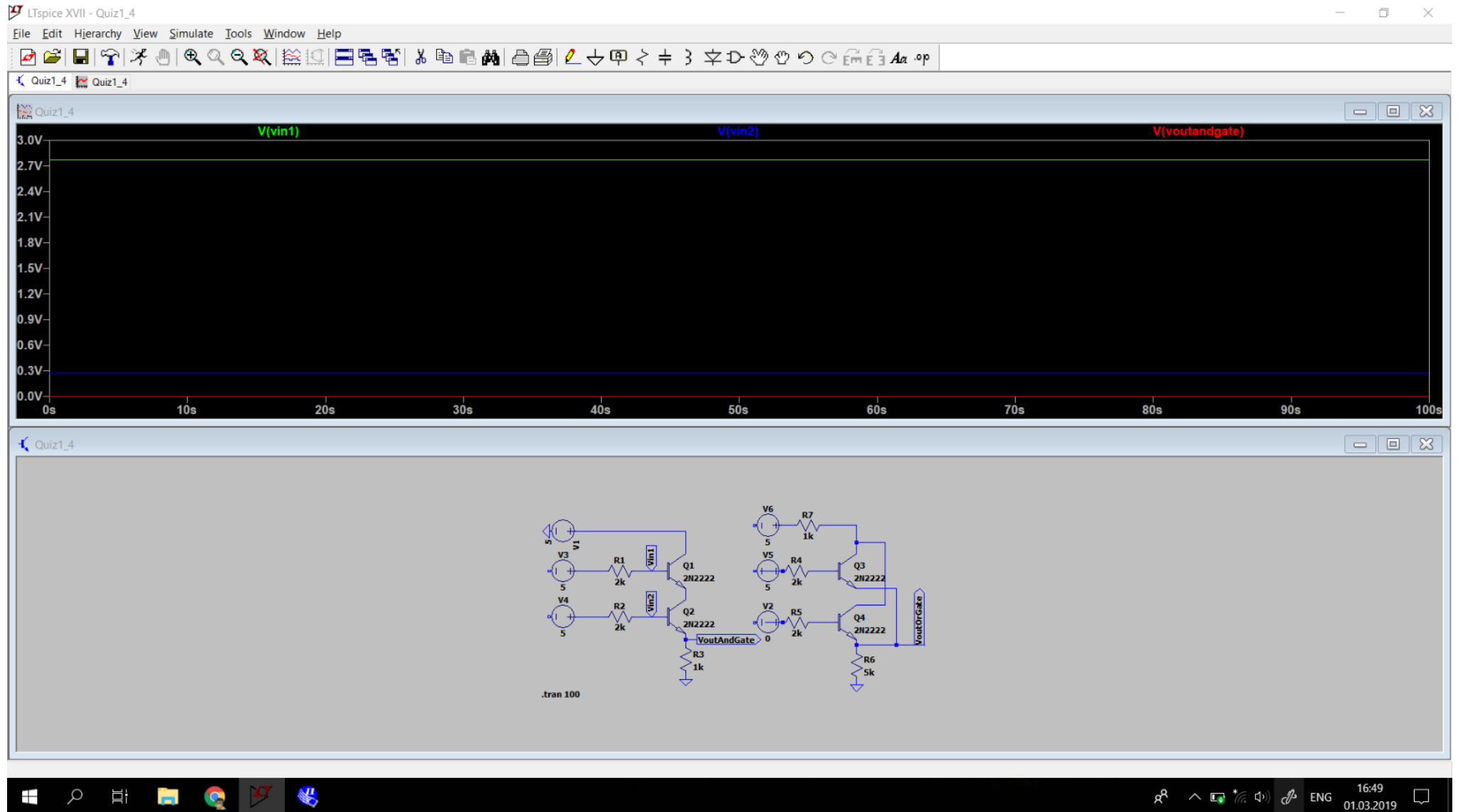
# XOR Gate



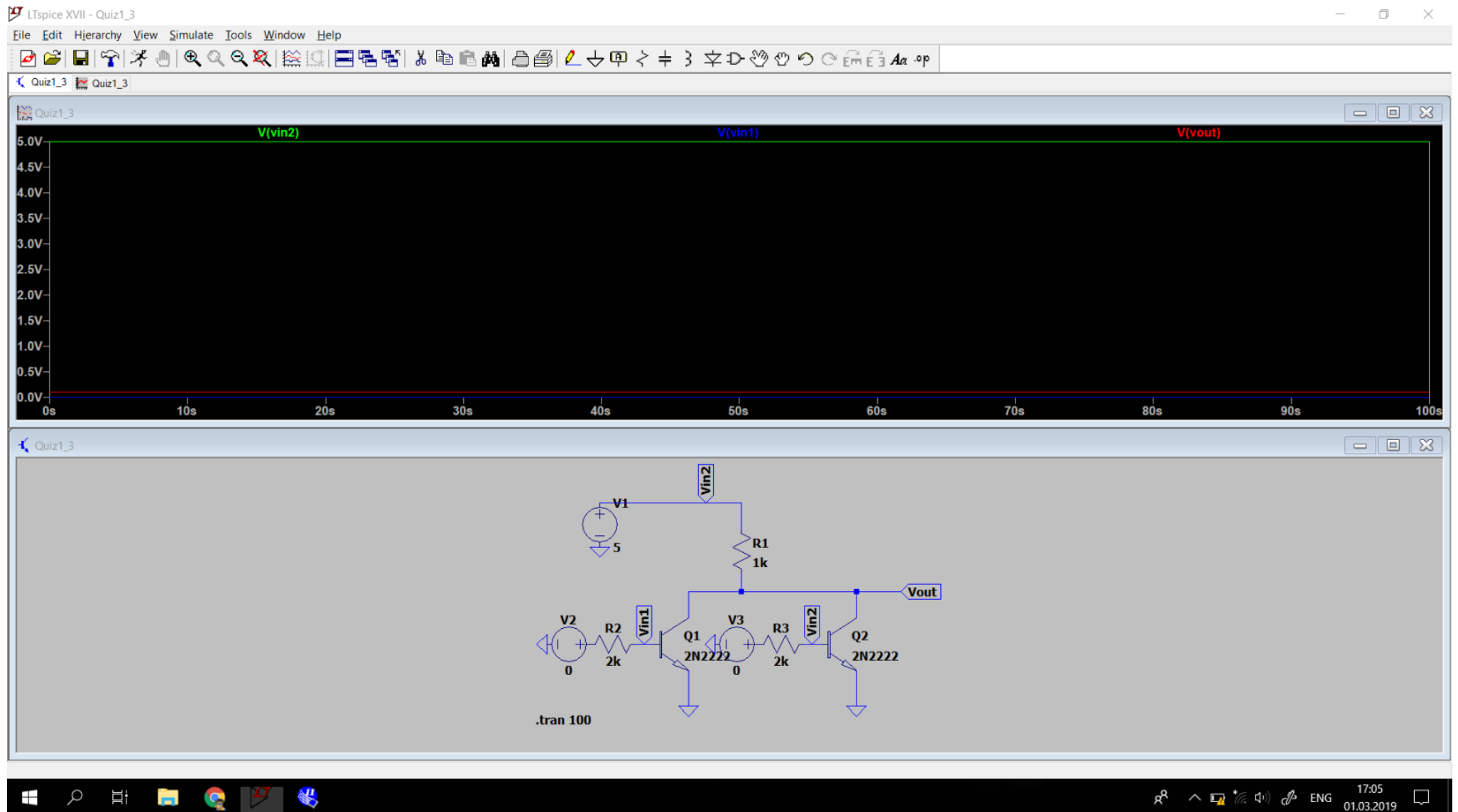
# OR Gate



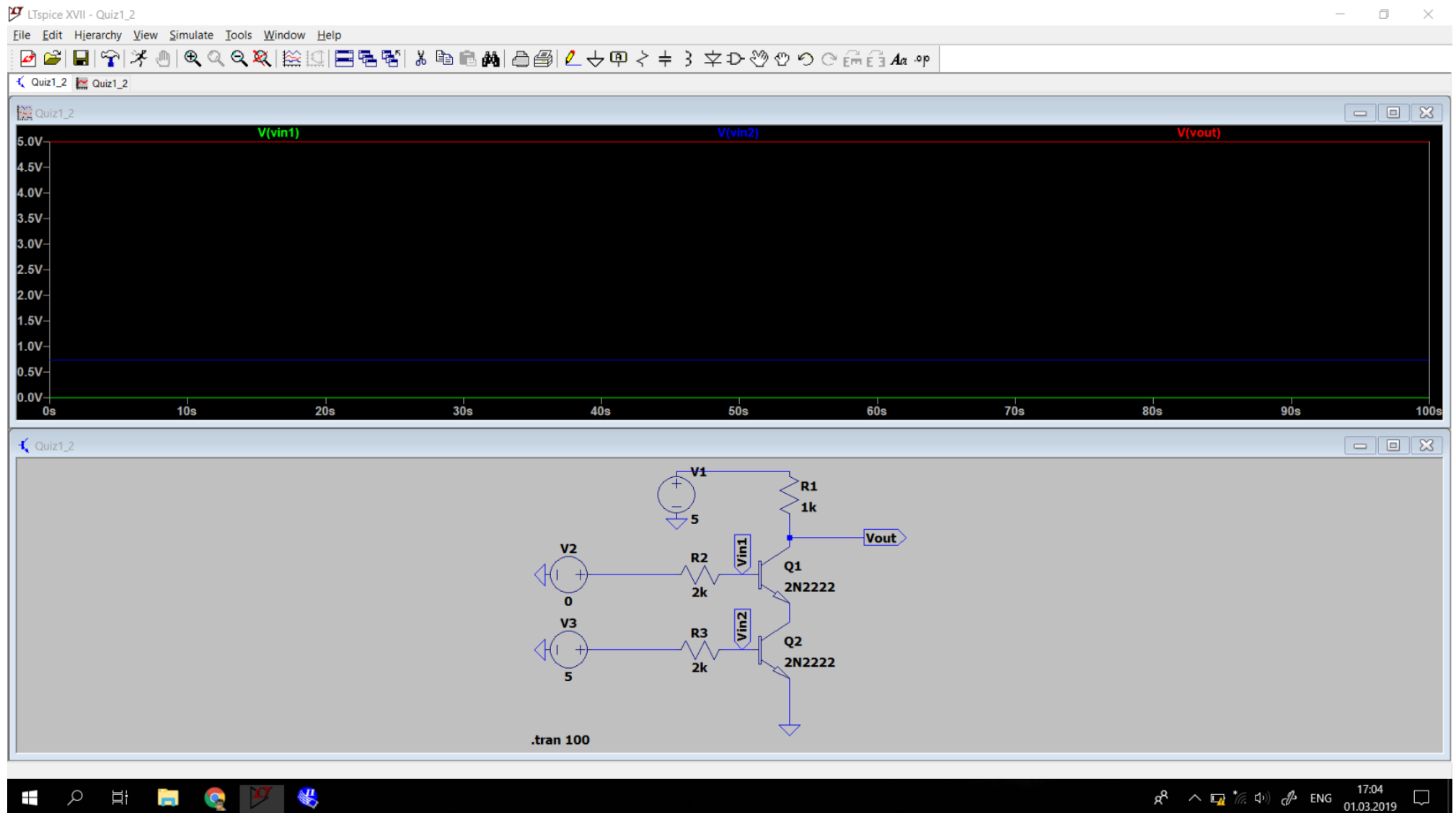
# AND Gate



# NOR Gate



# NAND Gate



# Not Gate

