601.445/601.645: Side Channel Attacks

Instructor: Matthew Green

Housekeeping:

- Midterm Weds
- This week, the weekly assignment will be the midterm
- New weekly assignment on Monday
- Programming Assignment 3 out next Weds

Current Events

Current Events

Israel passes emergency law to use mobile data for COVID-19 contact tracing

Natasha Lomas @riptari / 6:25 am EDT • March 18, 2020

Con



Current Events

How do we get smartphone location data?



Model 1: Tower data

- How do we get smartphone location data?
 - Some combination of cell tower triangulation & GPS/A-GPS (GPS with an assist from towers)
 - In U.S. we have E-911 laws that require A-GPS "pings" in selected circumstances
 - OS makers (e.g., Apple, Google) collect WiFi SSIDs, Bluetooth "beacon" signals

(Big privacy questions around this)

 Use Bluetooth beacons to measure proximity between phones

What is TraceTogether?

A community-driven contact tracing app to help stop the spread of COVID-19





Get notified quickly by contact tracers if you had been in close proximity with a COVID-19 case¹



Earlier notification means better protection for those around us



Everyone can play a part to combat the spread of COVID-19

Download the app, enable Bluetooth®, and protect your loved ones and yourself.

TraceTogether, Safer Together.

Jointly developed by:

In support of:









For more details, visit tracetogether.gov.sg

PraceTagether does not track your actual location, instead, we ask for location permissions to estimate your praximity to other phones, Your data will never be shared with contract tracers, unless you had close contact with a COVID-19 case.

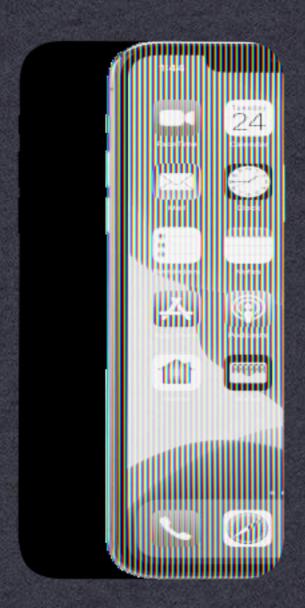
 Use Bluetooth beacons to measure proximity between phones



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 Each observed identifier is stored encrypted on the receiving phone



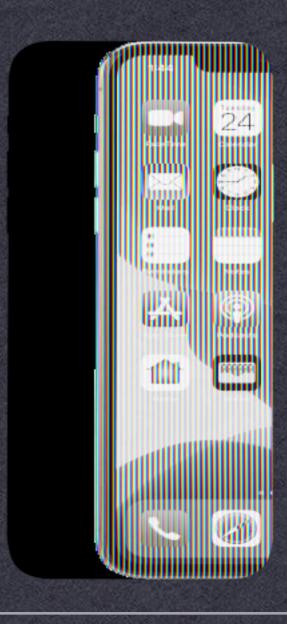




 If a contact tests positive, the identifier is broadcast to all users



D



Question

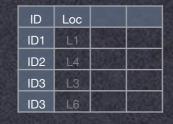
- How do we solve the privacy problems?
 - Cell provider version (hard)
 - Contact tracing version (possible)

Question

- How do we solve the privacy problems?
 - For cell providers
 this is fundamentally hard
 - Every phone has an IMSI (identifier) (Newer 5G specs add a temporary identifier for roaming only)
 - Provider basically knows roughly where you are at all times

(But there are multiple providers)

Set Intersection



AT&T

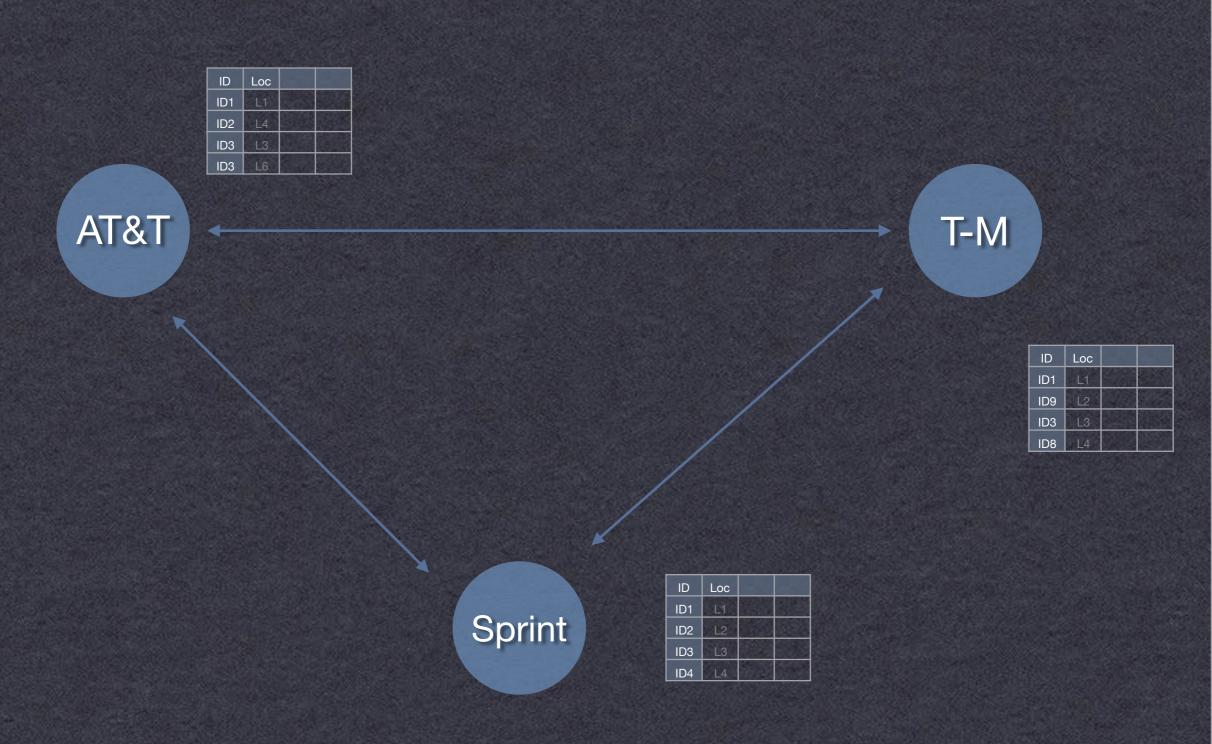
T-M

ID	Loc		-
ID1	L1	4.740	
ID9	L2		98
ID3	L3		
ID8	14	(73B)	

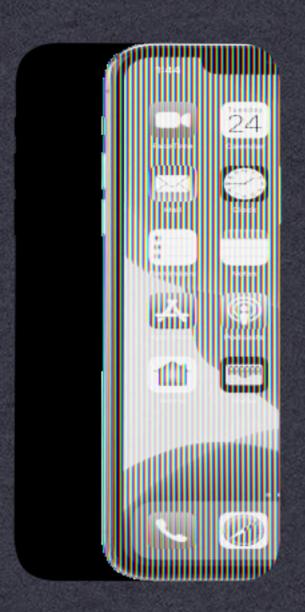
Sprint

1	ID	Loc	-	-
	ID1	L1		
į	ID2	L2		
	ID3	L3		
ŝ	ID4	L4	West.	1500

Private Set Intersection



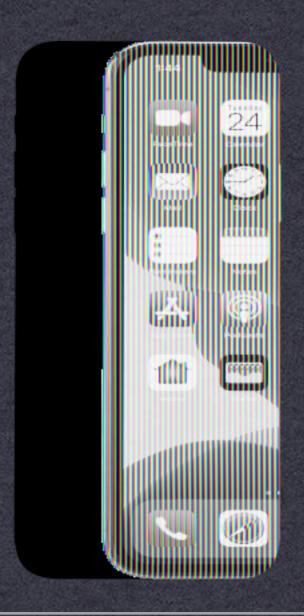
How do we deal with the tracking risk here?



D

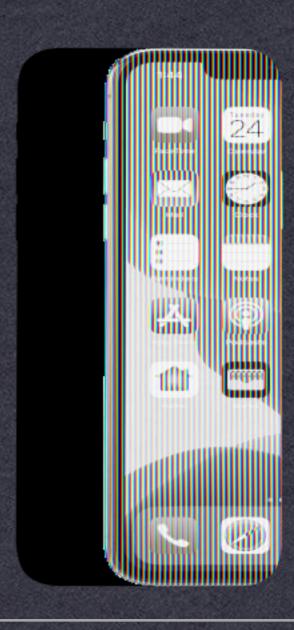


Constantly changing identifiers, changes every few minutes



Pseudonym

Generated from a single "seed", Changes every five minutes



Constantly changing identifiers, changes every few minutes

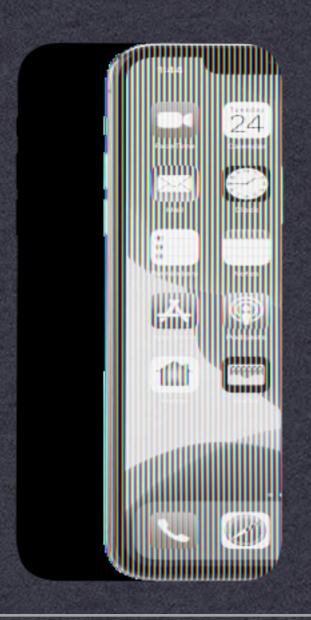


Pseudonym (pk_pseudo)

Generate a separate public/secret encryption keypair for each pseudonym



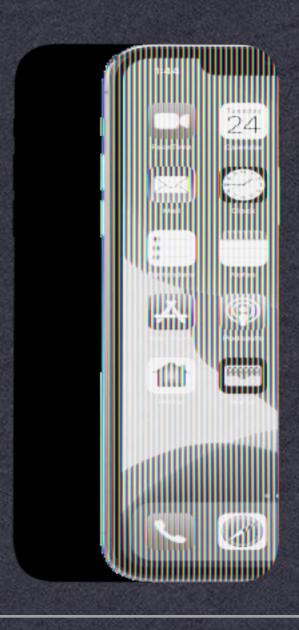
 Receiver can encrypt a location under public key, send to cloud



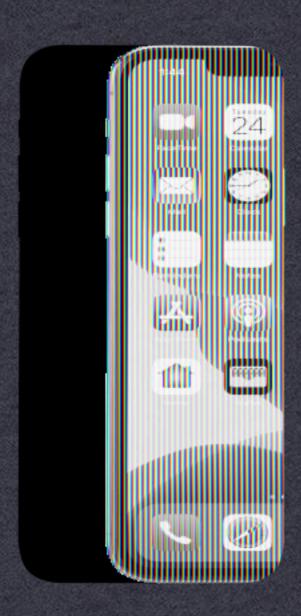
Pseudonym (pk_pseudo)

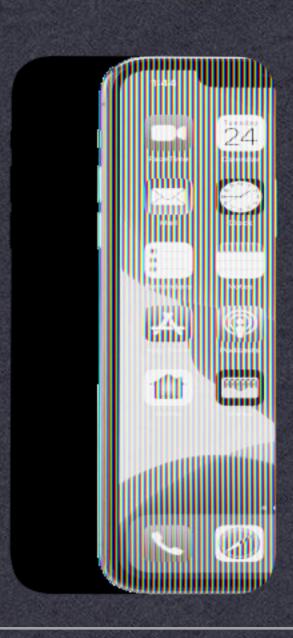
Phone can encrypt
Its location and identifier
under public key

Send ciphertext to cloud

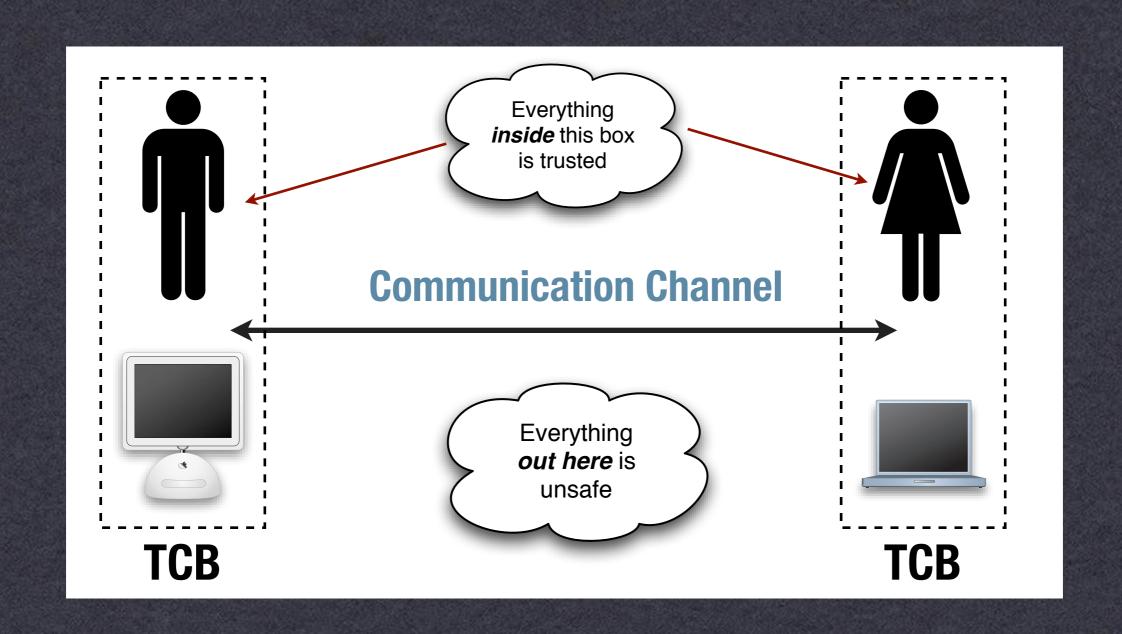


 Reveal seed, secret keys, allows discovery and decryption of all locations/IDs

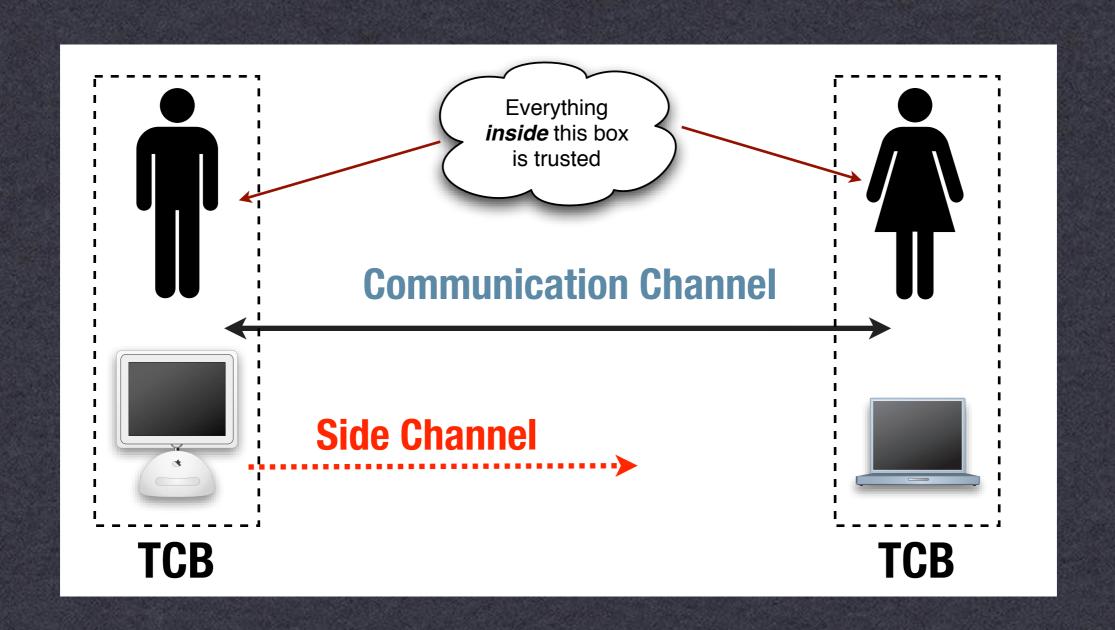




Review



Side Channels



Today

- A new class of side-channel attacks
 - Not specific to cryptography
 - But worth mentioning because they are so powerful

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- A new class of side-channel attacks
 - Not specific to cryptography
 - But worth mentioning because they are so powerful
 - Spectre, Meltdown, Foreshadow

How do we make CPUs faster?

- Basically all of the hardware gains are used up
- Today, we get major performance from parallelization and optimization: reduce memory latency, pipelining, speculative execution

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Memory Hierarchy Interface

Approach 1: Expose Hierarchy

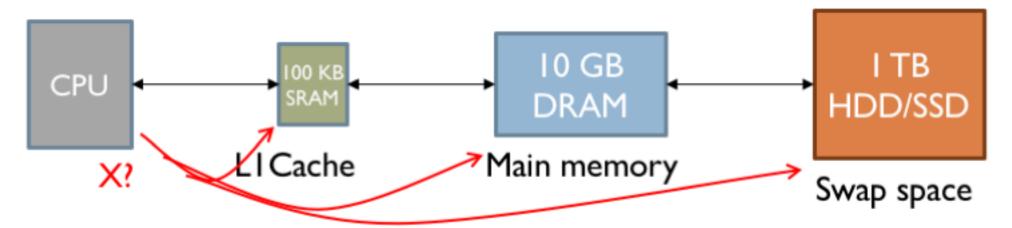
 Registers, SRAM, DRAM, Flash, Hard Disk each available as storage alternatives



Tell programmers: "Use them cleverly"

Approach 2: Hide Hierarchy

- Programming model: Single memory, single address space
- Machine transparently stores data in fast or slow memory, depending on usage patterns



Consider this program

What do we do if x is not in cache yet?

```
if (x == 1) {
    abc...
} else {
    xyz...
}
```

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slow solution: wait until x loads from DRAM

Consider this program

What do we do if x is not in memory yet?

```
if (x == 1) {
    abc...
} else {
    xyz...
}
```

fast solution: <u>speculatively execute both</u> branches (or one guess), discard invalid work

Conditional branch (Variant 1) attack

```
if (x < array1_size)
y = array2[array1[x]*512];</pre>
```

Attack scenario:

- Code runs in a trusted context
- Adversary wants to read memory and controls unsigned integer x
- Branch predictor will expect if () to be true
 (e.g. because prior calls had x < array1_size)
- array1_size and array2[] are not in cache

Memory & Cache Status

```
array1_size = 000000008
```

Memory at array1 base address:

8 bytes of data (value doesn't matter)

[... lots of memory up to array1 base+N...]

```
09 F1 98 CC 90 . . . (something secret)
```

```
array2[ 0*512]
array2[ 1*512]
array2[ 2*512]
array2[ 3*512]
array2[ 4*512]
array2[ 5*512]
array2[ 6*512]
array2[ 7*512]
array2[ 8*512]
array2[ 9*512]
array2[ 10*512]
array2[11*512]
```

Contents don't matter only care about cache status

Uncached

Cached

Conditional branch (Variant 1) attack

```
if (x < array1_size)
y = array2[array1[x]*512];</pre>
```

Attacker calls victim code with x=N (where N > 8)

- Speculative exec while waiting for array1_size
 - Predict that if() is true
 - Read address (array1 base + x) w/ out-of-bounds x
 - Read returns secret byte = 09 (fast in cache)
 - Request memory at (array2 base + 09*512)
 - Brings array2 [09*512] into the cache
 - Realize if() is false: discard speculative work
- Finish operation & return to caller

Attacker times reads from array2 [i*512]

Read for i=09 is fast (cached), revealing secret byte

Memory & Cache Status

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array1\_size = 000000008
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Memory at array1 base address: 8 bytes of data (value doesn't matter) [... lots of memory up to array1 base+N...]

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Why does this matter?

- We can learn information about data that we should not be able to access
- For example:
 - Kernel secrets
 - Secrets in the same process (keys)
 - Other applications
 - It <u>should</u> run user space code
 But attacker can cause it to leak information about data in the kernel

Intel SGX

- "Virtual trusted hardware" inside of an Intel CPU
- Basic ideas:
 - To run an SGX program ("enclave") turn in an isolation mode that is even stronger than Ring0
 - OS cannot read enclave memory, enclave cannot
 - read OS/app memory
 - Enforced by microcode

What software can we run?

- Enclave software has no access to I/O (network, keyboard, mouse). It has to interface with a real application via a strict API
- Every SGX ("enclave program") is hashed, then digitally signed under a certificate chain held by Intel

- Enclave can't access disks directly
- It has to send data out to an application via the API
- But if that program gets compromised, how does the enclave verify it's safe?

- Answer: lots of crypto
- Ever enclave gets its own secret encryption keys (generated by SGX base OS)
- Keys are generated as a combination of:
 Enclave software hash +
 System unique identifier +
 A system root secret key known to SGX

- Each enclave can "seal" (encrypt/MAC) data under this secret key, send it out to application
- Application can store the encrypted/ authenticated data for the enclave (e.g., on disk), hand back to enclave next time it boots

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- What about replay attacks?

Intel SGX attestation

- This all works great when I'm running software on my own computer
- What happens if I want to use Intel SGX in the cloud?
- How do I know my software is really being operated by SGX and not some malicious software pretending to be SGX?

Solution: attestation

- Every Intel processor has a secret signing key baked in at the factory*
- An enclave can ask the SGX OS to sign any block of data ("attestation")
- Signature includes:
 - Enclave hash (enforced by SGX OS)
 - Arbitrary data blob specified by application
 - Any other measurements, e.g., RAM hash
 - Certificate signed by Intel

So TL;DR

- As long as every single Intel signing key stays secret, a signature produced by a random SGX-enabled processor can be trusted
- Converse: if anyone steals a single SGX signing key (e.g., by attacking hardware) they can fake all these signatures, pretend to be an SGX enclave when they're not
- How do we deal with that?

Foreshadow

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Breaking the Virtual Memory Abstraction with Transient Out-of-Order Execution

Read the paper &

Cite 99

Watch a demo

Introduction

Foreshadow is a speculative execution attack on Intel processors which allows an attacker to steal sensitive information stored inside personal computers or third party clouds. Foreshadow has two versions, the original attack designed to extract data from <u>SGX</u> enclaves and a Next-Generation version which affects Virtual Machines (VMs), hypervisors (VMM), operating system (OS) kernel memory, and System Management Mode (SMM) memory.

0, 000.0...



Foreshadow AaaS @Foreshadow AaaS · Aug 15, 2018

Hi, I'm the Foreshadow AaaS bot! :-) I react to tweets I'm tagged in by providing a genuine Intel attestation that can be verified against Intel Attestation Service (IAS).

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Show this thread

Replies



Foreshadow AaaS @Foreshadow AaaS · Aug 15, 2018

Here is your attestation that "RT: Hi, I'm the Foreshadow AaaS bot! :-) I react to tweets I'm tagged in by providing a genuine " is a genuine SGX enclave github.com/TeeAaas/Foresh...

```
'QuoteSignType': 1,
'QuoteSignature': b'urItFiUHZEsHnwRMmAGwuqQ853CzIfqEg0X6lHID0sEKQib20zmtepsa'
b'HCi0EwD+TiwXkAJFQ862X8CeCcICG5c2A8e+cmkbvGL9+YB0j0mFiPNs'
b'oMZukYYonEIALXFA3QA88eeUG5jbZtljqQyUJ3JpDXzHISwnJUXISVYY'
b'lyekQ2tNUq+jb=IsaLSuHfmpw8suekFk/VPAP66Bo4e66mja2kI6btod'
b'YQx4gbzLFVZ4/ypHxipJVeYzXEc8qla4vL6kBQtJ33DxKts09V6e0V0Y'
b'M6kTFJr+vE5Vv8ITWCIrPNL5uI2LExgacBkAUvQ0dA5e5+Iu83tin3pf'
b'2CFJWEWMX8Mb/R9y+/X1T05AvF/XJ2U7HBCMatUtFvIC5wwYtCUy1Qw'
b'ESmoMRvnaAEAALMYP3trjrZ7LmZk65DGNv5n8wPUdiea9kMY5TYlWsR'
b'mD0awk2B4trxMrypcdeY7QizJ1E+QRZxodMYLM2/BgY-cJy8IeM65vxx'
b'qNhe8pAEhm7R6+X1k+L8uTZ2gobd5T8KSYdUHe4rBm0e+mioTgj5E1Yq'
b'ANDUwEun6CCvY1fQSpwdEr7090dndbexzKVx690zzvH/+hjAD/3nUbrc'
b'0efuc/WK0CSMIIWUU7P0P9E9Bi3fEltoy5YSJ1a79LmLiiuMLP+70emm'
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b'+u11ifEPSMm/K6TtBxyjG5icG0be0tBwxZjcyU163koQlk6nJNW9lcBq'
b'3QsAkbMS0d7wB21ViKPaeRugXViyDA6YpM0/WDYALn24+fBydeRQoHdw'
b'aeSpsz/JzZA=',
'QuoteXeID': 0}

'QuoteXeID': 0}

Foreshadow AaaS

@Foreshadow AaaS
```