Ahsanullah University of Science and Technology Department of Computer Science and Engineering Course No: CSE2106 Course Title: Digital Logic Design Lab Assignment 08

Group B1

- a. Design a 4-Bit CLA (Carry Look Ahead) Adder Circuit.
- b. Design a Magnitude Comparator for 4-Bit Using Logic Gates.
- c. Design a 4 to 2-Line Priority Encoder Where the Priority is-

$$I_0 > I_1 > I_2 > I_3$$