

*Ahsanullah University of Science and Technology
Department of Computer Science and Engineering
Course No: CSE2106 Course Title: Digital Logic Design Lab
Experiment 03*

Group B1

- a. Design a Combinational Circuit to Detect a Fibonacci Number of Four Bits.
- b. Design a One Bit Full Adder Using Basic Gates.
- c. Design a One Bit Full Subtractor Using Basic Gates.