

## **EXPERIMENT: 05**

### **Group: B1**

- a. Design a Switch (S) Controlled Even/Odd Parity Generator Circuit for 4-Bit Data.  
If S=0 then EVEN parity  
If S=1 then ODD Parity
- b. Design an Excess-4 to BCD Converter Circuit.
- c. Design a Switch Controlled 4-Bit Binary to Gray and Gray to Binary Code Conversion Circuit.  
If S=0 then Gray to Binary Code  
If S=1 then Binary Code to Gray