National University of Singapore Department of Mechanical Engineering

ME 4200 Microsystem Design and Applications

Assignment: Micro Actuator Design Project DUE on: JAN 05, 2024

Studants Names

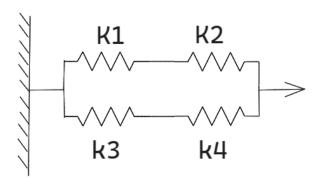
Zhang Shizhe U2302157 Li Ruisi U2302070 Li Wenyu U2302030 Sun Zhiming U2302143

Professor

Zhou Guangya

We are able to get the equal model of the system:

$$k = rac{12EI}{L^3} = rac{Etw^3}{L^3} = rac{1.65 imes 10^1 1 Pa imes 25 imes 10^{-6} imes (6 imes 10^{-6})^3}{(300 imes 10^{-6})^3} = 33N/m$$



$$k_{equ} = 2K = 66N/m$$

$$m_{eff} = m_p + rac{1}{4} m_t + rac{12}{35} m_b =
ho (V_p + rac{1}{4} V_t + rac{12}{35} V_b)$$

According to the volume, we can calculate the Vp,Vt and Vb:

$$egin{aligned} V_p = 25 imes 10^{-18} imes (300 imes 250 + 70 imes 4 imes 68 + 330 imes 25 imes 4 + 26 imes 8 imes 4) \ &= 3.1968 imes 10^{-12} m^3 \ V_t = 102 imes 8 imes 25 imes 10^{-18} imes 2 = 4.08 imes 10^{-14} m^3 \ V_b = 300 imes 6 imes 25 imes 10^{-18} imes 8 = 3.6 imes 10^{-13} m^3 \end{aligned}$$

then, we can get m_{eff}

$$m_{eff} =
ho(V_p + rac{1}{4}V_t + rac{12}{35}V_b) \ = 2330 imes (3.1968 imes 10^{-12} + rac{1}{4} imes 4.08 imes 10^{-14} + rac{12}{35} imes 3.6 imes 10^{-13}) \ = 7.76 imes 10^{-9} kg$$

(b)

As we know the equation:

$$f=rac{\omega}{2\pi}$$

$$\omega = \sqrt{rac{k_x}{m_{eff}}}$$

So, the resonant frequency of this MEMS resonator is:

$$f=rac{1}{2\pi}(rac{66}{7.76 imes10^{-9}})^{rac{1}{2}}=14.678KHz$$

From the three equations:

$$egin{aligned} F_e &= rac{\partial W_e'}{\partial x}igg|_v (1) \ W_e' &= rac{1}{2}CV^2 = rac{1}{2}(C_{fringing} + C_{plate})V^2(2) \ C_{plate} &= rac{2narepsilon t(l_0 + x)}{g} (3) \end{aligned}$$

we can get the Fe:

$$F_e = rac{narepsilon t}{g} V^2 \ F_m = kx = F_e V = \sqrt{rac{xkg}{narepsilon t}}$$

According to the question, we know the value below:

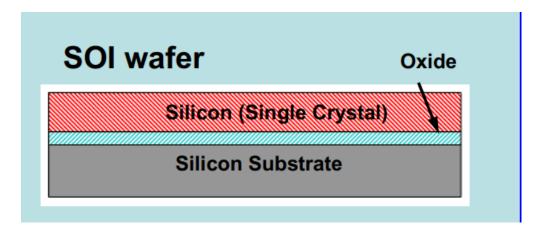
$$t=25 \mu m \ arepsilon=8.85 imes 10^{-12} \ x=1 \mu m \ g=2 \mu m \ n=34$$

from the data before, we can calculate the value of the voltage:

$$V = 132.47v$$

1. First step

- Get a wafer, cleaning/drying wafer
- Deposit SiO2(silica oxide layer)
- Deposit single crystal Si layer



2. Second step

• Spin wafer and apply a uniform thin coating of positive photoresist

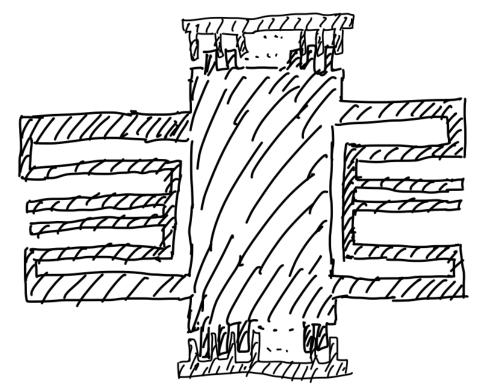


3. Soft bake

 After spin coating the photoresist, the photoresist is then baked at a temperature from 75 to 100 0C for about 10 minutes to remove solvents and stress and to promote adhesion of the photoresist layer to the wafer.

4. Exposure

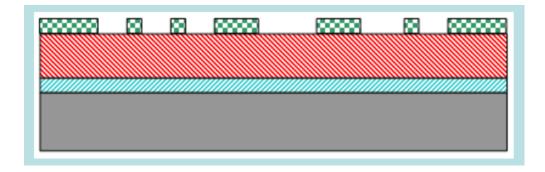
Use light field mask



• Photolithography (use ultra violet light source)

5. Development

• Photoresist undergoes chemical reactions to form masks.

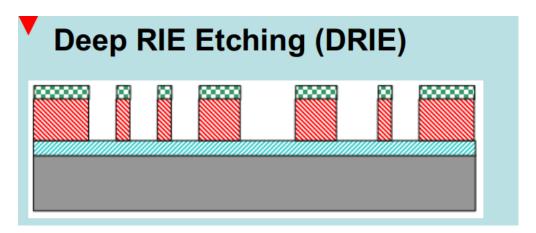


6. Post bake / hard bake

 After developing, the remaining photoresist must be baked at temperature for about 120°C for 20 minutes to remove residual developing solvents and to promote interfacial adhesion of the photoresist weakened by developer penetration along the photoresist substrate interface or by swelling of the negative photoresist

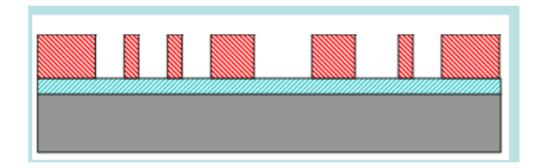
7. Etch of the thin film

• DRIE Etching



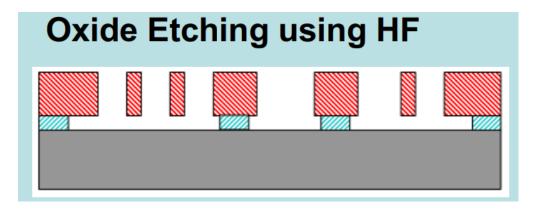
8. Resist Stripping

• remove the remaining photoresist



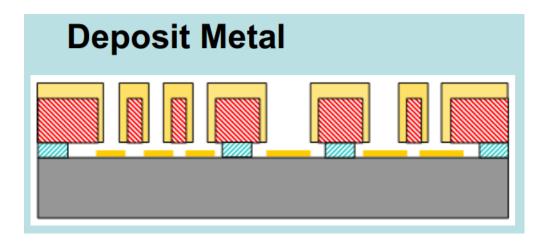
9. Oxide Etching

• Using HF to etch the SiO2 layer



10. Deposit Metal

· deposit metal



11. Assemble

