L represents Issue.	K	, ve	prese	en ts	K	ead	epe	erar	nds.	. E	= ,	epre	280	nts .	:Xe	ple	ion Les		И	/	repr	ense	ents	W	rite h	lesul
Instruction CC 1	2	. 3	4	5	в	7	8	9	10			13 1							20			72.5		60		2 (
, L.D Fb, 34 (RZ) I	R	E	W																							L
) L.D F2,45(R3)				Ī	R	E	W																			4
MUL.D FO, F2, F4					I			12										E	W							15
SUB.D F8, F2, F6						I		R		E	W															16
PIV. P Flo. Fo, Fb							Ī													R	,				EV	V 4
ADD 156, 18, 12						Ļ							3	E	_		,				W	_	<u> </u>			\perp \prime
Issue: be stalled unti	Ι,	inst	ruet	ion	1 th	teno	ling	g E	U W	rite	to	th	Q	same	1	egi	ster	c	re c	om	plet	ed	ca	woi	dV	1AU
Issue: be stalled unti Read operands: wait unti	il	all	ope	era	nds	bo	ed	me	au	rai	lat	le o	Ca	woi	d	R	4 M	1)								
Execution: start after	R																									
Write Result: be delayed wants to m	d	uni	til e	arl	rer	inst	rue	tion	ru –	<u> </u> и	uhic	ch 1	nti	end	to	re	ad	re	gust	ers	th	15	TILL	tru	ction	
wants to w	/rit	te	to -	_	ha	ve ,	DIM	uple	ted	Lt	hei	re	eae	1 of	erc	una	5	sti	$ag \epsilon$	۱ د	cav	701	d	WA	R)	
50 L.D Fb, 34 (R2) tak	es	4	C	loc	2 0	ycle	5,																			
L.D F2,45 (R3) tak																										
MULD FO, F2, F4 f																										
SUB.1> 1=8, F≥, Fb 7	ak	es	6 c	be	k	cycle	3																			
DIV, D Flo, Fo, Fb t	ake	3	45	clo	ck'	cycl	es																			
ADD D FL. FS. F.) +	We e	3	10	ch	ck	cycl	es																			
ADD Fb.F8,F2 to	,,,,,					()																				
	, ,,,,					O																				
(د)						<u> </u>						1.		1 ,		11										
(2)						<u> </u>		, N	/ ne	epre	onser	ts 1	Wi	rite A	lea	ılt										
I represents Issue			epre	sen:		<u> </u>		T			_		_		lesc 11	. T	7	7	z z	<u>.</u>	14	CC				
I represents Issue Instruction CC		- - 2	epre 3	sen:	ts .	<u> </u>	ıte	T		epre v 1	_	2 13	_		Τ.	. T	7	7	2 2	3	14	CC				
I represents Issue Instruction (1) L.D Fb, 34 (R2)		- y 2 E	epre 3	sen: 4	ts .	<u> </u>	ıte	T			_		_		Τ.	. T	7	7	2 2	3	514	3	•			
I represents Issue Instruction (1) L.D Fb, $34(R^2)$ (1) L.D F2, $45(R^3)$		- - 2	epre 3 (W E	sen: u	ts .	<u> </u>	ıte	T			_		3 1	4 13	Τ.	. T	7	7	2 5	ት	11	3 3	-			
I represents Issue Instruction (1) L.D Fb, 34 (R2) (1) L.D F2, 45 (R3) (10) MUL.D F0, F2, F4		- y 2 E	epre 3 (W E	sen: 4 W E	ts 5	Execu b 7	rte 8	T			_		3 1		Τ.	. T	7		2 2	<u>ځ</u> .	114	3 3 12				
I represents Issue Instruction (1) L.D Fb, 34 (R2) (1) L.D F2, 45 (R3) (10) MUL.D F0, F2, F4 (12) SVB.D F8, F2, Fb		- y 2 E	epre 3 (W E	sen: 4 W E	ts .	Execu b 7	ıte	T			_		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	4 13 V	Τ.	. T	7		2 2			3 3				
I represents Issue Instruction (1) L.D Fb, 34 (R2) (1) L.D F2, 45 (R3) (10) MUL.D F0, F2, F4 (2) SVB.P F8, F2, Fb 40) PIV.P F10, F0, Fb		- y 2 E	epre 3 (W E	sen: 4 W E	ts 5	Execu b 7	rte 8	T	7 1		_		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	4 13	Τ.	. T	7	3	2 5		M 214	3 3 12 4				
I represents Issue Instruction (1) L.D Fb, 34 (R2) (1) L.D F2, 45 (R3) (10) MUL.D F0, F2, F4 (12) SVB.D F8, F2, F6 (140) PIV.D F10, F0, Fb 21) ADD D Fb F8, F2	, <u>[</u> <u> </u> <u> </u>	- - 2 E I	epre 3 (W E	sen: 44 W E I	ts 5 []	Execu b 7	ite 8	3 (9 11 N	υ <u>Ι</u>	I (2 13	; , , v	V 13	11	,					W	33124				
I represents Issue Instruction (1) L.D Fb, 34 (R2) (1) L.D F2, 45 (R3) (10) MUL.D F0, F2, F4 (12) SVB.P F8, F2, F6 (140) PIV.P F10, F0, Fb 21) ADD D F6 F8, F2 ISSUE: be issued if a	I I	2 E I	3 W E II	sen: W E I	5 5 1 ar	Execute 7	ite 8 W E mese	V	y 1	0 1	1 1	2 13	V E	4 13 V	11	,					W	33124		L.		
I represents Issue Instruction (1) L.D Fb, 34 (R2) (1) L.D F2, 45 (R3) (10) MUL.D F0, F2, F4 (2) SVB.P F8, F2, Fb (40) PIV.P F10, F0, Fb 2) ADD D F6 F8, F2 ISSUE: be issued if a Registers are	I I I	E I	epre 3 W E 1 I	sen: 44 W E I	E 1 ar	Execute 7	ite 8	v v	9 1	n	stan	2 13	V E	4 13 V	nec	roly	01		lse.	tll	W	33124		L.		
I represents Issue Instruction (1) L.D Fb, 34 (R2) (1) L.D F2, 45 (R3) (10) MUL.D F0, F2, F4 (10) SVB.D F8, F2, F6 (10) PIV.D F10, F0, Fb (11) ADD D F6 F8, F2 ISSUE: be issued if a Registers are Execute: be delayed	I I I I I I I I I I I I I I I I I I I	E I Openai	B I I I I I I I I I I I I I I I I I I I	sen: 44 W E I	E 1 ar	Execute 7	ite 8	v v	9 1	n	stan	2 13	V E	4 13 V	nec	roly	01		lse.	tll	W	33124		L.		
I represents Issue Instruction (1) L.D Fb, 34 (R2) (1) L.D F2, 45 (R3) (10) MUL.D F0, F2, F4 (12) SVB.P F8, F2, F6 (140) PIV.P F10, F0, Fb (12) ADD D F6 F8, F2 ISSUE: be issued if a Registers are Execute: be delayed Write result: start-a	I. I. re	E I Openain	epree 3 WE 1 I I I I I I I I I I I I I I I I I I	w E I ds	E 1 ar	Execute 7	ute 8 W = d	v v	9 1	n	stan	2 13	V E	4 13 V	nec	roly	01		lse.	tll	W	33124		L.		
I represents Issue Instruction (1) L.D Fb, 34 (R2) (1) L.D Fb, 34 (R2) (1) L.D Fo, F2, F4 (2) SVB.P F8, F2, Fb (3) PIV.P F10, F0, Fb (4) PIV.P F10, F0, Fb (5) ADD D F6 F8, F2 Issue: be issued if a Registers are Execute: be delayed Write result: start a SO L.D Fb, 34 (R2) ta	I I I ste	2 E I I OP 2 Na 1	eranne of this E	sen: W E I ds cloc	ts 5 E 1 ar (a k	Execute 7 I i od 10 volumba	ute E W = d d d	v v	9 1	n	stan	2 13	V E	4 13 V	nec	roly	01		lse.	tll	W	33124		L.		
I represents Issue Instruction (1) L.D Fb, 34 (R2) (1) L.D F2, 45 (R3) (10) MUL.D F0, F2, F4 (12) SVB.P F8, F2, F6 (140) PIV.D F10, F0, Fb 2) ADD D F6 F8, F2 Issue: be issued if a Registers are Execute: be delayed Write result: start a SO L.D F6, 34 (R2) ta	I I I ste	2 E I I OP 2 Na 1	eranne of this E	sen: W E I ds cloc	ts 5 E 1 ar (a k	Execute 7 I i od 10 volumba	ute E W = d	v v	9 1	n	stan	2 13	V E	4 13 V	nec	roly	01		lse.	tll	W	33124		L.		
I represents Issue Instruction (1) L.D Fb, 34 (R2) (1) L.D F2, 45 R3) (10) MUL.D F0, F2, F4 (12) SVB.P F8, F2, F6 (14) PIV.P F10, F0, F6 (15) ADD D Fb F8, F2 Issue: be issued if a Registers are Execute: be delayed Write result: start - a SO L.D Fb, 34 (R2) ta AUULD FD, F2, F4	I I I I I I I I I I I I I I I I I I I	E I I OPPARIN	eran E I I E E E E E E E E E E E E E E E E E	w E I ds stocker	E 1 ar	Execute 1 1 is a solution of the contract of t	ute E V	v v	9 1	n	stan	2 13	V E	4 13 V	nec	roly	01		lse.	tll	W	33124		L.		
I represents Issue Instruction (1) L.D Fb, 34 (R2) (1) L.D F2, 45 R3) (10) MUL.D F0, F2, F4 (12) SVB.P F8, F2, F6 (14) PIV.P F10, F0, F6 (15) ADD D Fb F8, F2 Issue: be issued if a Registers are Execute: be delayed Write result: start - a SO L.D Fb, 34 (R2) ta AUULD FD, F2, F4	I I I I I I I I I I I I I I I I I I I	E I I OPPARIN	eran E I I E E E E E E E E E E E E E E E E E	w E I ds stocker	E 1 ar	Execute 1 1 is a solution of the contract of t	ute E W E C C C C C C C C C	v v	9 1	n	stan	2 13	V E	4 13 V	nec	roly	01		lse.	tll	W	33124		L.		
I represents Issue Instruction (1) L.D Fb, 34 (R2) (1) L.D F2, 45 (R3) (10) MUL.D F0, F2, F4 (12) SVB.D F8, F2, F6 (140) PIV.D F10, F0, Fb 12) ADDD F6 F8, F2 Issue: be issued if a Reg issers are Execute: be delayed Write result: start a SD L.D F6, 34 (R2) ta	I I I I I I I I I I I I I I I I I I I	E I OPPORT	E I I E I I I I I I I I I I I I I I I I	ds ds clocked clocked clocked	E 1 arep k (a ck sick sick)	Execute 1 1 is a solution of the contract of t	rese	V V V V W A U E	9 1	n	stan	2 13	V E	4 13 V	nec	roly	01		lse.	tll	W	33124		L		