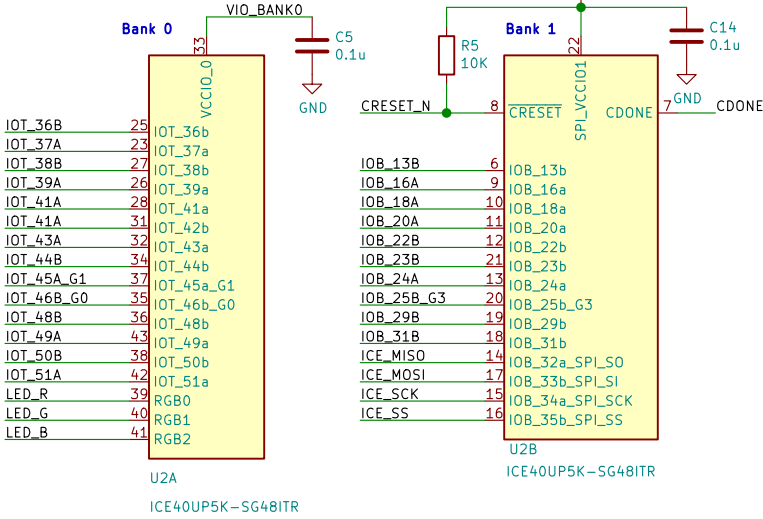
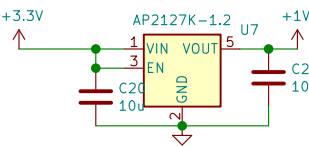


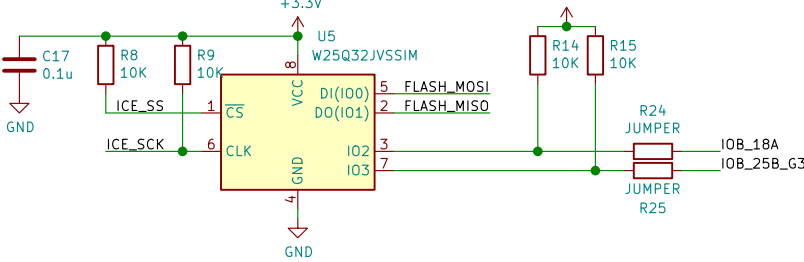
FPGA Banks



Regulator (300mA max

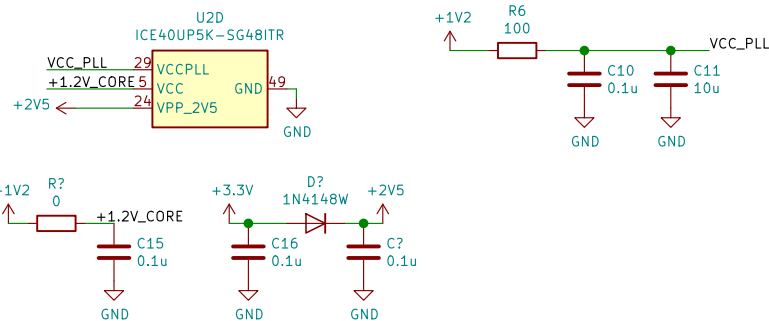


qSPI/DTR Flash

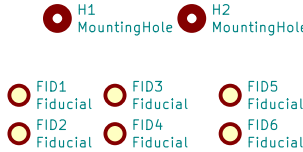
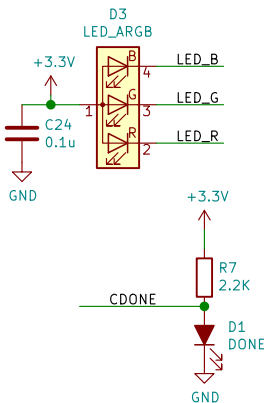
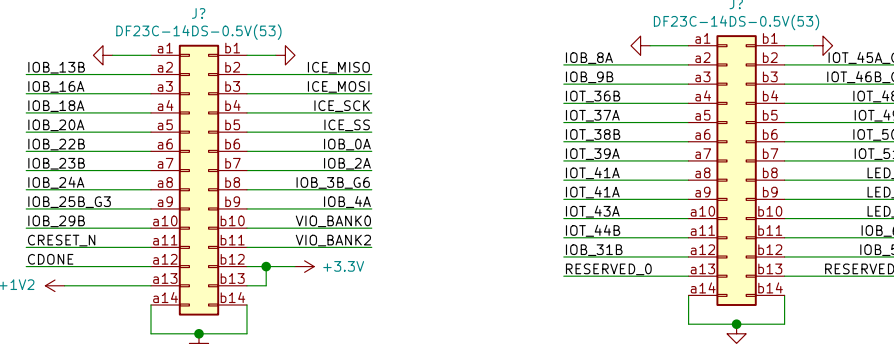


LED's:
should these be on
the module at all?

FPGA Power



Module Interface



tinyVision.ai Inc

Sheet: /
File: UPduino.sc

Title: ICE40 SoM

Size: A3	Date:
KiCad E.D.A. kicad (5.0.2)-1	

Rev: 3.0 v0.1

Id: 1/1