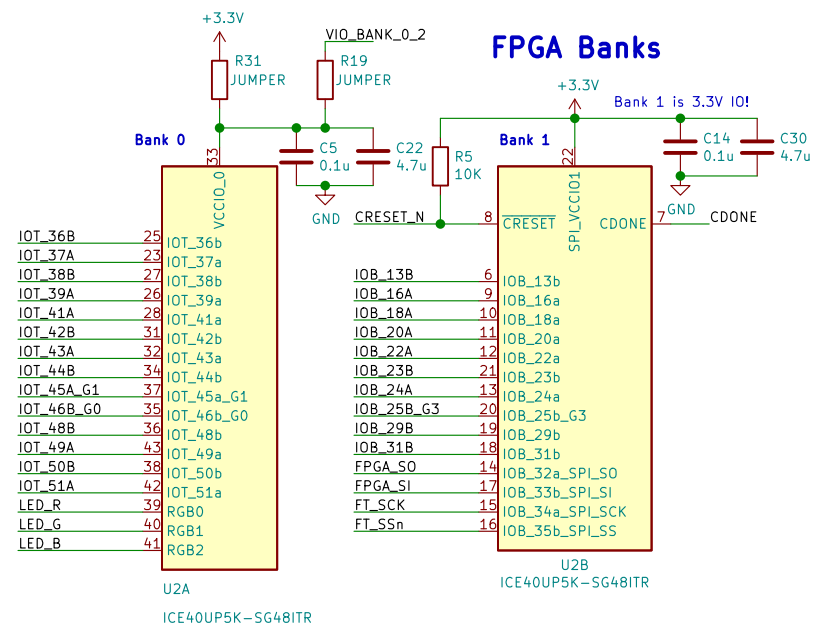
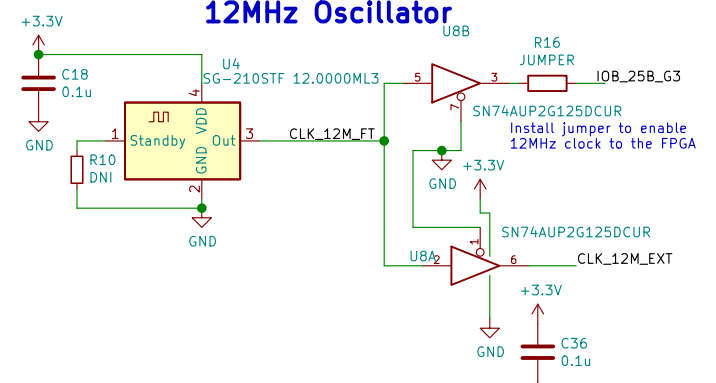


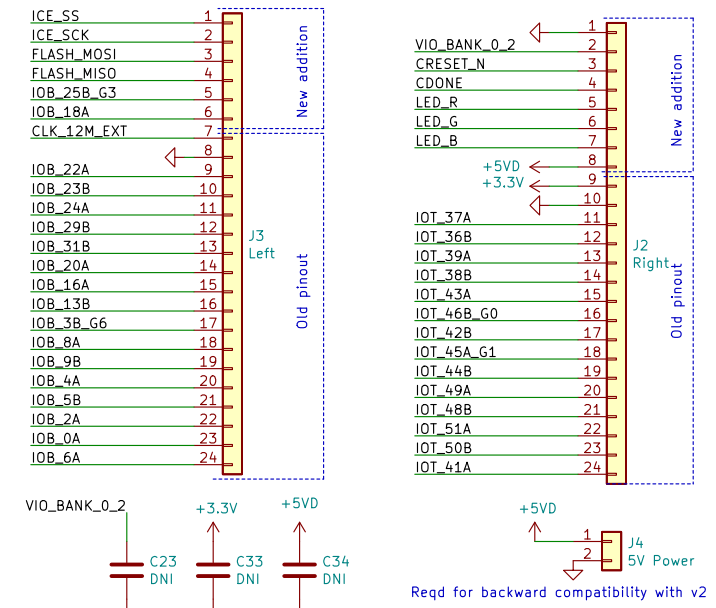
FPGA Banks



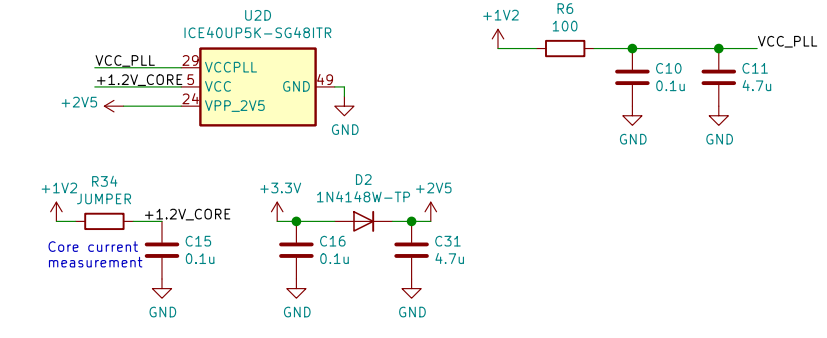
12MHz Oscillator



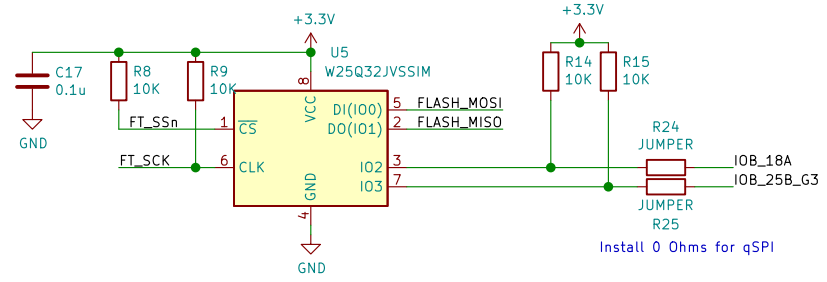
Board Connections



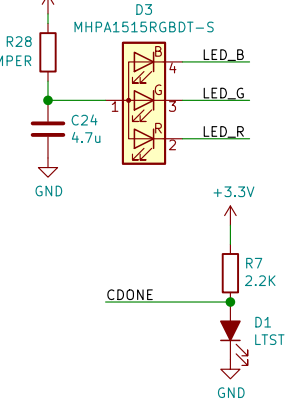
FPGA Power



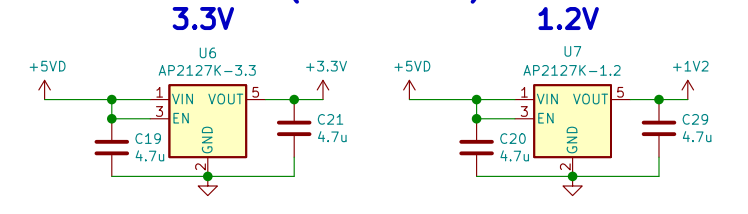
qSPI/DTR Flash



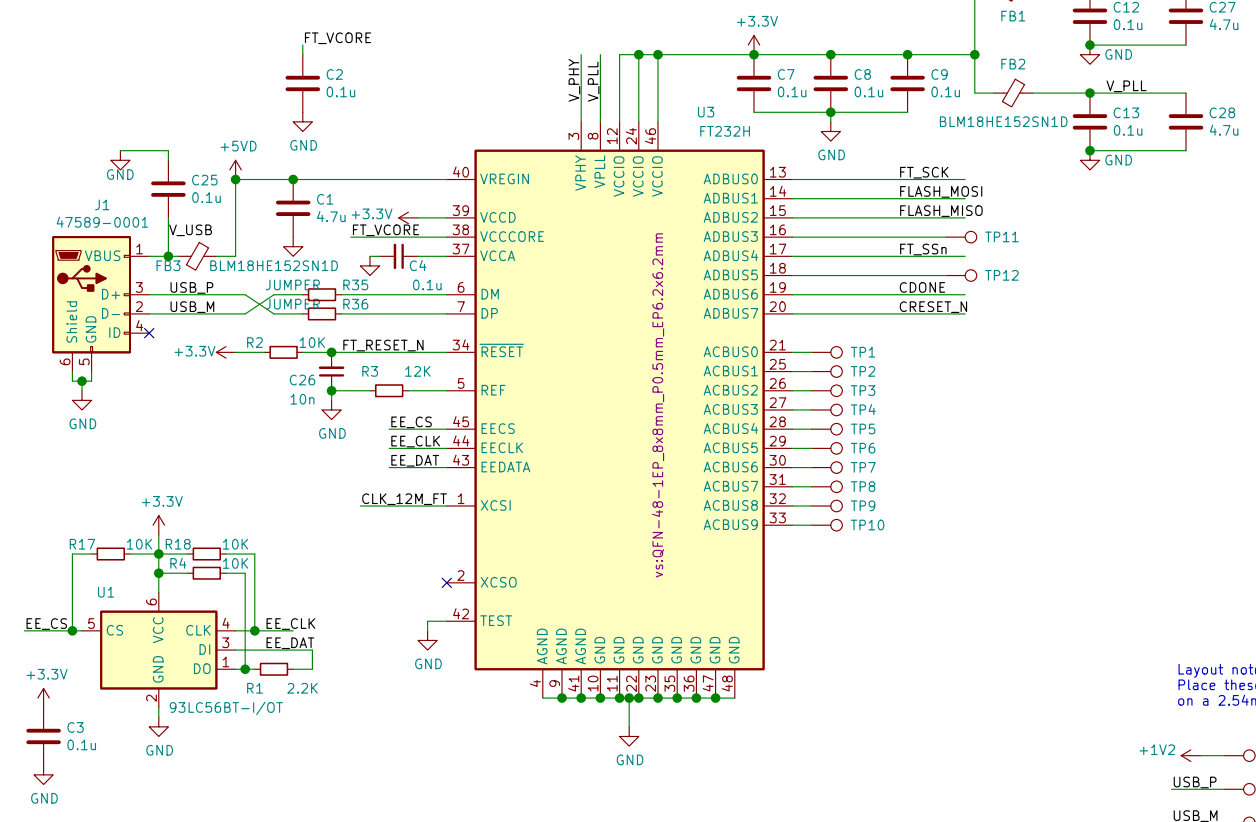
LED



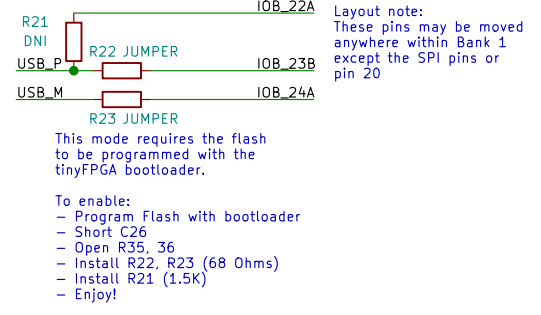
Regulators (300mA max)



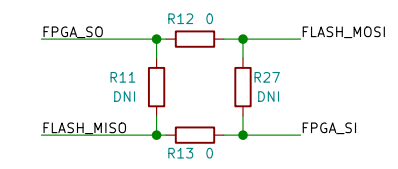
FTDI programmer



tinyFPGA Bootloader



FPGA CRAM/Flash programming

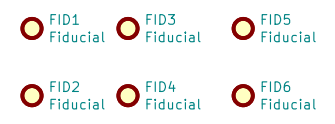


Programming modes:

Flash: Default, horizontal short

CRAM: Optional, vertical short

Layout note: Place these testpoints on the bottom of the board on a 2.54mm grid.



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Sheet: /

File: UPduino_v3.0.sch

Title: UPduino

Size: A3

Date: /

KiCad E.D.A. kicad (5.1.5)-3

Rev: 3.0 v0.1

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