

Question 1. What is a thread?

Question 2. What is the main thread? What are interrupt threads?

Question 3. What are the five steps that occur automatically (in hardware) as the context switches from the main thread to an interrupt thread?

Question 4. Define the following terms as they relate to interrupts.

- Hardware trigger

- Interrupt enable bit I in the PRIMASK register

- Interrupt enable bit in the NVIC_EN0_R register

- Interrupt priority in the NVIC_SYS_PRI3_R or NVIC_PRI1_R register

- Interrupt arm bit like bit1 (INTEN) in the NVIC_ST_CTRL_R register

- Interrupt vector

Question 5. What is an interrupt acknowledge? How does the SysTick interrupt get acknowledged and how is SysTick acknowledge different from the other interrupts?