Pin Definitions for MRR ESPE

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ESP32 pins

0: unused (IO0; must not be pulled low for normal bootup)

1: TX

2: HEATER\_0\_PIN

3: RX

4: HEATER\_BED\_PIN

5: SS\_PIN (SDSS)

12: Z\_DIR\_PIN

13: FAN\_PIN

14: Z\_STEP\_PIN

15: Z\_MIN\_PIN

16: E0\_STEP\_PIN

17: E0\_DIR\_PIN

18: SCK\_PIN

19: MISO\_PIN

21: unused (SDA)

22: unused (SCL)

23: MOSI\_PIN

25: X\_ENABLE\_PIN (shared with Y, Z, and E0)

26: X\_DIR\_PIN

27: X\_STEP\_PIN

32: Y\_DIR\_PIN

33: Y\_STEP\_PIN

34: X\_MIN\_PIN

35: Y\_MIN\_PIN

36 (VP): TEMP\_0\_PIN

39 (VN): TEMP\_BED\_PIN