Data Lab: Manipulating Bits Documentation

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1 Comments for bits.c

1.1 Bit Manipulations

1.1.1 bitAnd(x,y)

```
1 int bitAnd(int x, int y) {
2   return ~((~x)|(~y));
3 }
```

Description: x&y using only and |

Comments: See **x** and **y** as two sets X, Y, we have **x&y** to be the intersection of them $X \cap Y$ which is equivalent to $(X^c \cup Y^c)^c$.

1.1.2 getByte(x,n)

```
1 | int getByte(int x, int n) {
2 | return (x>>(n<<3))&0xff;
3 | }
```

Description: Extract byte n from word x Comments: The byte n from word x contains bit 8n to bit 8(n+1) of x, thus we right shift by 8n and get the last 8 bits with a mask 0xff. We can easily get 8n by n<<3.

1.1.3 logicalShift(x,n)

```
int logicalShift(int x, int n) {
   int TMin = 1 << 31;
   int repeat_msb = ((TMin&x)>>n) << 1;
   return (x>>n)^repeat_msb;
}
```

Comments: We know that right shift in C does arithmetic right shift, which will repeat the highest bit (namely, the Most Significant Bit). To reset these new bits shifted from left, we do XOR with repeat_msb, which only copies the Most Significant Bit of x in those new shifted bits from left and set others to 0. Note that our repeat_msb is constructed by trickly utilizing arithmetic right shift.

1.1.4 bitCount(x)

```
1
    int bitCount(int x) {
 2
      int tmp2 = (0x55) \mid (0x55 << 8);
 3
      int mask2 = tmp2 \mid (tmp2 << 16);
 4
      int tmp4 = (0x33) \mid (0x33 << 8);
 5
      int mask4 = tmp4 \mid (tmp4 << 16);
 6
      int tmp8 = (0x0f) \mid (0x0f << 8);
 7
      int mask8 = tmp8 \mid (tmp8 << 16);
 8
      x = (x \& mask2) + (x >> 1 \& mask2);
 9
      x = (x \& mask4) + (x >> 2 \& mask4);
10
      x = (x + (x >> 4)) \& mask8;
      x = (x + (x >> 8));
11
12
      x = (x + (x >> 16)) \& 0xff;
13
      return x:
14
```

Description: returns count of number of 1's in word

Comments: Let us first see the mask2: it after all is done.

1.1.5 bang(x)

```
int bang(int x) {
1
2
      x=(x>>1)|x;
3
     x=(x>>2)|x;
4
      x=(x>>4)|x;
      x=(x>>8)|x;
6
      x=(x>>16)|x;
7
      \mathbf{return} \sim \mathbf{x} \& 1;
8
```

has the shape **01 01 01 01 ... 01 01** in binary code. Thus after doing x = (x & mask2) + (x & mask2)>> 1 & mask2), we get every two bits of x count the number of 1 in these two bits. So do as x =(x & mask4) + (x >> 2 & mask4); with mask4= 0011 0011 ... 0011, which makes every 4 bits of x count the number of 1's in these 4 bits, that is to say, $B2U(x_{k+3}, x_{k+2}, x_{k+1}, x_k)$ where $k \mod 4 = 0$ is equal to the number of 1's on bit k+3, k+2, k+1 and k.

Notice that we MUST do the mask operation &mask2, &mask4, because it prevents those value polluting the count number when they are not masked. This kind of masking is not necessary after masking with mask8 because the clean lowest 8 bits is enough to store the maximum of the answer 32 which only needs 6 bits.

Then we extract the lowest 8 bits by &0xff

Description: Compute !x without using ! Comments: Compute !x is equivalent to find whether there is any 1 in x's binary code. Just like what we do in bitCount, after x=(x>>1)|x;, the lowest bit of every two bits represents whether there is 1 in those two bits. And then we do the same for every 4, 8, 16, 32 bits. Finally, the lowest bit of x represents whether there is 1 in all 32 bits. We extract it and return its opposite.

1.2 Two's Complement Arithmetic

1.2.1 tmin()

```
1 int tmin(void) {
2 return 1<<31;
3 }
```

Description: return minimum two's complement integer

Comments: It is clear by the knowledge of two's complement that TMin has 1 on the highest bit and 0 on others bits.

1.2.2 fitsBits(x,n)

Description: return 1 if x can be represented as an n-bit, two's complement integer

Comments: That **x** can be represented as an n-bit, two's complement integer is equiva-

lent to

$$-2^{n-1} \le x < 2^{n-1}$$

1.2.3 divpwr2(x,n)

```
int divpwr2(int x, int n) {
   int TMin = 1 << 31;
   int neg_mask = (x&TMin)>> 31;
   int bias = neg_mask & ((1 << n) + (~0));
   return (x+bias)>>n;
}
```

Description: Compute $x/(2^n)$, for $0 \le$

 $n \leq 30$. Round toward zero

Comments: We know that division in C always rounds toward $-\infty$. For non-negative result, that is what we expect to. But for negative result, we need a round-to-ceil division, by add a bias which is only less by 1 than the divisor, that is $2^n - 1$ in this case (we use $\mathbf{0}$ to denote -1 once again).

1.2.4 negate(x)

```
1 | int negate(int x) {
2 | return (~x+1);
3 | }
```

Description: return -x

Comments: It is clear by the knowledge of two's complement that -x has the binary code $\tilde{x}+1$.

1.2.5 isPositive(x)

```
1    int isPositive(int x) {
2        int not_neg = !(x&(1<<31));
3        int not_zero = !!x;
4        return not_neg & not_zero;
5    }</pre>
```

Description: return 1 if x > 0, return 0 otherwise

Comments: We determine whether x is non-nagative by see the highest bit of it and determine whether it is 0 by use! twice.

1.2.6 isLessOrEqual(x,y)

```
1
   int isLessOrEqual(int x, int y) {
2
     int TMin = 1 << 31;
3
     int x neg y pos = (x\&TMin)\&(\sim y\&
          TMin);
4
     int x_pos_y_neg = (\sim x\&TMin)\&(y\&
          TMin);
5
     int sub = y+(\sim x+1);
6
     int sub pos = \sim sub\&TMin;
7
     return !!((x_neg_y_pos \mid sub_pos) \& \sim
         x_pos_y_neg);
8
```

Description: if $x \le y$ then return 1, else return 0

Comments: Considering the influence of int overflow, we first determine whether x is negative and y is non-negative and denote it with x_neg_y_pos, and also determine whether x is non-negative and y is negative and denote it with x_pos_y_neg. Then for the remaining cases the difference between x and y is always in the range of int. We see the highest bit of the difference sub to determine its sign (note that we use ~x+1 to denote -x since operator - is illegal).

1.2.7 ilog2(x)

```
1
   int ilog2(int x) {
2
     int res = 0;
3
     res = (!!(x >> 16)) << 4;
     res = res + ((!!(x >> (8 + res))) << 3);
4
5
     res = res + ((!!(x >> (4+res))) << 2);
6
     res = res + ((!!(x >> (2 + res))) << 1);
7
     res = res + (!!(x >> (1+res)));
8
     return res;
9
```

Description: return floor(log base 2 of x), where x > 0

Comments: This method is kind of similar with binary search algorithm. We first determine whether the answer is greater or equal to 16 by seeing the highest 16 bits. If the higher 16 bits are non-zero, than the answer is added by 16 and we pay our attention to the higher 16 bits; otherwise we pay our attention to the lower 16 bits. The same things are down for those 8, 4, 2, 1 bit(s) of interest.

1.3 Floating-Point Operations

1.3.1 float_neg(uf)

```
unsigned float_neg(unsigned uf) {
unsigned exp = uf&0x7f800000;
unsigned fra = uf&0x007fffff;
if (!(exp^0x7f800000) && fra) // is nan
return uf;
return uf^(1<<31);
}</pre>
```

Description: Return bit-level equivalent of expression -f for floating point argument f.

Comments: We need to process NaN specially (By extract exp and frac using mask, we can judge whether uf is not a number). For other cases, it is easy to inverse the highest bit which denotes the sign of uf and then we get the opposite.

1.3.2 float_i2f(x)

```
1
    unsigned float i2f(int x) {
 2
      int tmp = x;
 3
      unsigned sgn = x\&(1 << 31);
      unsigned \exp = 0;
 4
 5
      unsigned fra = 0;
      if (x==-2147483648) return 0xcf000000;
 6
      if (x==0) return 0;
 7
      if (sgn)\{x = -x; tmp = x; \}
 8
 9
      while (tmp>1){
10
        \exp = \exp + 1; tmp = tmp >> 1;
11
12
      fra = x - (1 < \exp);
13
      if (\exp < =23) fra = fra < <(23-\exp);
14
15
        int len = \exp{-23}, mid = 1 < < (len-1);
16
        tmp = fra; fra = fra >> len;
17
        tmp = tmp - (fra << len);
18
        if ((tmp == mid \&\& (fra\&1)) || tmp >
             mid)
          fra = fra+1;
19
20
21
      return sgn + ((exp+127) << 23) + fra;
22
```

Comments: (Lines 6-7) We process - 2147483648 and 0 specially: for the former, its opposite can not be represented by int, and for the latter, it is a denormalized number (other integers are always normalized number).

(Lines 9-12) Then we try to extract exp and frac use a while loop which divides absolute value of x until it turns to 0. And by ignoring the highest non-zero bit, we get frac.

(Lines 13-20) The last thing to do is rounding frac. If exp is not greater than 23, we do a left shift to align frac. Otherwise, we prune the last exp-23 bits, and if the tail is greater than a half or equal to a half but the remaining frac is odd, then the remaining frac is added by 1, which behaves exactly as a rounding toward nearest even.

1.3.3 float_twice(uf)

```
unsigned float twice(unsigned uf) {
 1
 2
       unsigned sgn = uf\&0x800000000;
 3
       unsigned \exp = uf\&0x7f800000;
 4
       unsigned fra = uf\&0x007fffff;
 5
       if (!(exp^0x7f800000) && fra) // is nan
 6
           return uf;
 7
      if (!(exp^0x7f800000) && !fra) // is inf
 8
           return uf;
 9
      if (\exp)\{ // \text{ normalized} \}
10
           \exp = \exp + 0x00800000;
11
          if (!(exp^0x7f800000))
12
               fra = 0;
13
           return sgn + exp + fra;
14
15
       // denormalized
16
      fra = fra << 1;
17
      return sgn + exp + fra;
18
```

Description: Return bit-level equivalent of expression 2*f for floating point argument f.

Comments: We first extract sgn, exp, frac with masks. And we do the categorized discussion:

- 1. nan and inf is same with the double of themselves;
- for normalized number, we need to add exp by 1, and if the result is larger than inf, we round it to inf;
- 3. for denormalized number, we just do a left shift to frac (it is fortunately to see that when the highest bit of frac is moved to the region of exp then it naturally turns to normalized number).

2 Impressions of Data Lab

By solving Data Lab, I get more familiar with bit-level representations of integers and floating point numbers and can skillfully manipulate bits to give some interesting operations.

Some of these puzzles are pretty easy but some are really challenge which makes me enjoy this process of puzzling out.